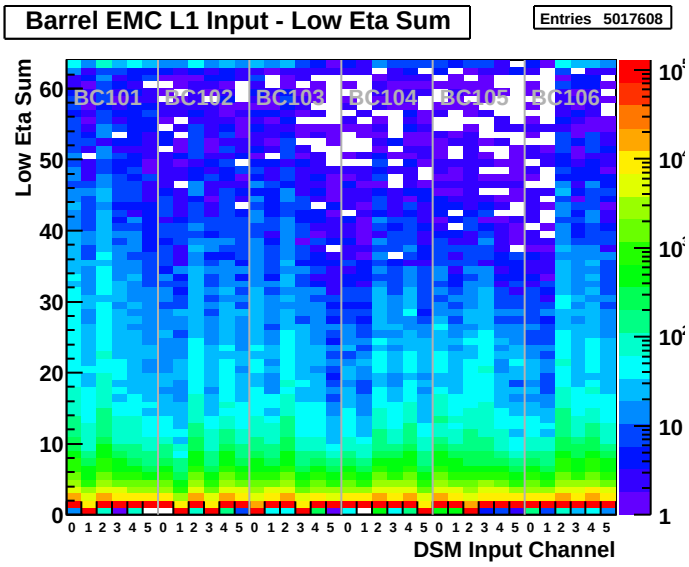
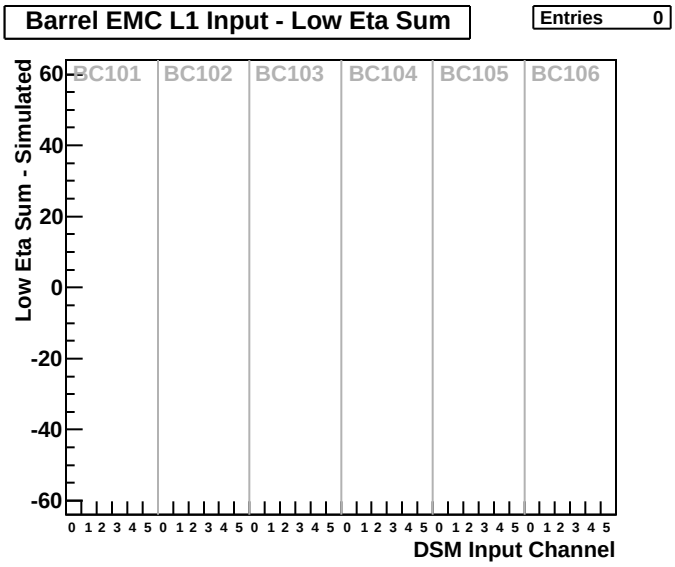
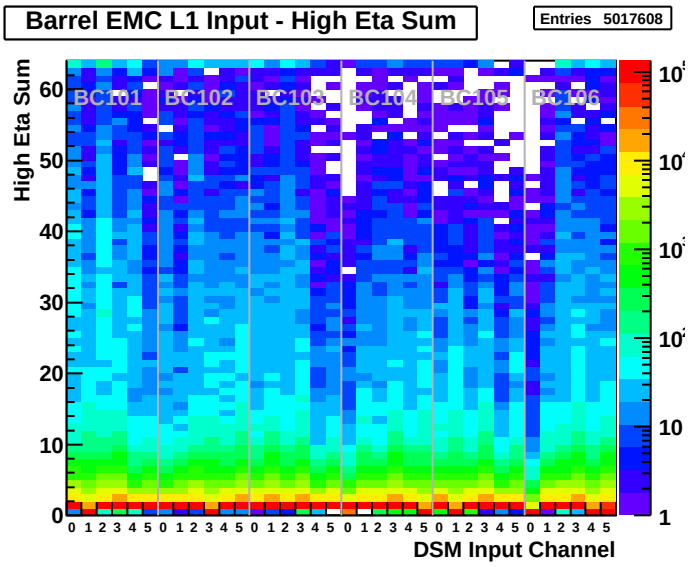
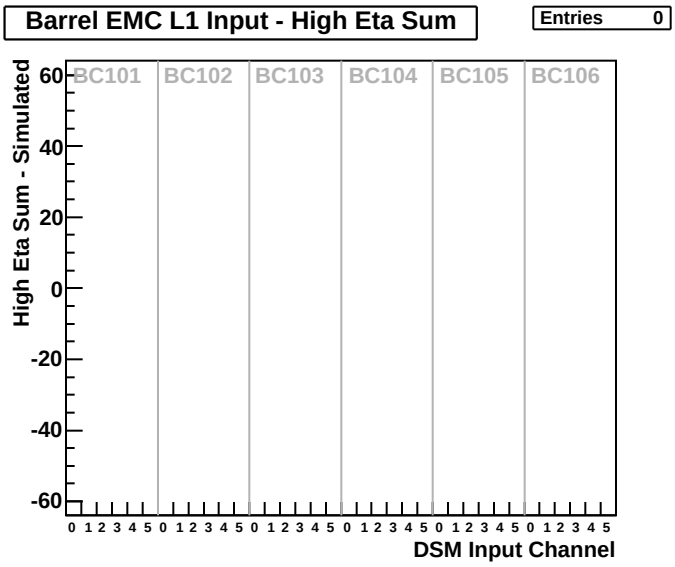
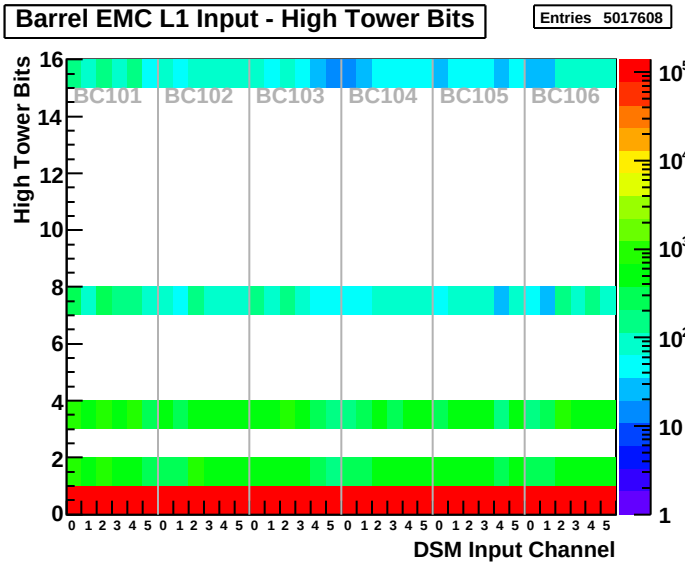
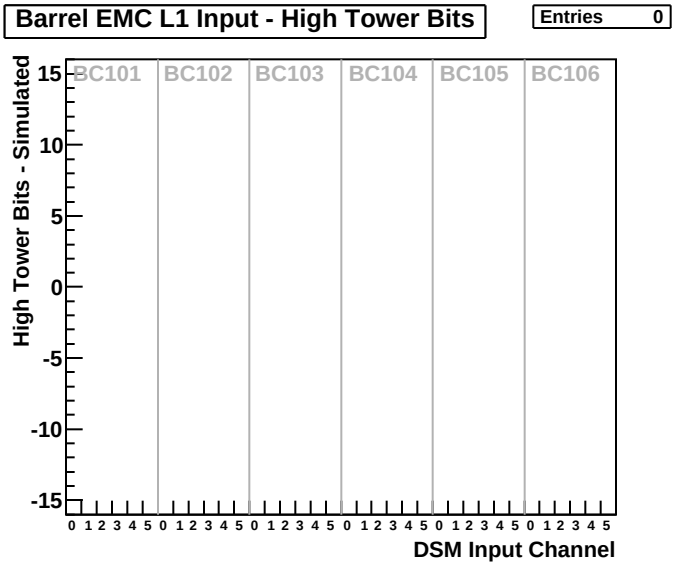

Barrel EMC L1 Input - Low Eta Sum
Entries 5017608
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

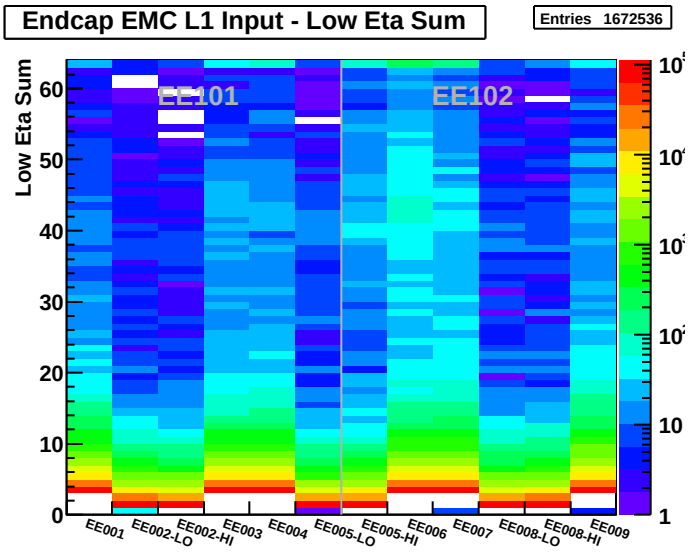

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

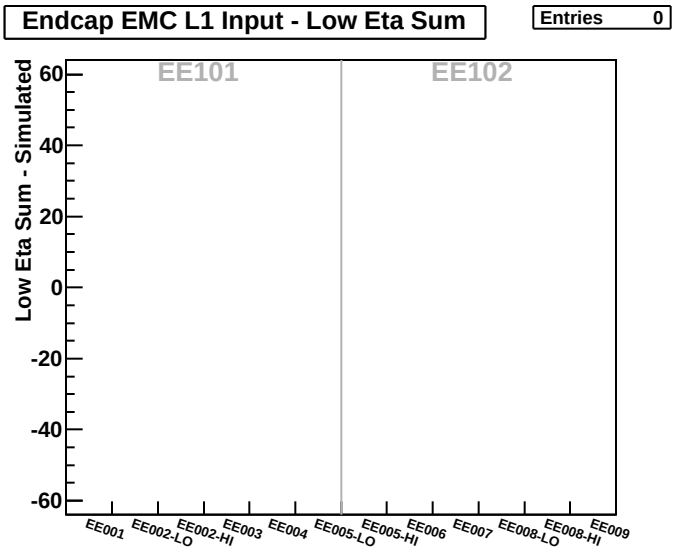

Barrel EMC L1 Input - High Eta Sum
Entries 5017608
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

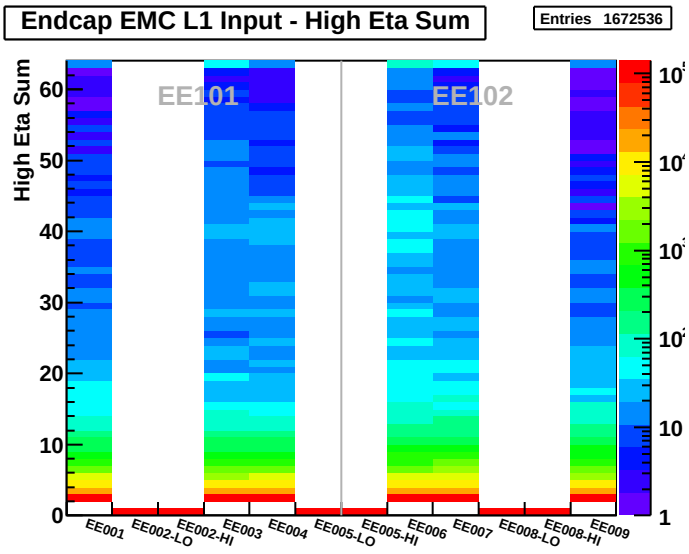

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

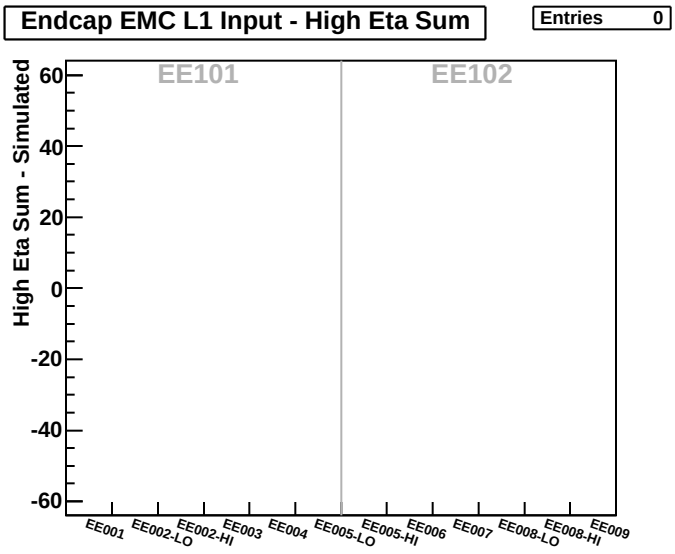

Barrel EMC L1 Input - High Tower Bits
Entries 5017608
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

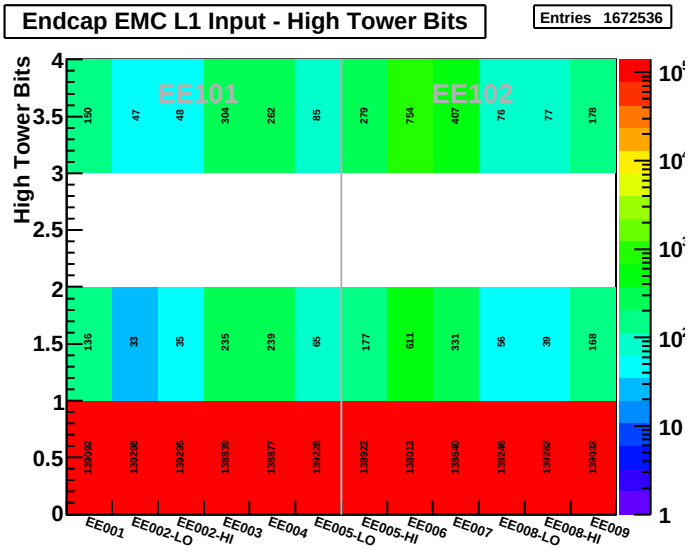

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

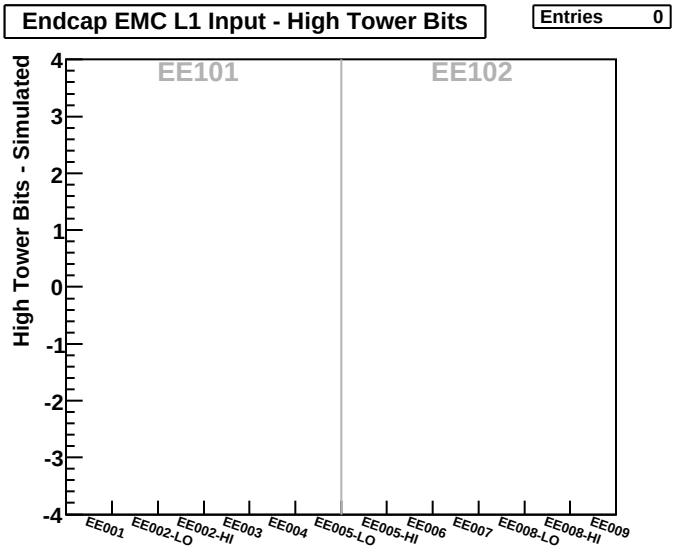

Endcap EMC L1 Input - Low Eta Sum
Entries 1672536
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

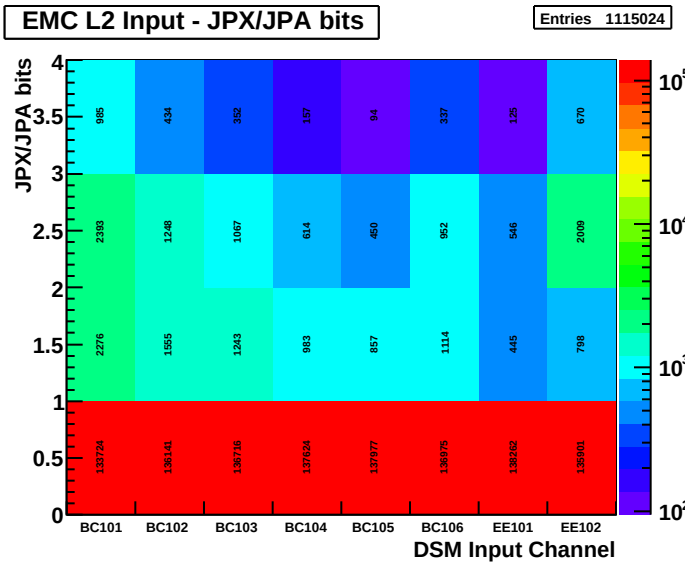

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

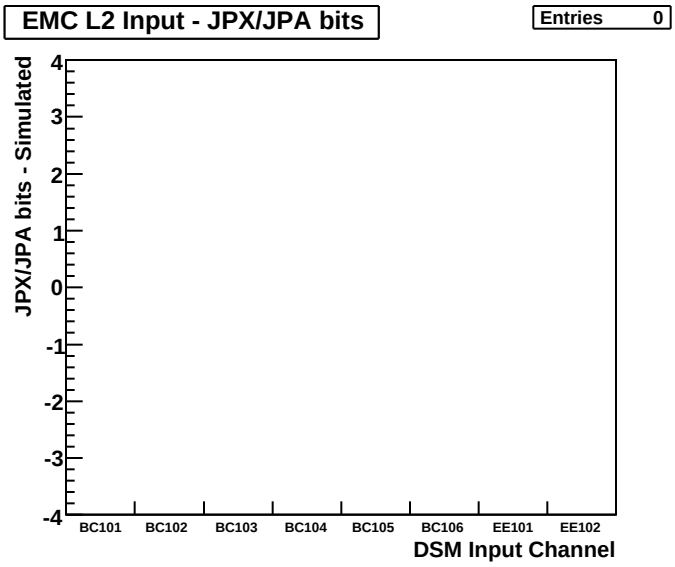

Endcap EMC L1 Input - High Eta Sum
Entries 1672536
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

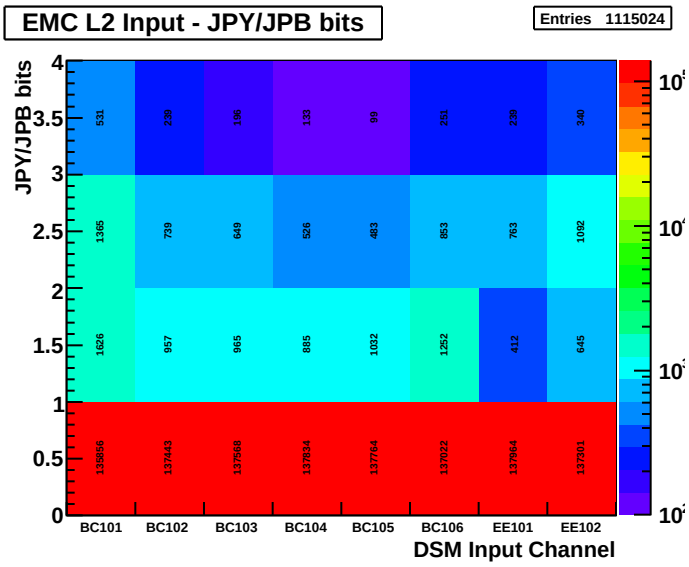

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

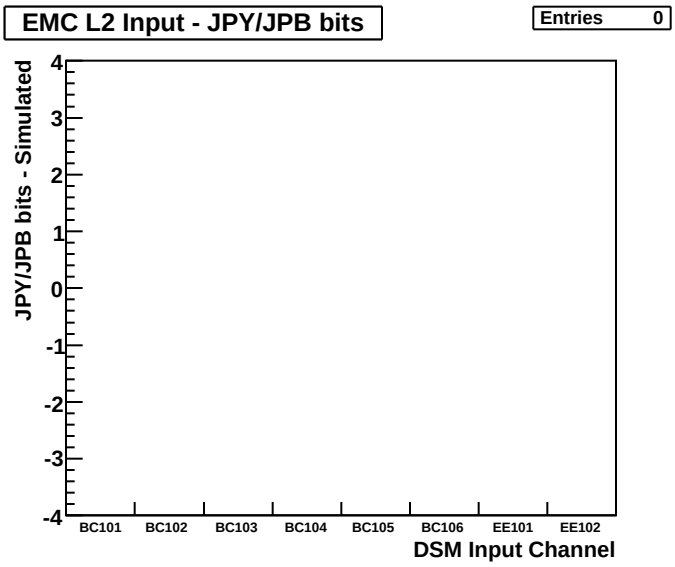

Endcap EMC L1 Input - High Tower Bits
Entries 1672536
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

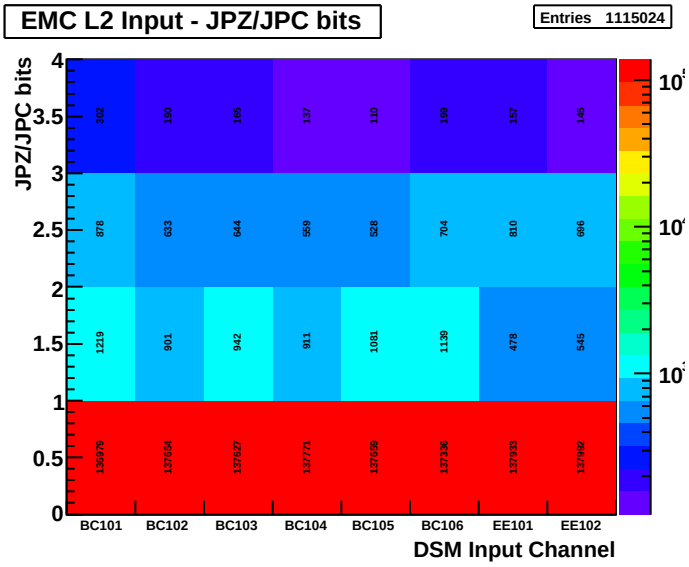

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

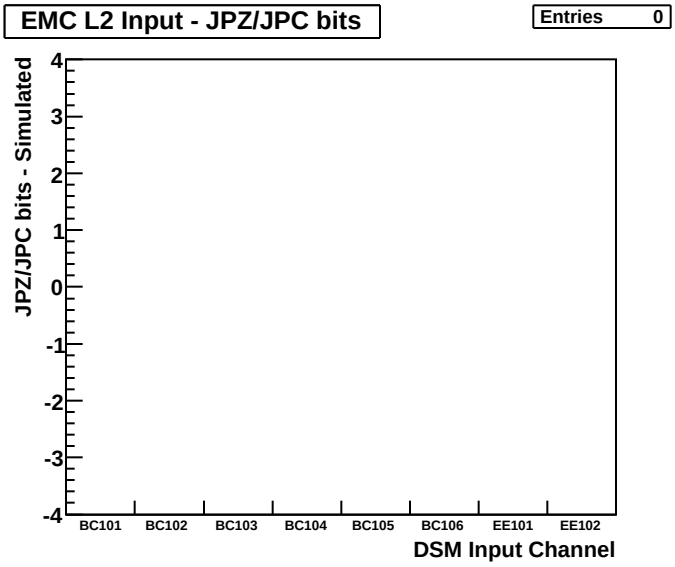

EMC L2 Input - JPX/JPA bits
Entries 1115024
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
985 434 352 157 94 337 125 670
2393 1248 1067 614 450 952 546 2000
2276 1555 1243 983 857 1114 445 798
133724 136141 138716 137624 137977 136975 138262 135901

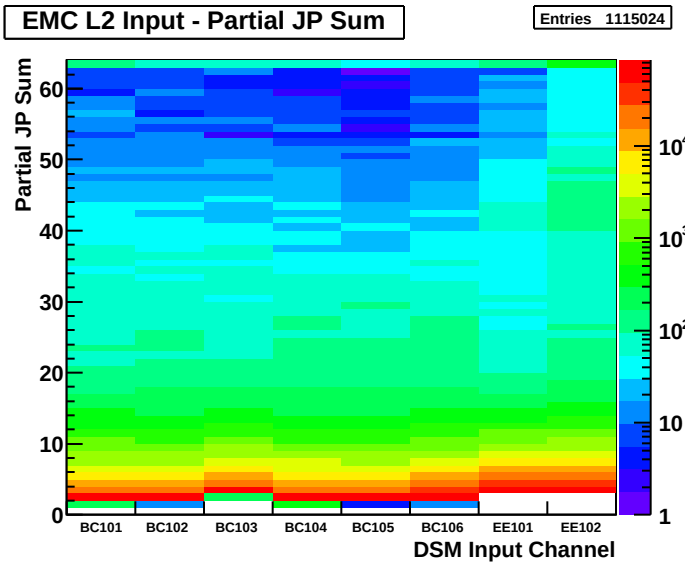

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

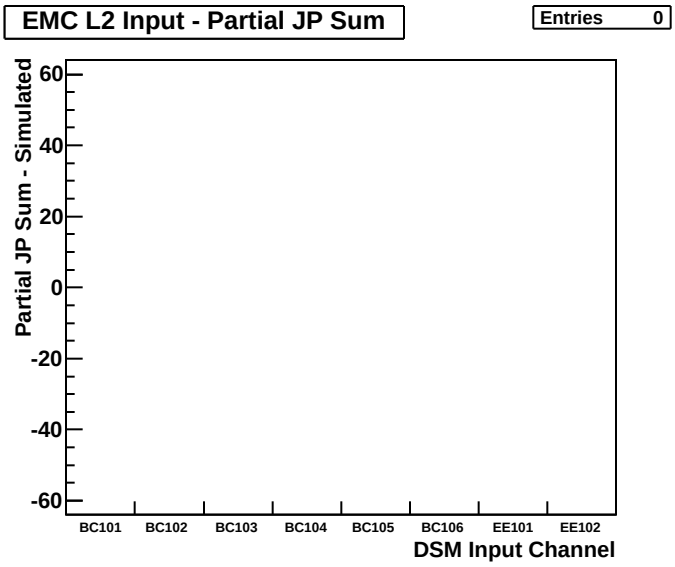

EMC L2 Input - JPY/JPB bits
Entries 1115024
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
531 239 196 133 99 251 239 340
1365 739 649 526 483 853 763 1092
1626 957 965 885 1032 1252 412 645
138856 137443 137568 137934 137764 137022 137964 137301

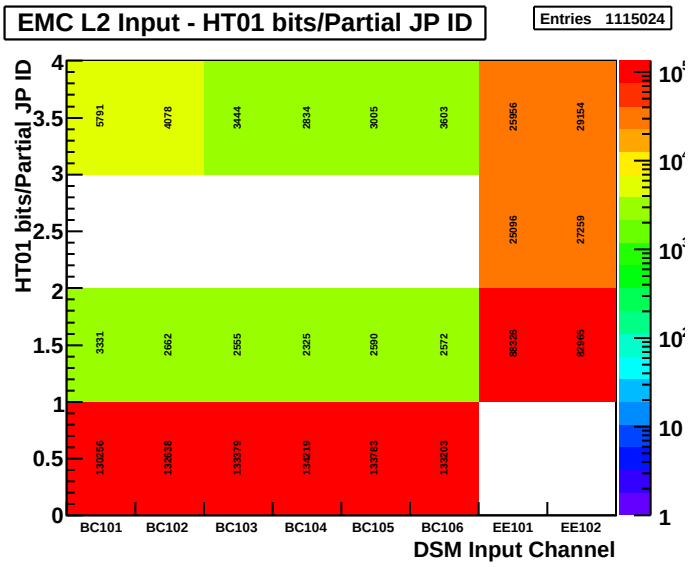

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

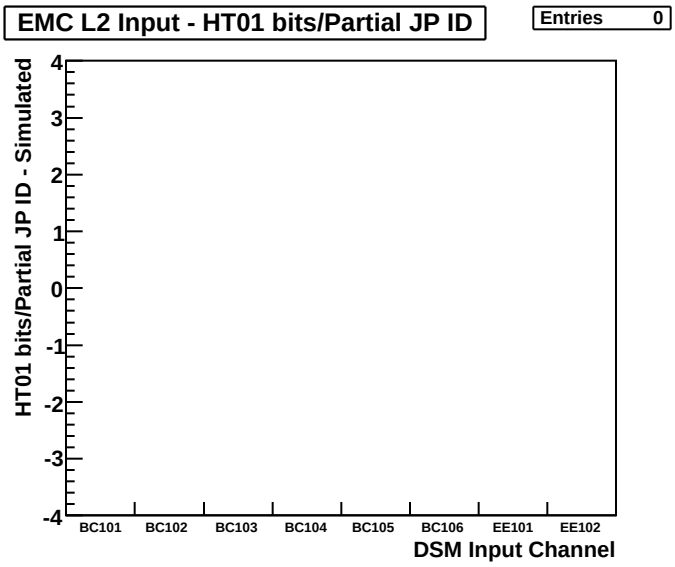

EMC L2 Input - JPZ/JPC bits
Entries 1115024
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
382 190 165 137 110 199 157 145
878 633 644 559 528 704 810 696
1219 901 942 911 1081 1139 478 545
136979 137654 137627 137771 137659 137338 137933 137932

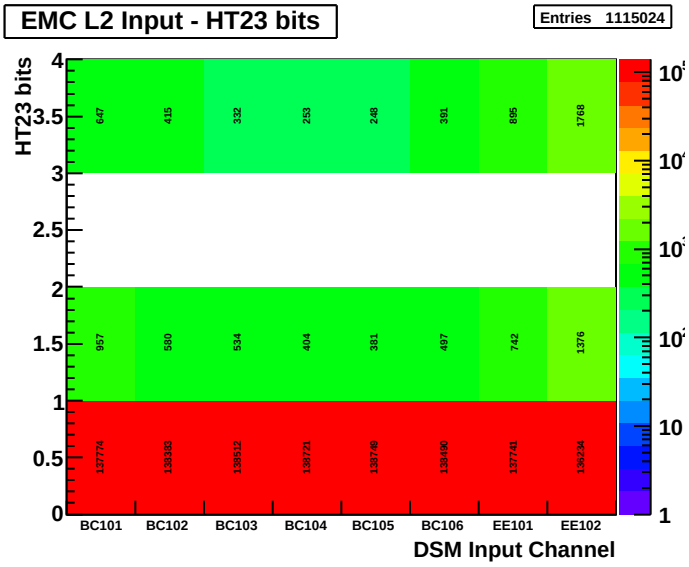

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

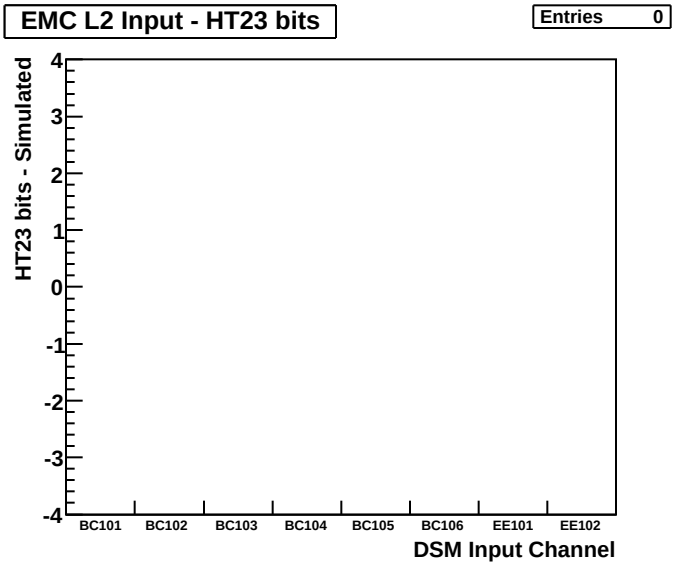

EMC L2 Input - Partial JP Sum
Entries 1115024
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

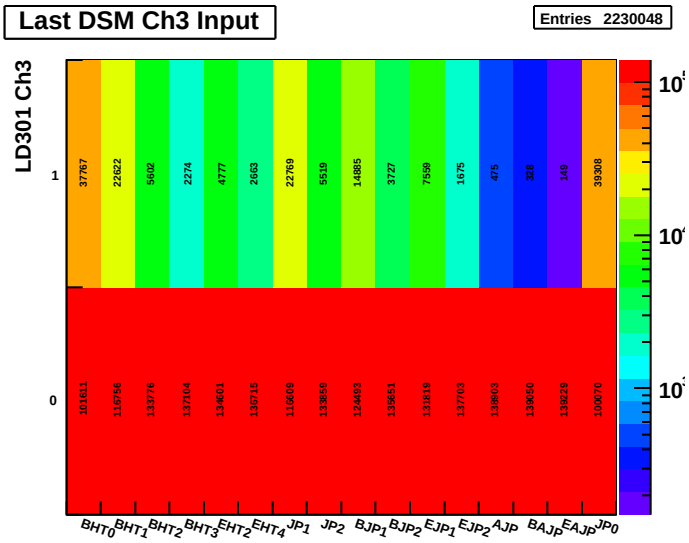

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

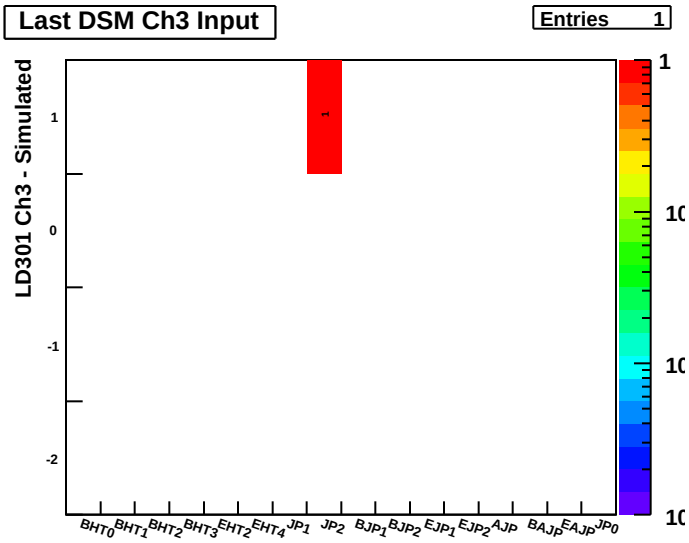

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1115024
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

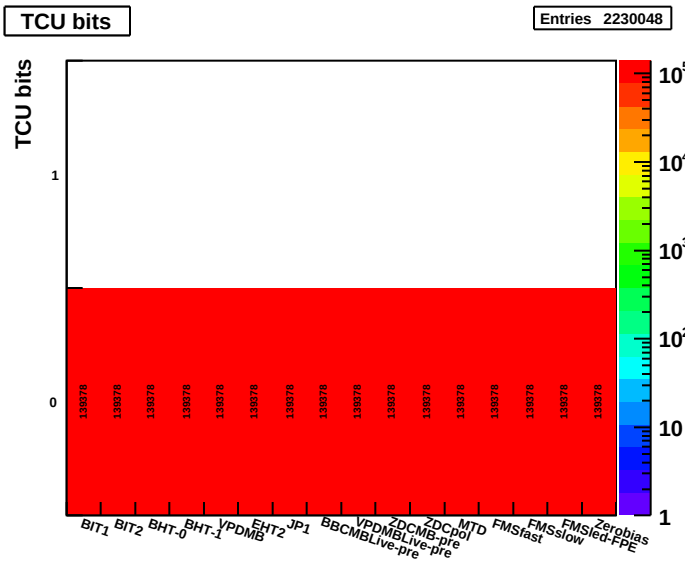

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

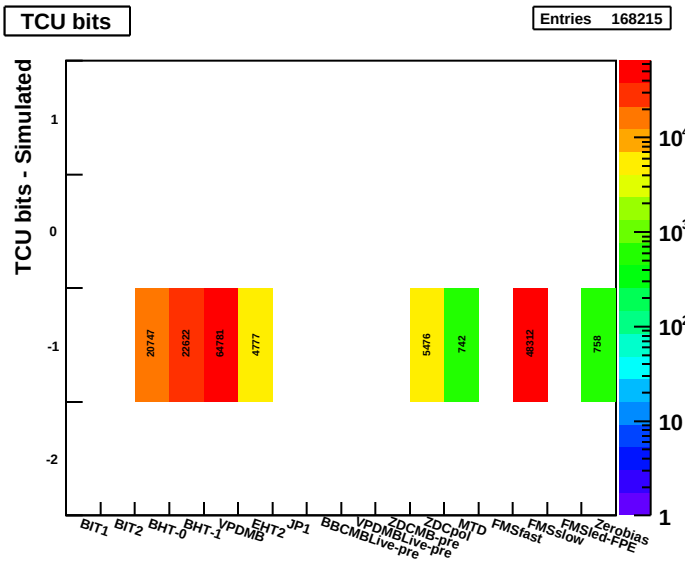

EMC L2 Input - HT23 bits
Entries 1115024
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

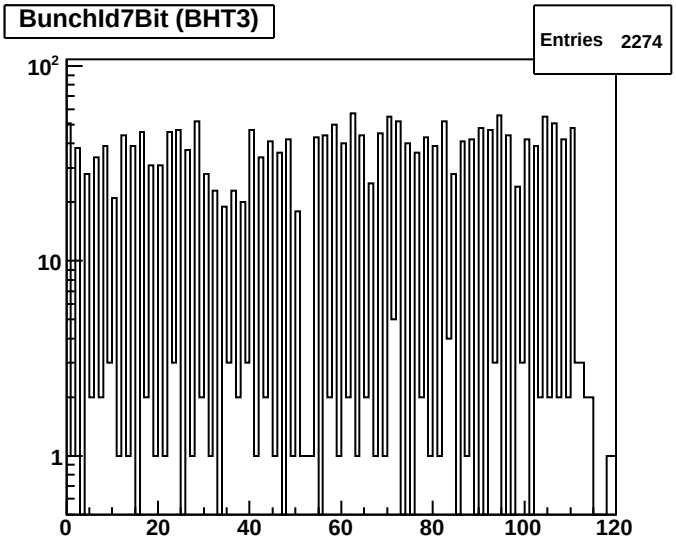

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

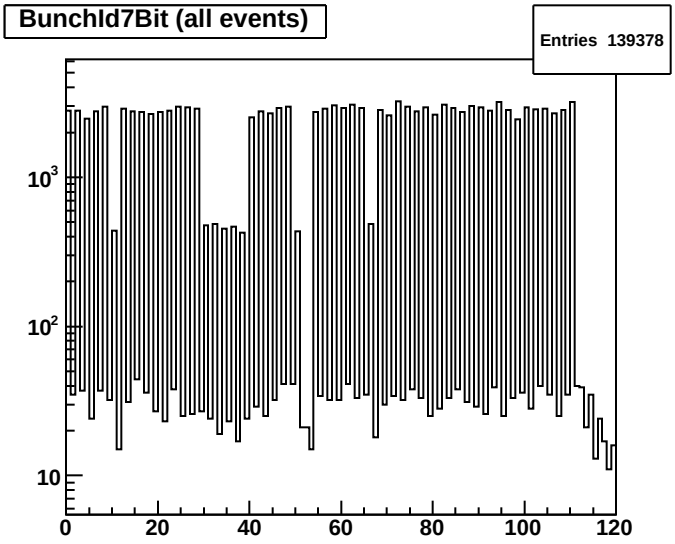

Last DSM Ch3 Input
Entries 2230048
LD301 Ch3
37767 22622 5602 2274 4777 2663 22769 5519 14885 3727 7590 1675 475 228 149 39308
101611 116756 133776 137104 134601 136715 116609 133859 124493 135651 131819 137703 138903 139050 139229 100070
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

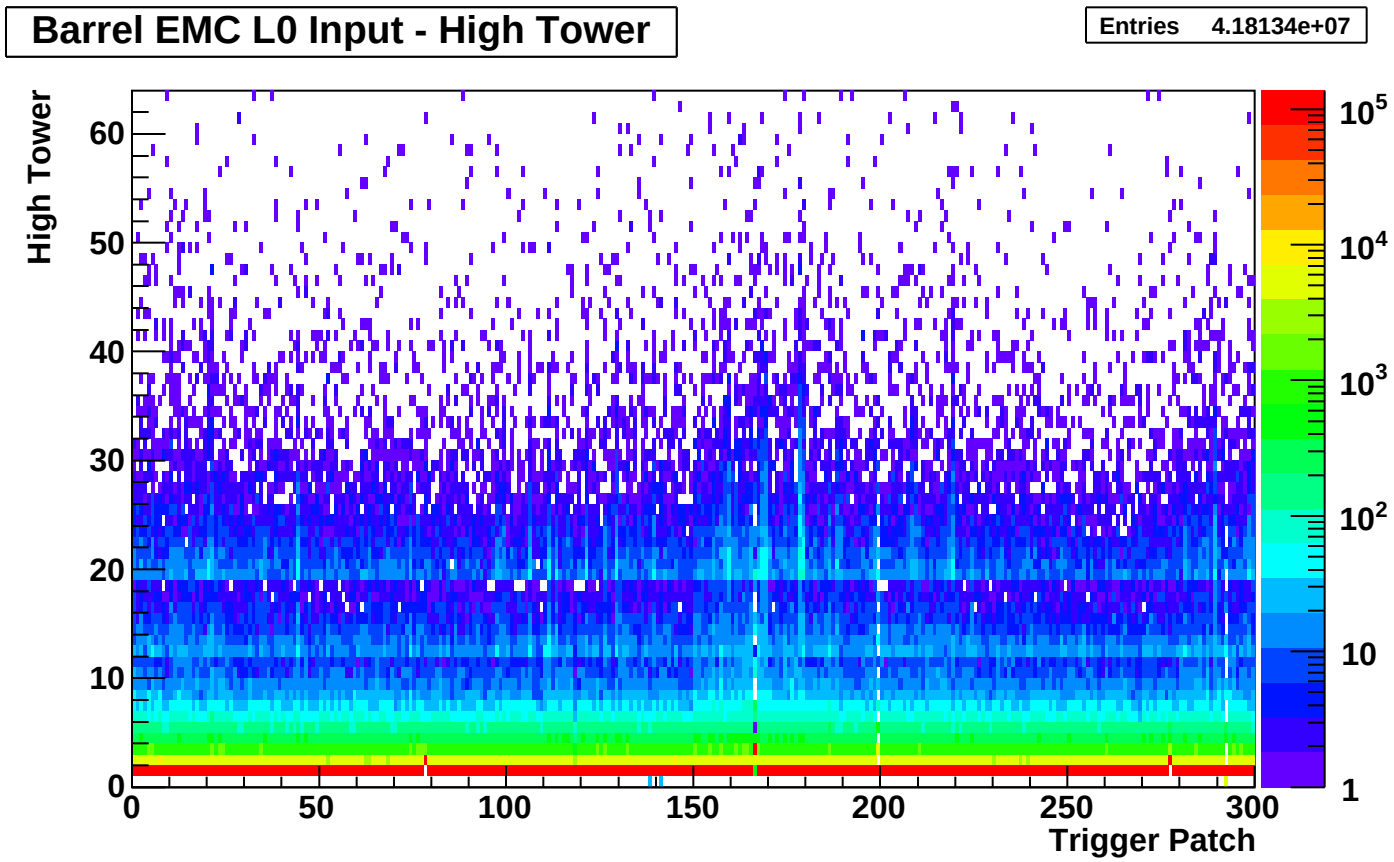

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

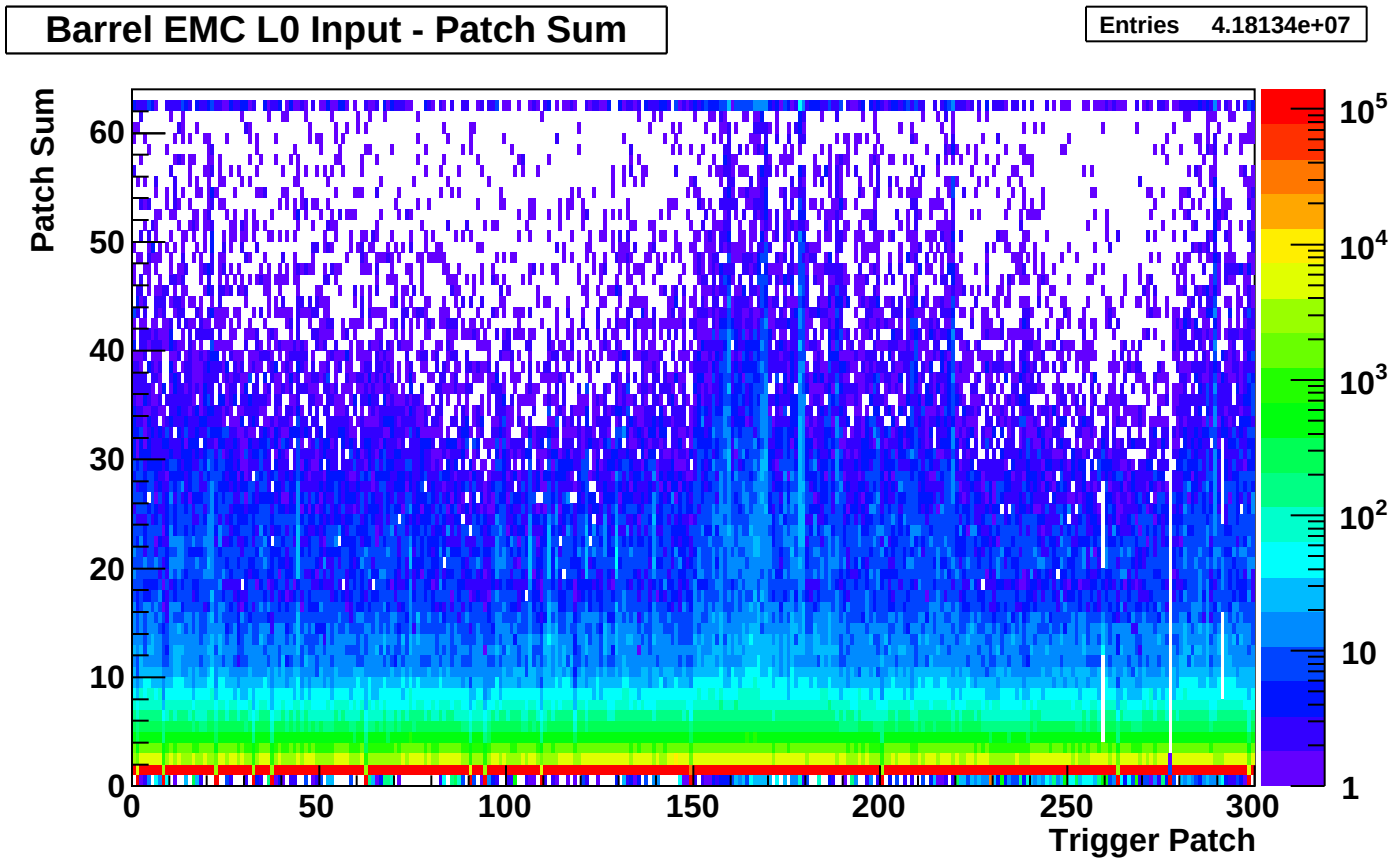

TCU bits
Entries 2230048
TCU bits
139378
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

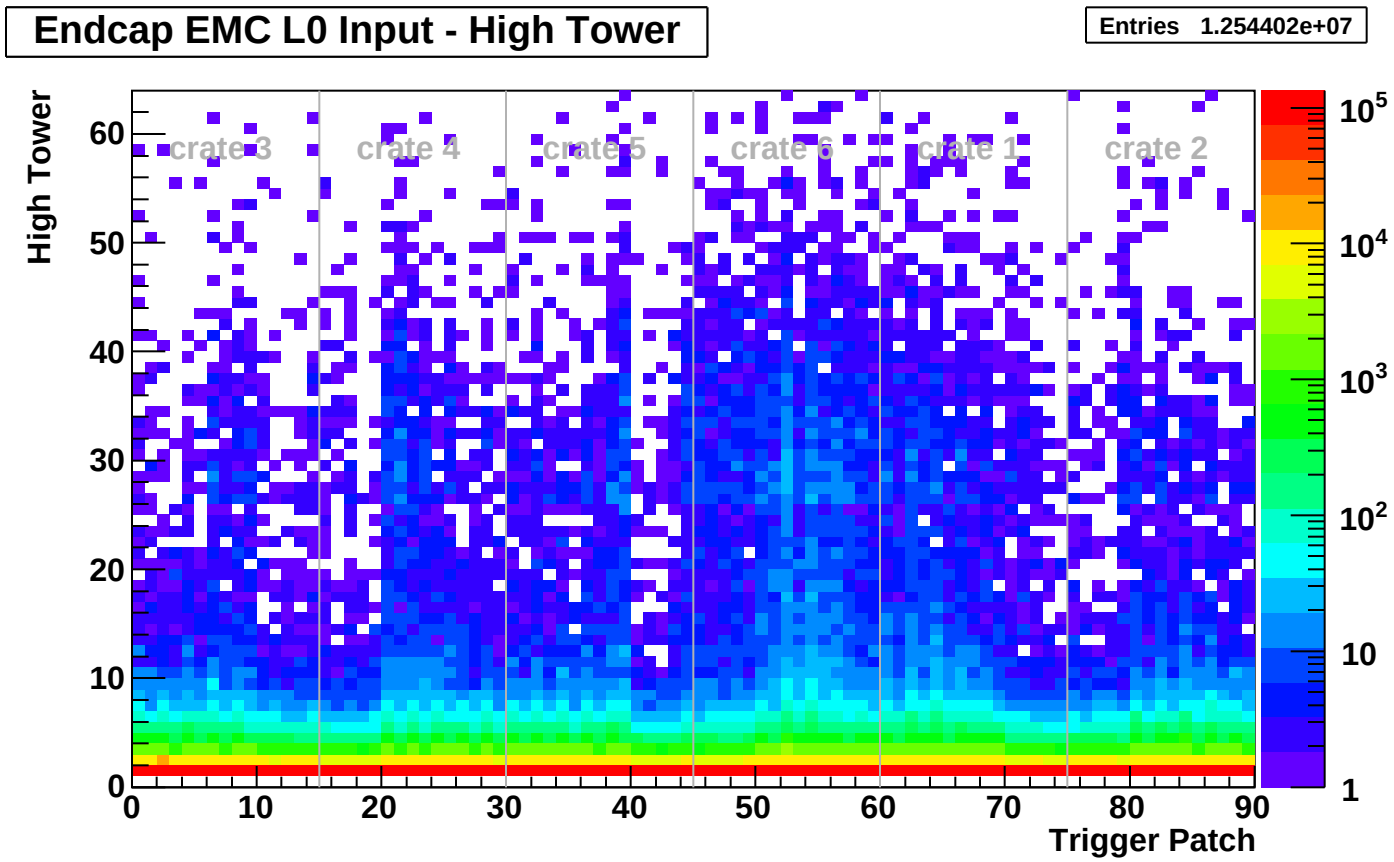

TCU bits
Entries 168215
TCU bits - Simulated
20747 22622 64781 4777 5476 742 48312 758
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

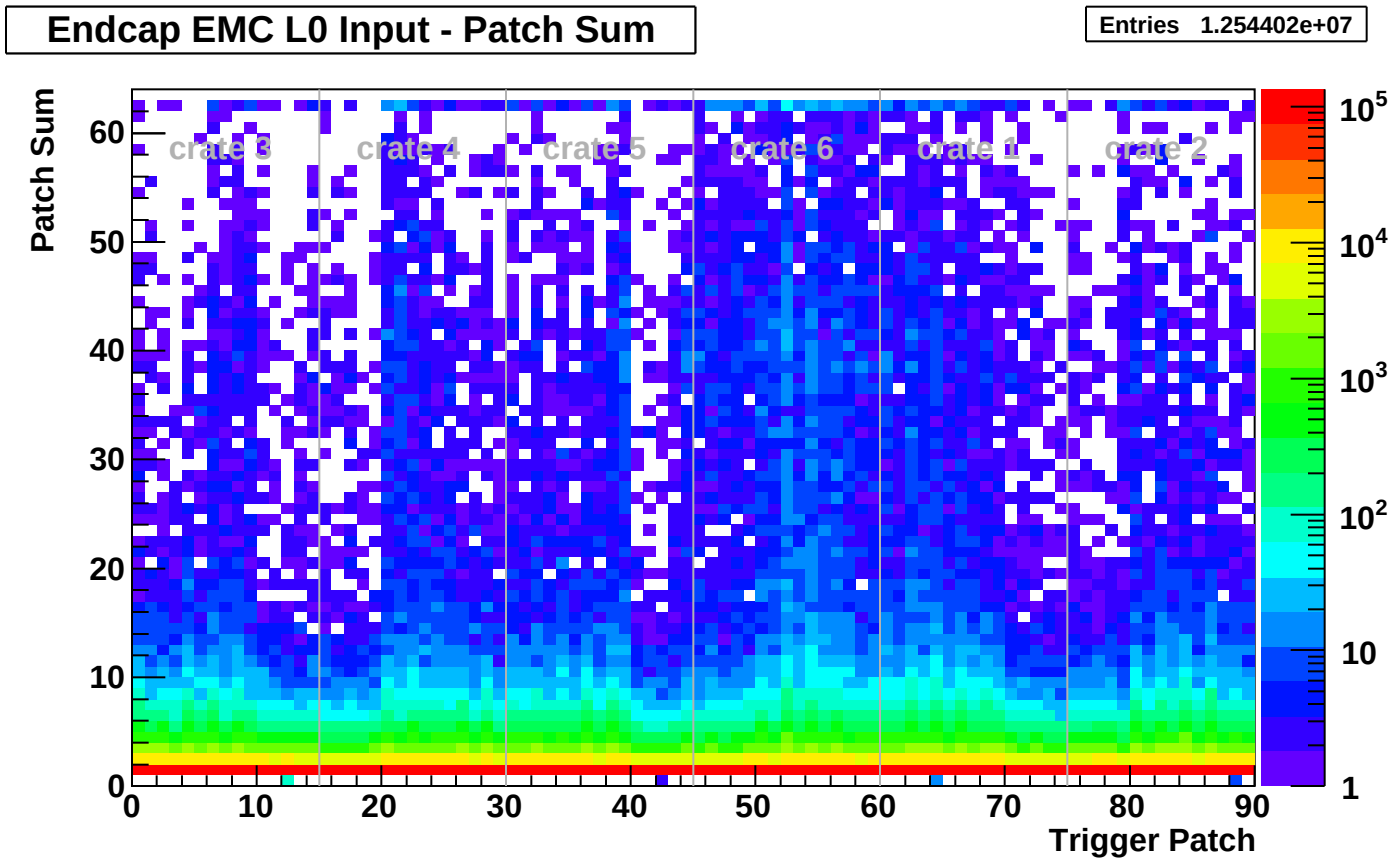

BunchId7Bit (BHT3)
Entries 2274

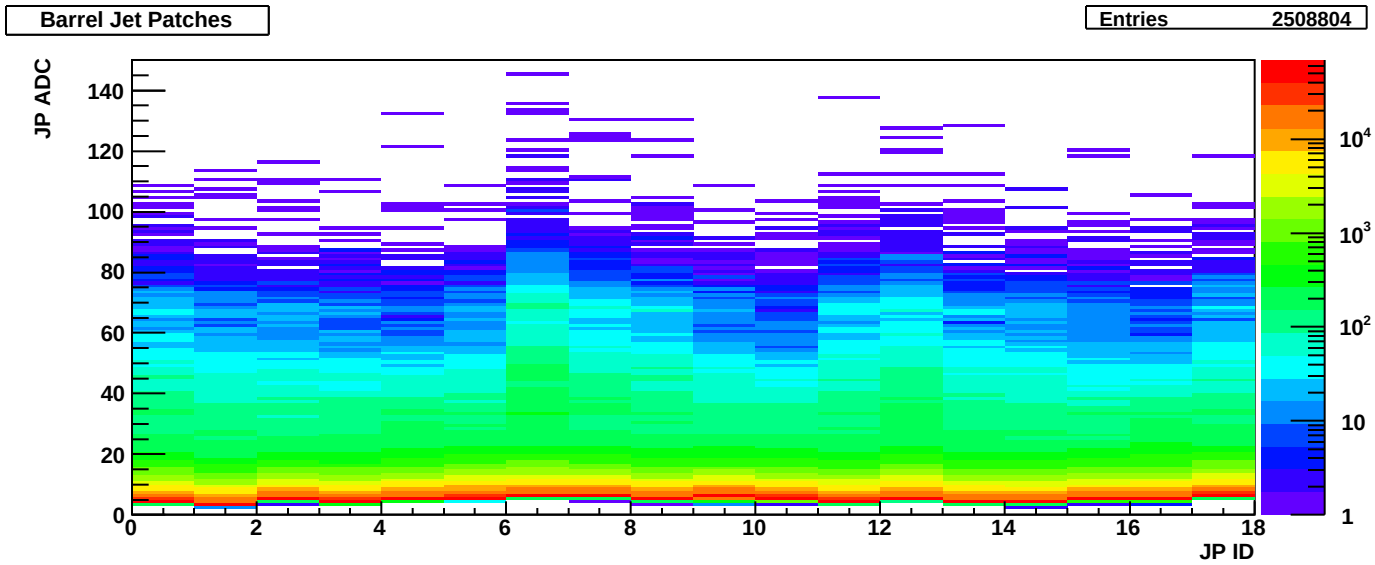

BunchId7Bit (all events)
Entries 139378

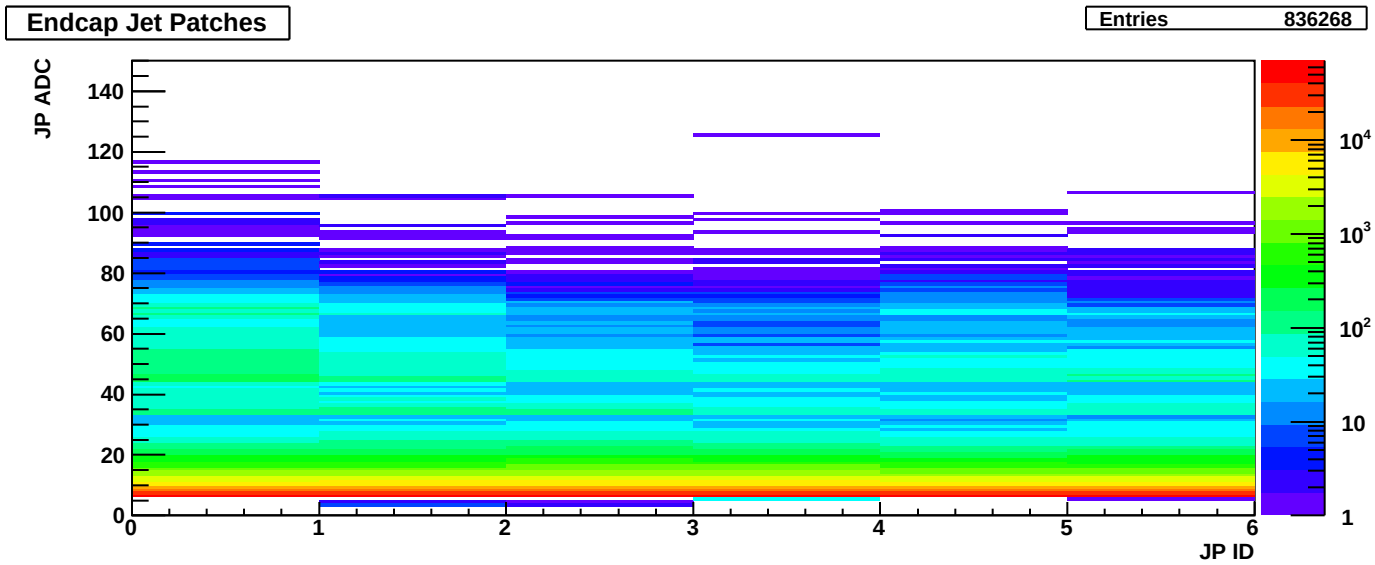

Barrel EMC L0 Input - High Tower
Entries 4.18134e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

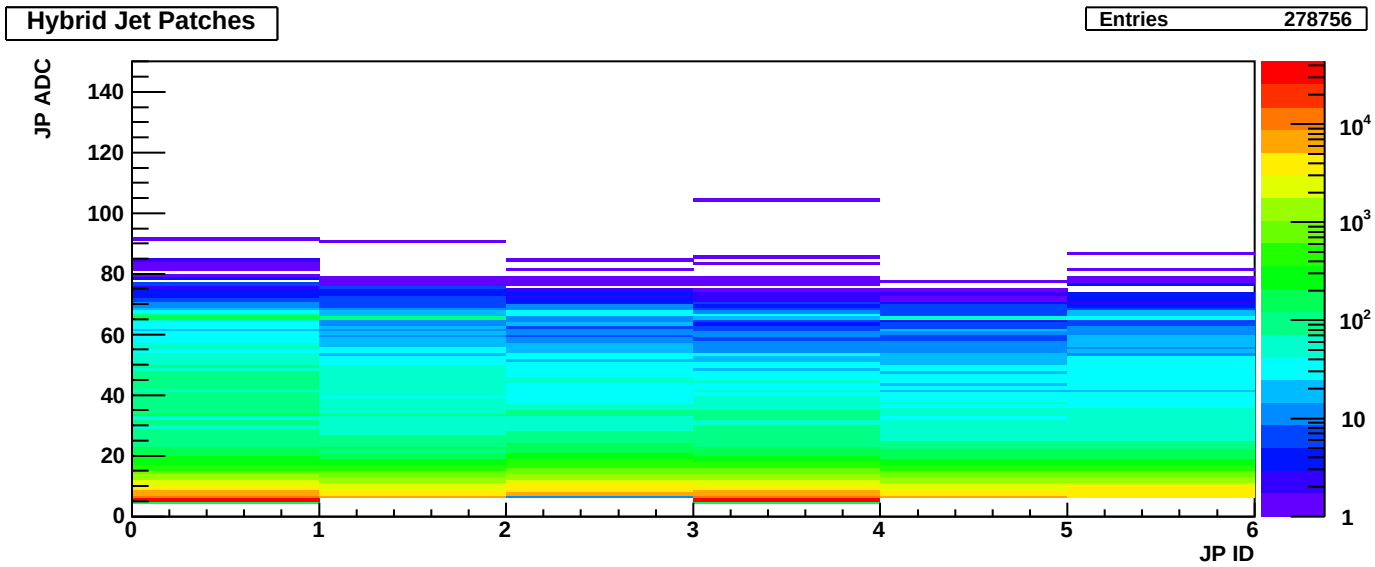

Barrel EMC L0 Input - Patch Sum
Entries 4.18134e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

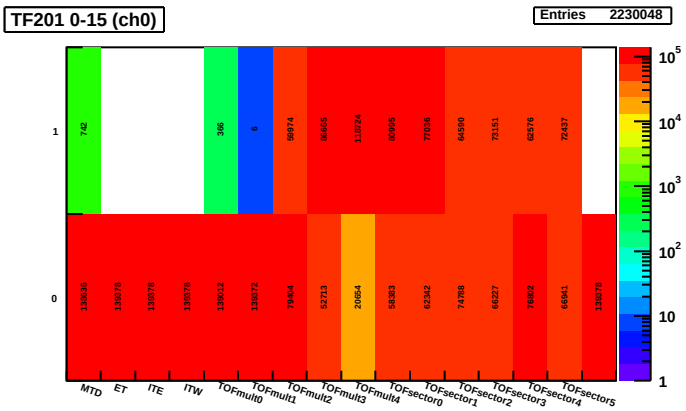

Endcap EMC L0 Input - High Tower
Entries 1.254402e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

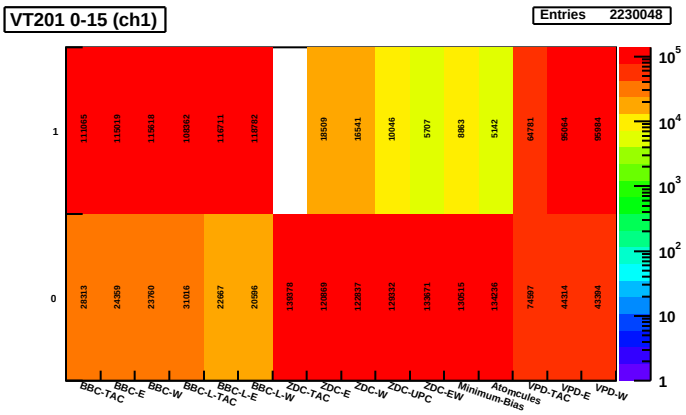

Endcap EMC L0 Input - Patch Sum
Entries 1.254402e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

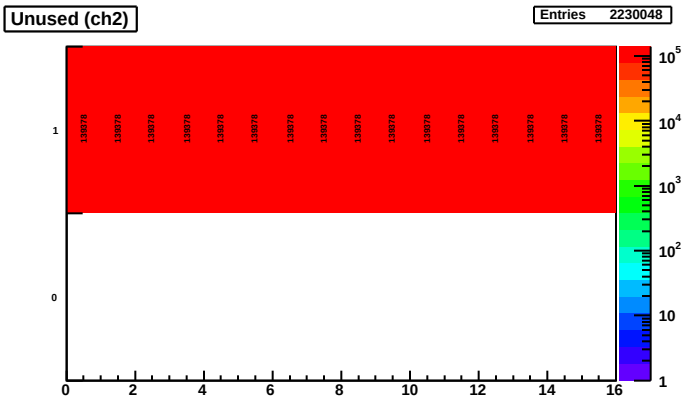

Barrel Jet Patches
Entries 2508804
JP ADC
JP ID

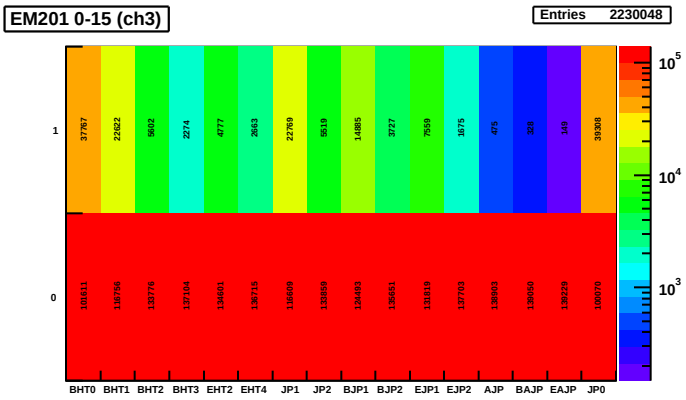

Endcap Jet Patches
Entries 836268
JP ADC
JP ID

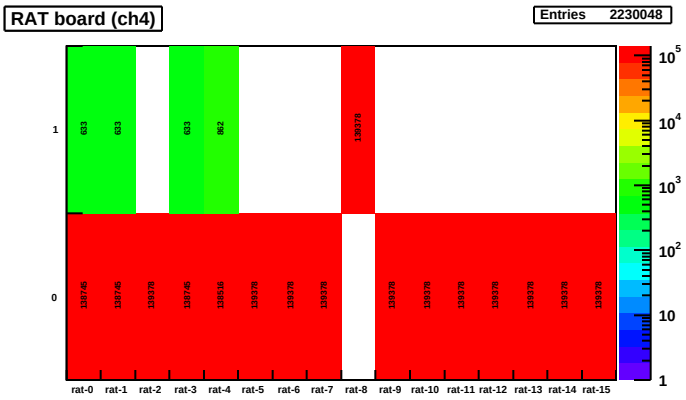

Hybrid Jet Patches
Entries 278756
JP ADC
JP ID

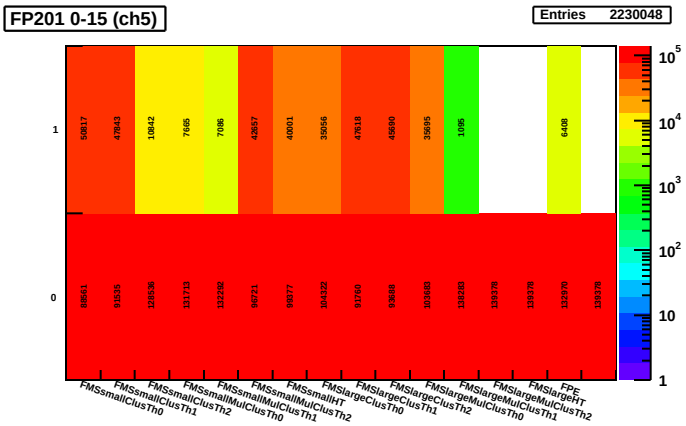

TF201 0-15 (ch0)
Entries 2230048
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

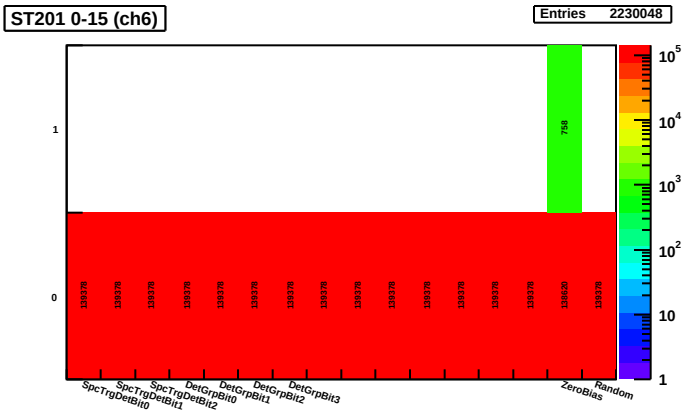

VT201 0-15 (ch1)
Entries 2230048
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

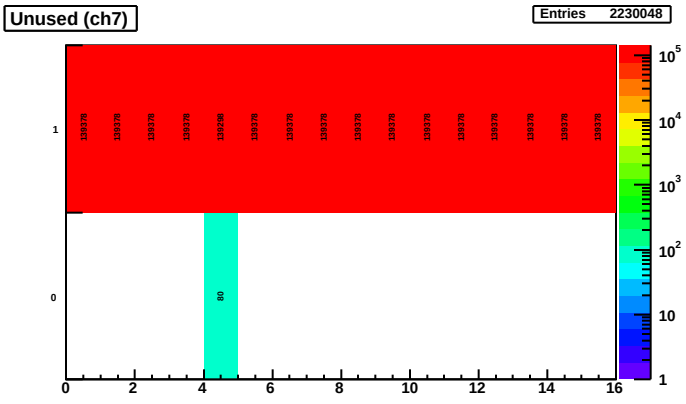

Unused (ch2)
Entries 2230048

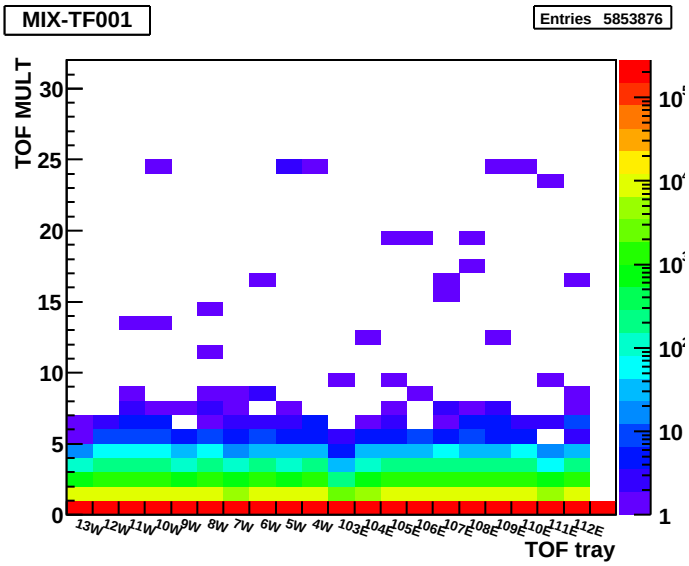

EM201 0-15 (ch3)
Entries 2230048
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

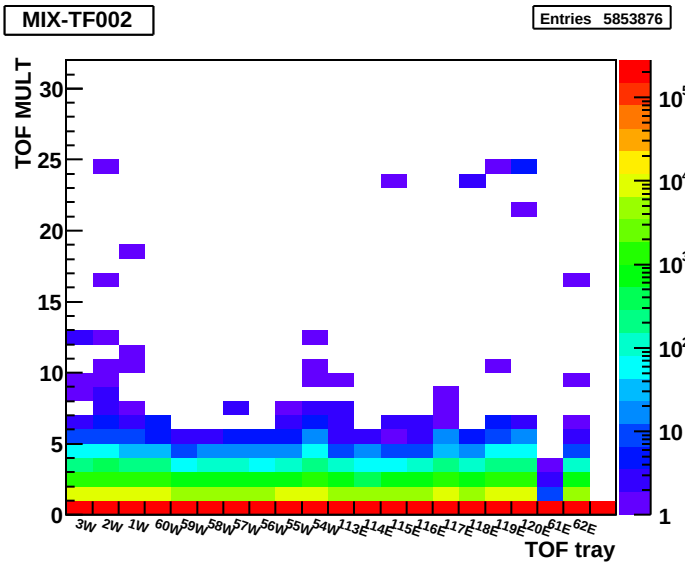

RAT board (ch4)
Entries 2230048
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

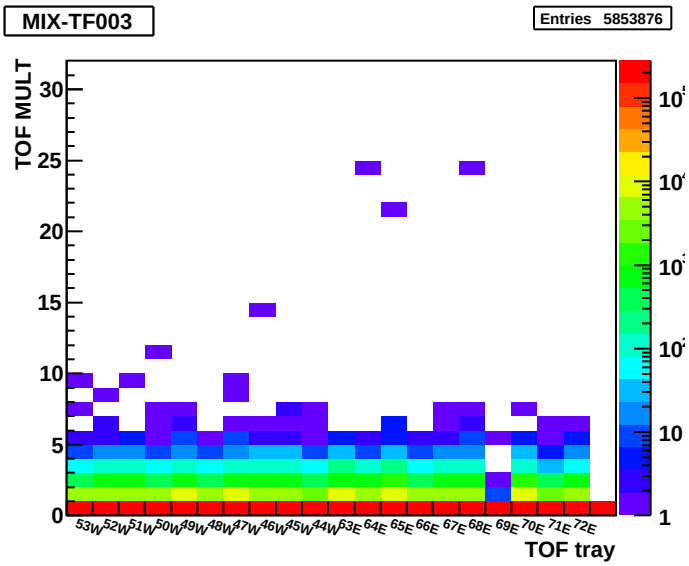

FP201 0-15 (ch5)
Entries 2230048
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

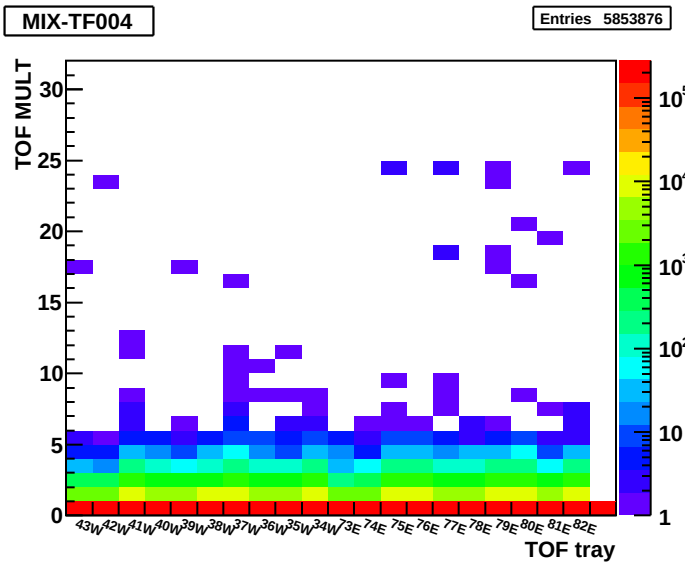

ST201 0-15 (ch6)
Entries 2230048
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

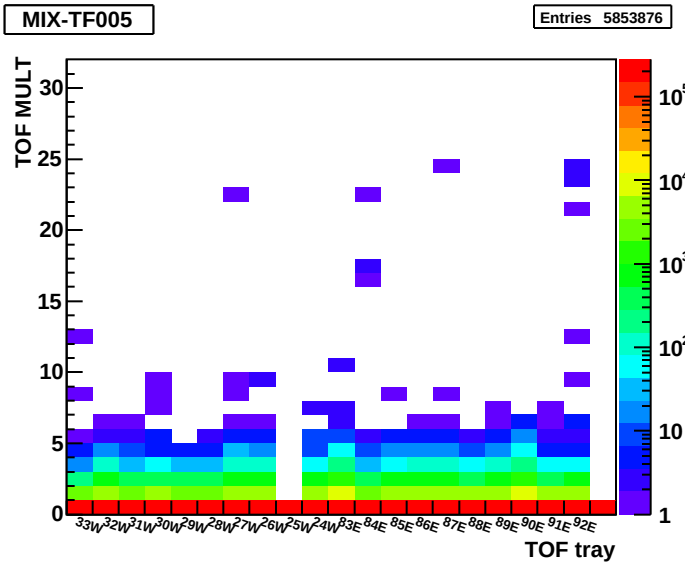

Unused (ch7)
Entries 2230048

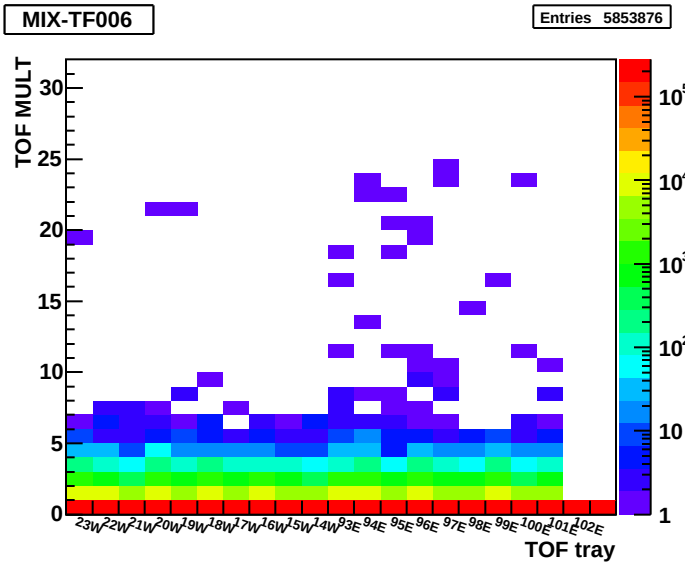

MIX-TF001
Entries 5853876
TOF MULT
TOF tray

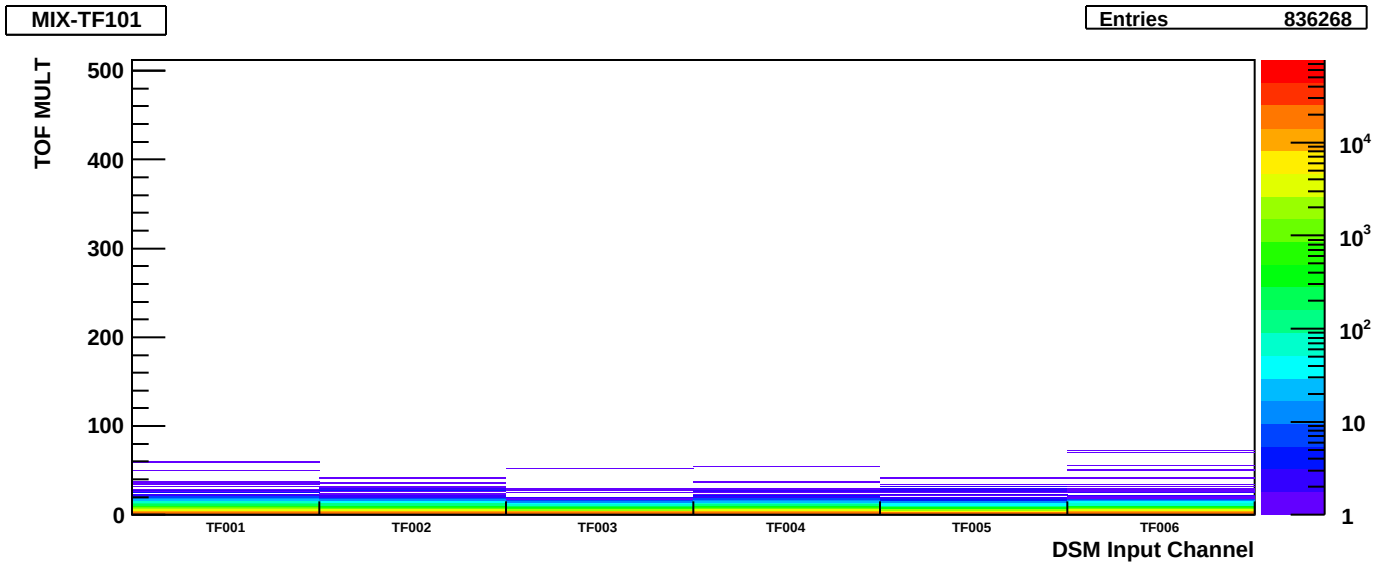

MIX-TF002
Entries 5853876
TOF MULT
TOF tray

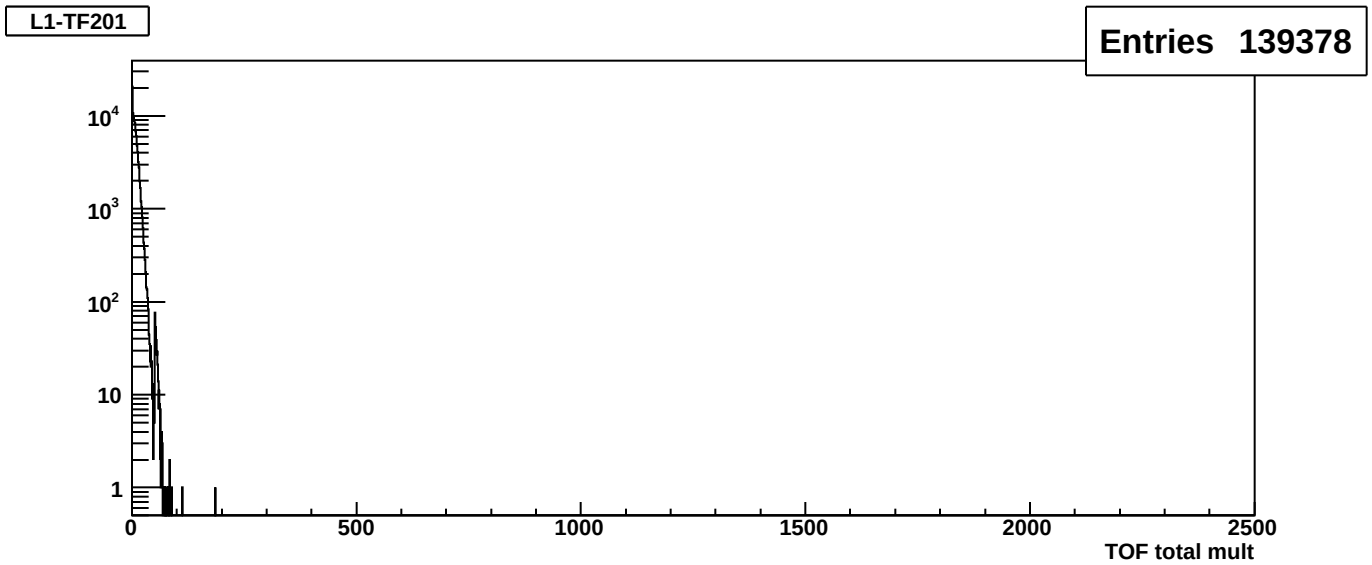

MIX-TF003
Entries 5853876
TOF MULT
TOF tray

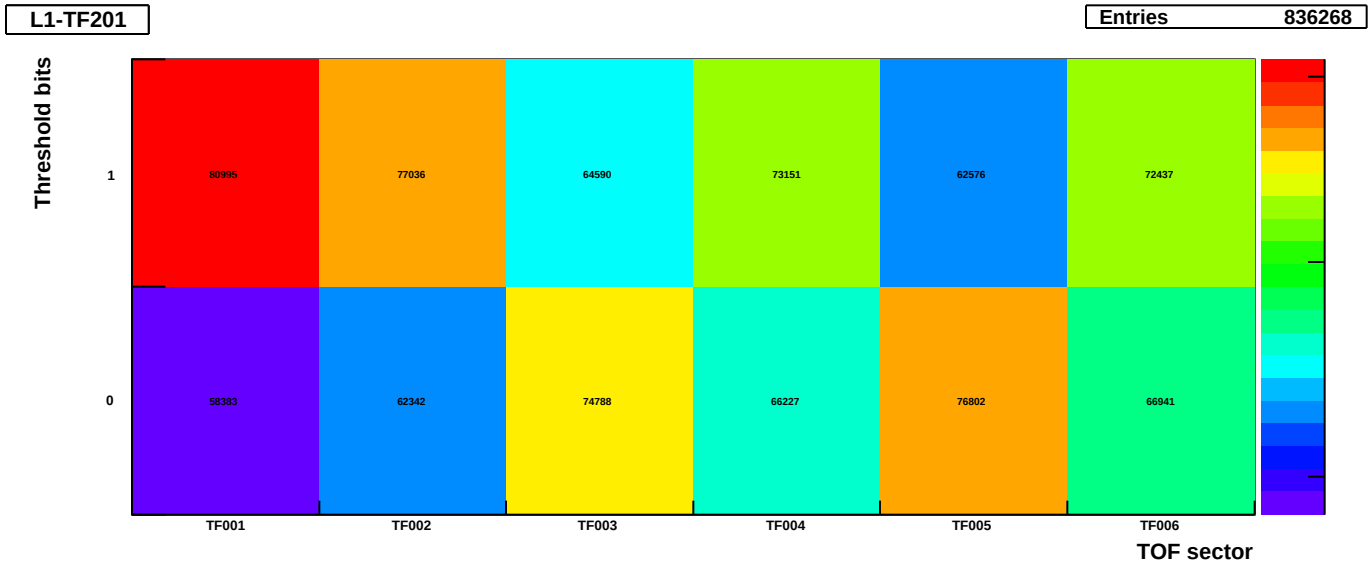

MIX-TF004
Entries 5853876
TOF MULT
TOF tray

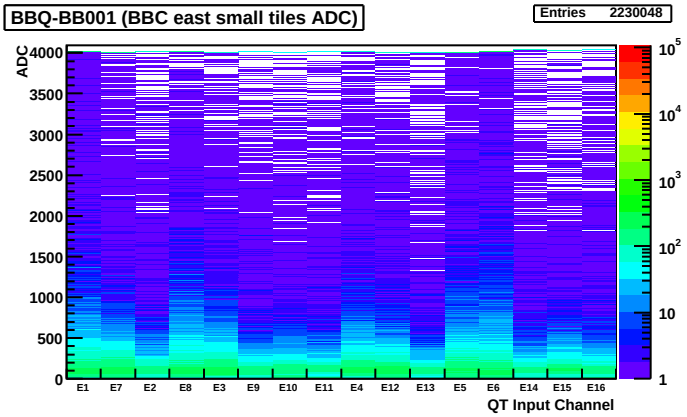

MIX-TF005
Entries 5853876
TOF MULT
TOF tray

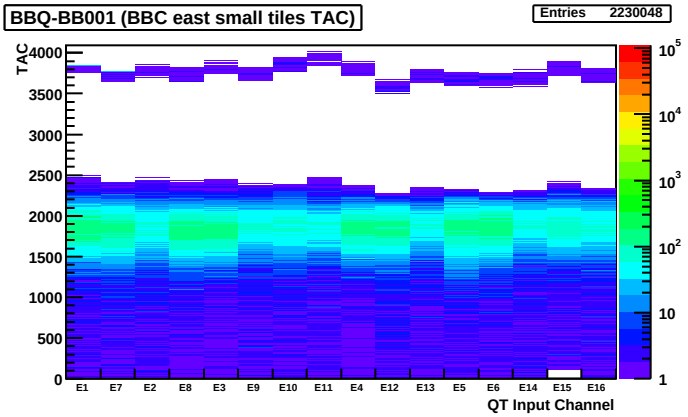

MIX-TF006
Entries 5853876
TOF MULT
TOF tray

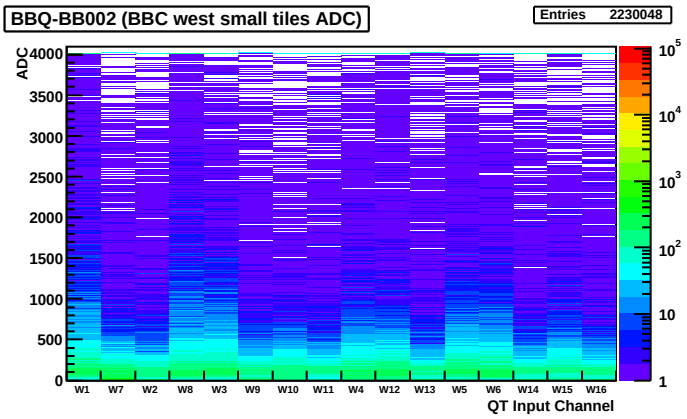

MIX-TF101
Entries 836268
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

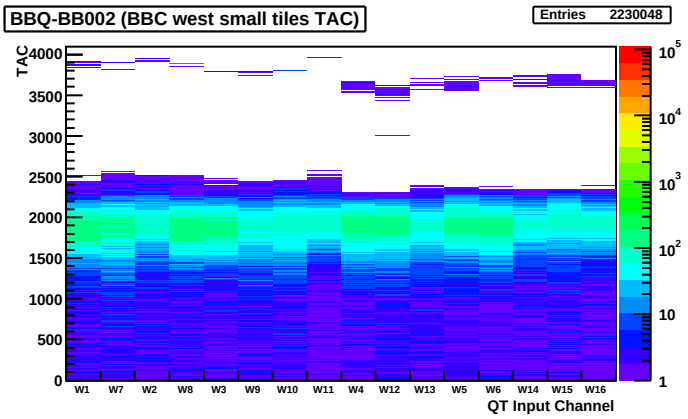

L1-TF201
Entries 139378
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

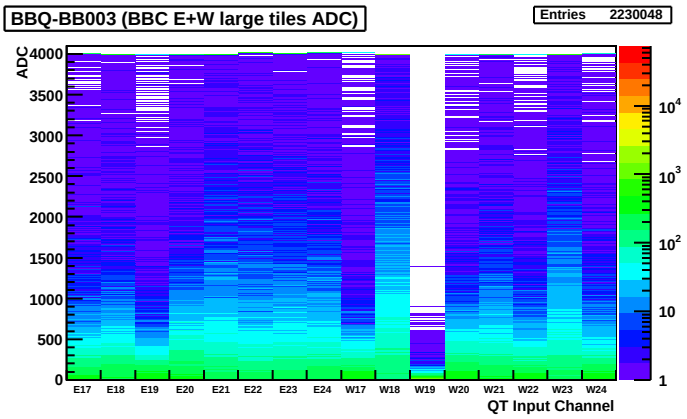

L1-TF201
Entries 836268
Threshold bits
1
0
80995
77036
64590
73151
62576
72437
58383
62342
74788
66227
76802
66941
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

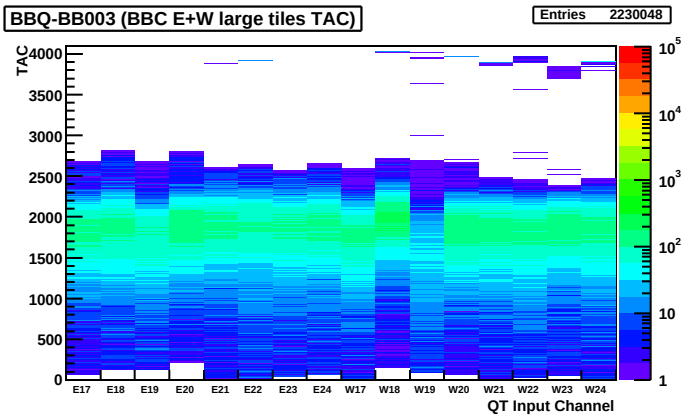

BBQ-BB001 (BBC east small tiles ADC)
Entries 2230048
ADC
QT Input Channel

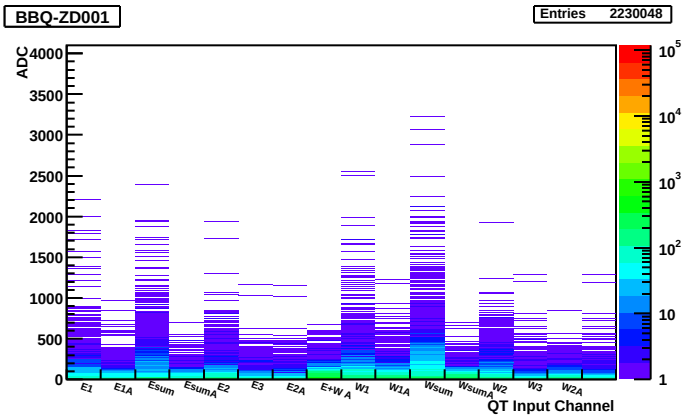

BBQ-BB001 (BBC east small tiles TAC)
Entries 2230048
TAC
QT Input Channel

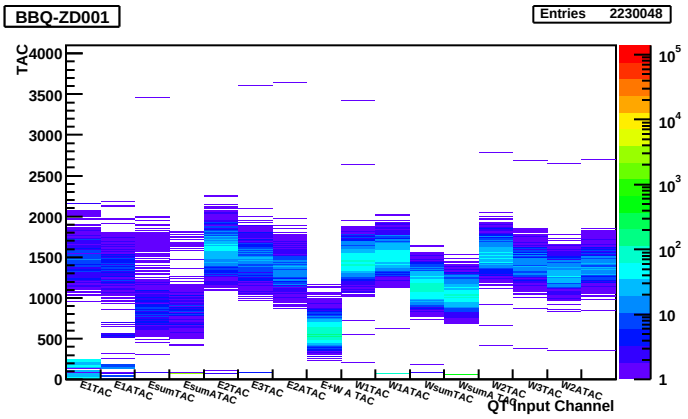

BBQ-BB002 (BBC west small tiles ADC)
Entries 2230048
ADC
QT Input Channel

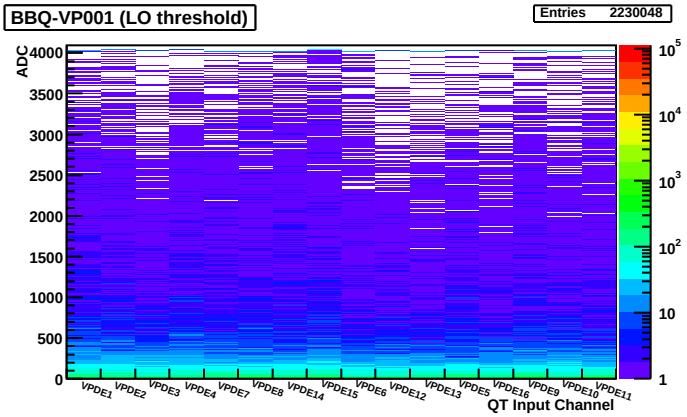

BBQ-BB002 (BBC west small tiles TAC)
Entries 2230048
TAC
QT Input Channel

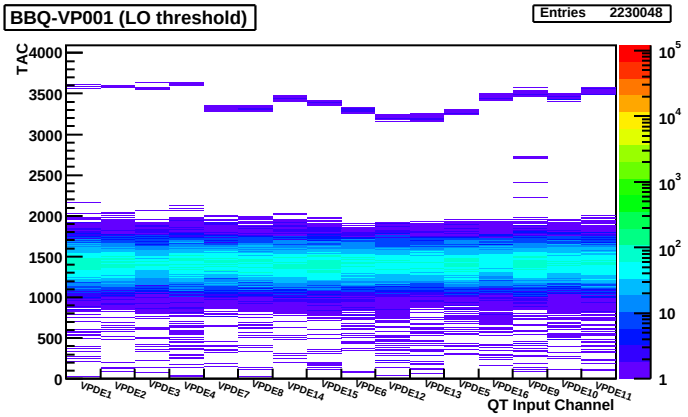

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2230048
ADC
QT Input Channel

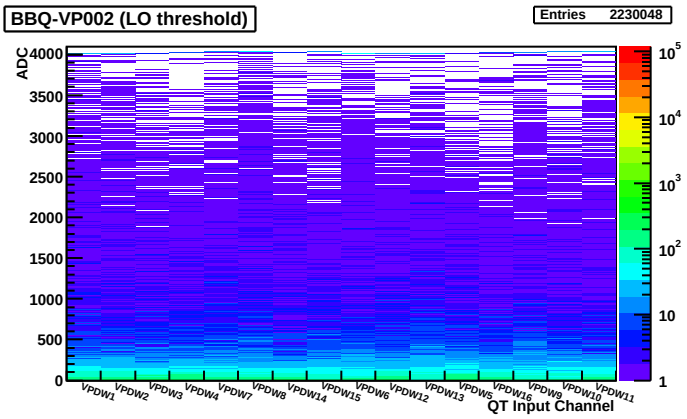

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2230048
TAC
QT Input Channel

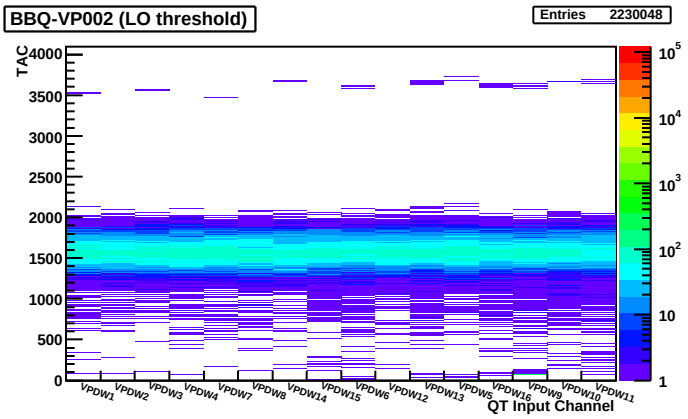

BBQ-ZD001
Entries 2230048
ADC
QT Input Channel

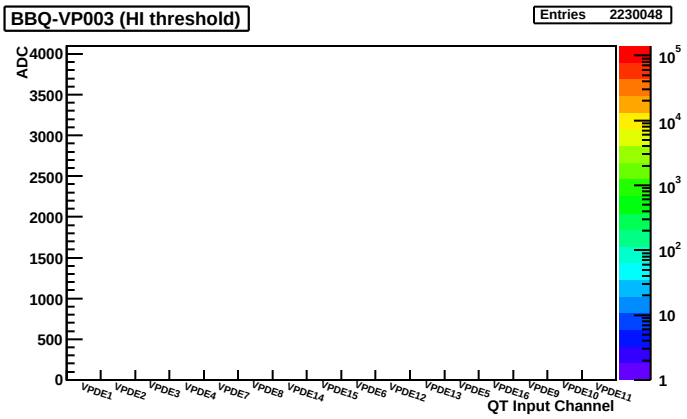

BBQ-ZD001
Entries 2230048
TAC
QT Input Channel

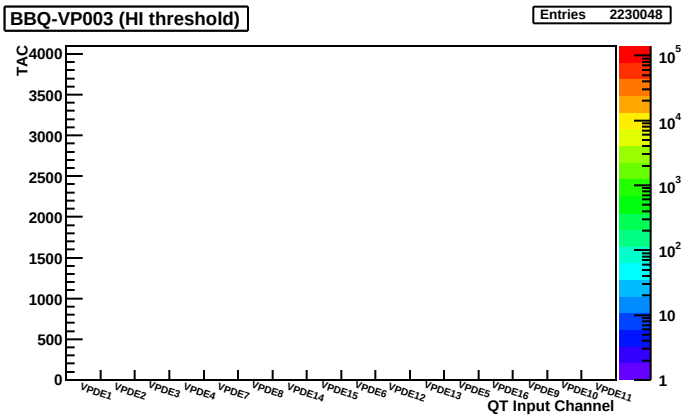

BBQ-VP001 (LO threshold)
Entries 2230048
ADC
QT Input Channel

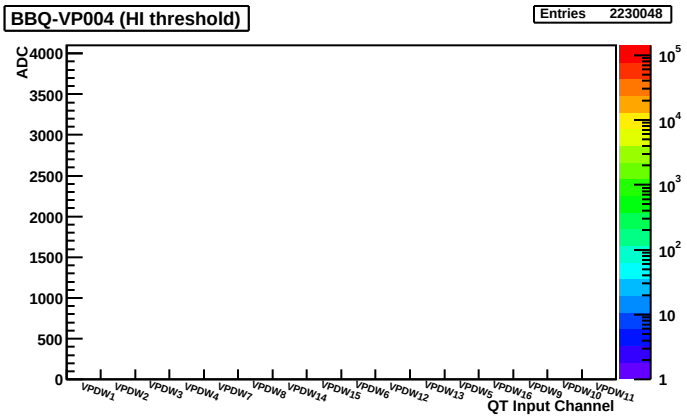

BBQ-VP001 (LO threshold)
Entries 2230048
TAC
QT Input Channel

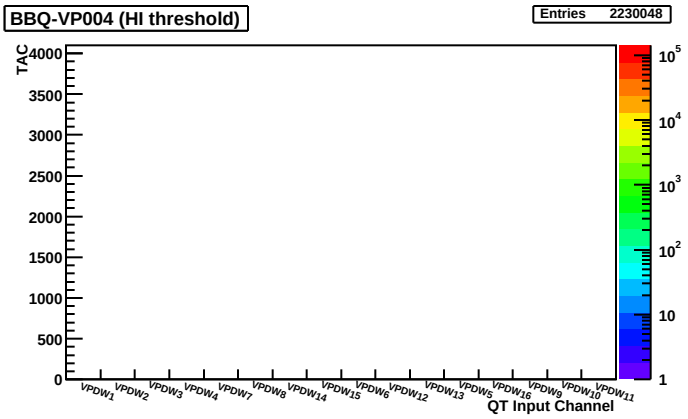

BBQ-VP002 (LO threshold)
Entries 2230048
ADC
QT Input Channel

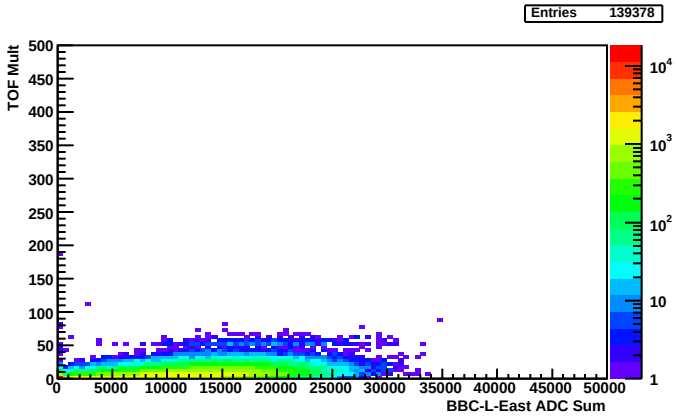

BBQ-VP002 (LO threshold)
Entries 2230048
TAC
QT Input Channel

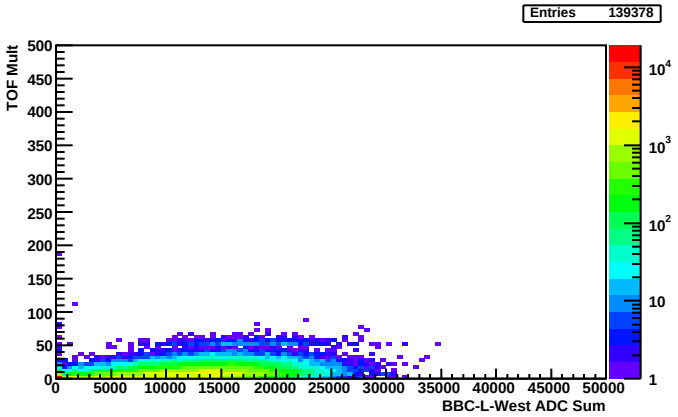

BBQ-VP003 (HI threshold)
Entries 2230048
ADC
QT Input Channel

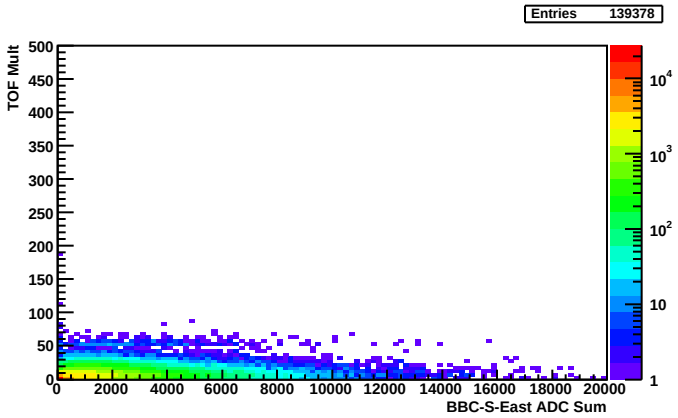

BBQ-VP003 (HI threshold)
Entries 2230048
TAC
QT Input Channel

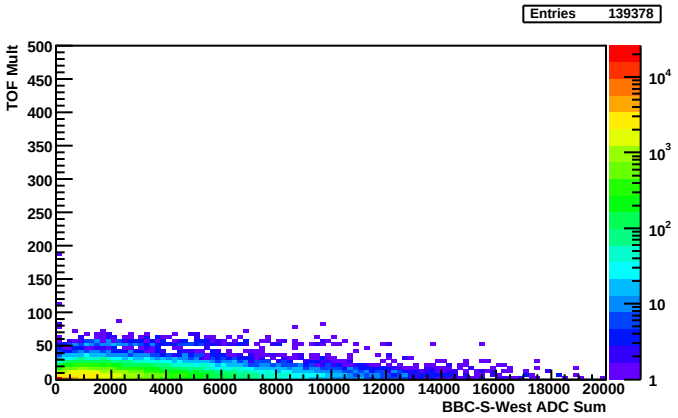

BBQ-VP004 (HI threshold)
Entries 2230048
ADC
QT Input Channel

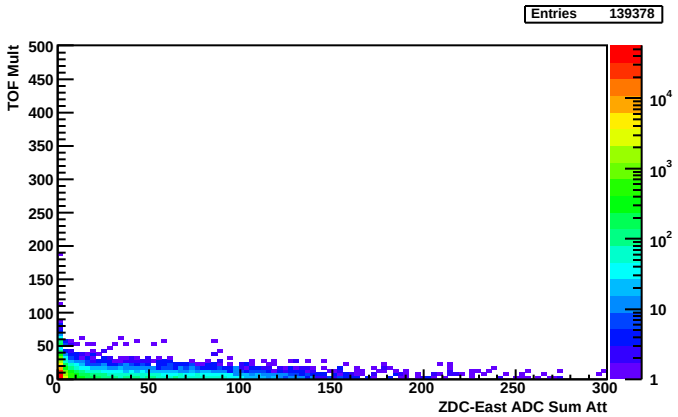

BBQ-VP004 (HI threshold)
Entries 2230048
TAC
QT Input Channel

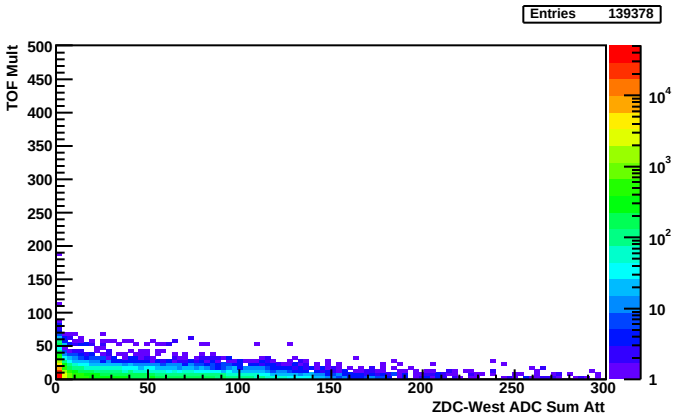

Entries 139378
TOF Mult
BBC-L-East ADC Sum

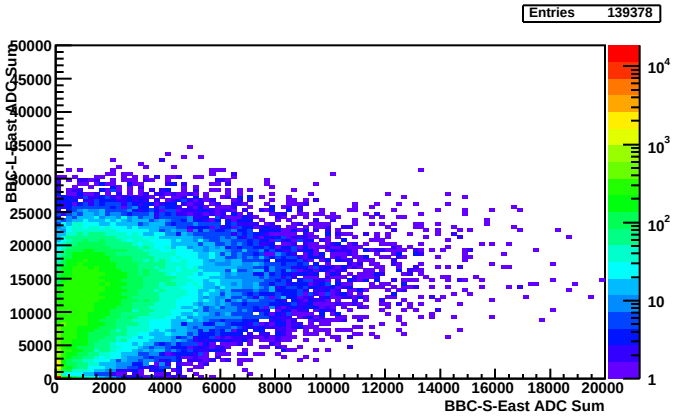

Entries 139378
TOF Mult
BBC-L-West ADC Sum

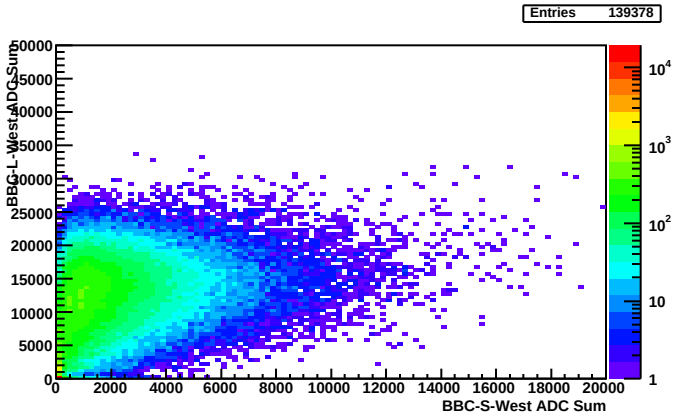

Entries 139378
TOF Mult
BBC-S-East ADC Sum

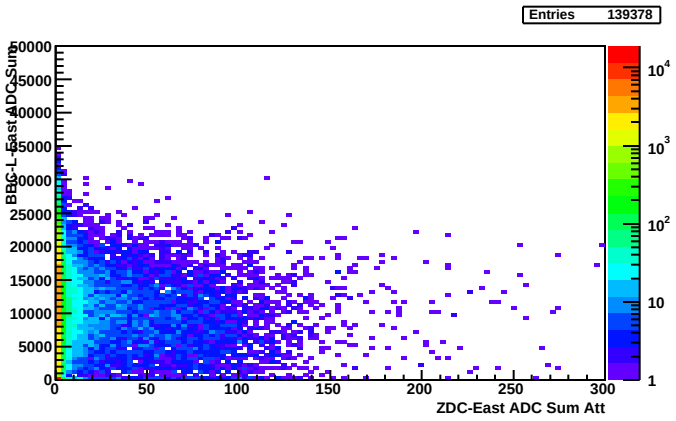

Entries 139378
TOF Mult
BBC-S-West ADC Sum

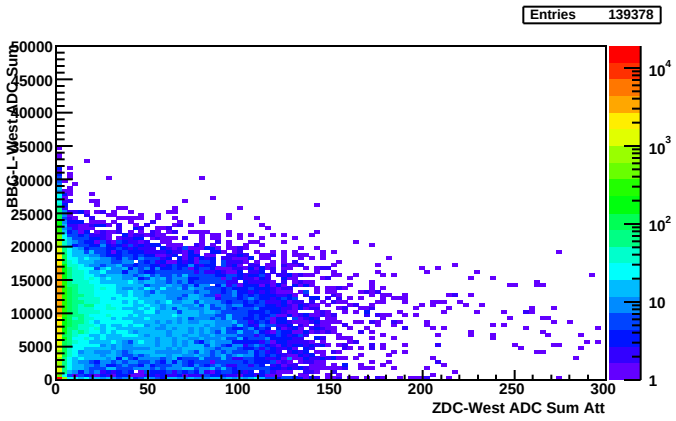

Entries 139378
TOF Mult
ZDC-East ADC Sum Att

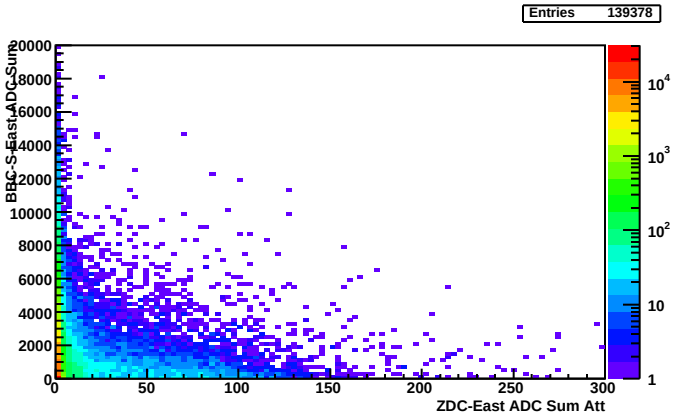

Entries 139378
TOF Mult
ZDC-West ADC Sum Att

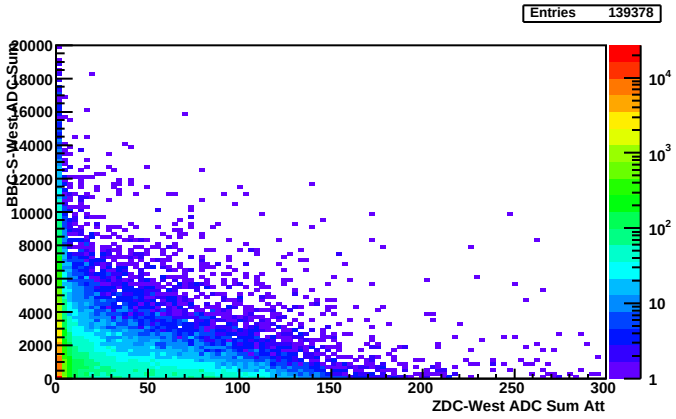

Entries 139378
BBC-L-East ADC Sum
BBC-S-East ADC Sum

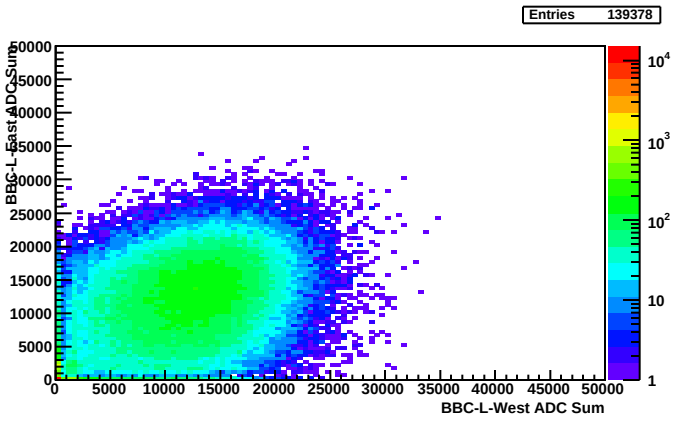

Entries 139378
BBC-L-West ADC Sum
BBC-S-West ADC Sum

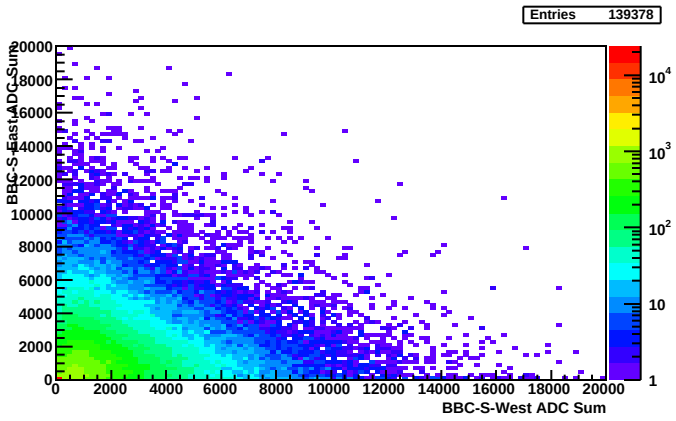

Entries 139378
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

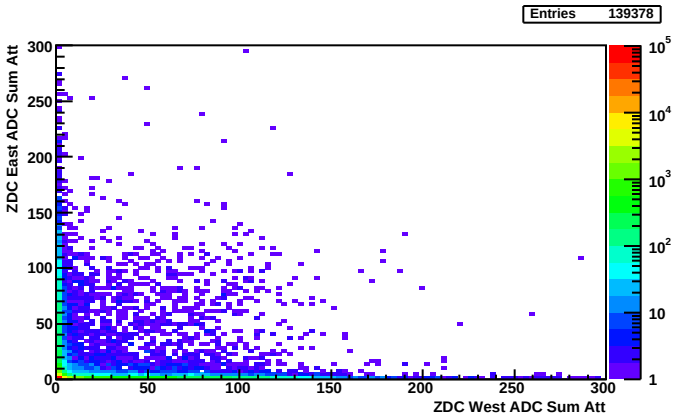

Entries 139378
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

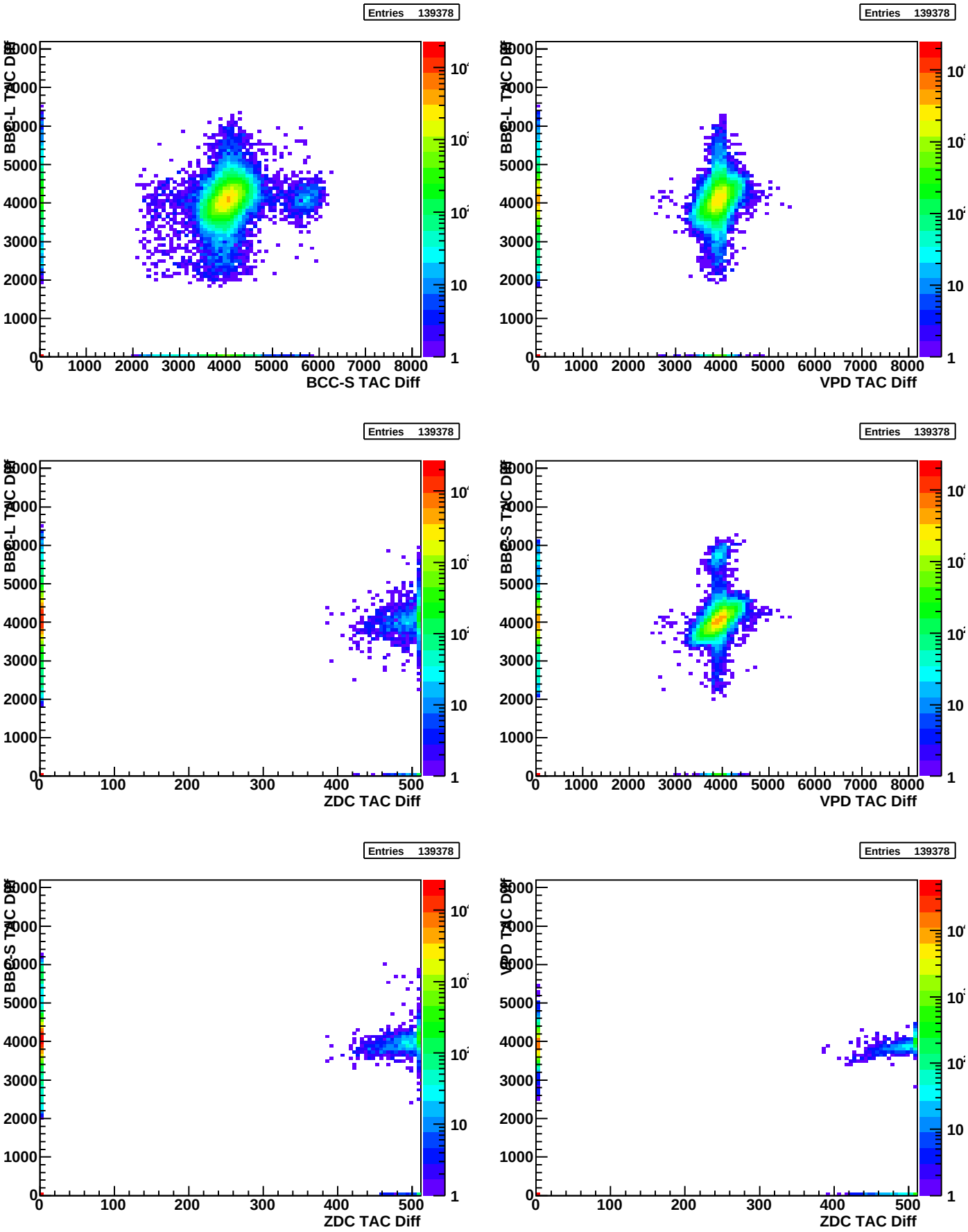

Entries 139378
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

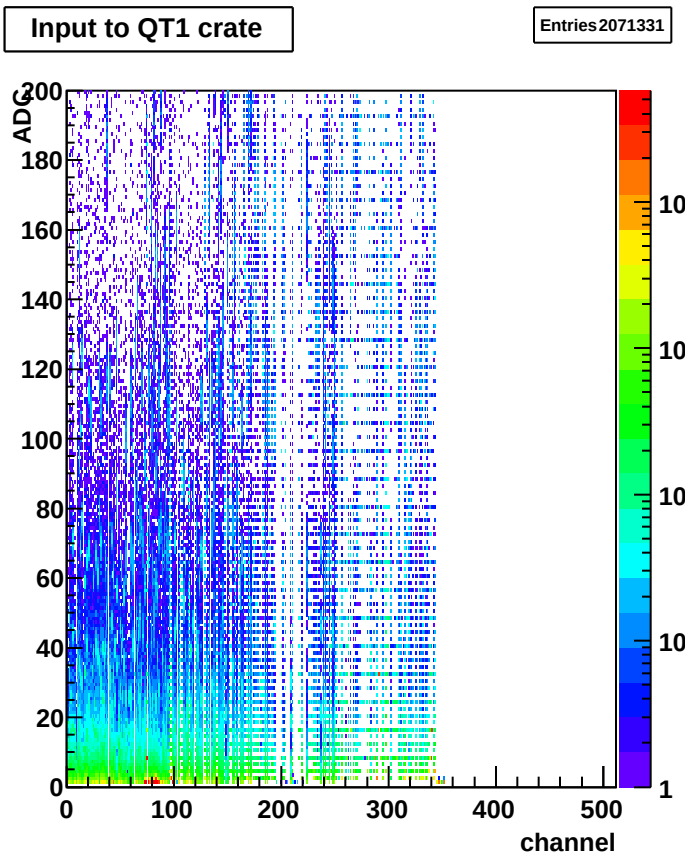

Entries 139378
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

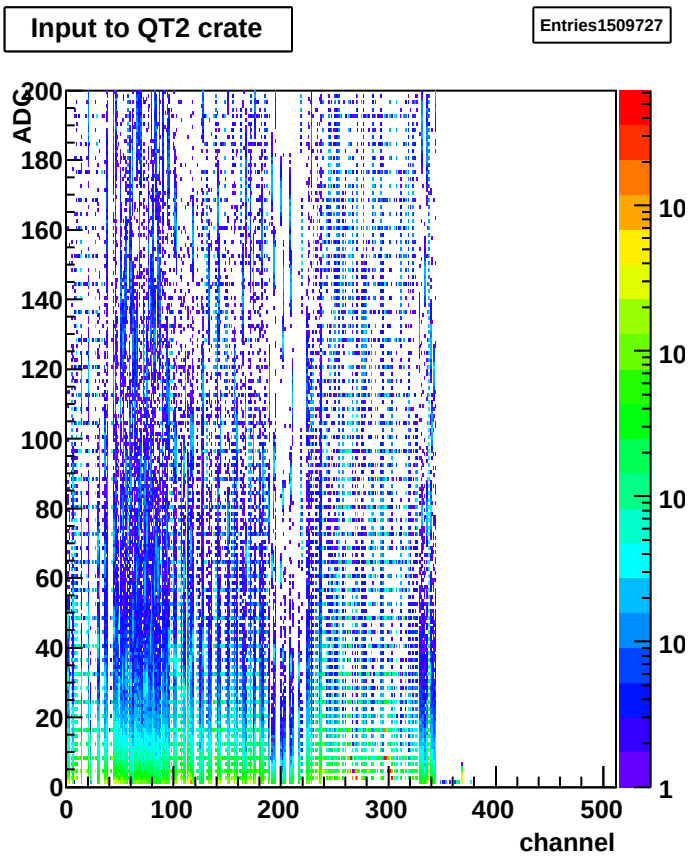

Entries 139378
BBC-L-East ADC Sum
BBC-L-West ADC Sum

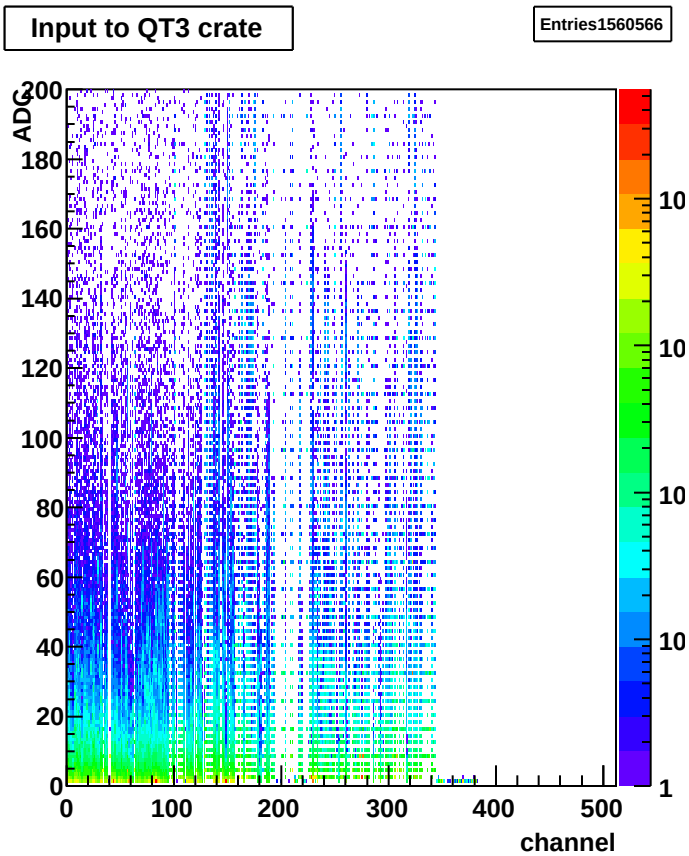

Entries 139378
BBC-S-East ADC Sum
BBC-S-West ADC Sum

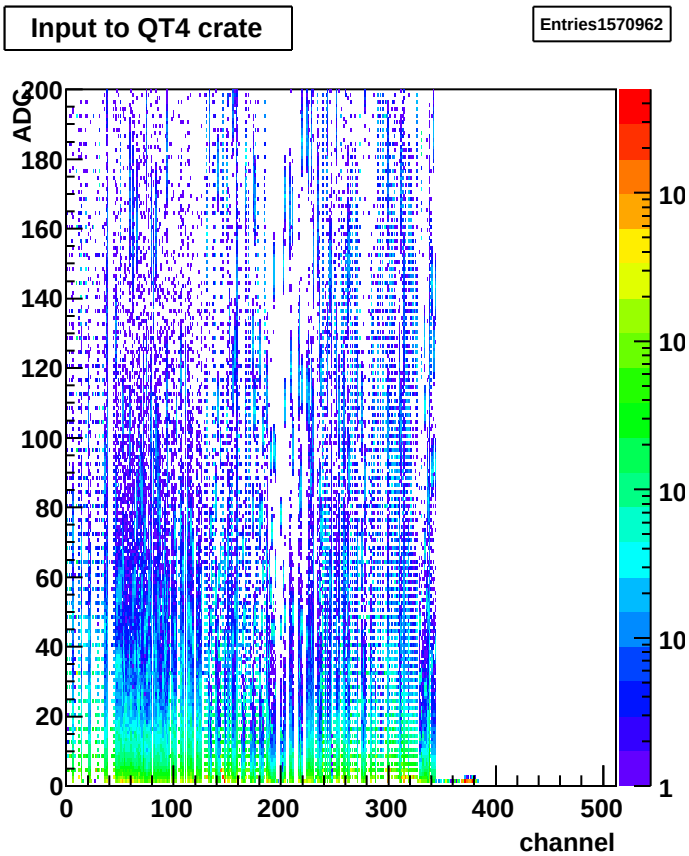

Entries 139378
ZDC East ADC Sum Att
ZDC West ADC Sum Att

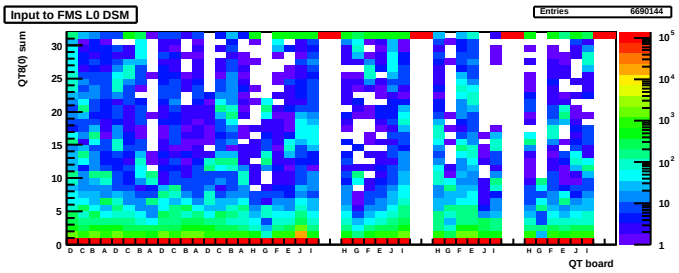

Entries 139378
BBC-L TAC Diff
BCC-S TAC Diff
Entries 139378
BBC-L TAC Diff
VPD TAC Diff
Entries 139378
BBC-L TAC Diff
ZDC TAC Diff
Entries 139378
BBC-S TAC Diff
VPD TAC Diff
Entries 139378
BBC-S TAC Diff
ZDC TAC Diff
Entries 139378
VPD TAC Diff
ZDC TAC Diff

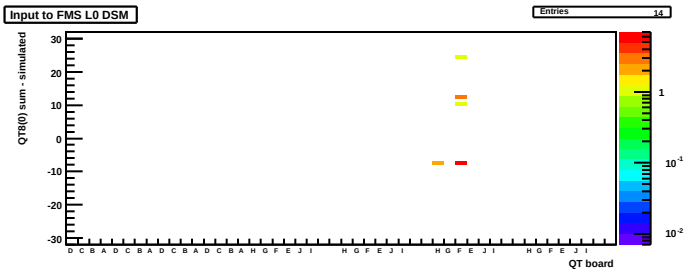

Input to QT1 crate
Entries 2071331
ADC
channel

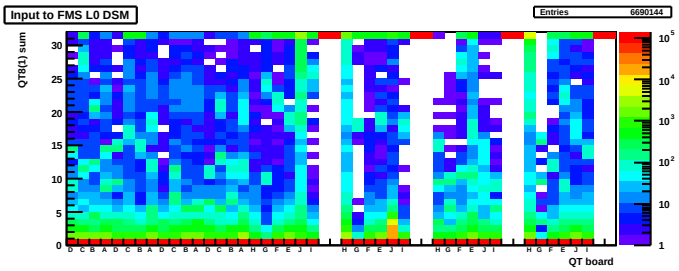

Input to QT2 crate
Entries 1509727
ADC
channel

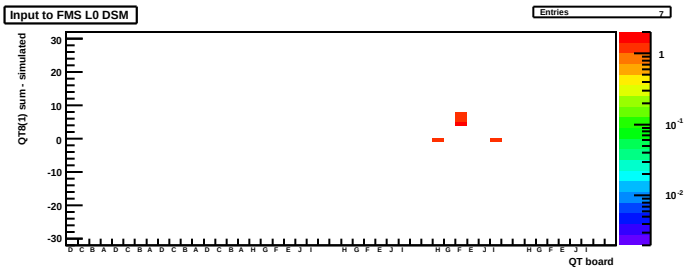

Input to QT3 crate
Entries 1560566
ADC
channel

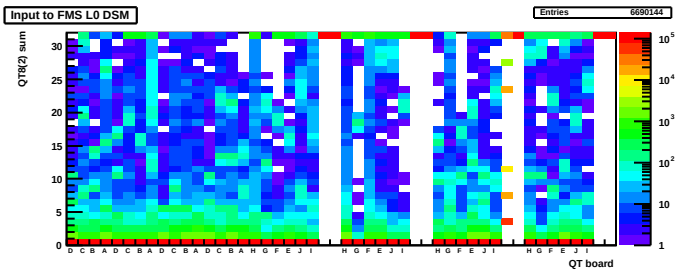

Input to QT4 crate
Entries 1570962
ADC
channel

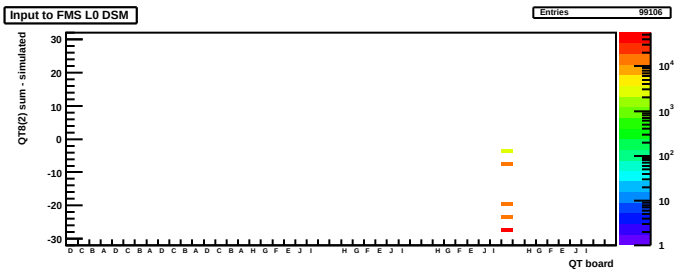

Input to FMS L0 DSM
Entries 6690144
QT8(0) sum
QT board

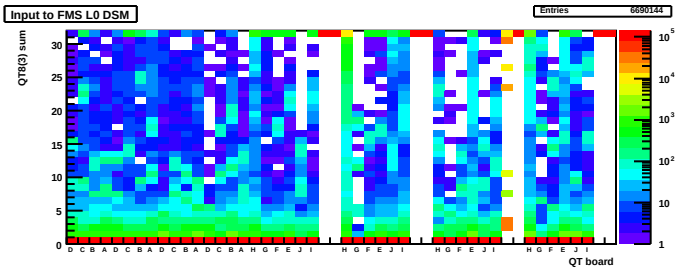

Input to FMS L0 DSM
Entries 14
QT8(0) sum - simulated
QT board

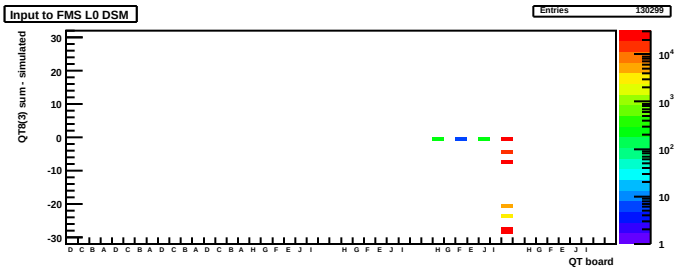

Input to FMS L0 DSM
Entries 6690144
QT8(1) sum
QT board

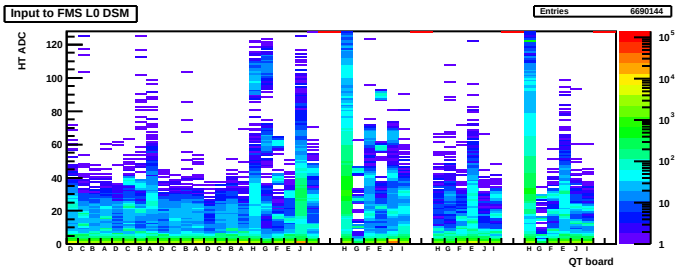

Input to FMS L0 DSM
Entries 7
QT8(1) sum - simulated
QT board

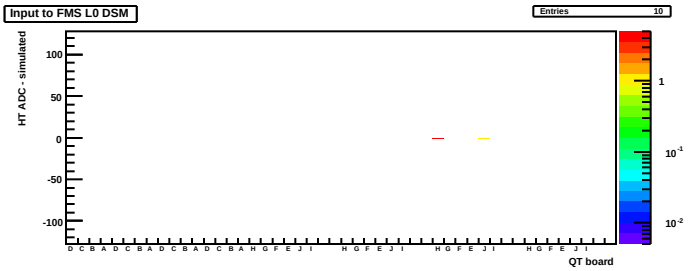

Input to FMS L0 DSM
Entries 6690144
QT8(2) sum
QT board

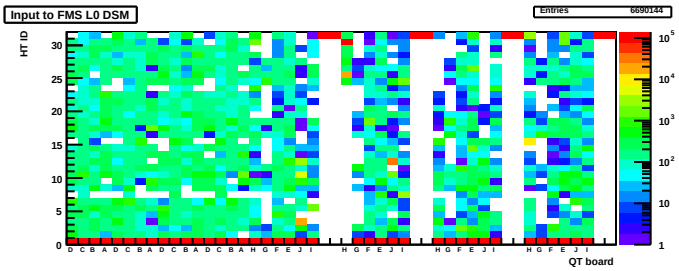

Input to FMS L0 DSM
Entries 99106
QT8(2) sum - simulated
QT board

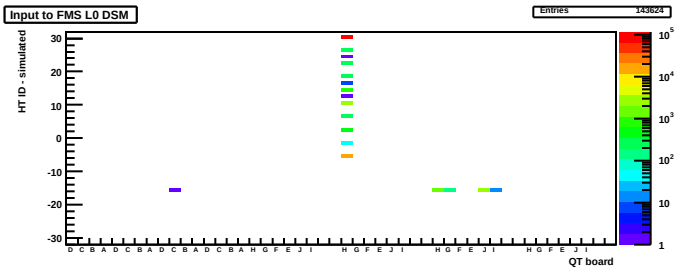

Input to FMS L0 DSM
Entries 6690144
QT8(3) sum
QT board

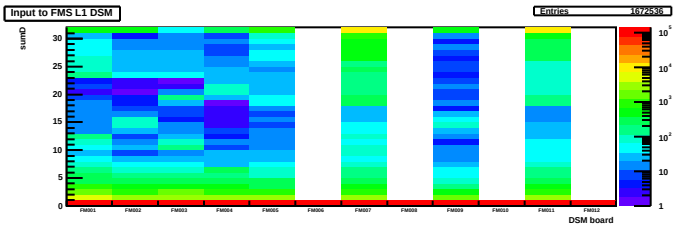

Input to FMS L0 DSM
Entries 130299
QT8(3) sum - simulated
QT board

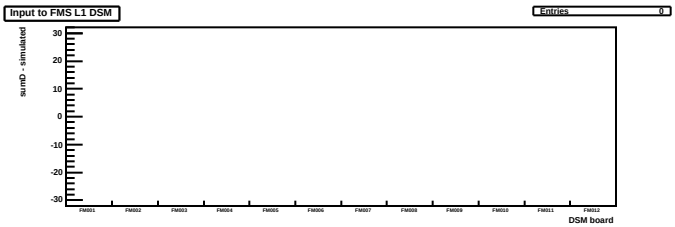

Input to FMS L0 DSM
Entries 6690144
HT ADC
QT board

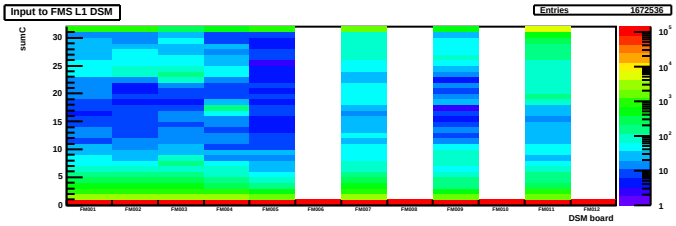

Input to FMS L0 DSM
Entries 10
HT ADC - simulated
QT board

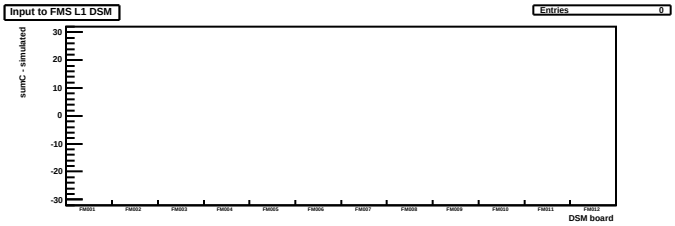

Input to FMS L0 DSM
Entries 6690144
HT ID
QT board

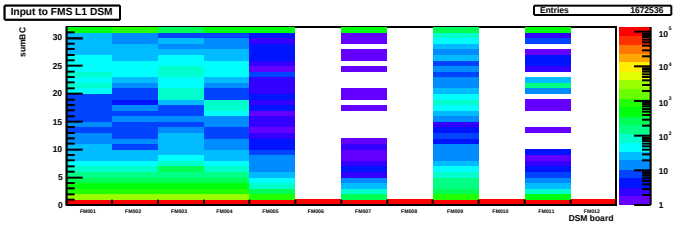

Input to FMS L0 DSM
Entries 143624
HT ID - simulated
QT board

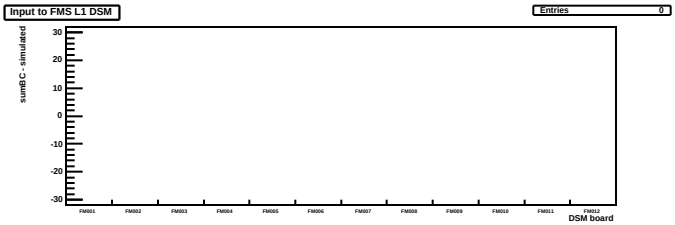

Input to FMS L1 DSM
Entries 1672536
sumD
DSM board

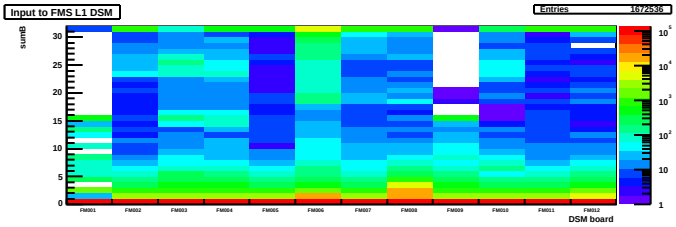

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

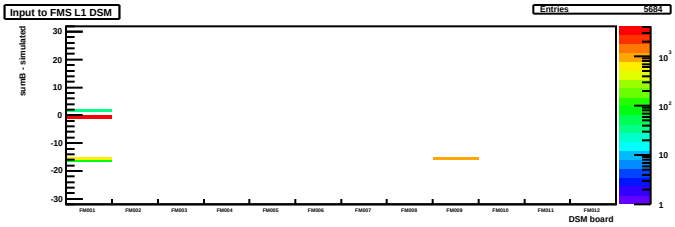

Input to FMS L1 DSM
Entries 1672536
sumC
DSM board

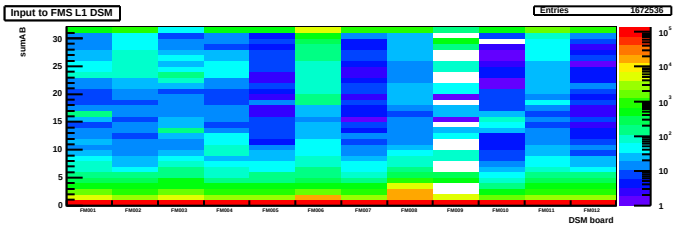

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

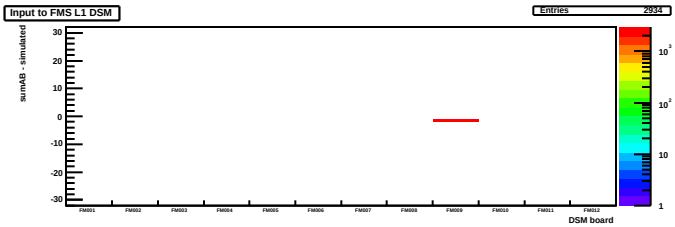

Input to FMS L1 DSM
Entries 1672536
sumBC
DSM board

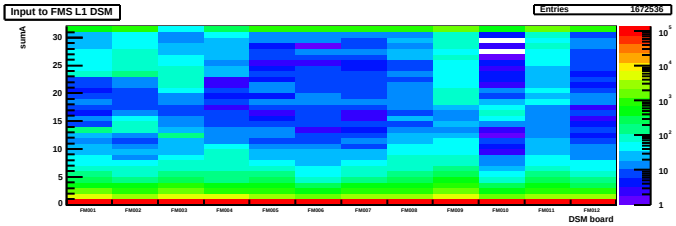

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

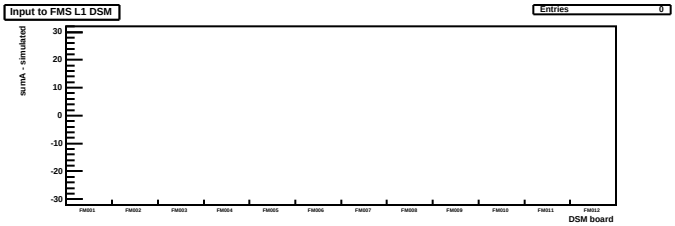

Input to FMS L1 DSM
Entries 1672536
sumB
DSM board

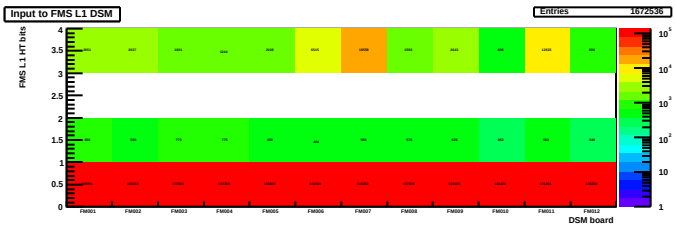

Input to FMS L1 DSM
Entries 5684
sumB - simulated
DSM board

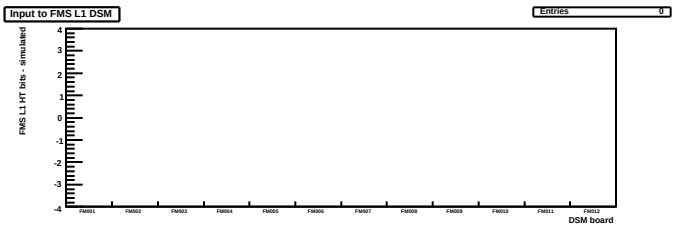

Input to FMS L1 DSM
Entries 1672536
sumAB
DSM board

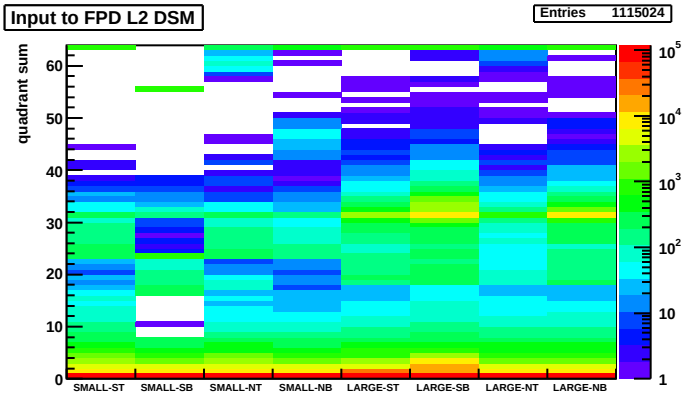

Input to FMS L1 DSM
Entries 2934
sumAB - simulated
DSM board

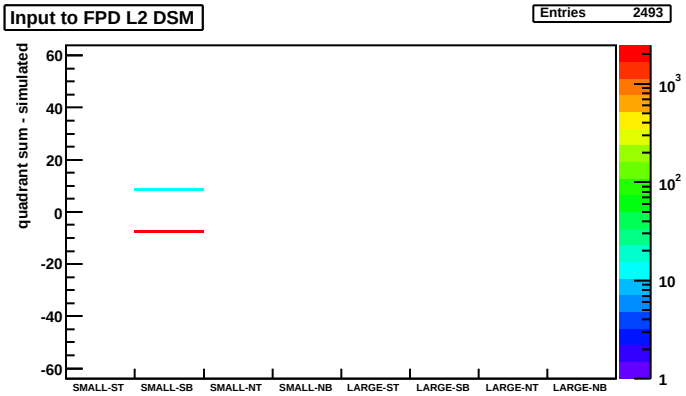

Input to FMS L1 DSM
Entries 1672536
sumA
DSM board

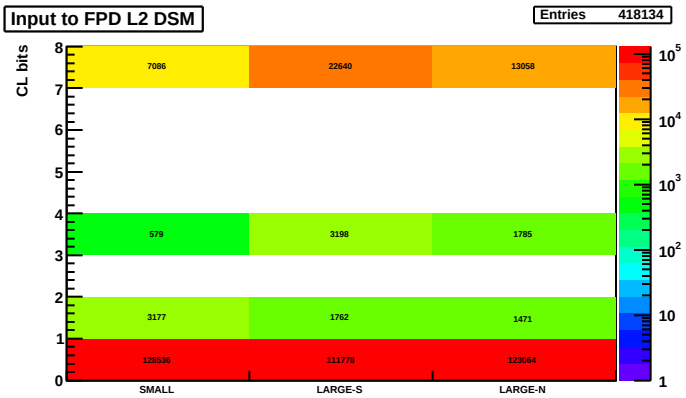

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

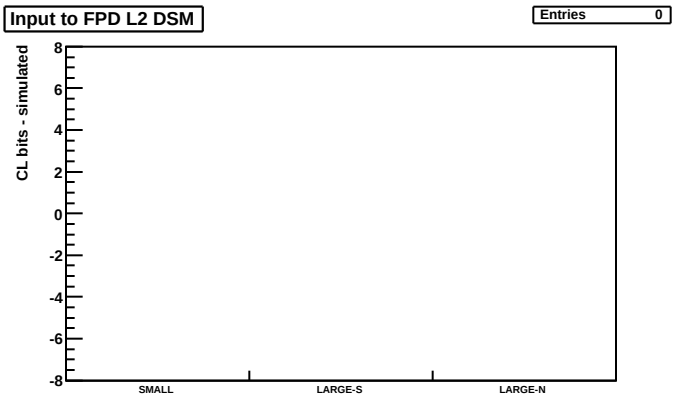

Input to FMS L1 DSM
Entries 1672536
FMS L1 HT bits
DSM board

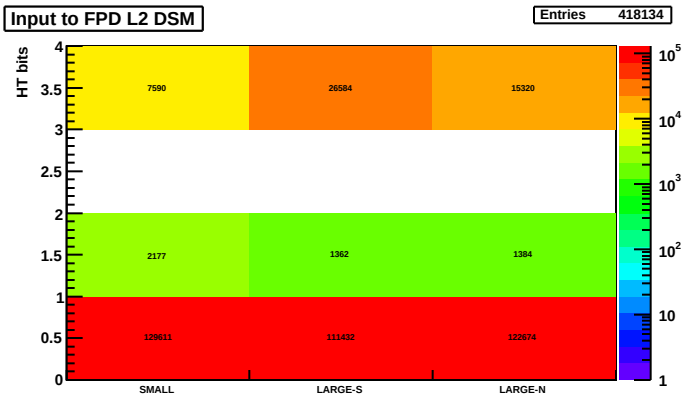

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

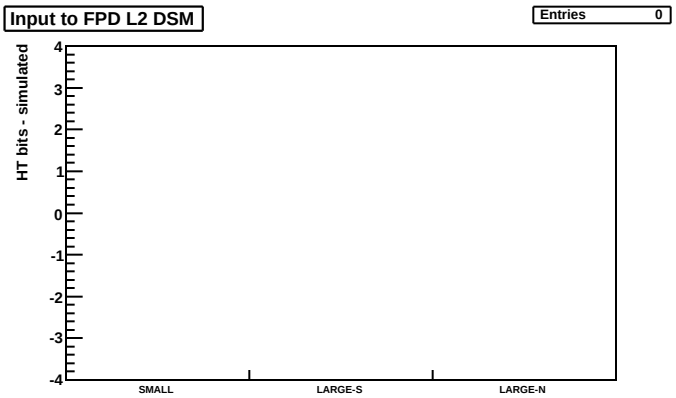

Input to FPD L2 DSM
Entries 1115024
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

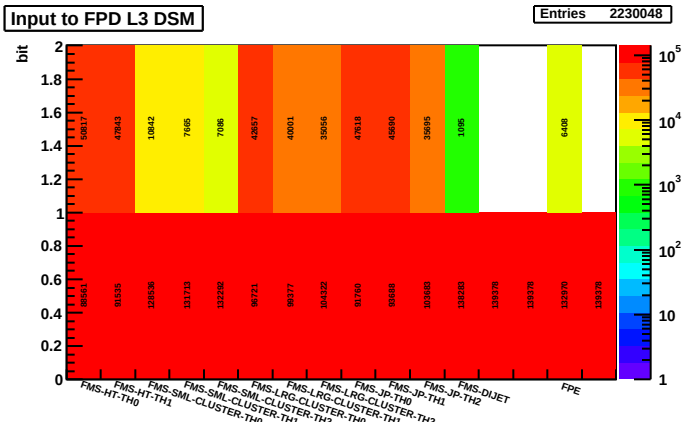

Input to FPD L2 DSM
Entries 2493
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

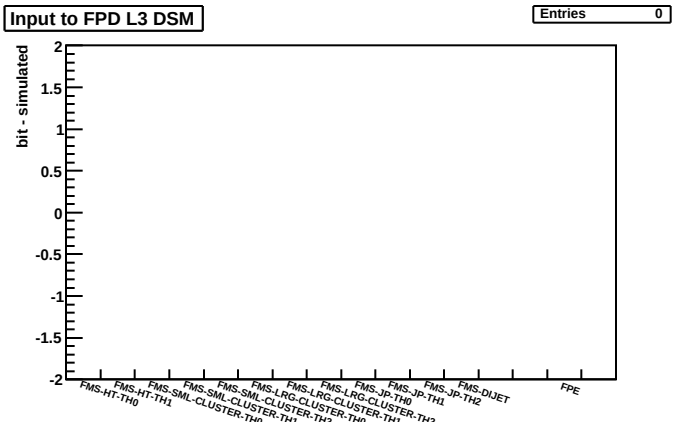

Input to FPD L2 DSM
Entries 418134
CL bits
7086
22640
13058
579
3198
1785
3177
1762
1471
128536
111778
123064
SMALL
LARGE-S
LARGE-N

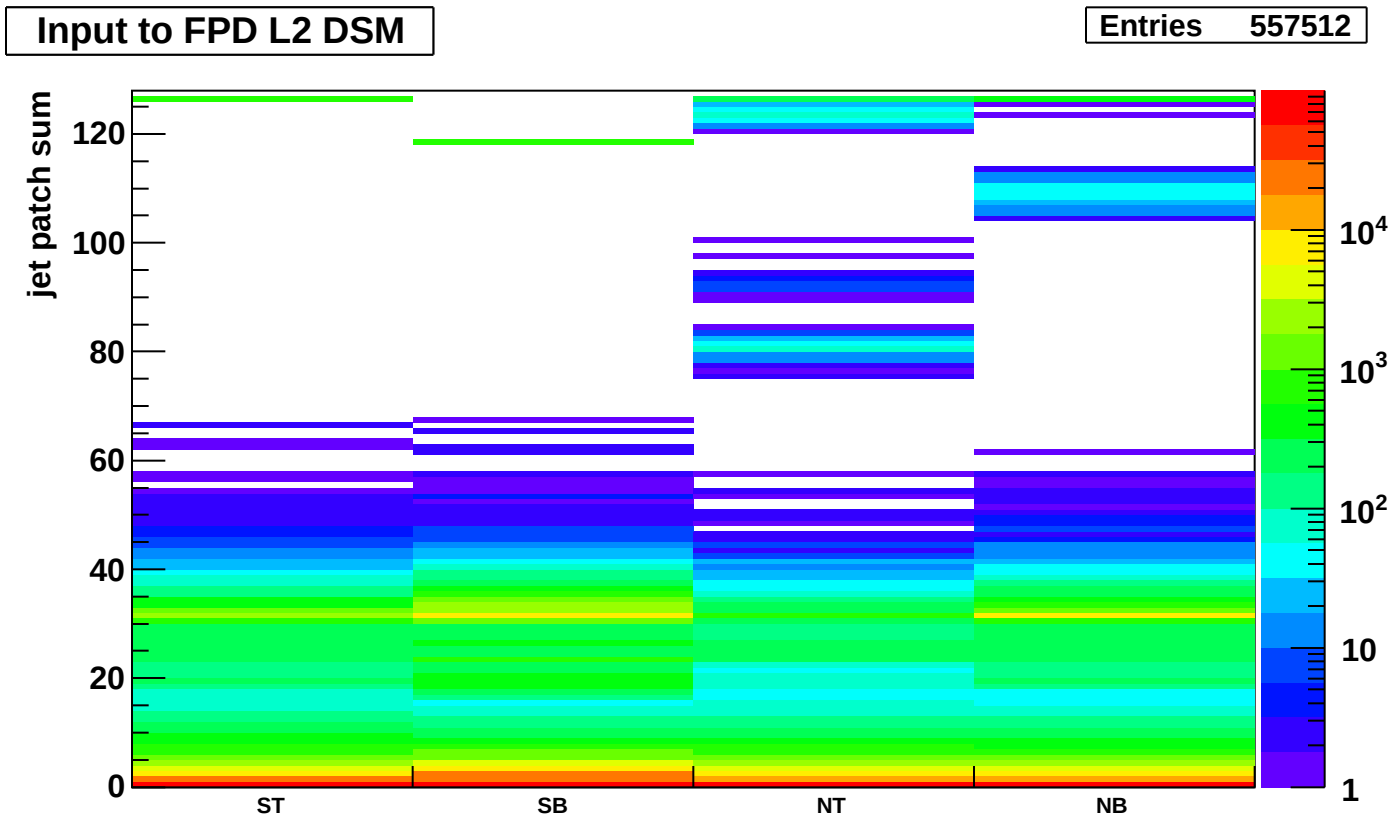

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

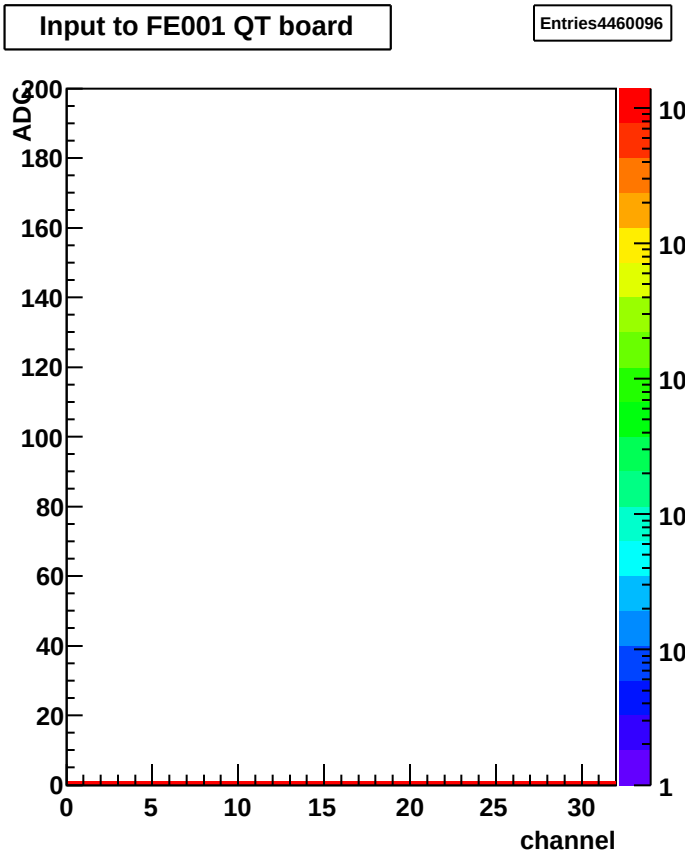

Input to FPD L2 DSM
Entries 418134
HT bits
7590
26584
15320
2177
1362
1384
129611
111432
122674
SMALL
LARGE-S
LARGE-N

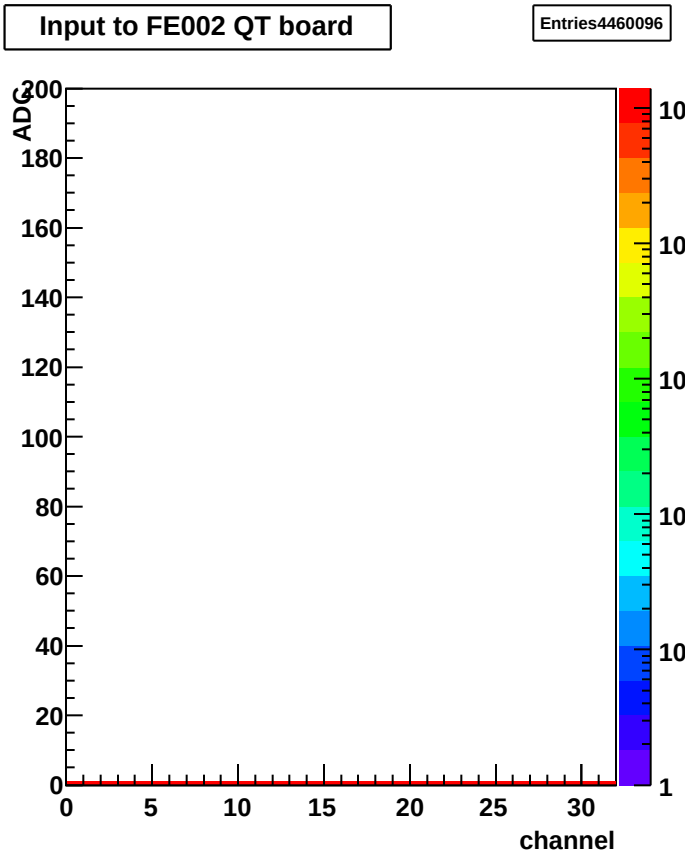

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

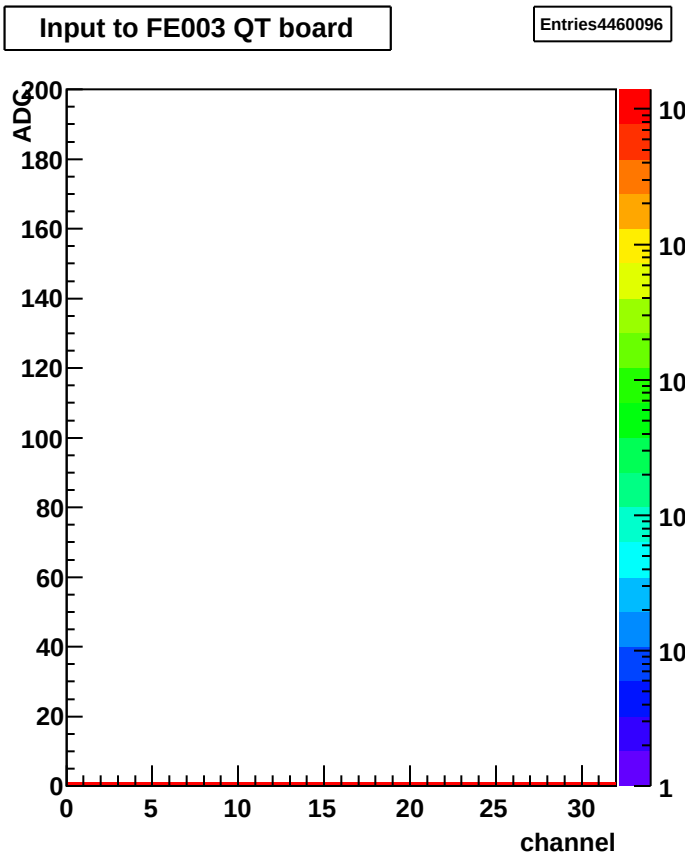

Input to FPD L3 DSM
Entries 2230048
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

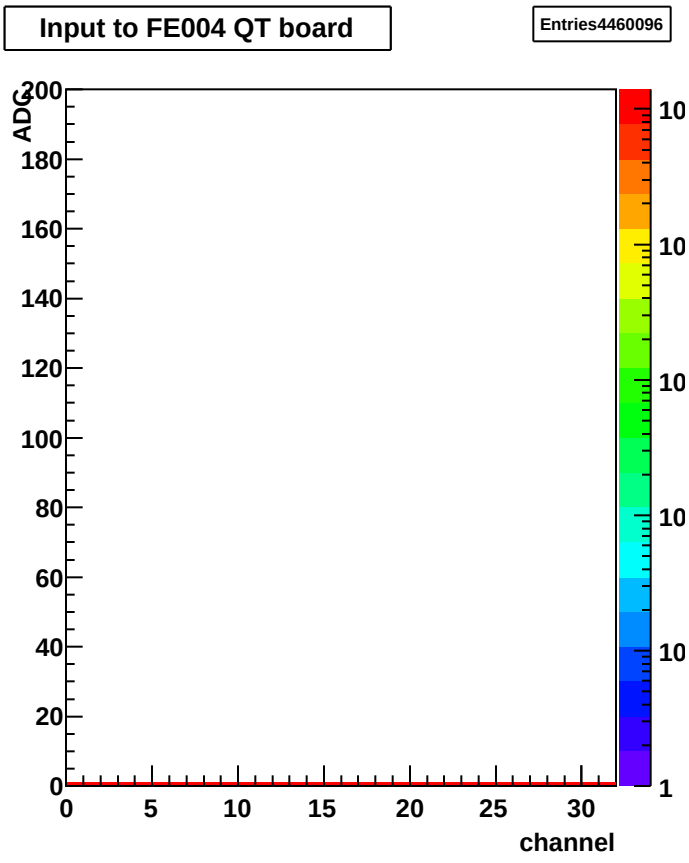

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

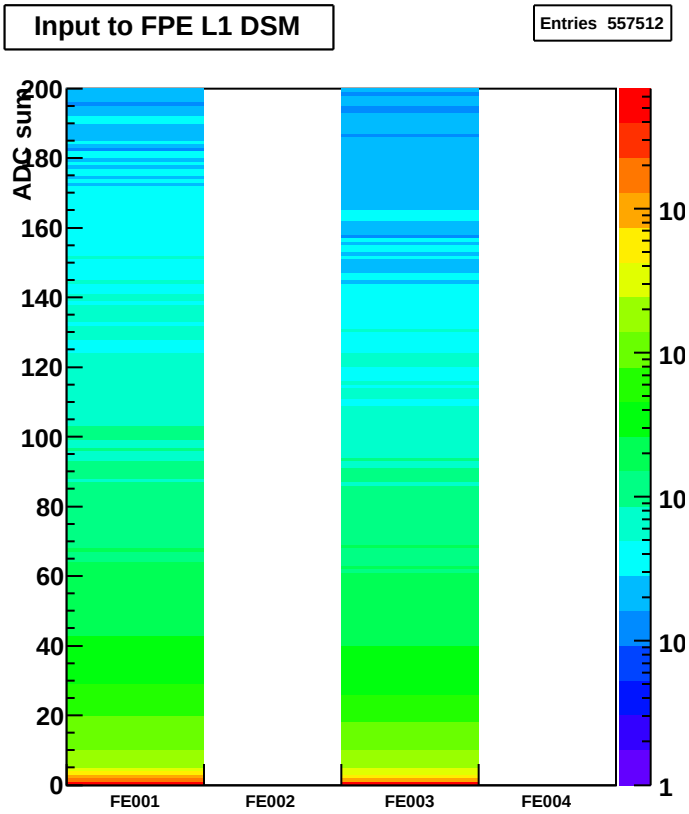

Input to FPD L2 DSM
Entries 557512
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

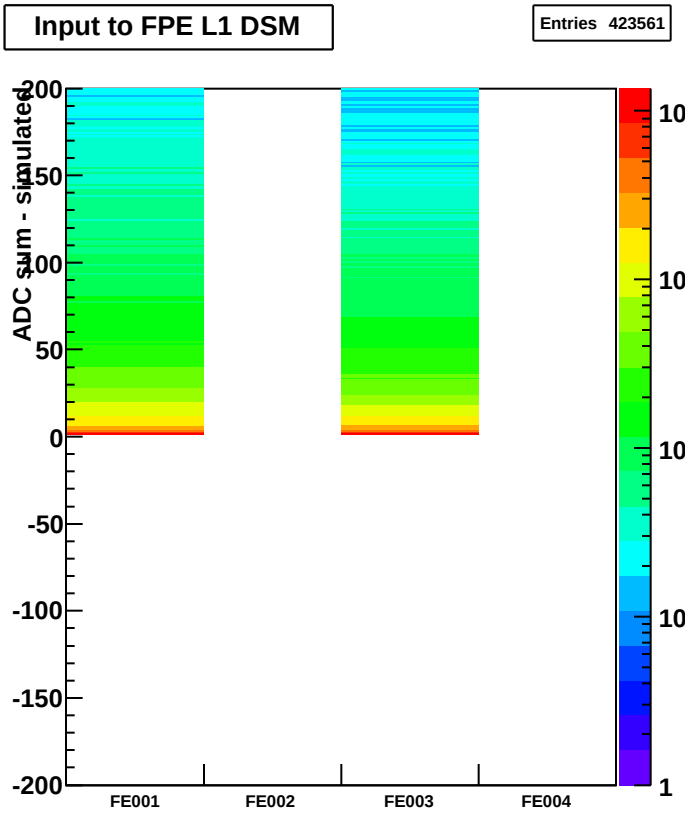

Input to FE001 QT board
Entries4460096
ADC
channel

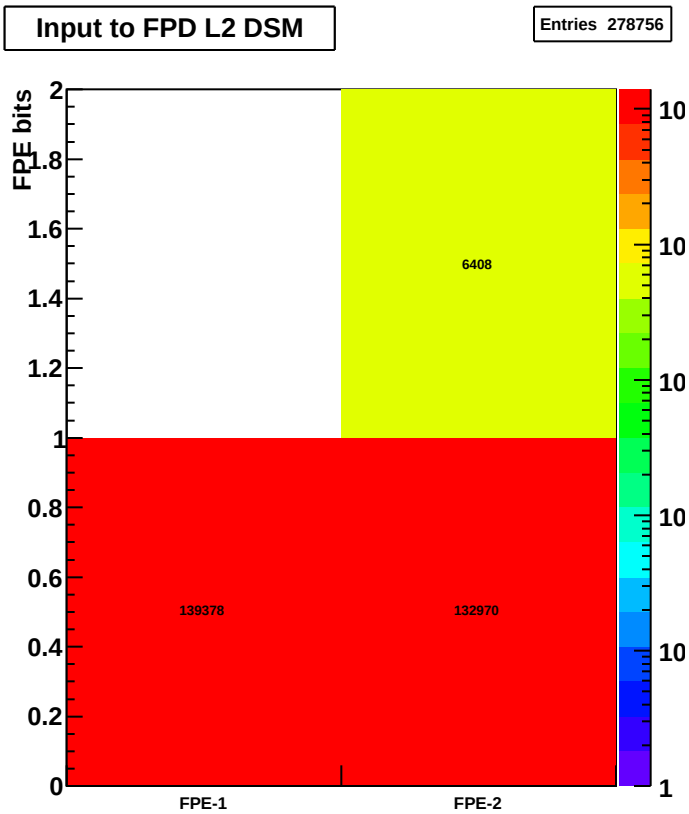

Input to FE002 QT board
Entries4460096
ADC
channel

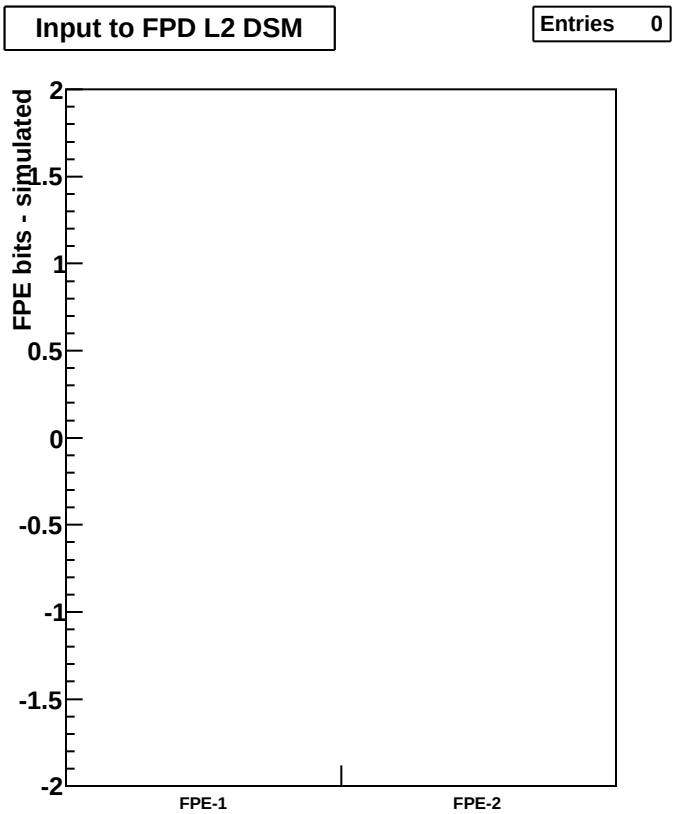

Input to FE003 QT board
Entries4460096
ADC
channel


Input to FE004 QT board
Entries4460096
ADC
channel


Input to FPE L1 DSM
Entries 557512
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 423561
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 278756
FPE bits
6408
139378
132970
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2