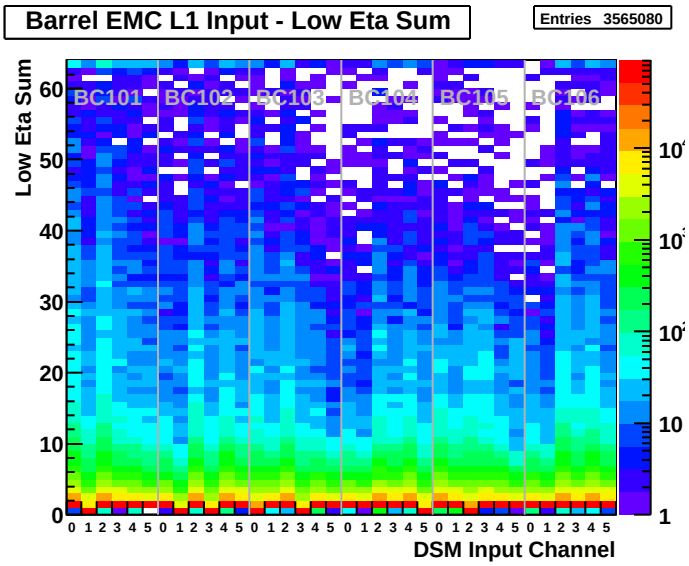
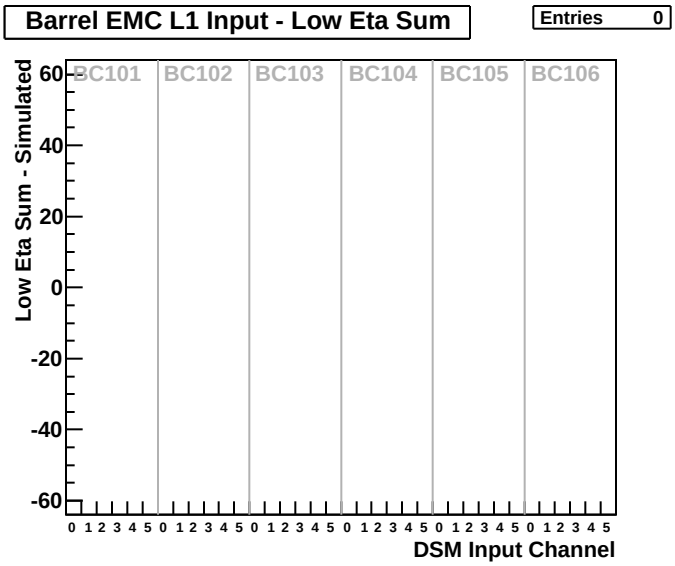
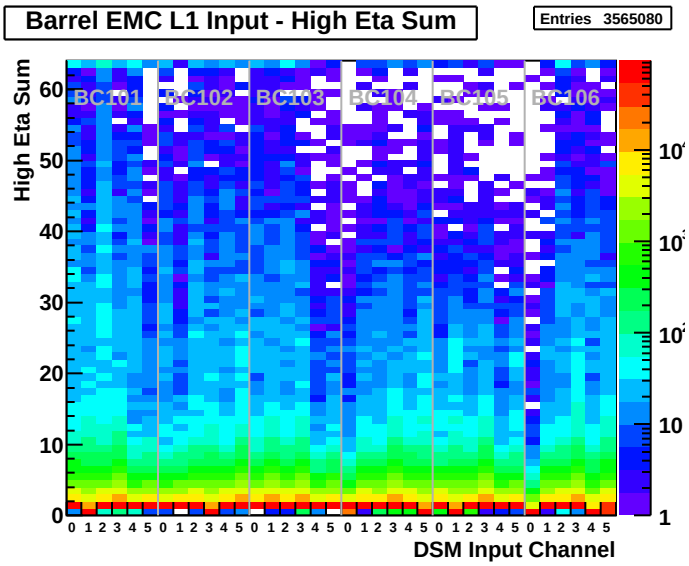
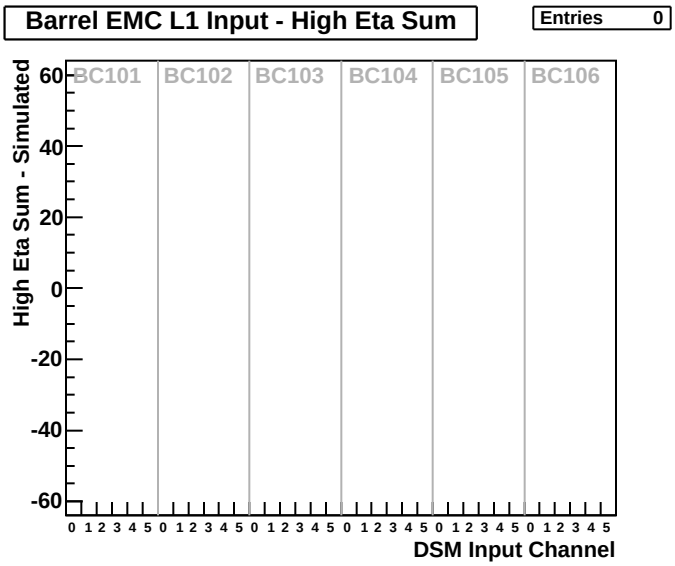
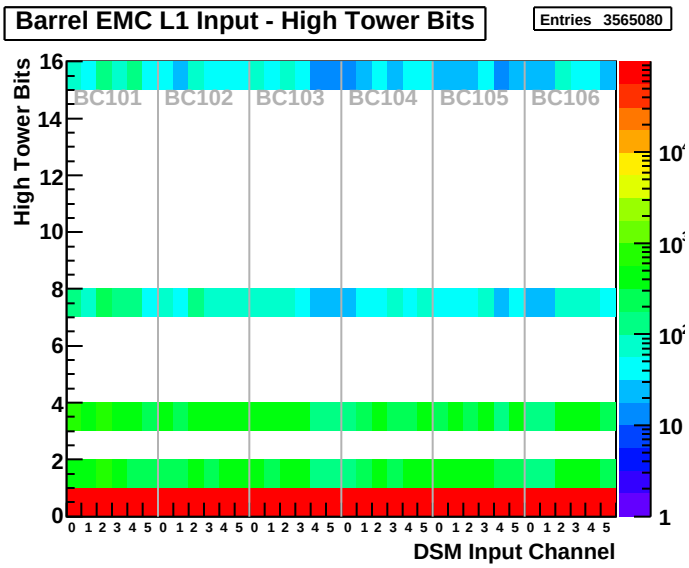
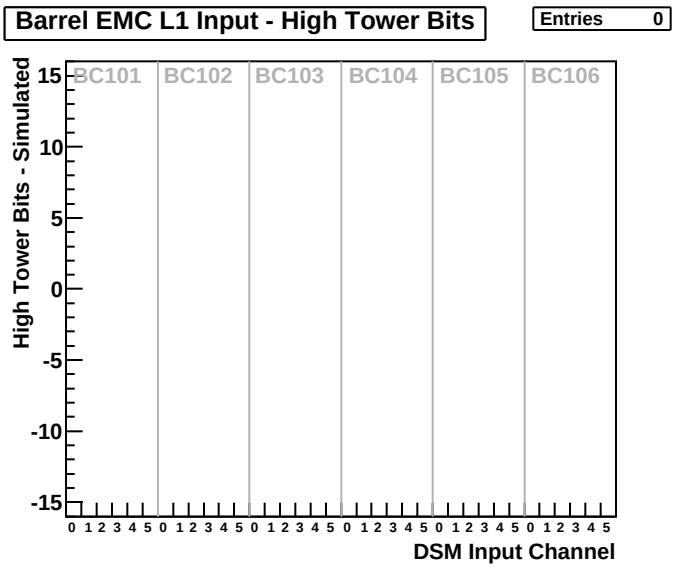

Barrel EMC L1 Input - Low Eta Sum
Entries 3565080
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

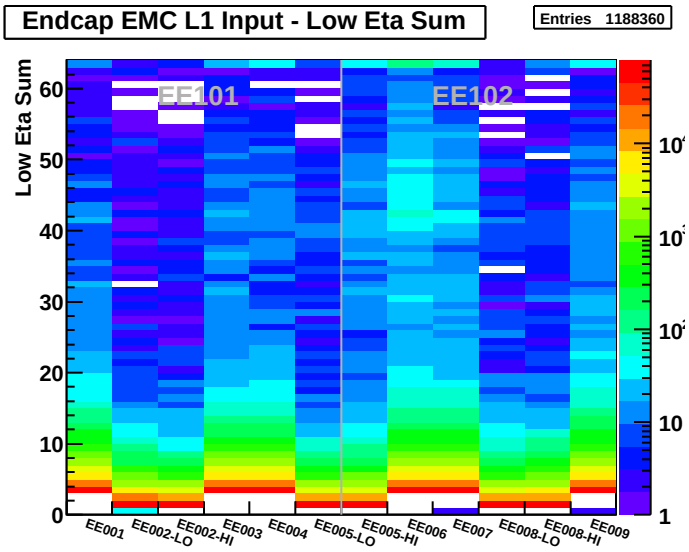

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

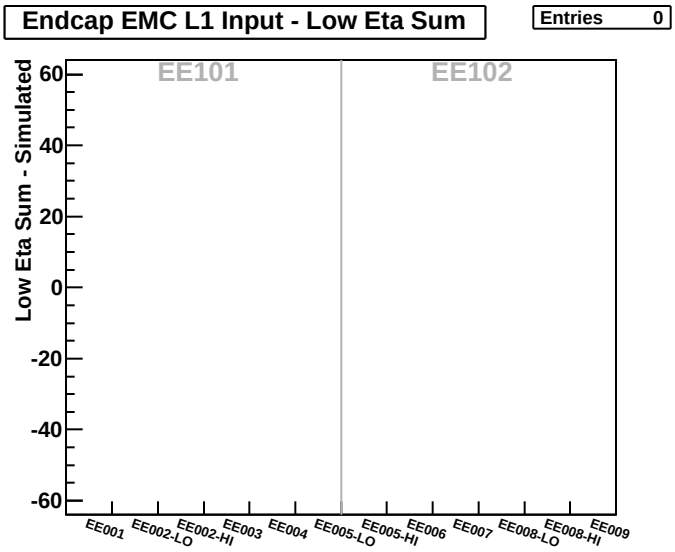

Barrel EMC L1 Input - High Eta Sum
Entries 3565080
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

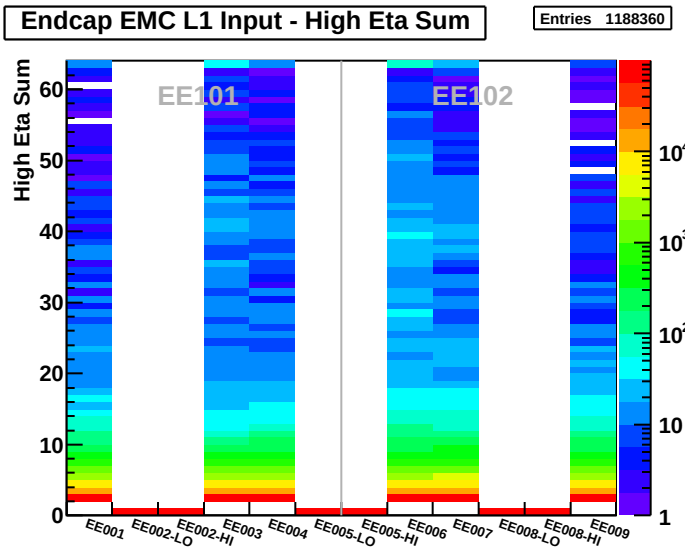

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

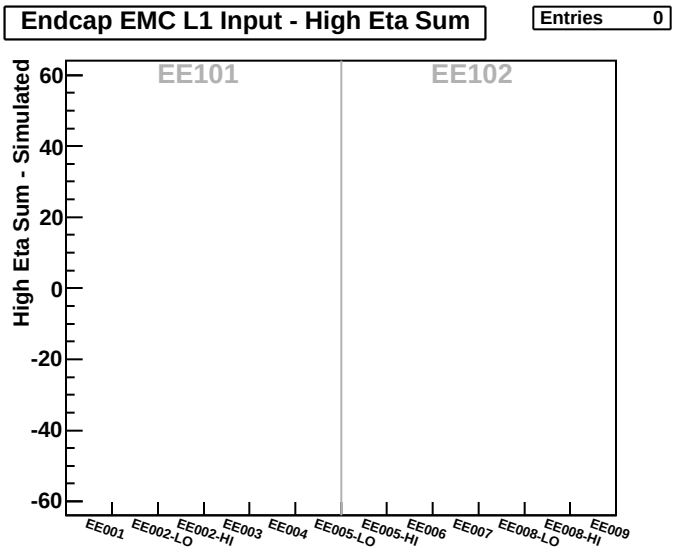

Barrel EMC L1 Input - High Tower Bits
Entries 3565080
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

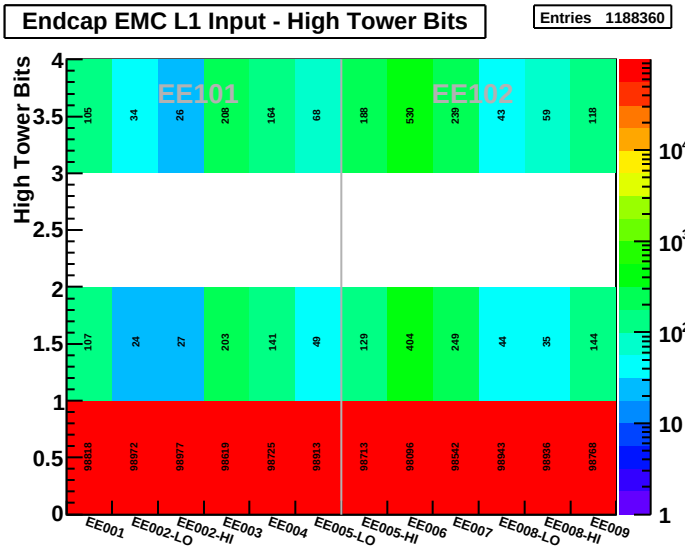

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

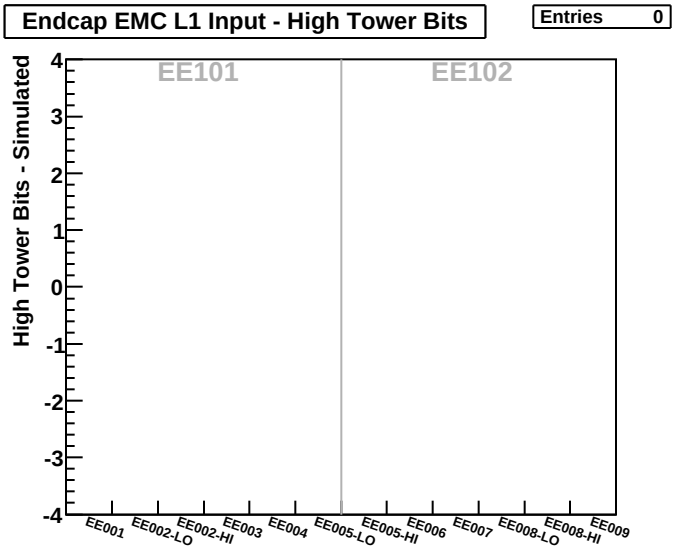

Endcap EMC L1 Input - Low Eta Sum
Entries 1188360
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

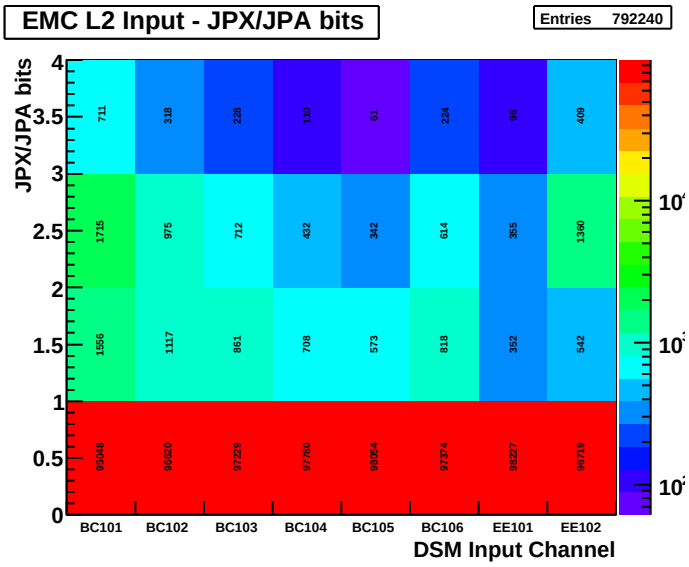

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

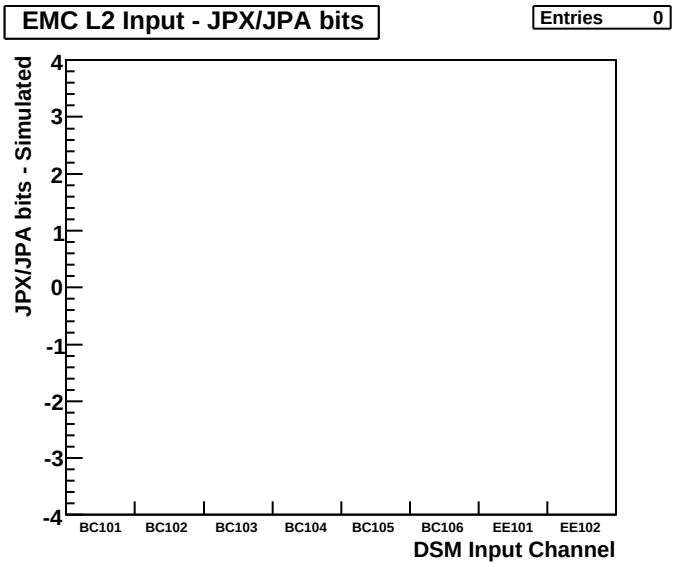

Endcap EMC L1 Input - High Eta Sum
Entries 1188360
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

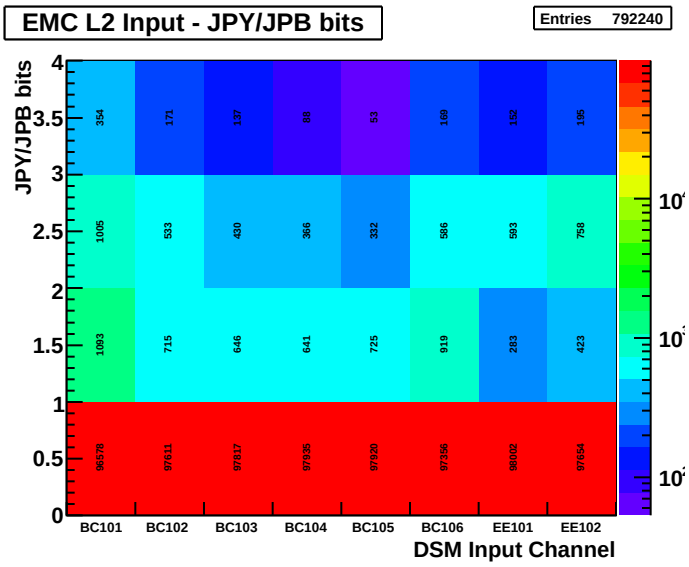

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

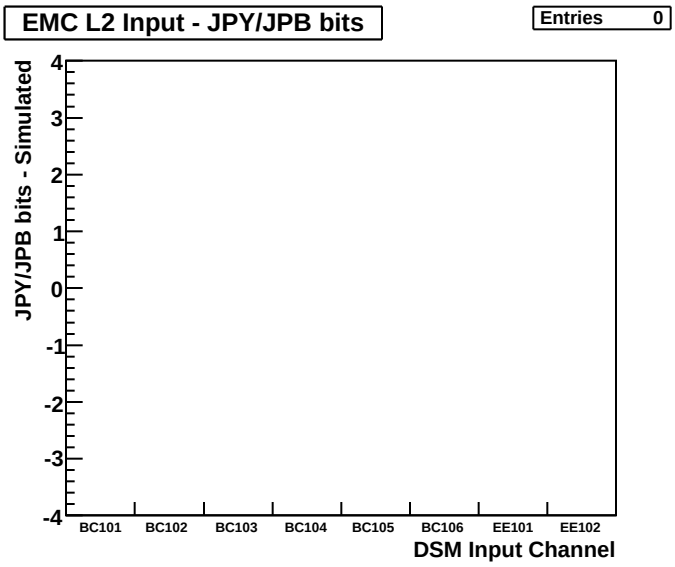

Endcap EMC L1 Input - High Tower Bits
Entries 1188360
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

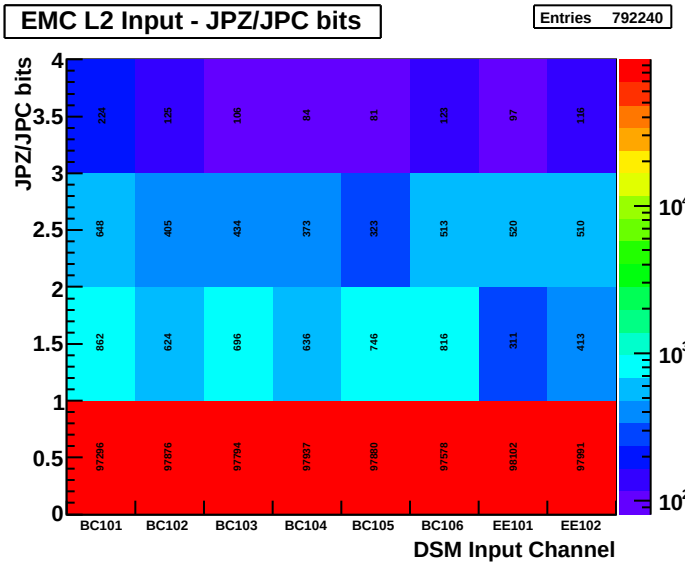

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

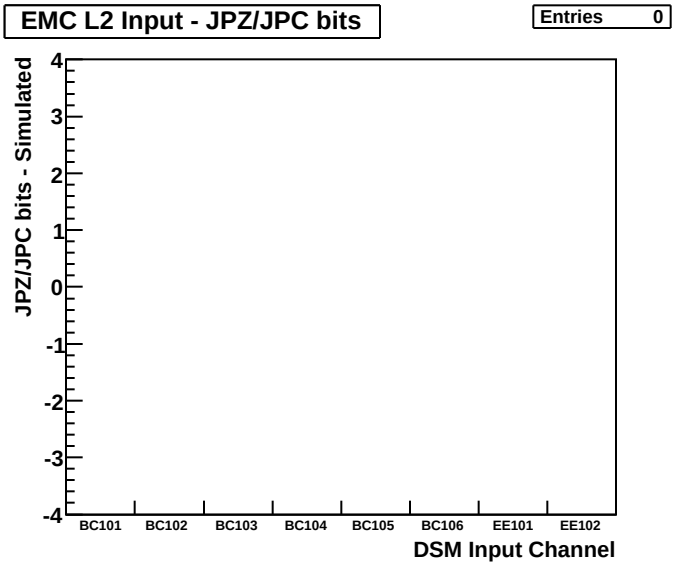

EMC L2 Input - JPX/JPA bits
Entries 792240
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
711 318 228 110 61 224 96 409
1715 975 712 432 342 614 355 1360
1556 1117 861 708 573 818 352 542
95048 96620 97229 97780 98054 97374 98227 96719

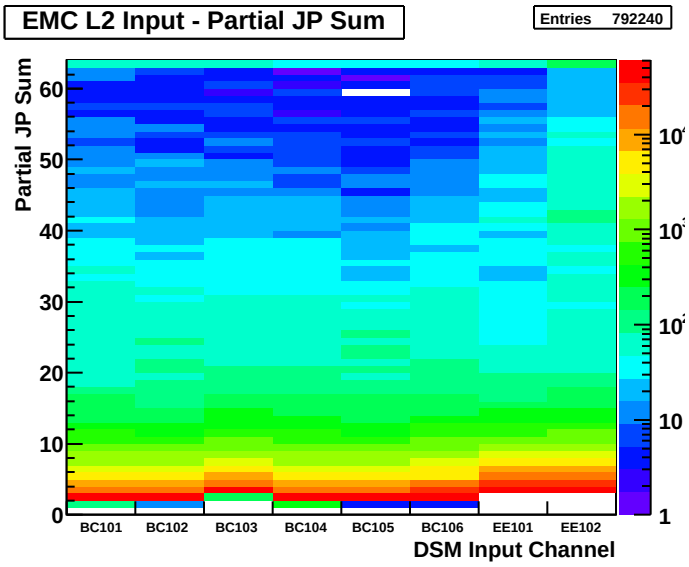

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

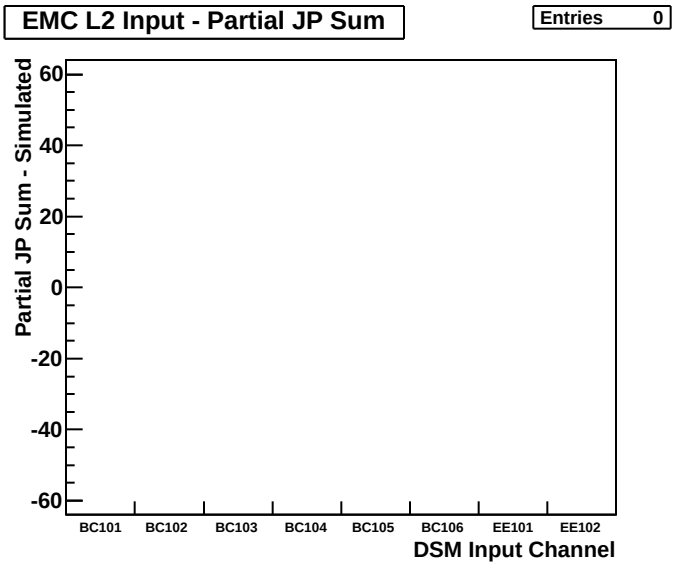

EMC L2 Input - JPY/JPB bits
Entries 792240
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
354 171 137 88 53 169 152 195
1005 533 430 366 332 586 593 758
1093 715 646 641 725 919 283 423
96578 97611 97817 97935 97920 97356 98002 97654

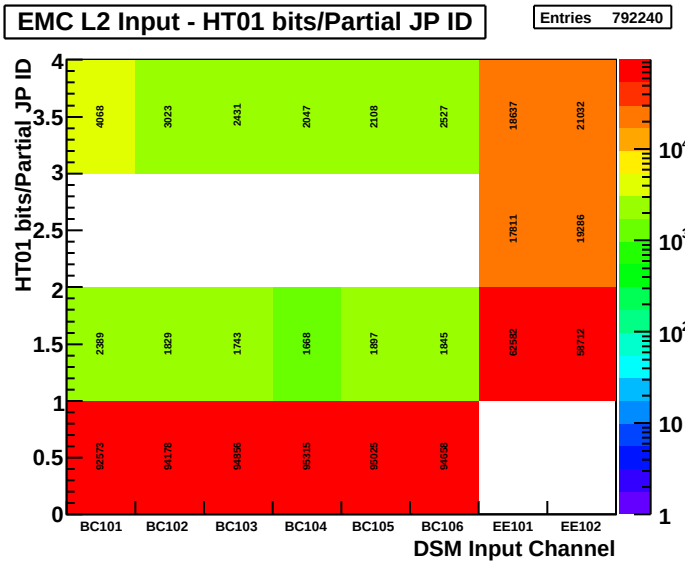

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

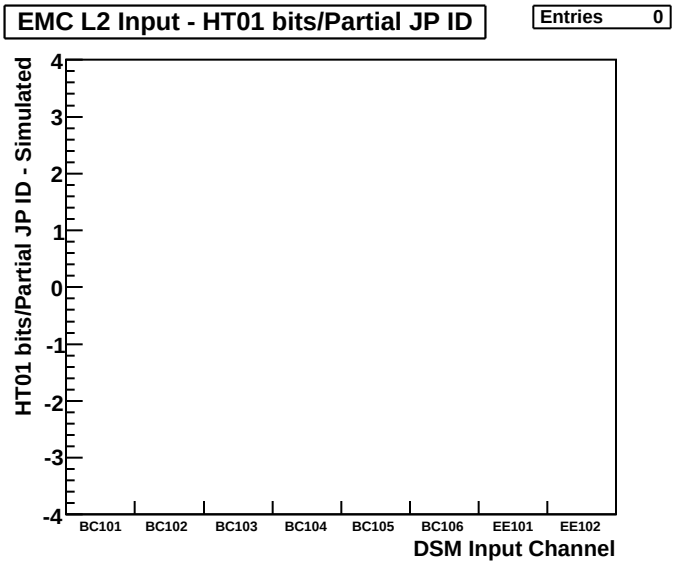

EMC L2 Input - JPZ/JPC bits
Entries 792240
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
224 125 106 84 81 123 97 116
648 405 434 373 323 513 520 510
862 624 696 636 746 816 311 413
97296 97876 97794 97937 97880 97578 98102 97901

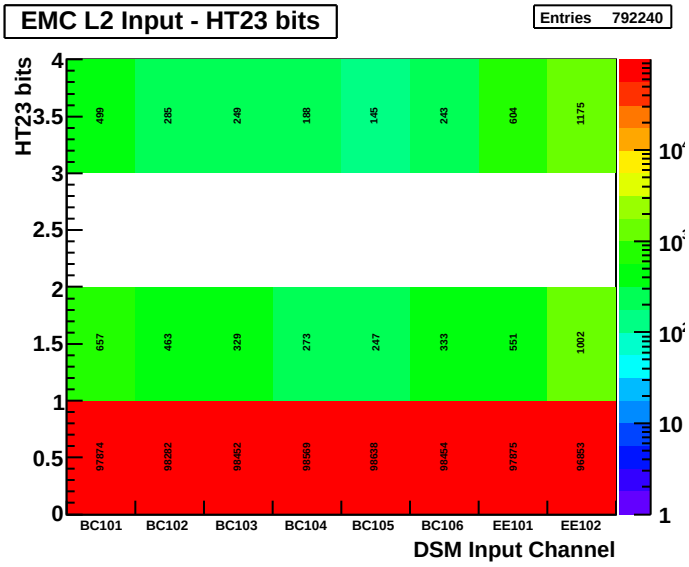

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

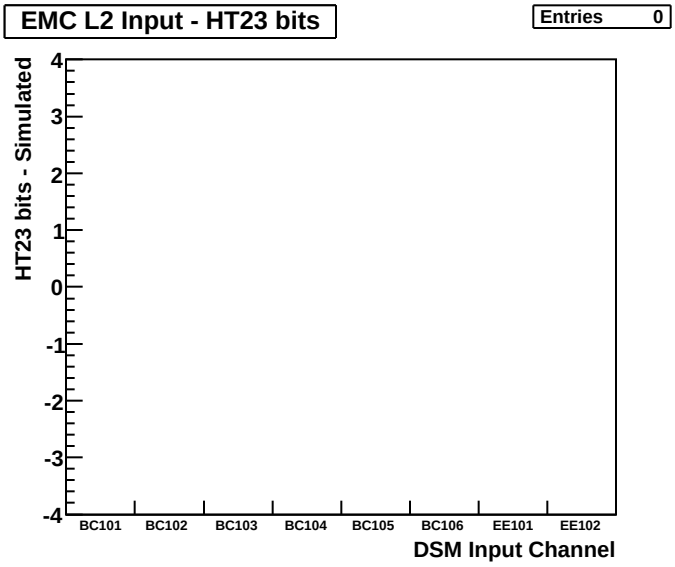

EMC L2 Input - Partial JP Sum
Entries 792240
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

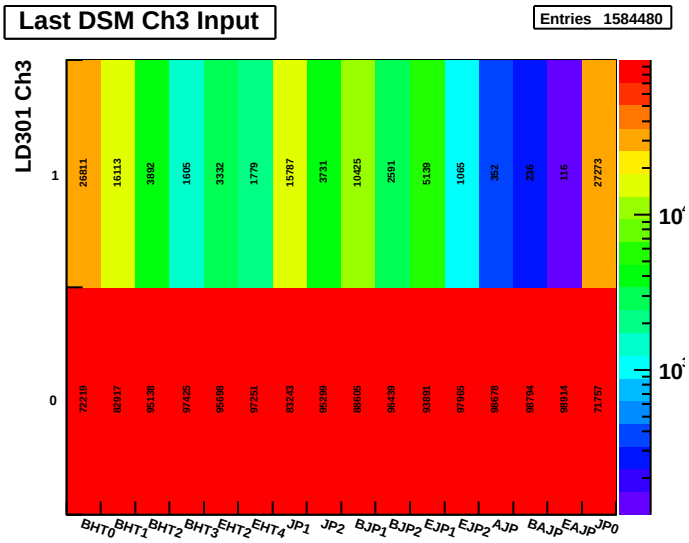

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

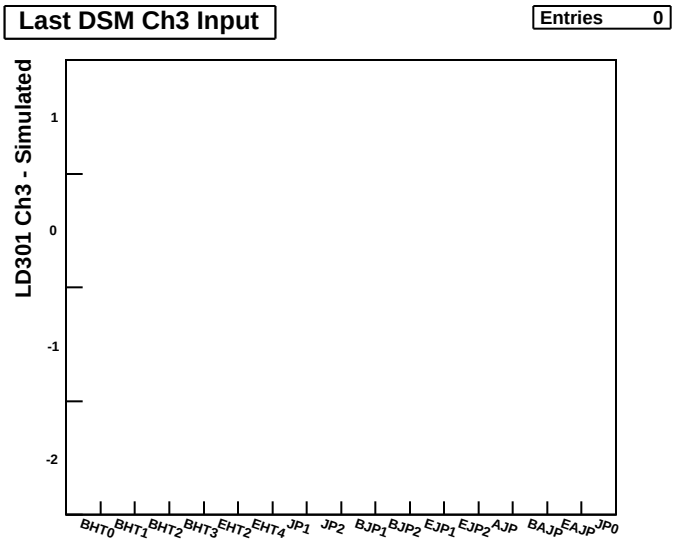

EMC L2 Input - HT01 bits/Partial JP ID
Entries 792240
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

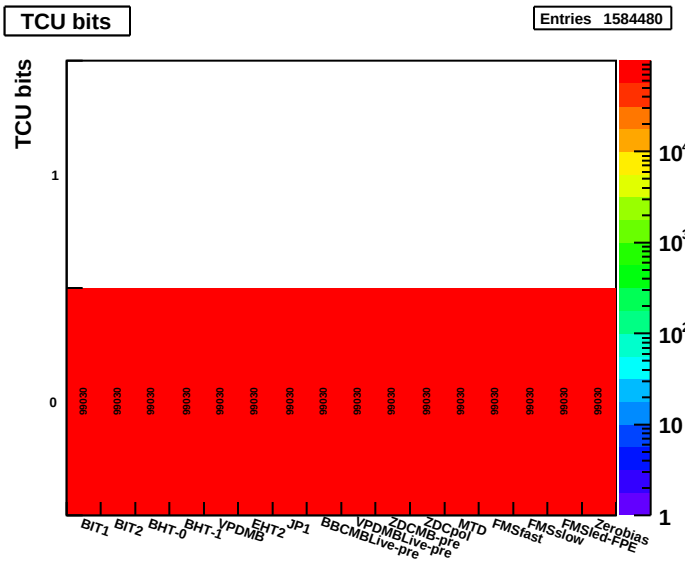

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

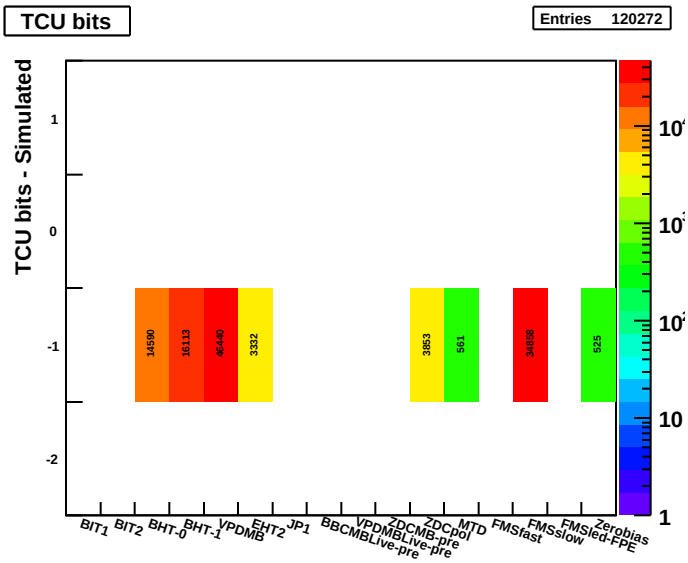

EMC L2 Input - HT23 bits
Entries 792240
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

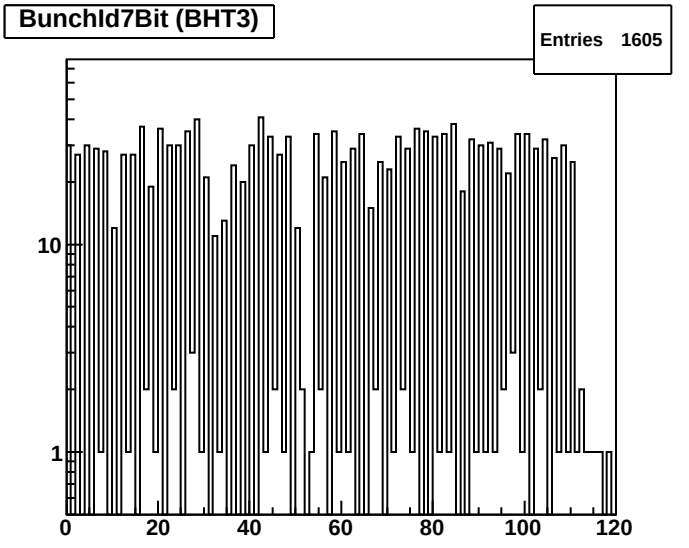

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

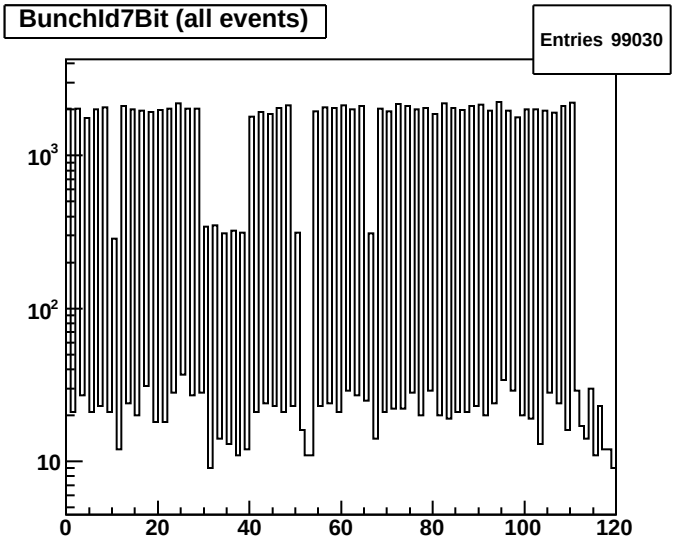

Last DSM Ch3 Input
Entries 1584480
LD301 Ch3
26811 16113 3892 1605 3332 1779 15787 3731 10425 2591 5139 1065 352 236 116 27273
72219 82917 95138 97425 95698 97251 83243 95299 88605 96439 93891 97965 98678 98794 98914 71757
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

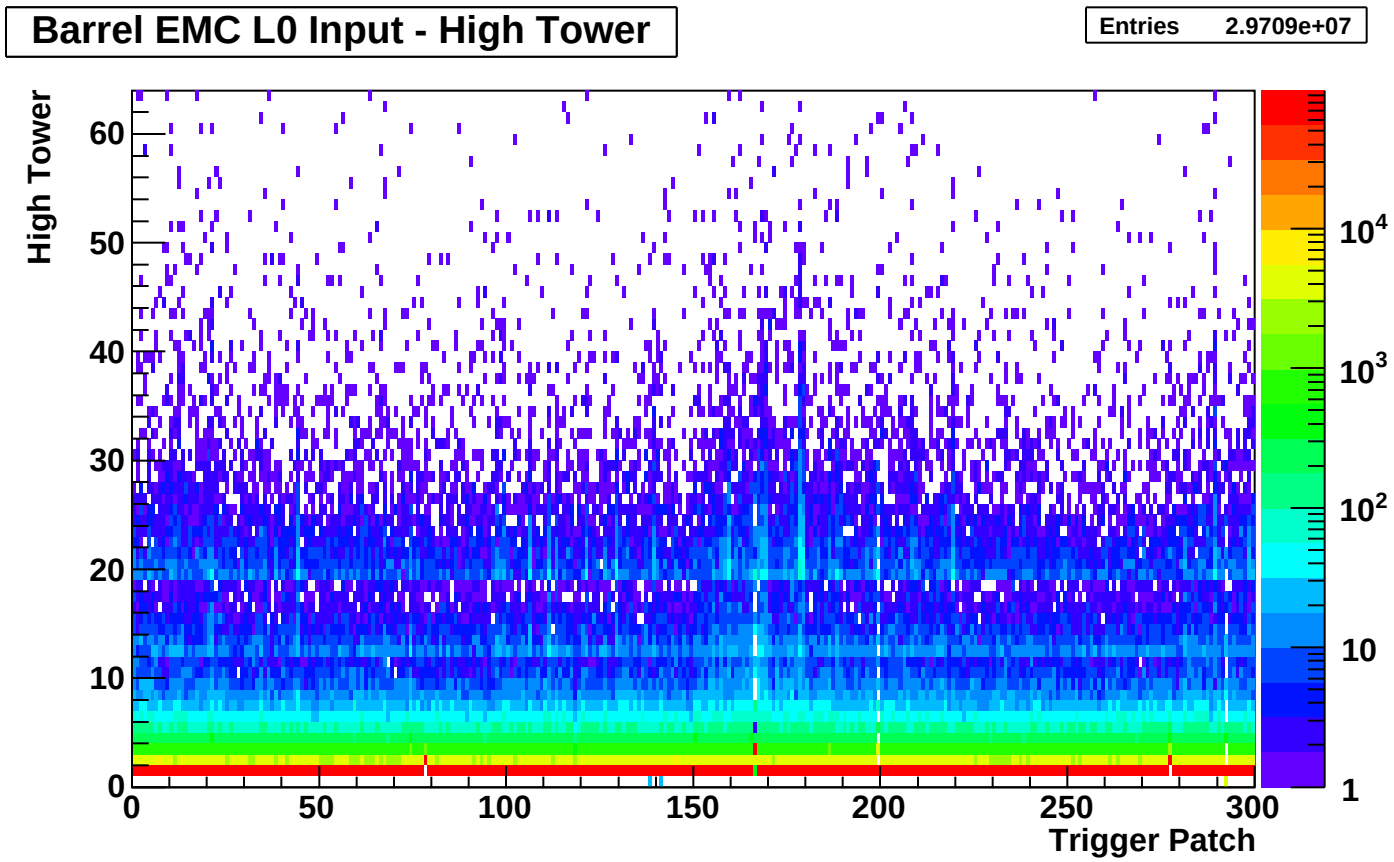

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

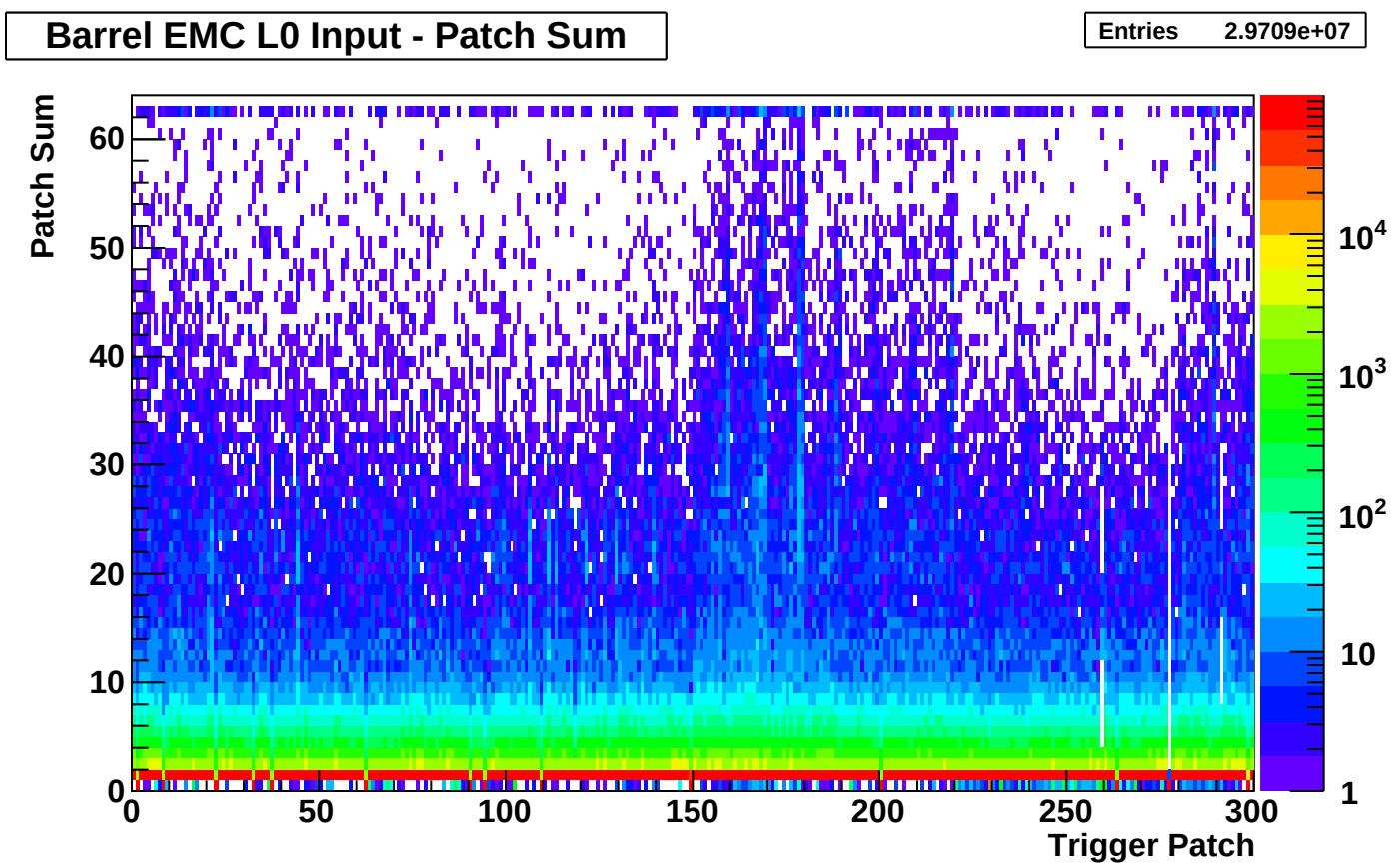

TCU bits
Entries 1584480
TCU bits
99030 99030 99030 99030 99030 99030 99030 99030 99030 99030 99030 99030 99030 99030 99030 99030
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

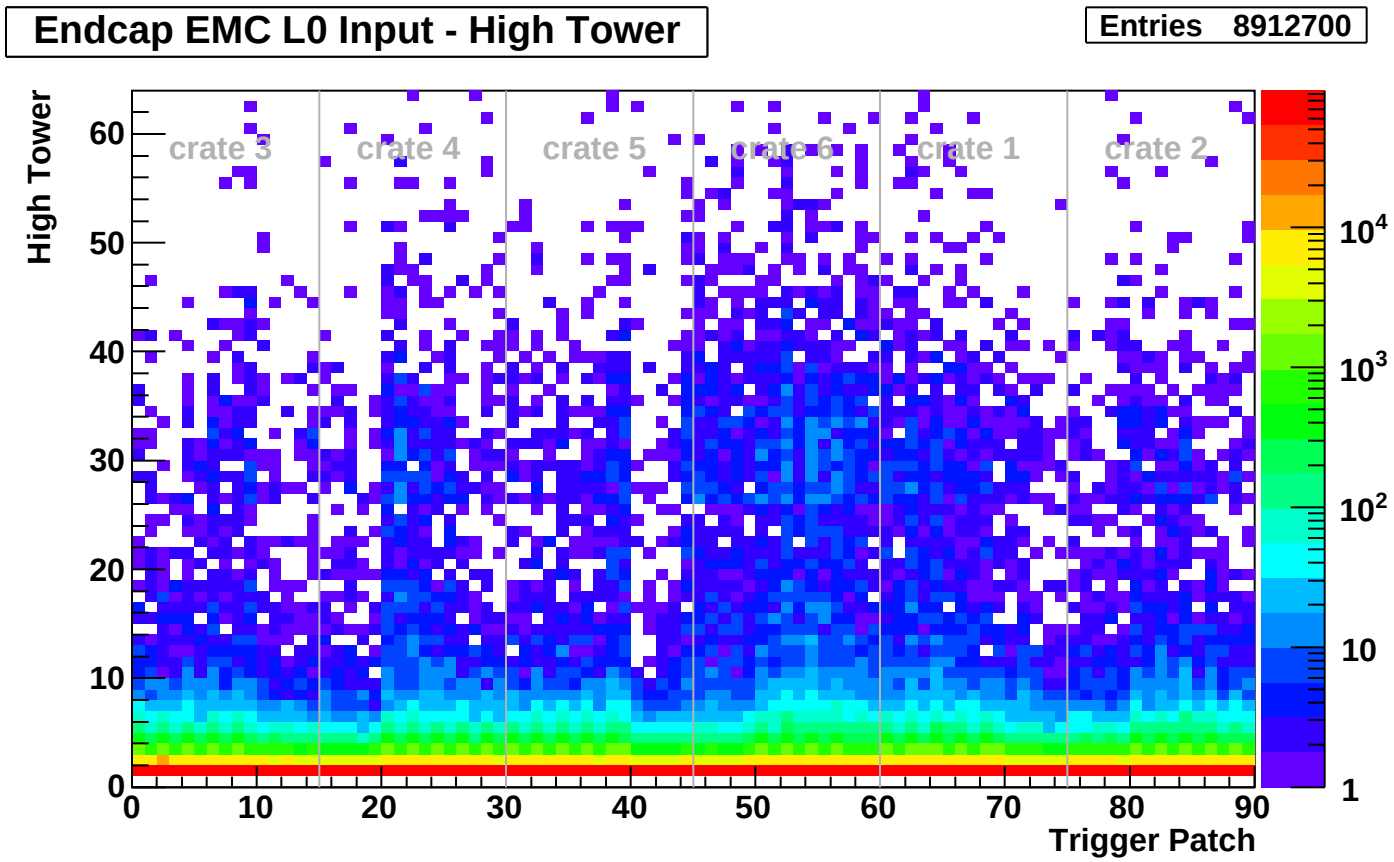

TCU bits
Entries 120272
TCU bits - Simulated
14590 16113 46440 3332 3863 561 34858 525
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

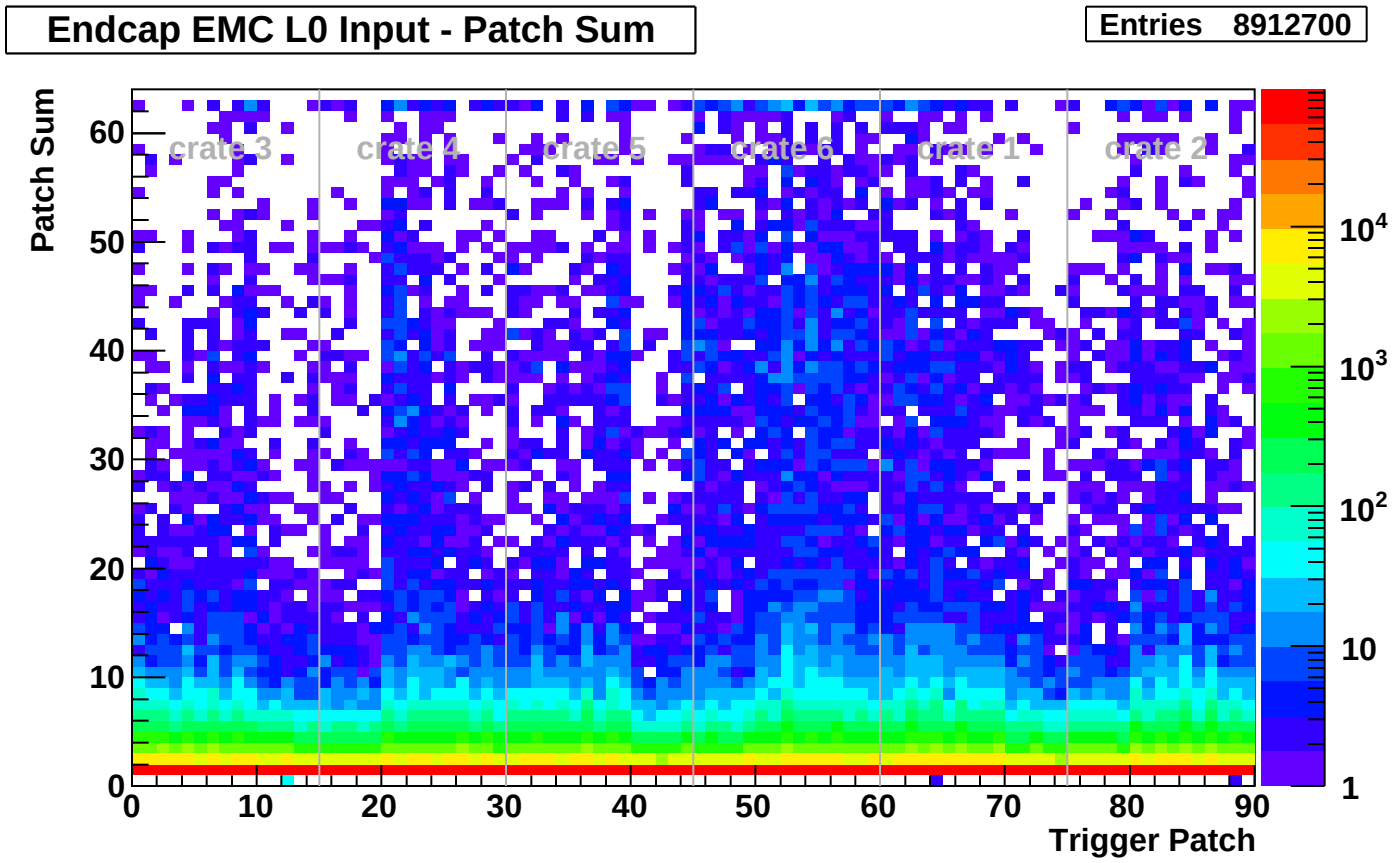

BunchId7Bit (BHT3)
Entries 1605

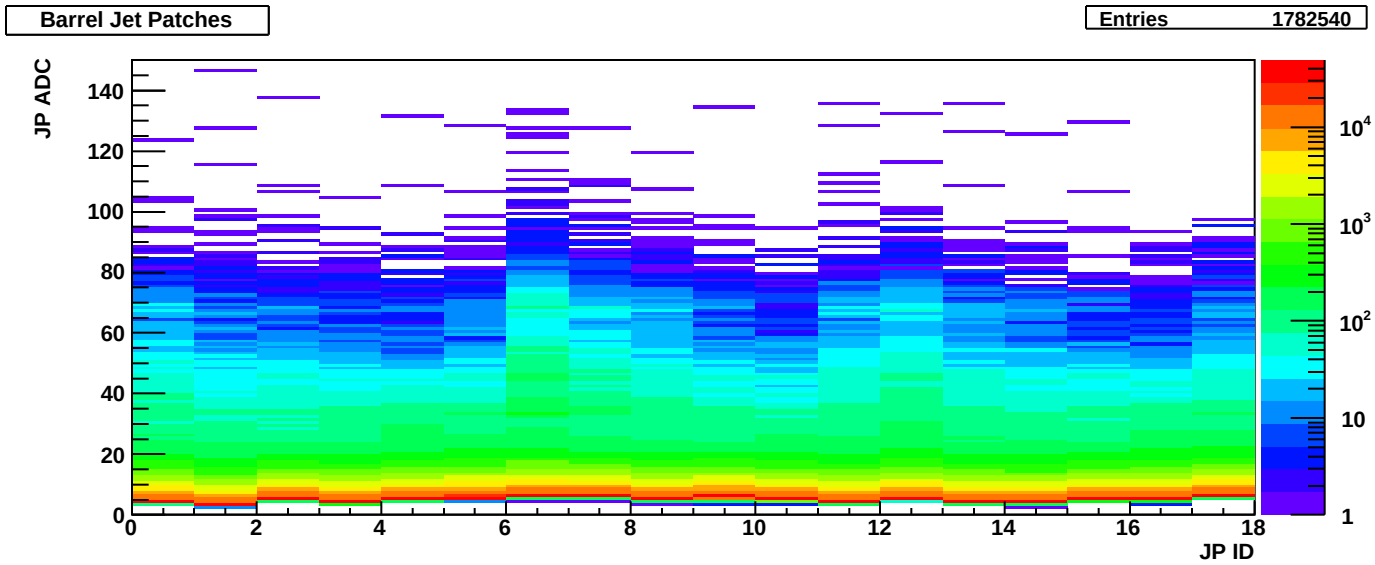

BunchId7Bit (all events)
Entries 99030

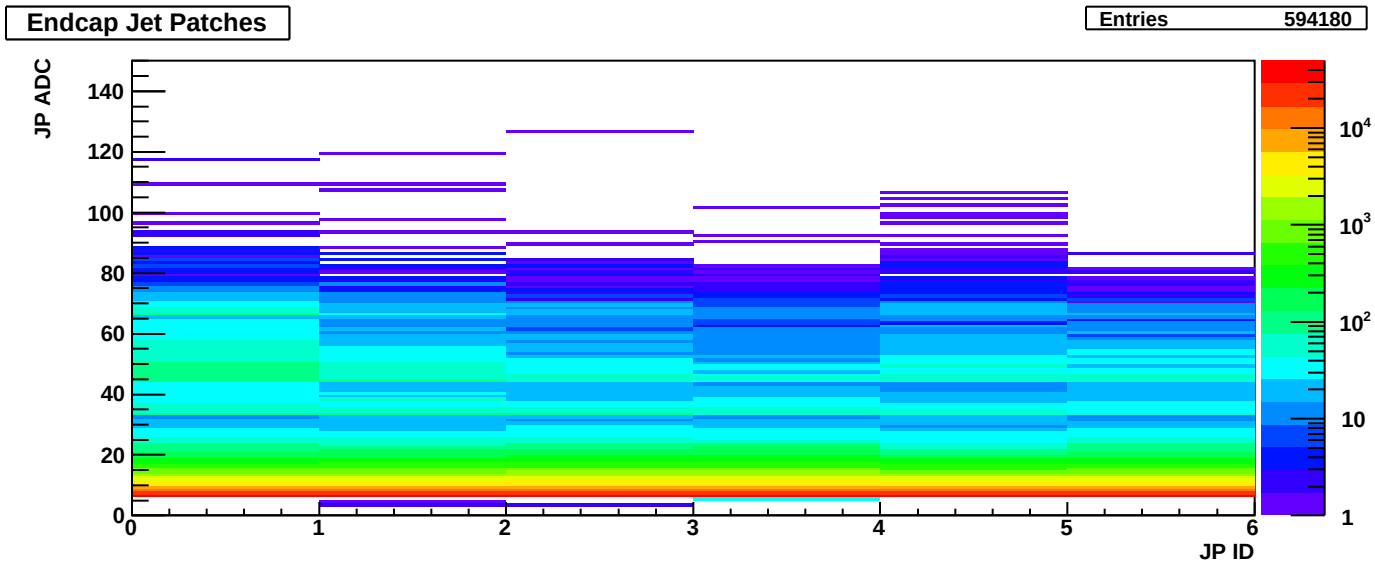

Barrel EMC L0 Input - High Tower
Entries 2.9709e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

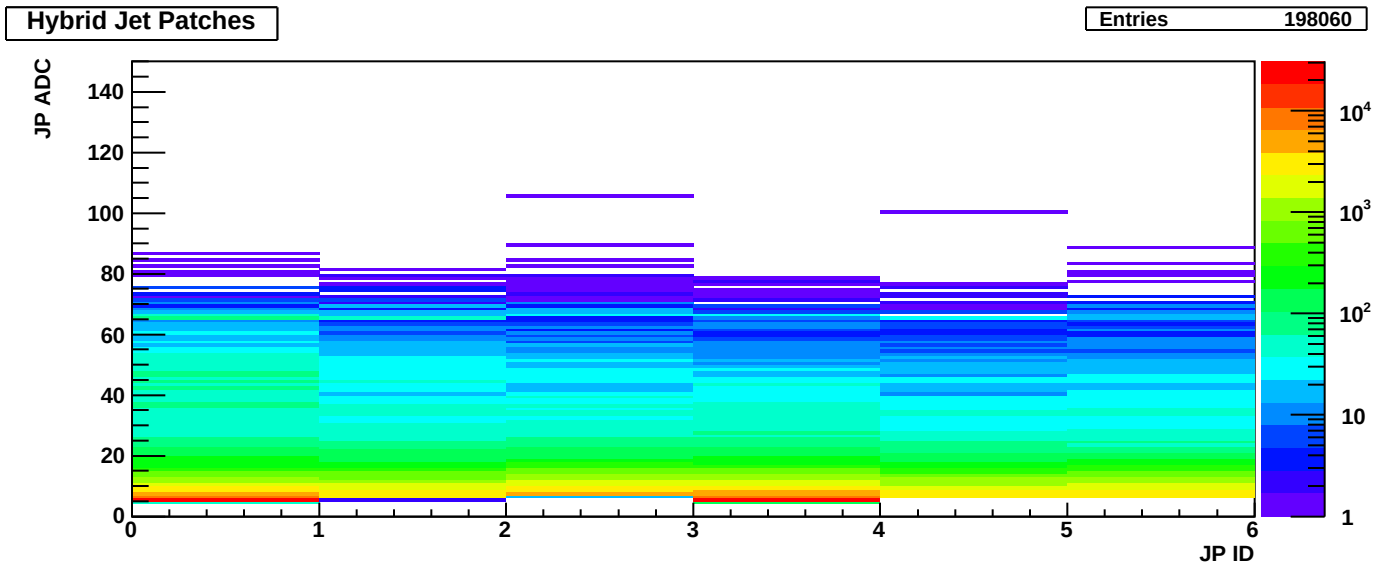

Barrel EMC L0 Input - Patch Sum
Entries 2.9709e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

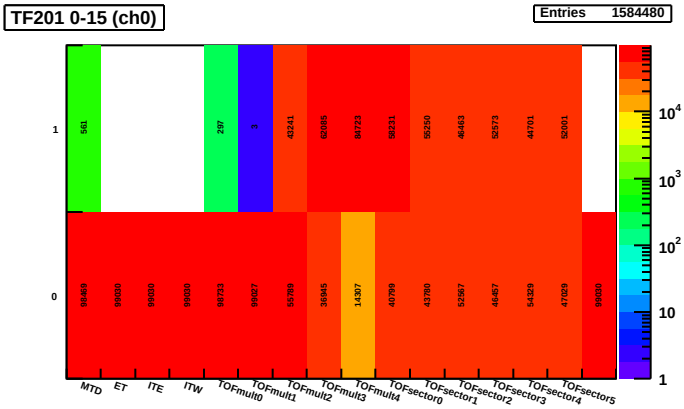

Endcap EMC L0 Input - High Tower
Entries 8912700
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

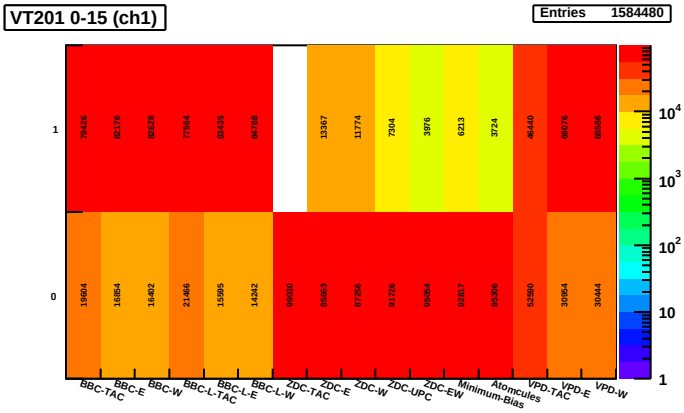

Endcap EMC L0 Input - Patch Sum
Entries 8912700
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

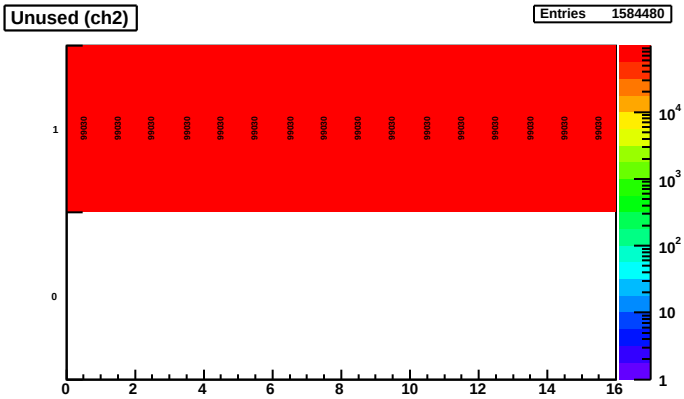

Barrel Jet Patches
Entries 1782540
JP ADC
JP ID

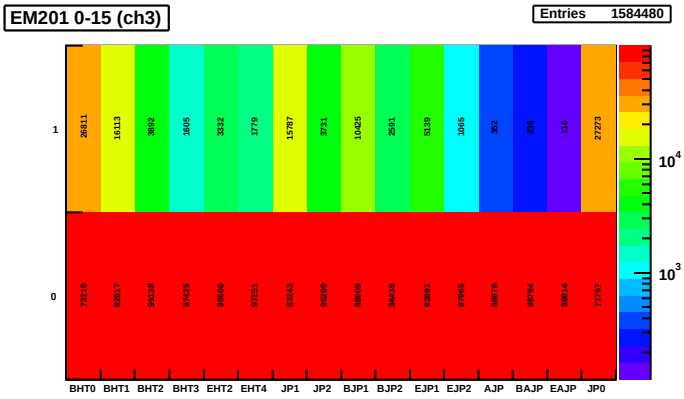

Endcap Jet Patches
Entries 594180
JP ADC
JP ID

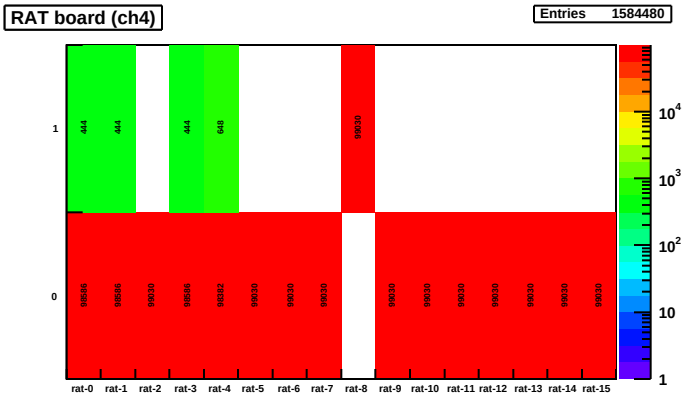

Hybrid Jet Patches
Entries 198060
JP ADC
JP ID

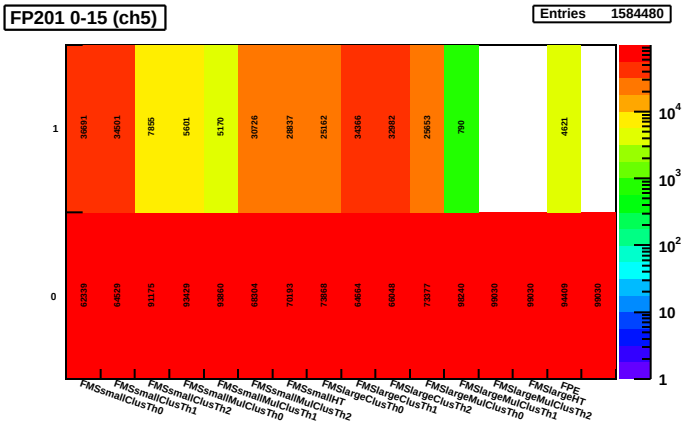

TF201 0-15 (ch0)
Entries 1584480
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

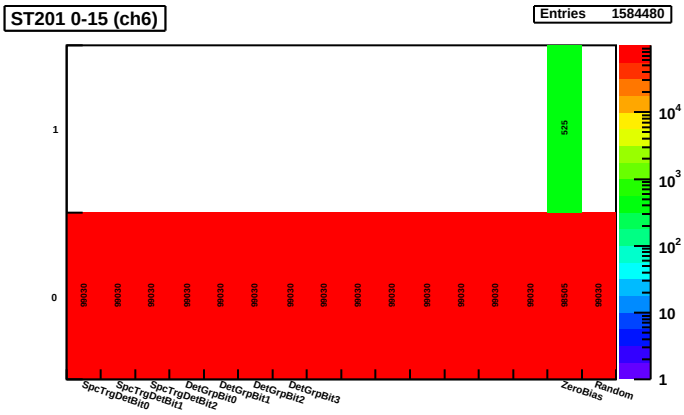

VT201 0-15 (ch1)
Entries 1584480
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

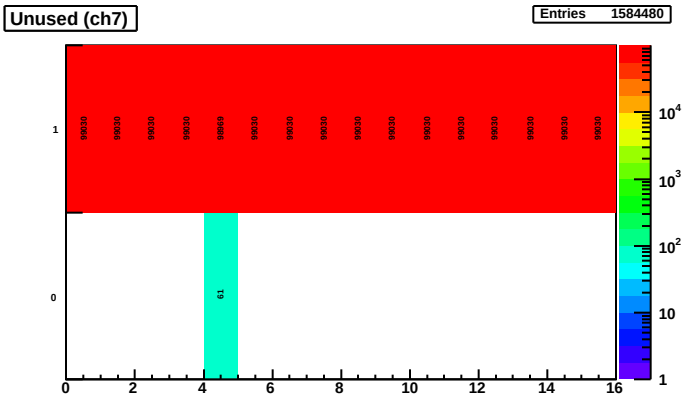

Unused (ch2)
Entries 1584480

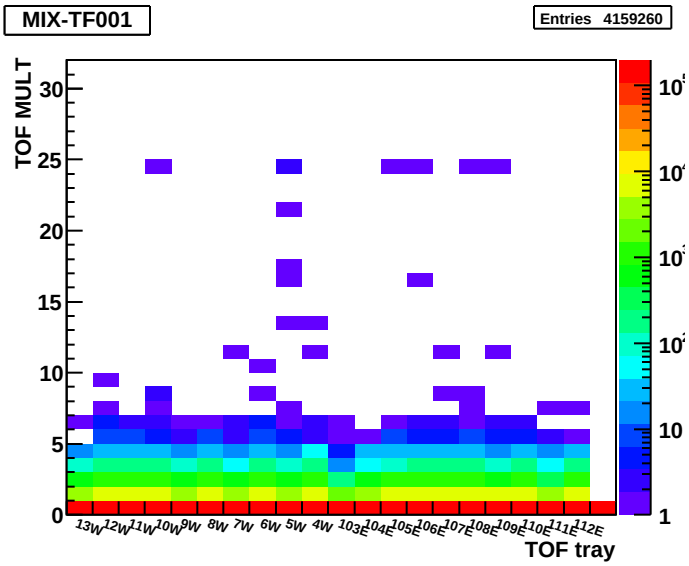

EM201 0-15 (ch3)
Entries 1584480
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

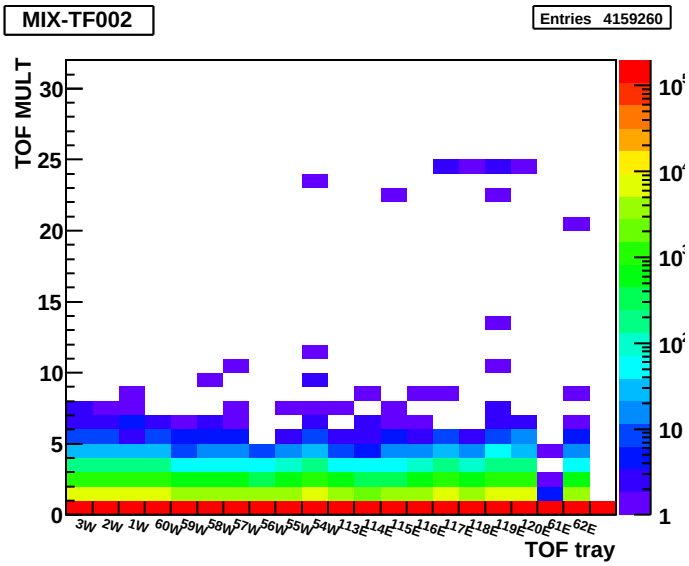

RAT board (ch4)
Entries 1584480
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

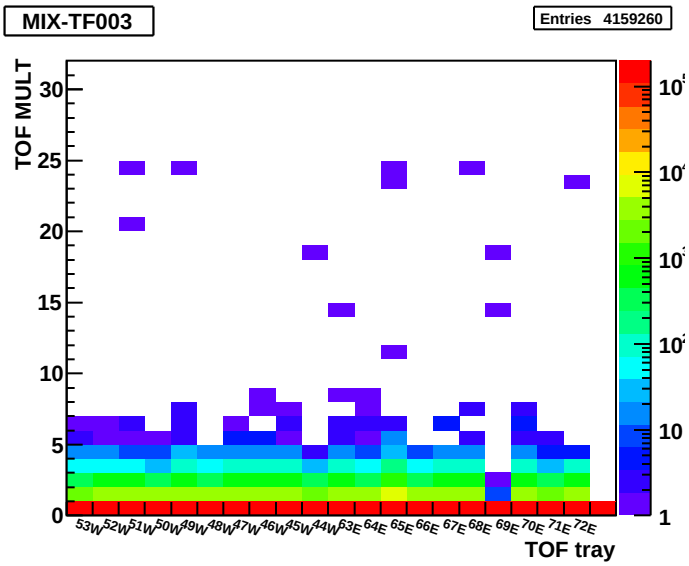

FP201 0-15 (ch5)
Entries 1584480
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

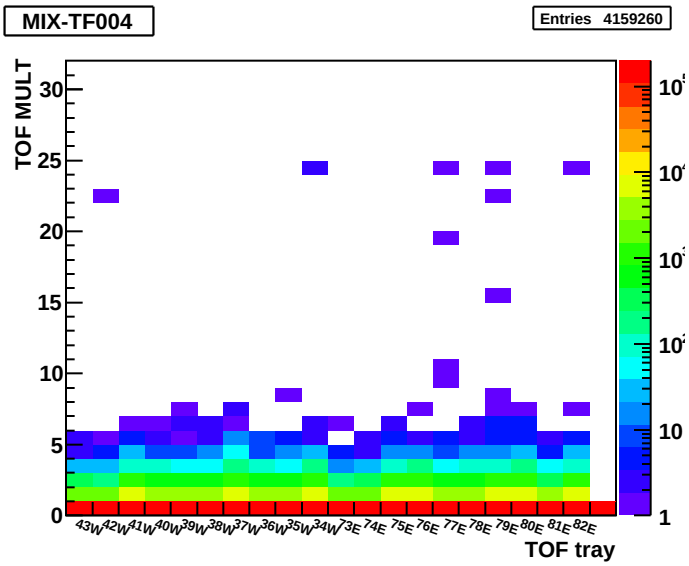

ST201 0-15 (ch6)
Entries 1584480
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

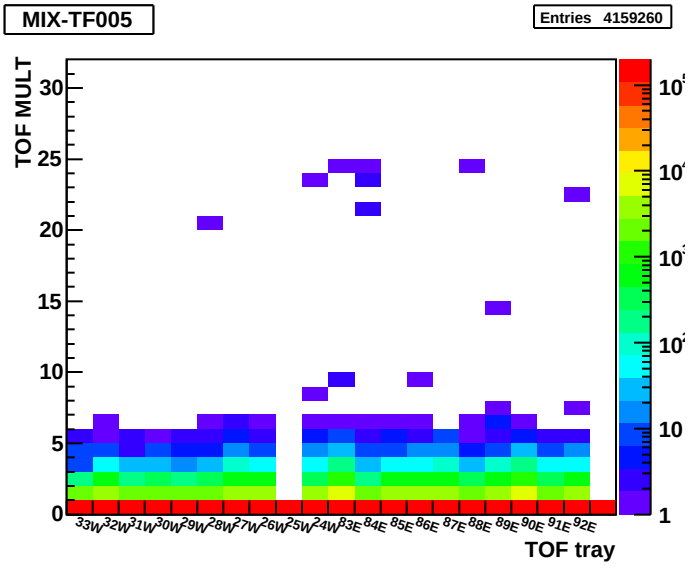

Unused (ch7)
Entries 1584480

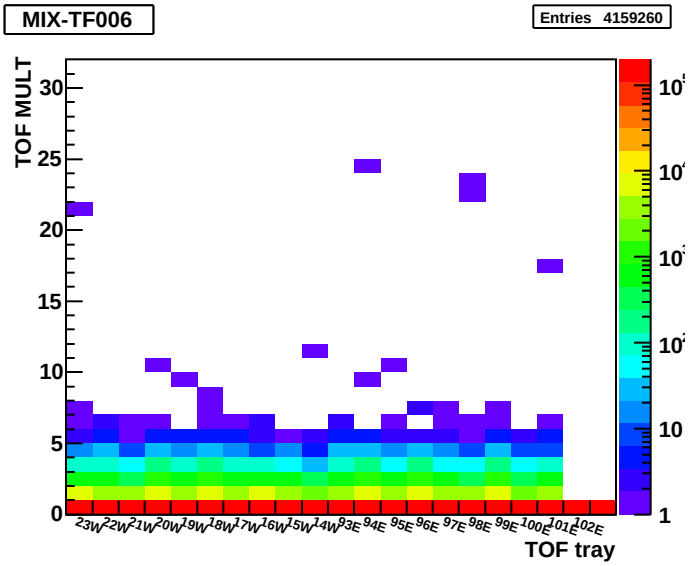

MIX-TF001
Entries 4159260
TOF MULT
TOF tray

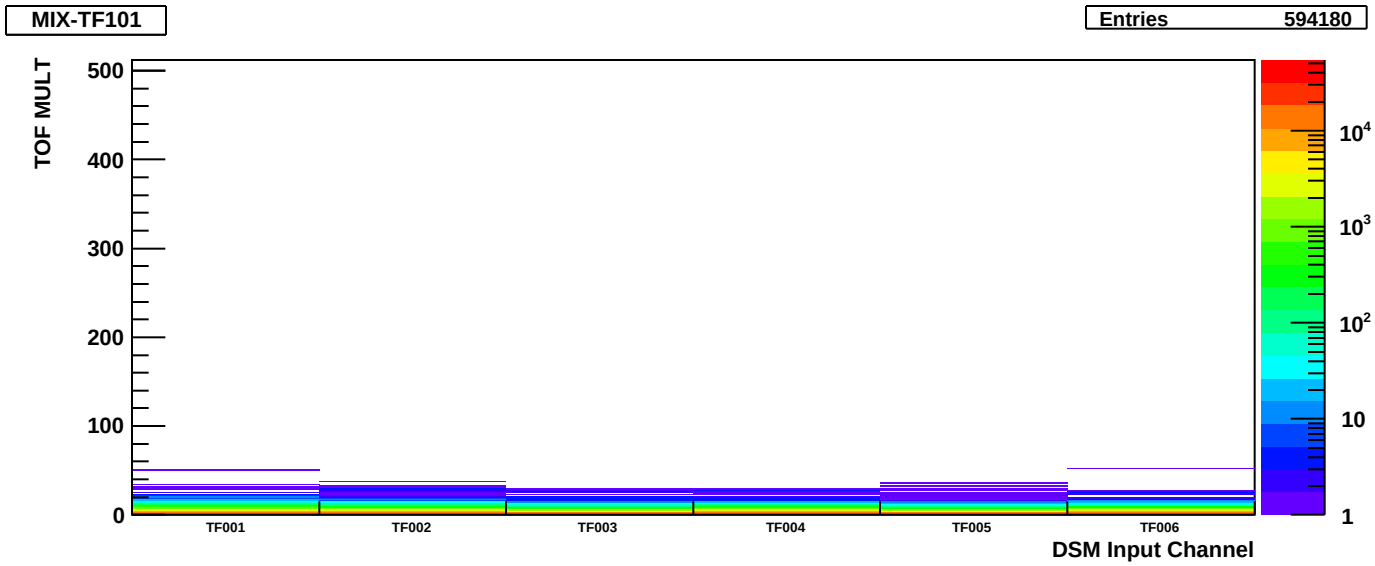

MIX-TF002
Entries 4159260
TOF MULT
TOF tray

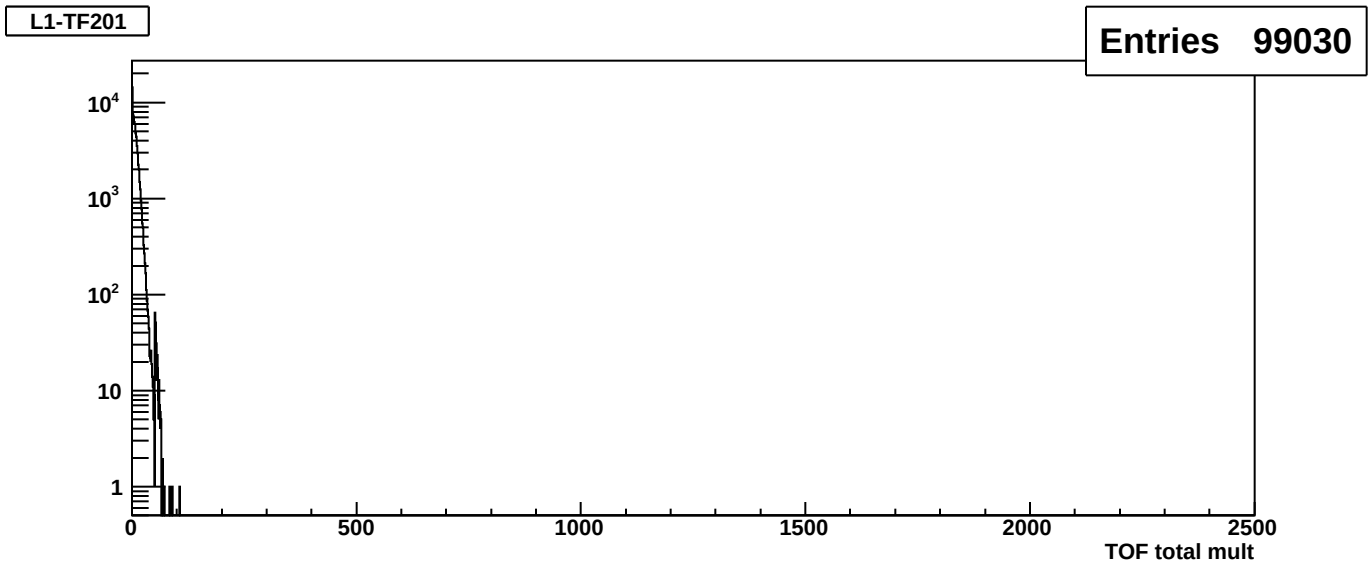

MIX-TF003
Entries 4159260
TOF MULT
TOF tray

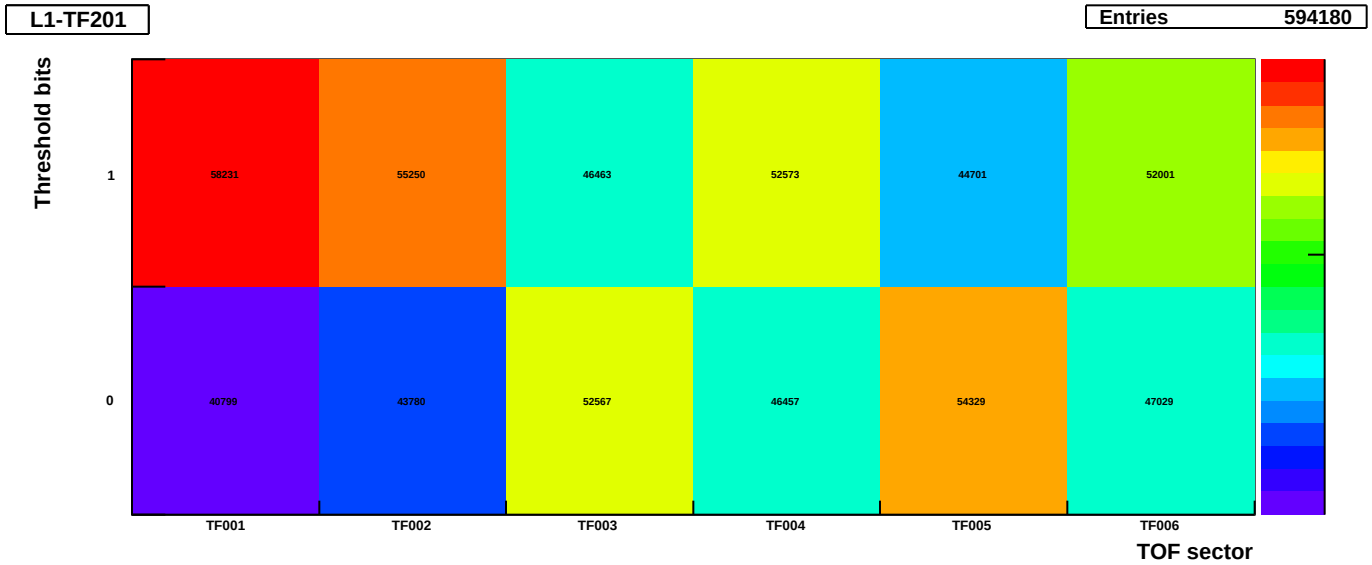

MIX-TF004
Entries 4159260
TOF MULT
TOF tray

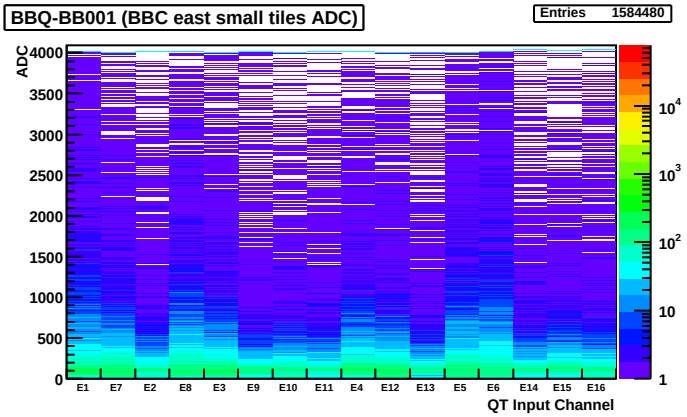

MIX-TF005
Entries 4159260
TOF MULT
TOF tray

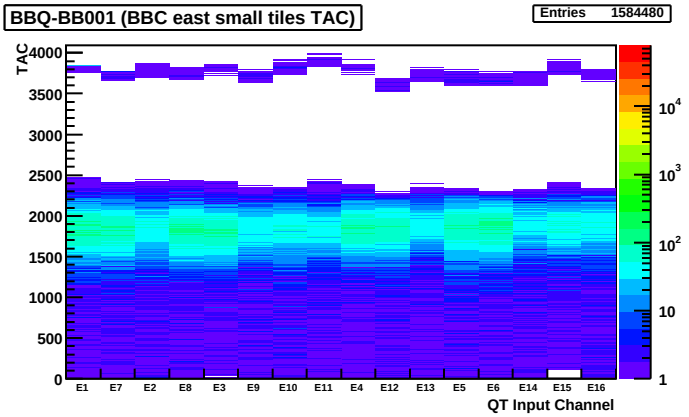

MIX-TF006
Entries 4159260
TOF MULT
TOF tray

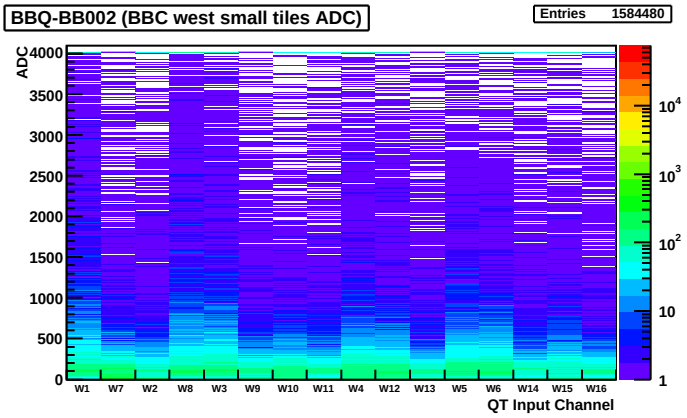

MIX-TF101
Entries 594180
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

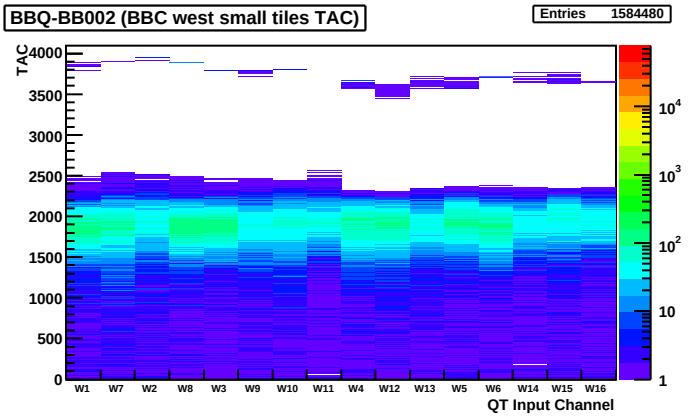

L1-TF201
Entries 99030
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

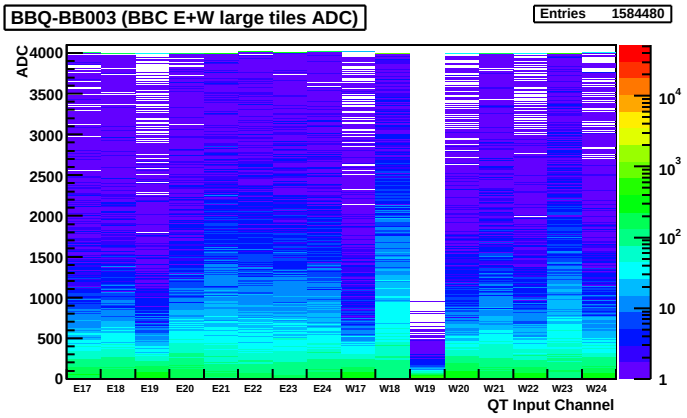

L1-TF201
Entries 594180
Threshold bits
1
0
58231
55250
46463
52573
44701
52001
40799
43780
52567
46457
54329
47029
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

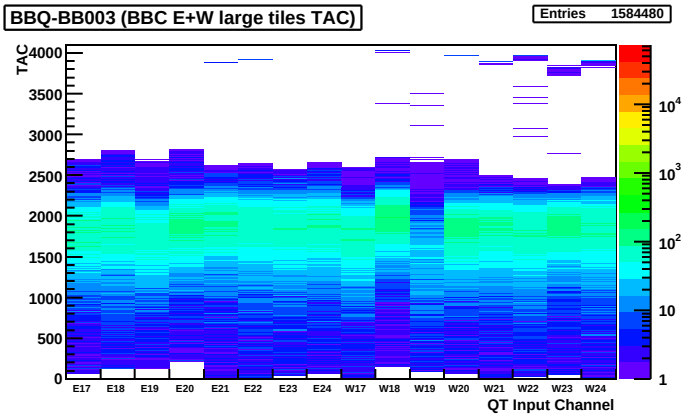

BBQ-BB001 (BBC east small tiles ADC)
Entries 1584480
ADC
QT Input Channel

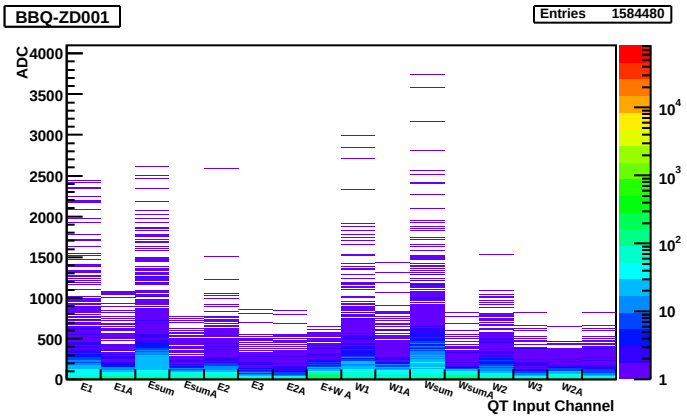

BBQ-BB001 (BBC east small tiles TAC)
Entries 1584480
TAC
QT Input Channel

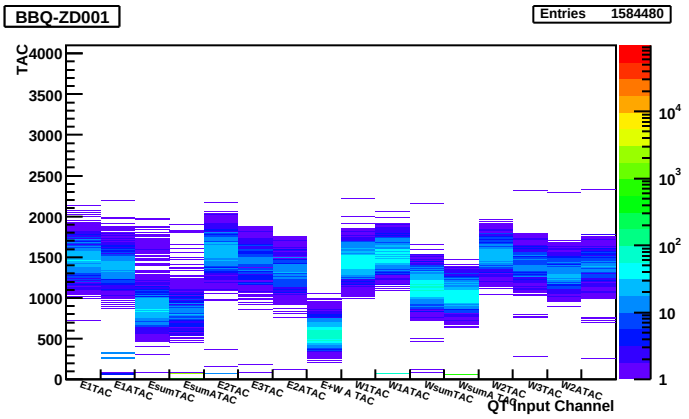

BBQ-BB002 (BBC west small tiles ADC)
Entries 1584480
ADC
QT Input Channel

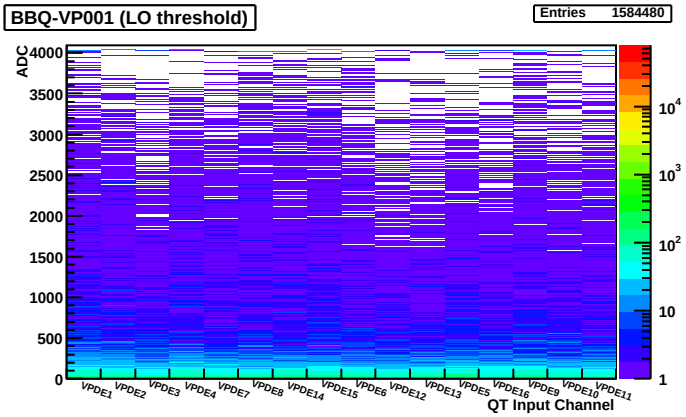

BBQ-BB002 (BBC west small tiles TAC)
Entries 1584480
TAC
QT Input Channel

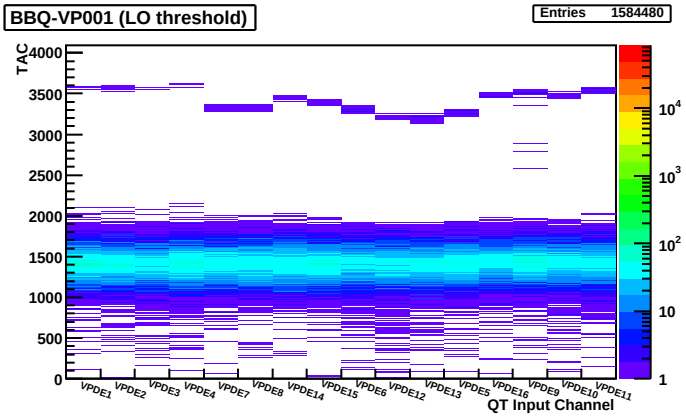

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1584480
ADC
QT Input Channel

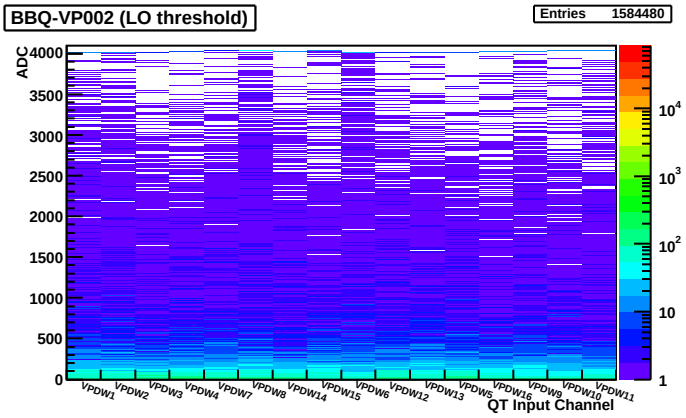

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1584480
TAC
QT Input Channel

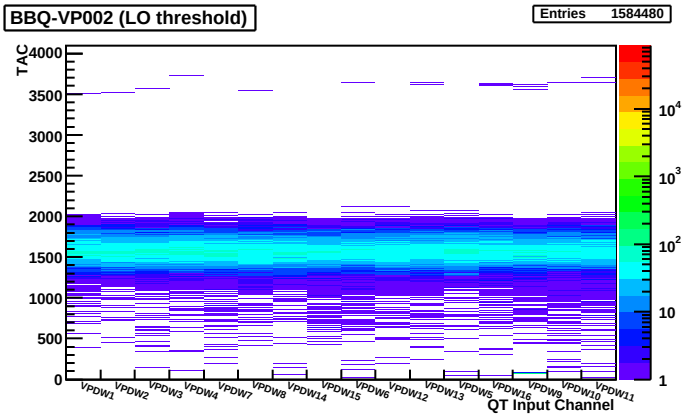

BBQ-ZD001
Entries 1584480
ADC
QT Input Channel

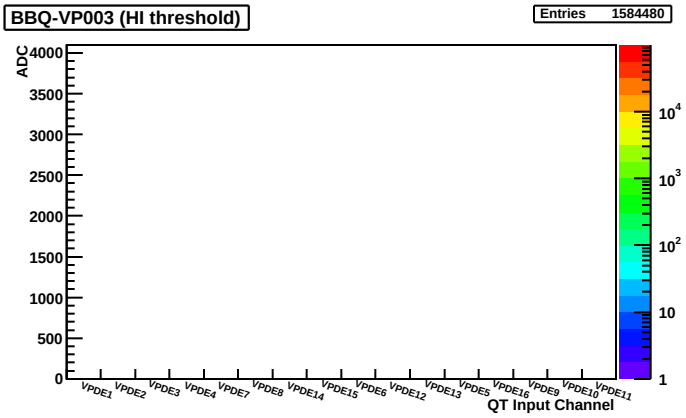

BBQ-ZD001
Entries 1584480
TAC
QT Input Channel

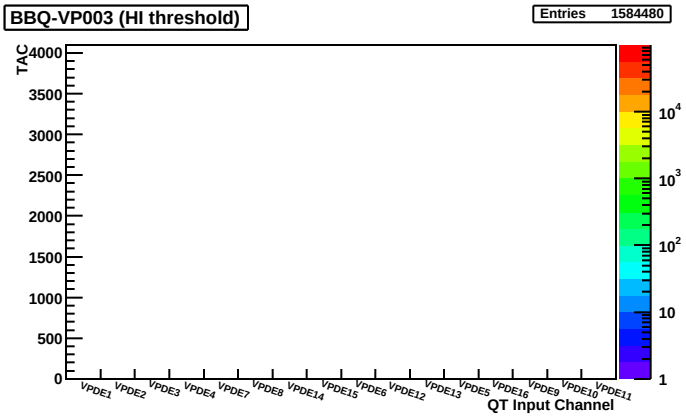

BBQ-VP001 (LO threshold)
Entries 1584480
ADC
QT Input Channel

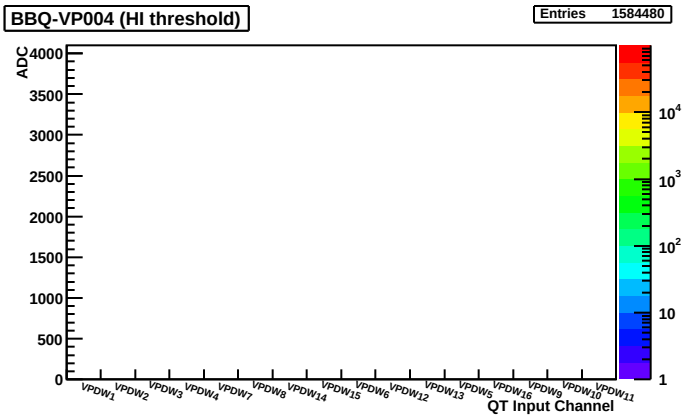

BBQ-VP001 (LO threshold)
Entries 1584480
TAC
QT Input Channel

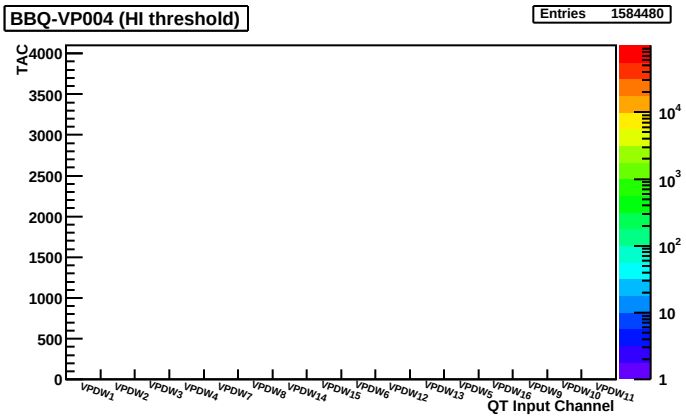

BBQ-VP002 (LO threshold)
Entries 1584480
ADC
QT Input Channel

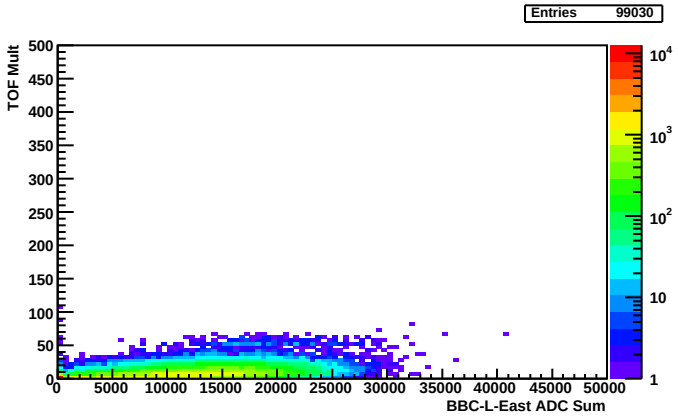

BBQ-VP002 (LO threshold)
Entries 1584480
TAC
QT Input Channel

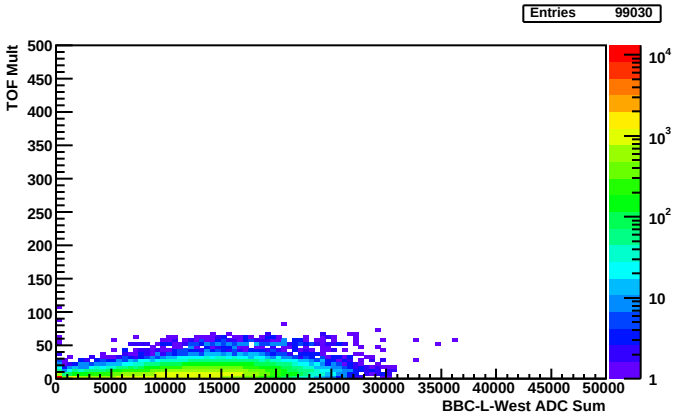

BBQ-VP003 (HI threshold)
Entries 1584480
ADC
QT Input Channel

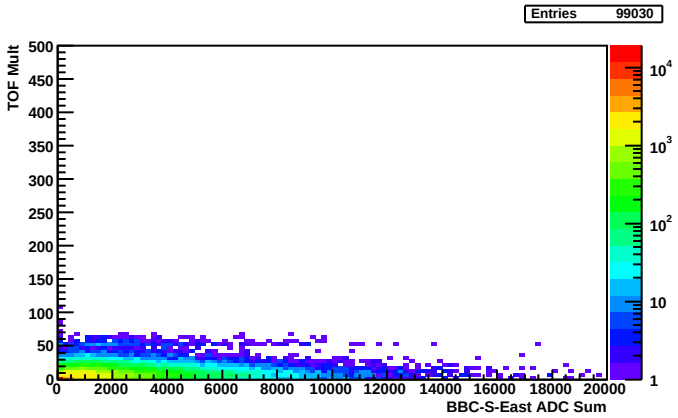

BBQ-VP003 (HI threshold)
Entries 1584480
TAC
QT Input Channel

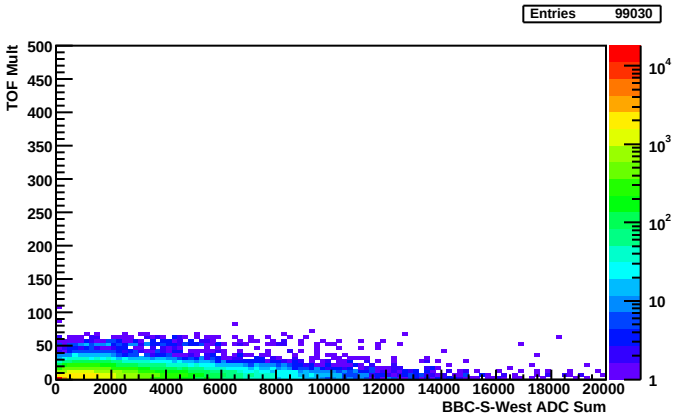

BBQ-VP004 (HI threshold)
Entries 1584480
ADC
QT Input Channel

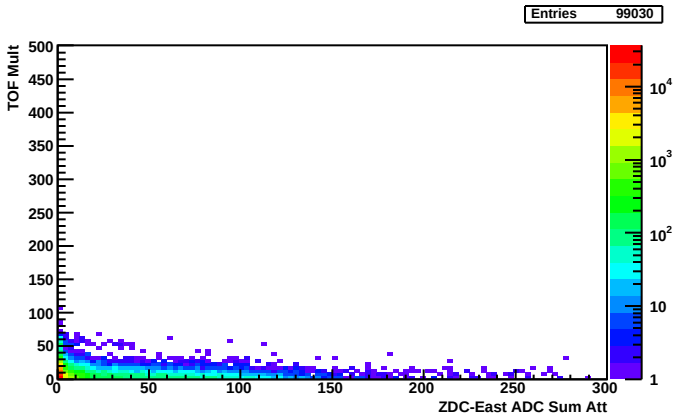

BBQ-VP004 (HI threshold)
Entries 1584480
TAC
QT Input Channel

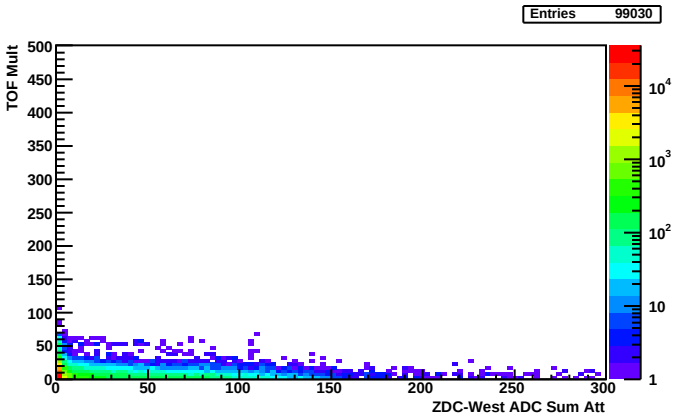

Entries 99030
TOF Mult
BBC-L-East ADC Sum

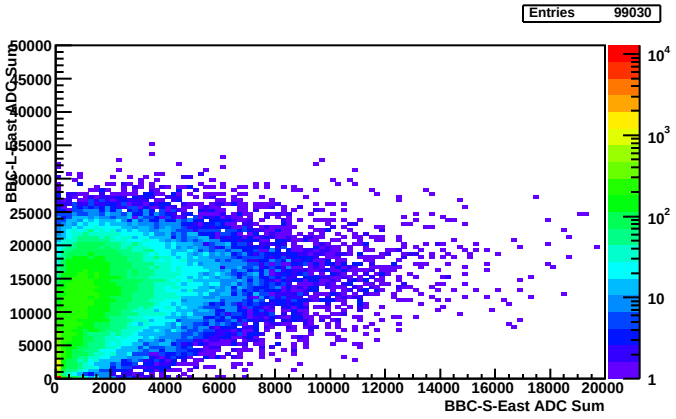

Entries 99030
TOF Mult
BBC-L-West ADC Sum

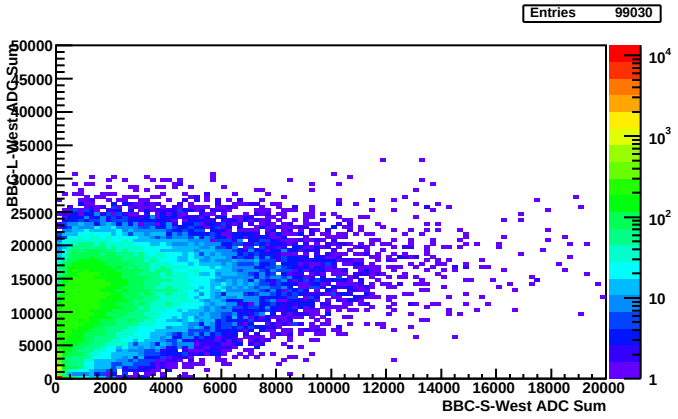

Entries 99030
TOF Mult
BBC-S-East ADC Sum

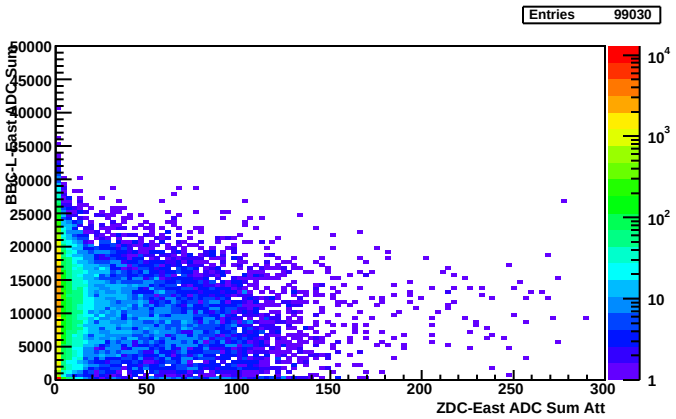

Entries 99030
TOF Mult
BBC-S-West ADC Sum

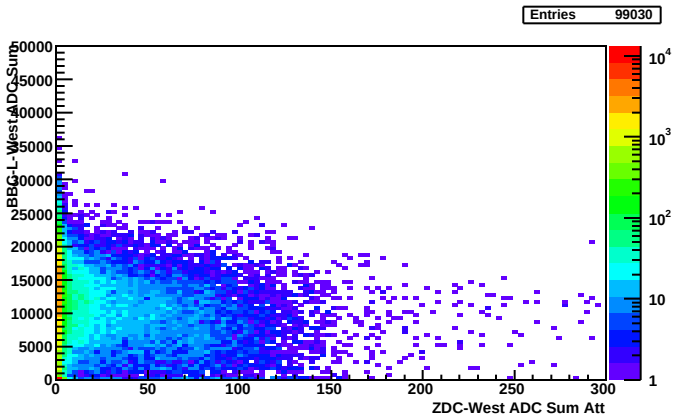

Entries 99030
TOF Mult
ZDC-East ADC Sum Att

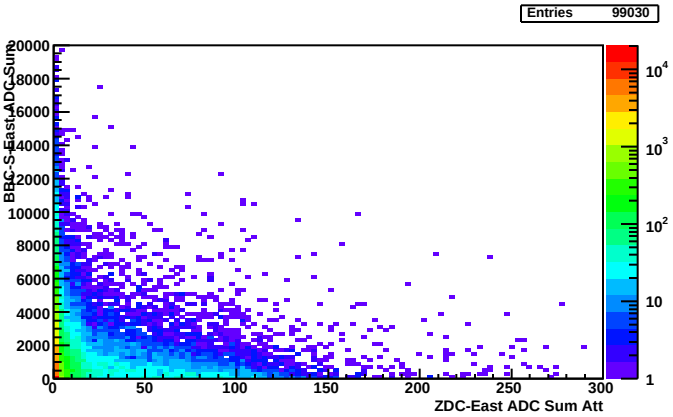

Entries 99030
TOF Mult
ZDC-West ADC Sum Att

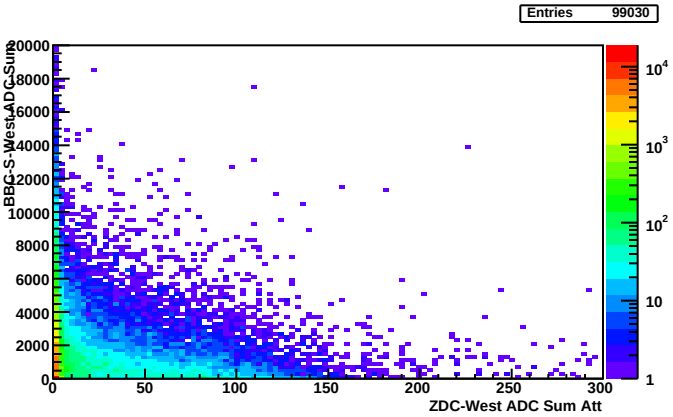

Entries 99030
BBC-L-East ADC Sum
BBC-S-East ADC Sum

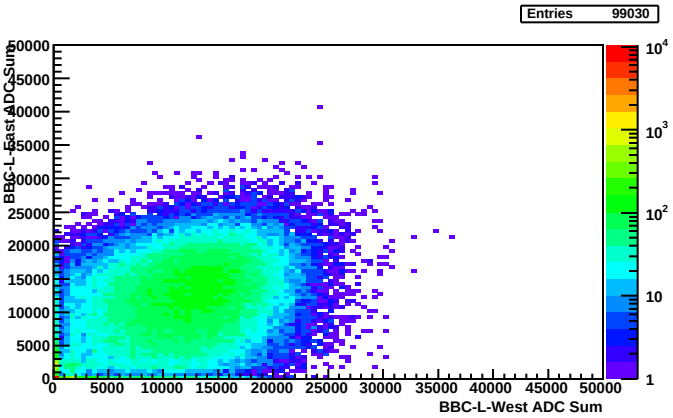

Entries 99030
BBC-L-West ADC Sum
BBC-S-West ADC Sum

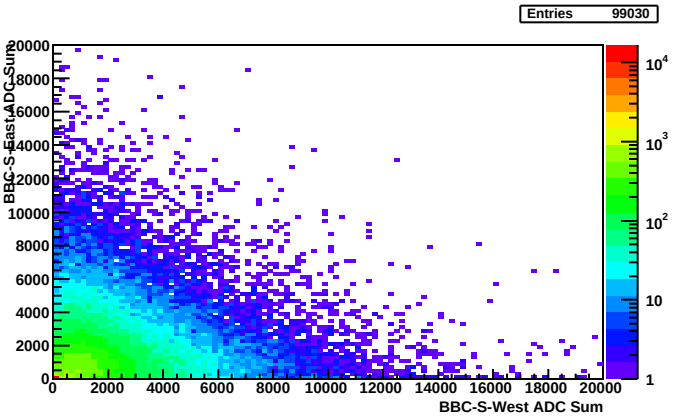

Entries 99030
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

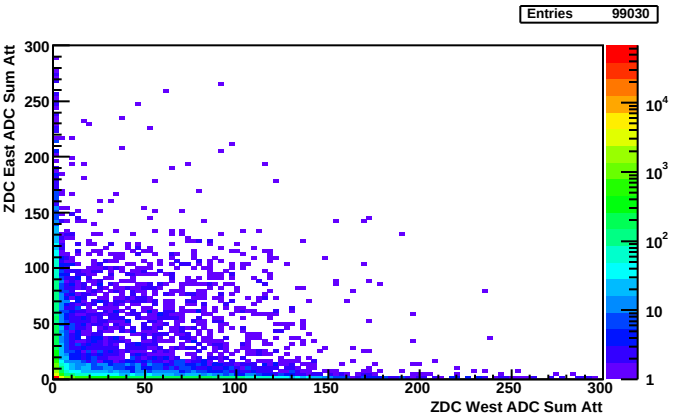

Entries 99030
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

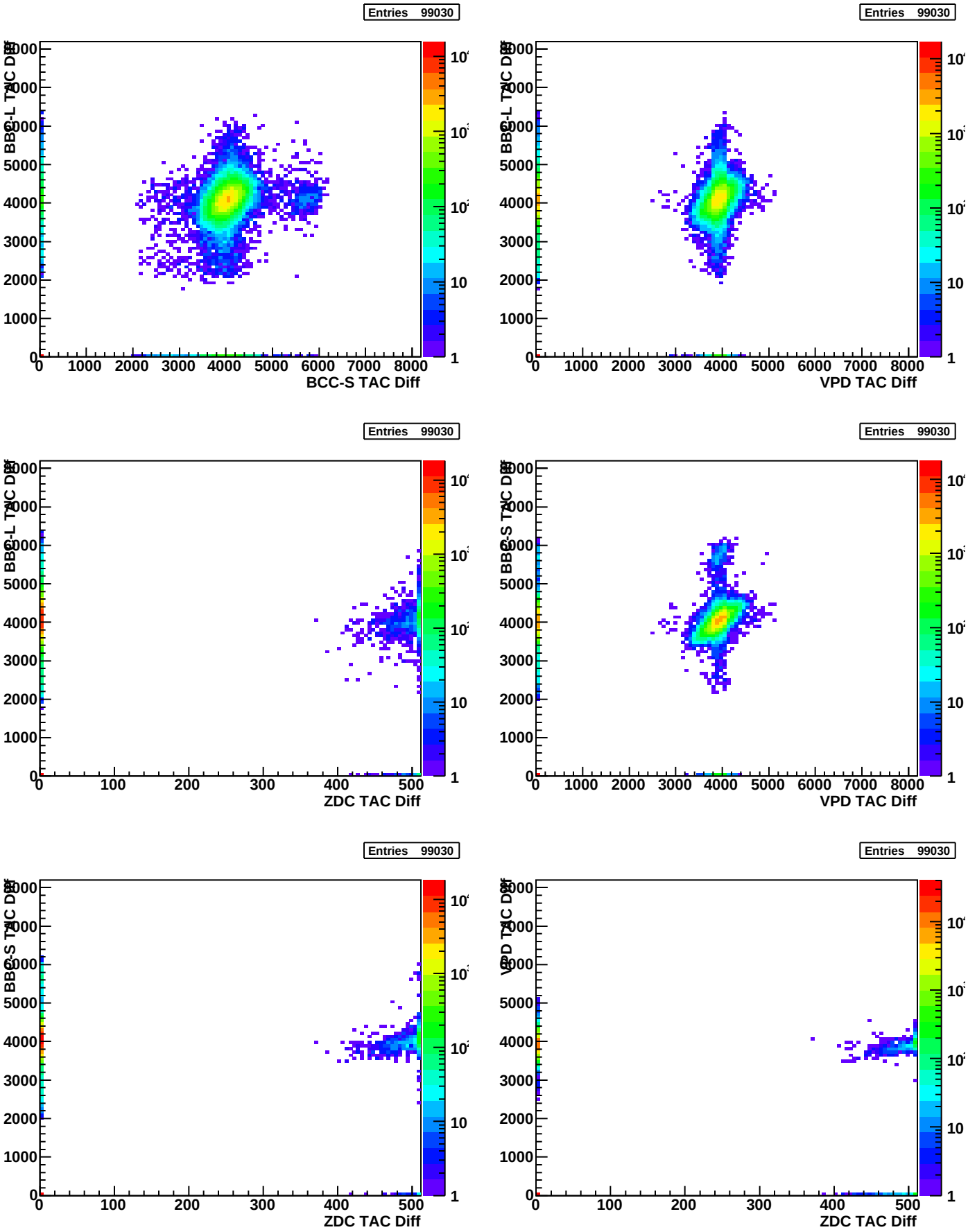

Entries 99030
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

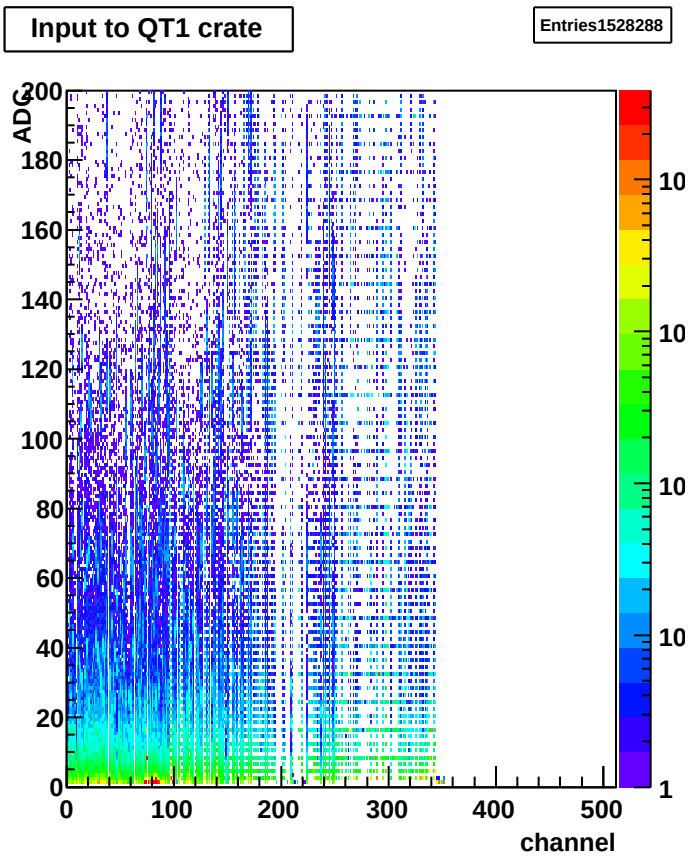

Entries 99030
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

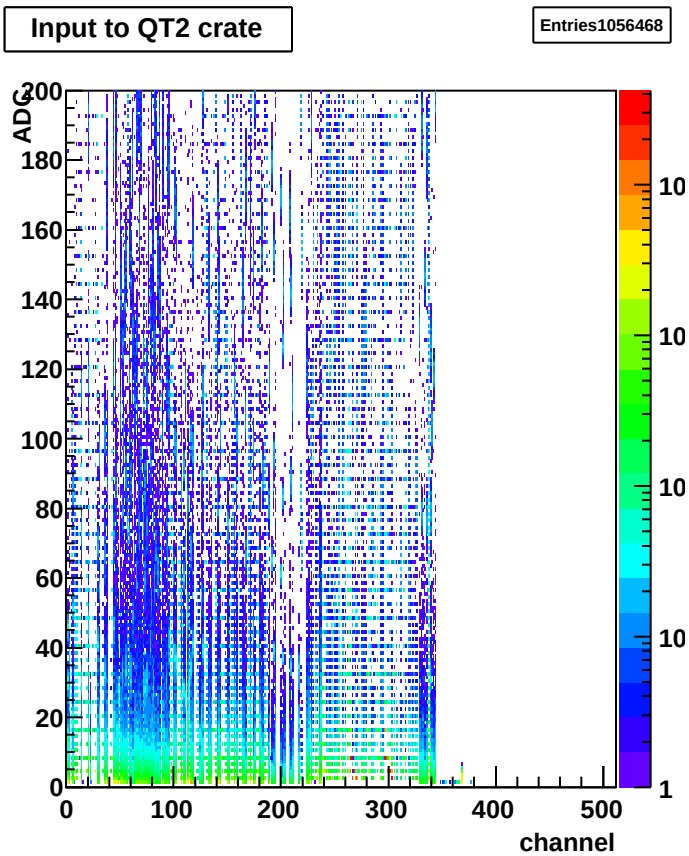

Entries 99030
BBC-L-East ADC Sum
BBC-L-West ADC Sum

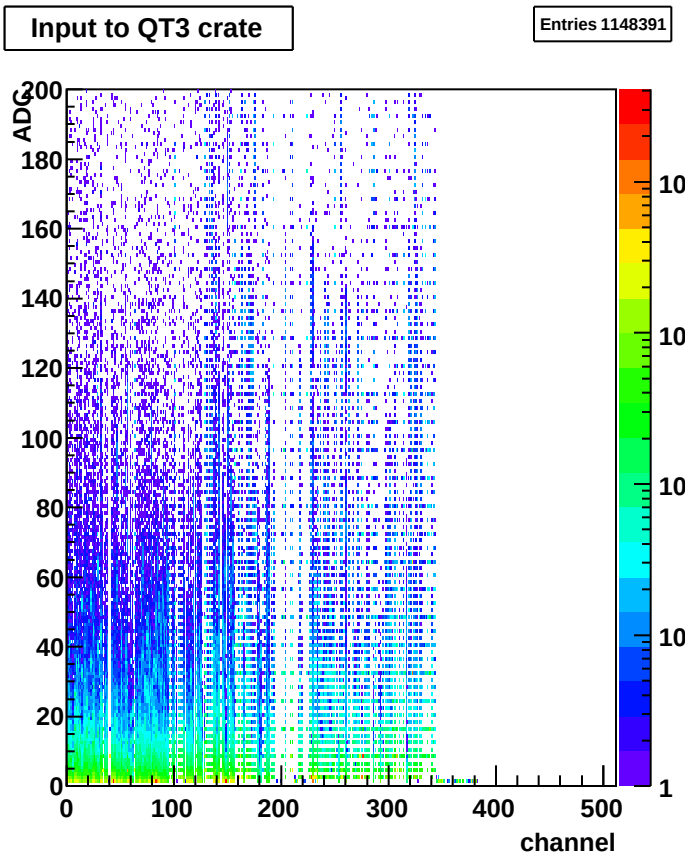

Entries 99030
BBC-S-East ADC Sum
BBC-S-West ADC Sum

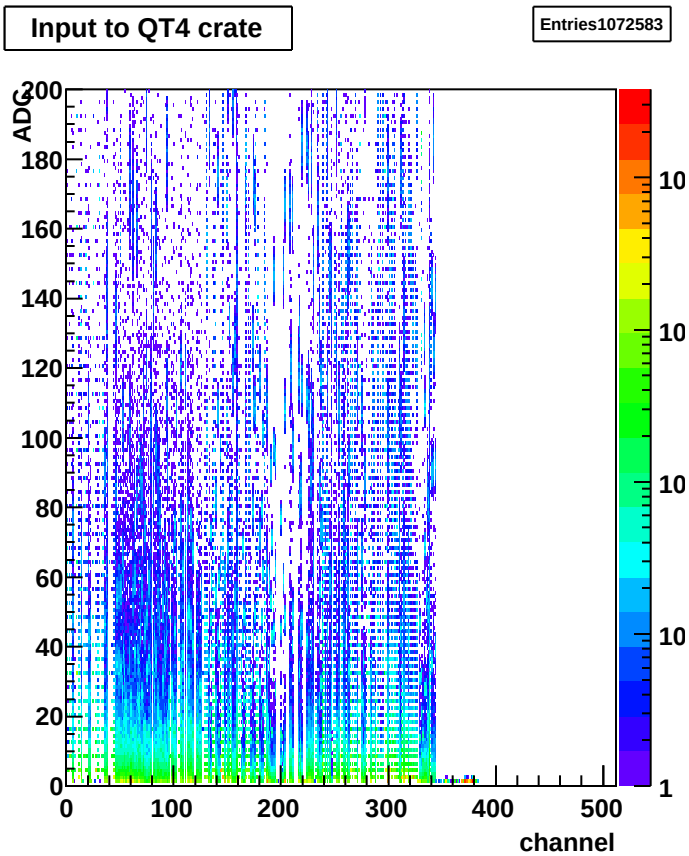

Entries 99030
ZDC East ADC Sum Att
ZDC West ADC Sum Att

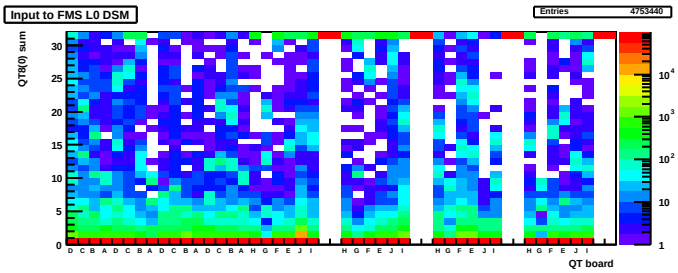

Entries 99030
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff

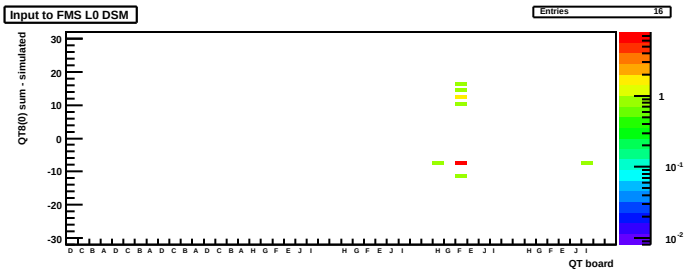

Input to QT1 crate
Entries1528288
ADC
channel

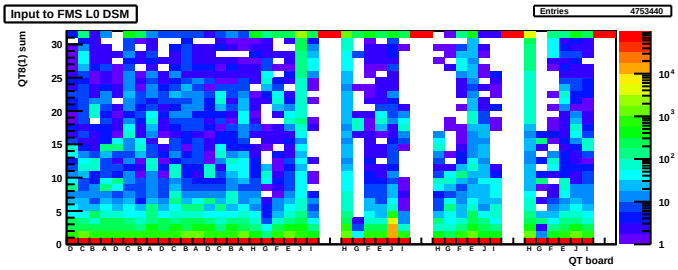

Input to QT2 crate
Entries1056468
ADC
channel

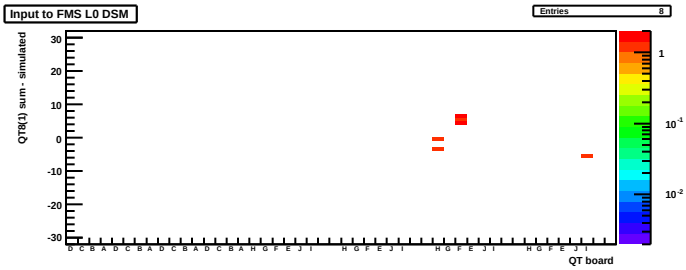

Input to QT3 crate
Entries 1148391
ADC
channel

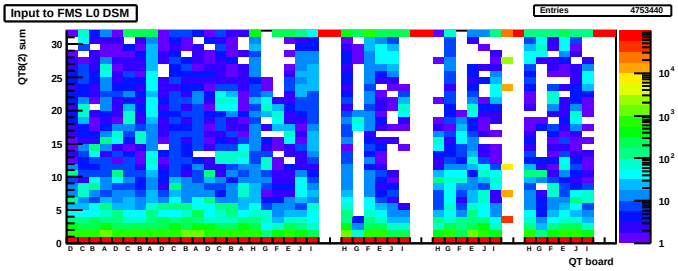

Input to QT4 crate
Entries1072583
ADC
channel

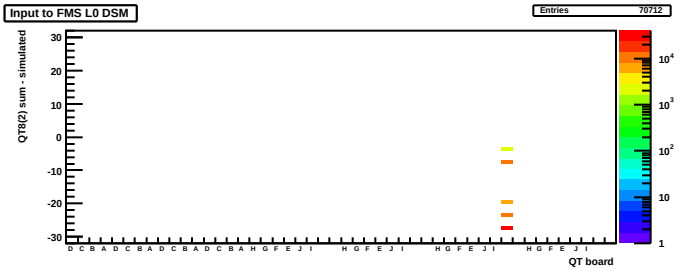

Input to FMS L0 DSM
Entries 4753440
QT8(0) sum
QT board

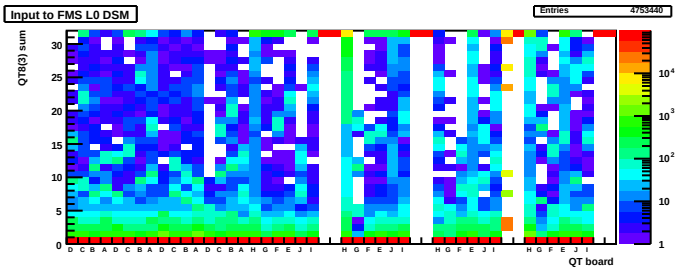

Input to FMS L0 DSM
Entries 16
QT8(0) sum - simulated
QT board

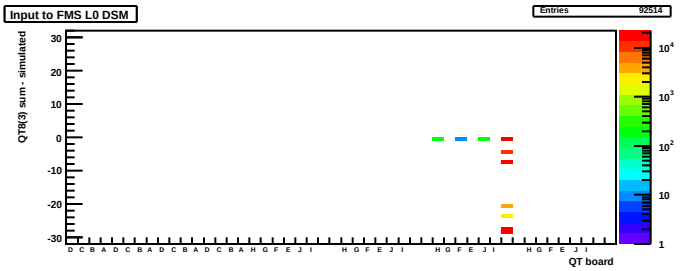

Input to FMS L0 DSM
Entries 4753440
QT8(1) sum
QT board

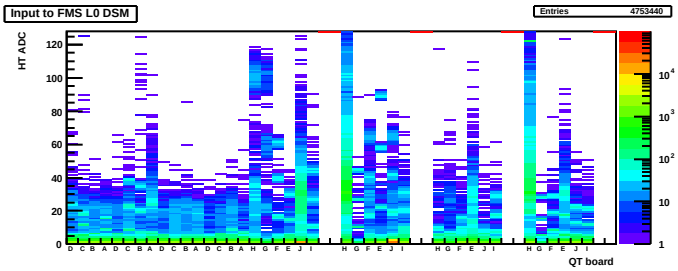

Input to FMS L0 DSM
Entries 8
QT8(1) sum - simulated
QT board

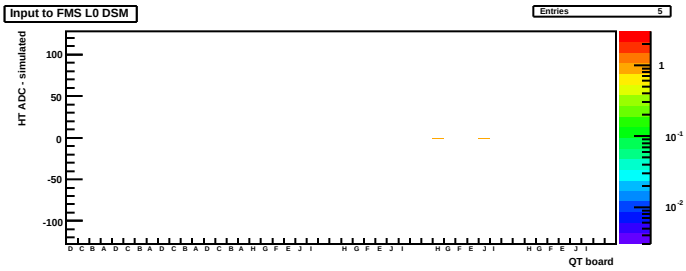

Input to FMS L0 DSM
Entries 4753440
QT8(2) sum
QT board

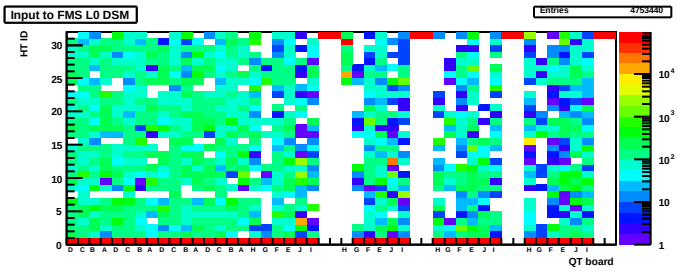

Input to FMS L0 DSM
Entries 70712
QT8(2) sum - simulated
QT board

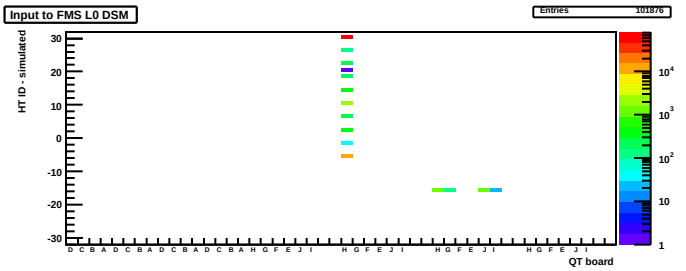

Input to FMS L0 DSM
Entries 4753440
QT8(3) sum
QT board

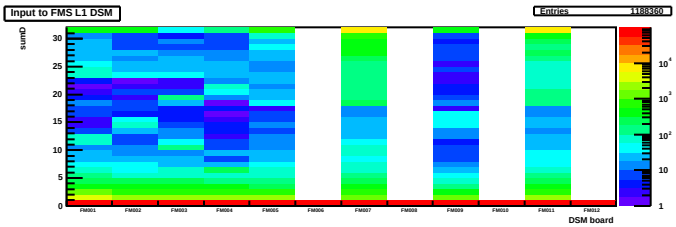

Input to FMS L0 DSM
Entries 92514
QT8(3) sum - simulated
QT board

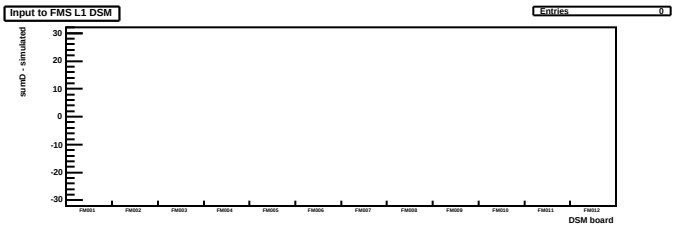

Input to FMS L0 DSM
Entries 4753440
HT ADC
QT board

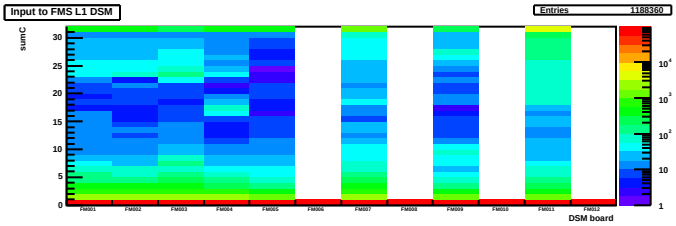

Input to FMS L0 DSM
Entries 5
HT ADC - simulated
QT board

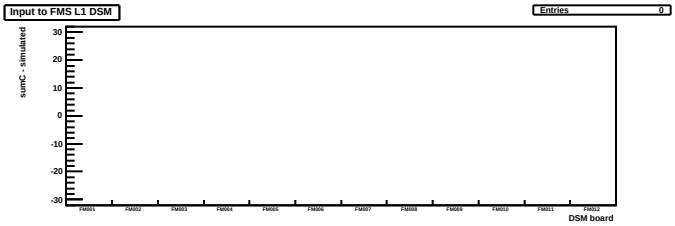

Input to FMS L0 DSM
Entries 4753440
HT ID
QT board

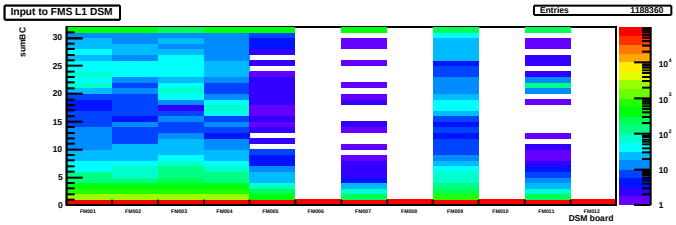

Input to FMS L0 DSM
Entries 101876
HT ID - simulated
QT board

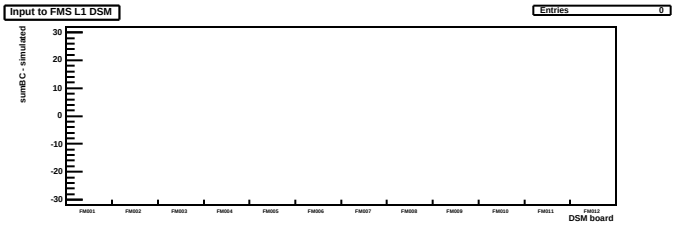

Input to FMS L1 DSM
Entries 1188360
sumD
DSM board

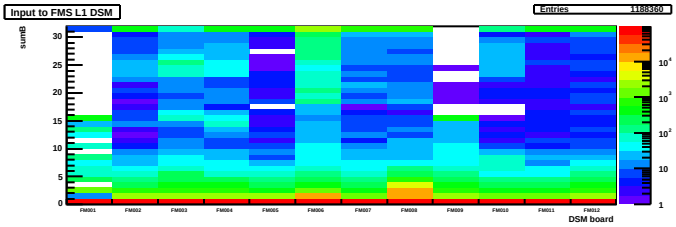

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

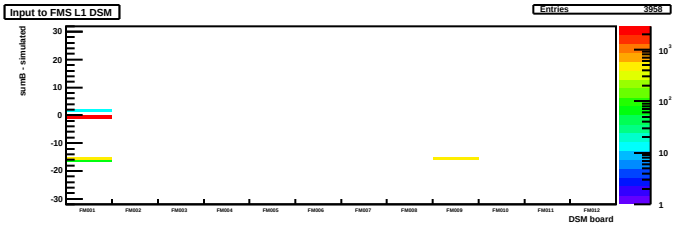

Input to FMS L1 DSM
Entries 1188360
sumC
DSM board

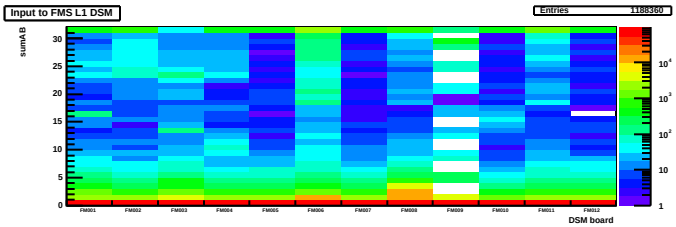

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

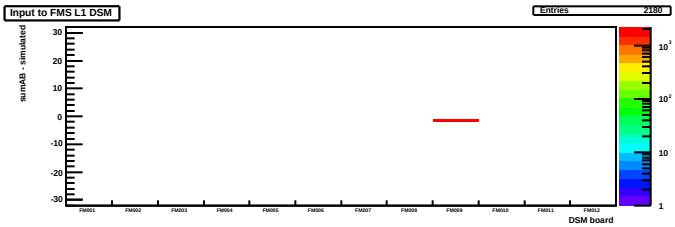

Input to FMS L1 DSM
Entries 1188360
sumBC
DSM board

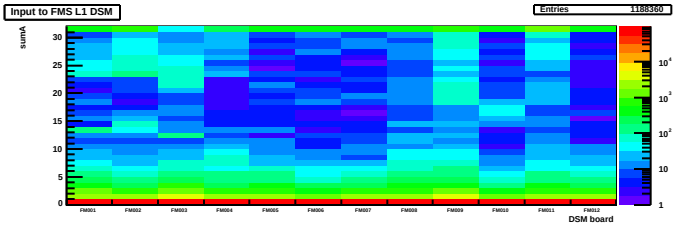

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

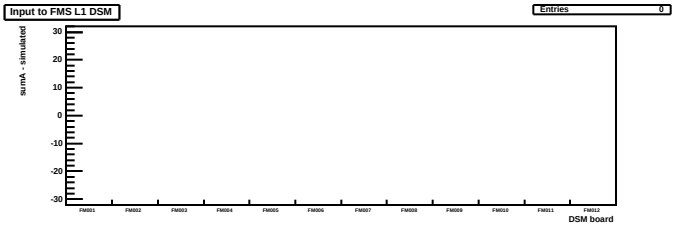

Input to FMS L1 DSM
Entries 1188360
sumB
DSM board

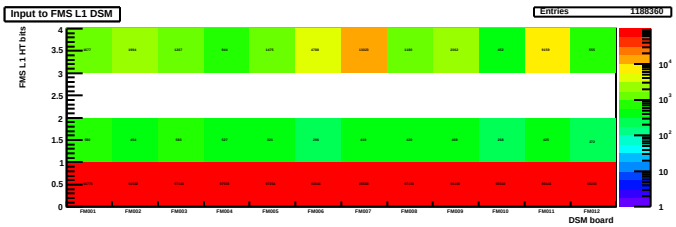

Input to FMS L1 DSM
Entries 3958
sumB - simulated
DSM board

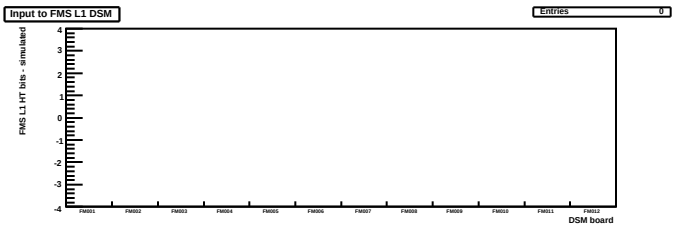

Input to FMS L1 DSM
Entries 1188360
sumAB
DSM board

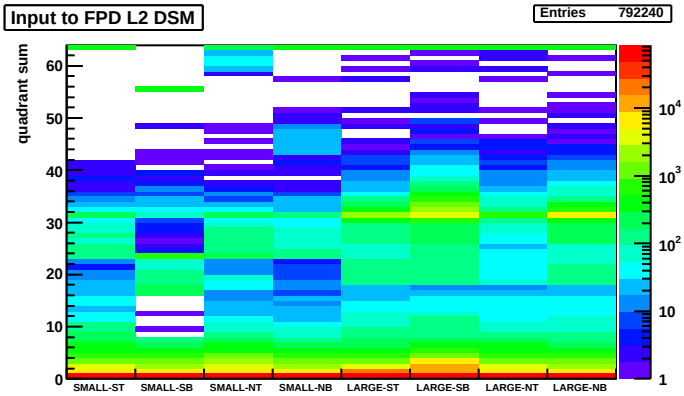

Input to FMS L1 DSM
Entries 2180
sumAB - simulated
DSM board

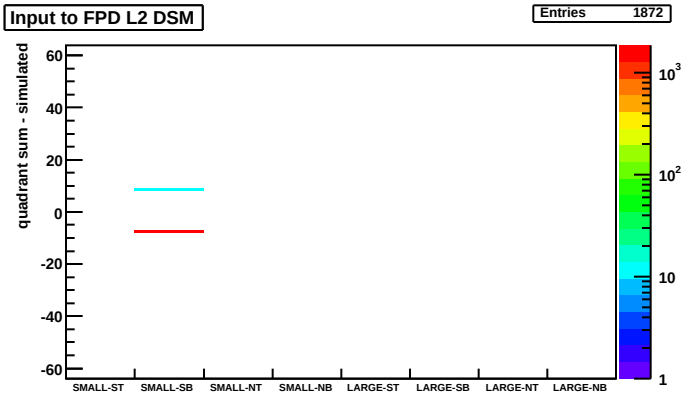

Input to FMS L1 DSM
Entries 1188360
sumA
DSM board

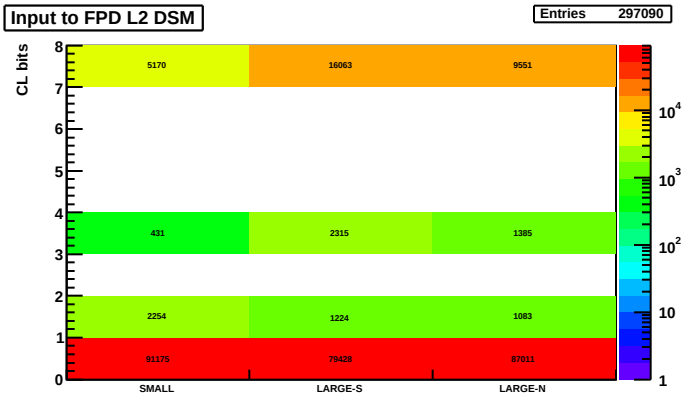

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

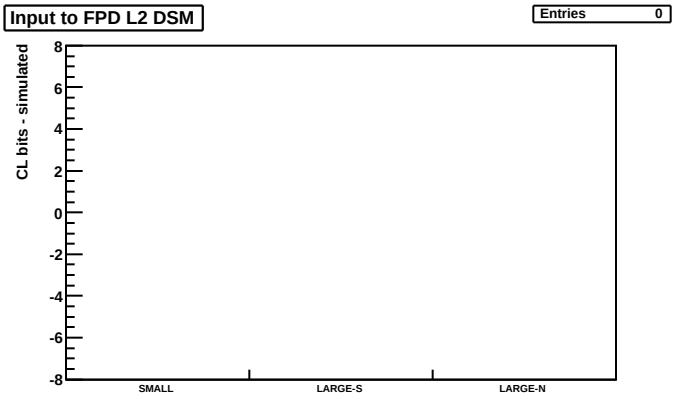

Input to FMS L1 DSM
Entries 1188360
FMS L1 HT bits
DSM board

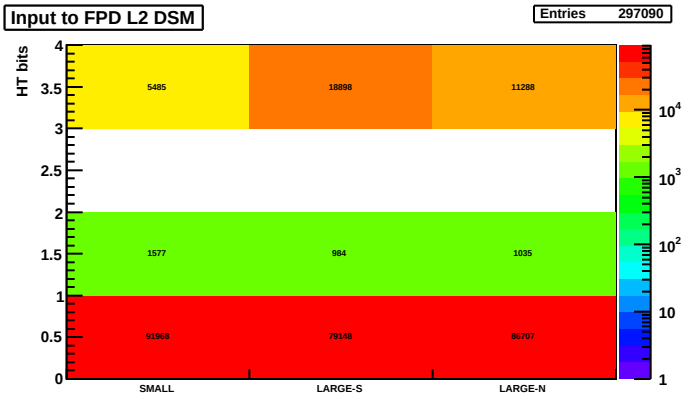

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

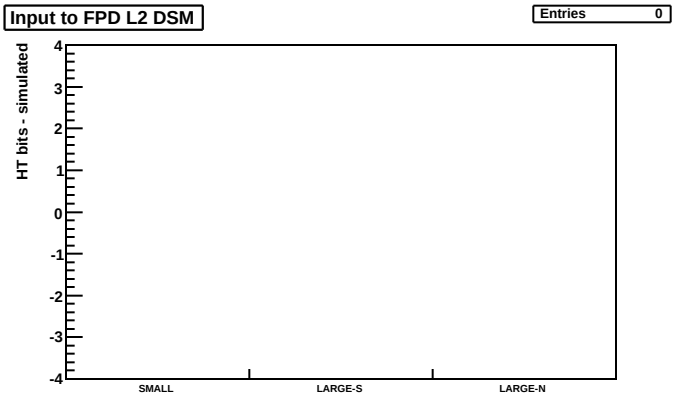

Input to FPD L2 DSM
Entries 792240
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

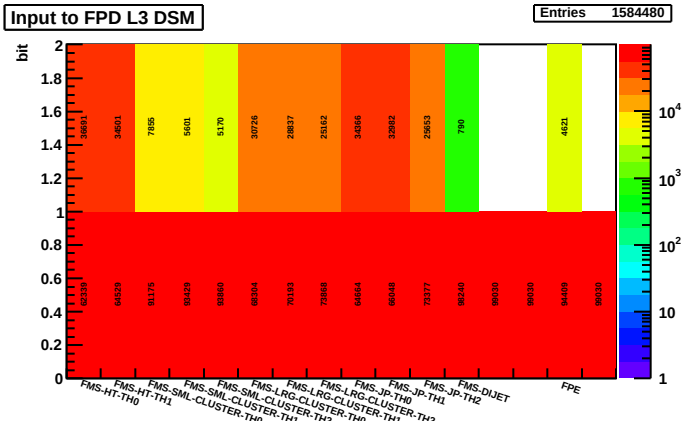

Input to FPD L2 DSM
Entries 1872
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

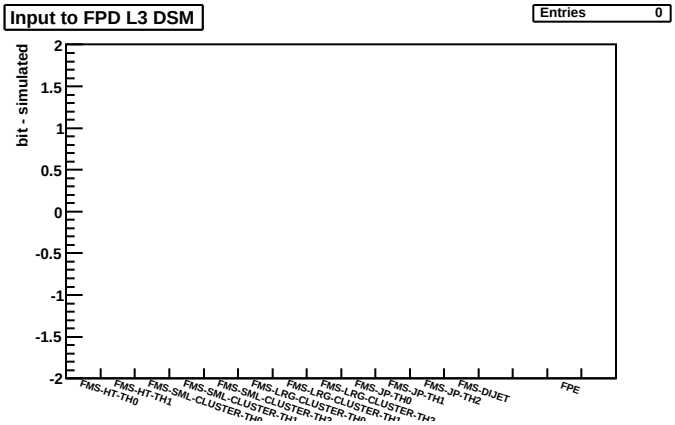

Input to FPD L2 DSM
Entries 297090
CL bits
5170
16063
9551
431
2315
1385
2254
1224
1083
91175
79428
87011
SMALL
LARGE-S
LARGE-N

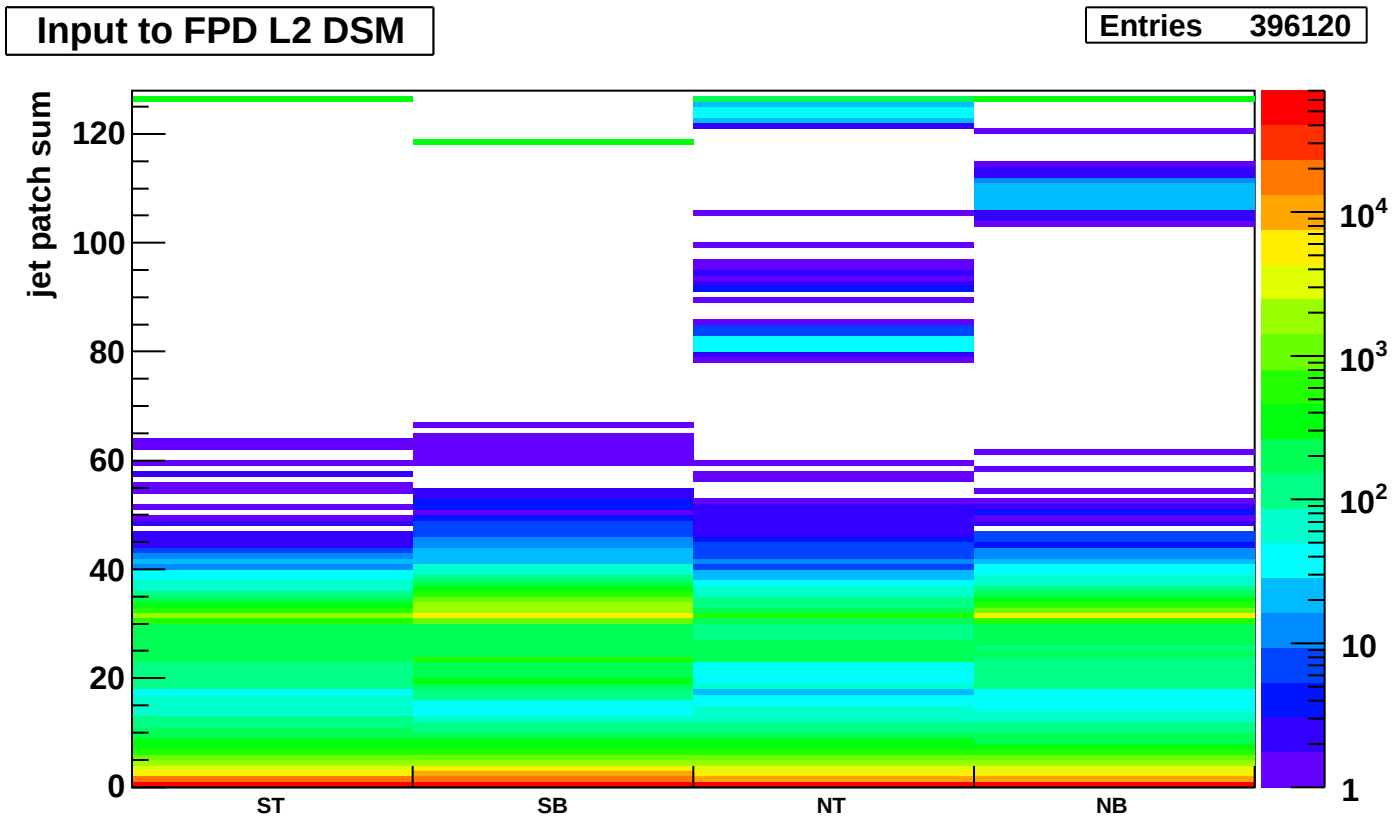

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

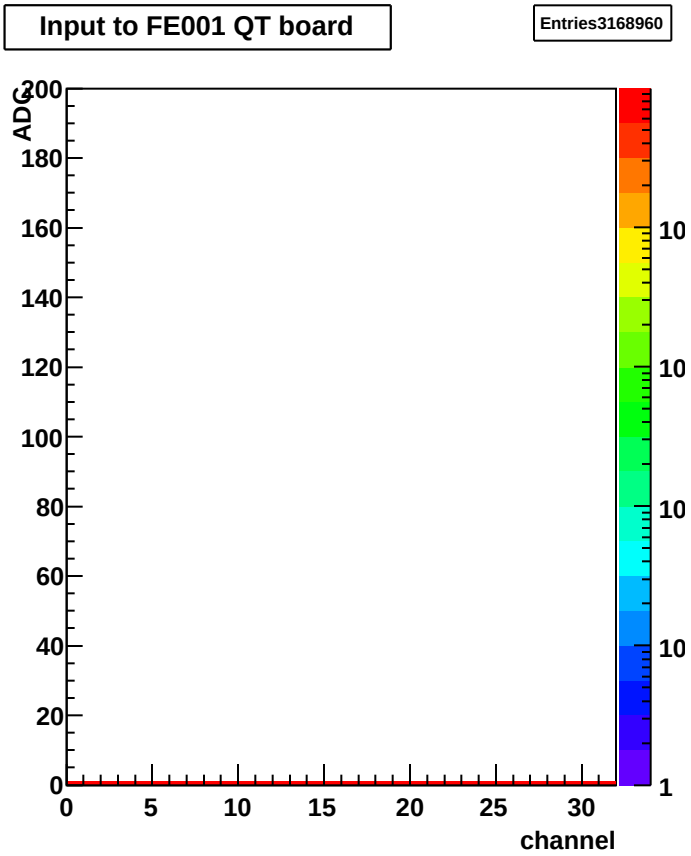

Input to FPD L2 DSM
Entries 297090
HT bits
5485
18898
11288
1577
984
1035
91968
79148
86707
SMALL
LARGE-S
LARGE-N

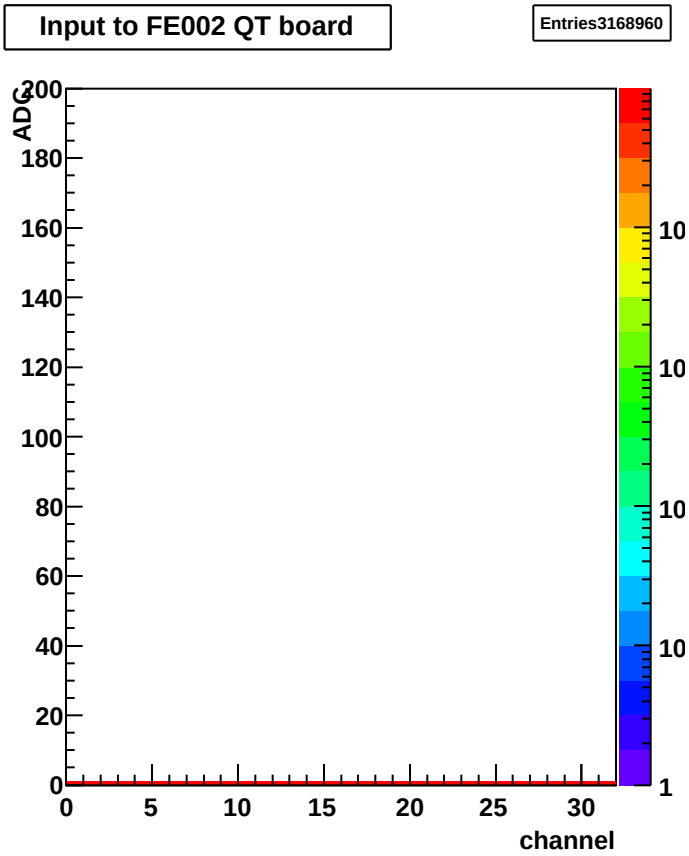

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

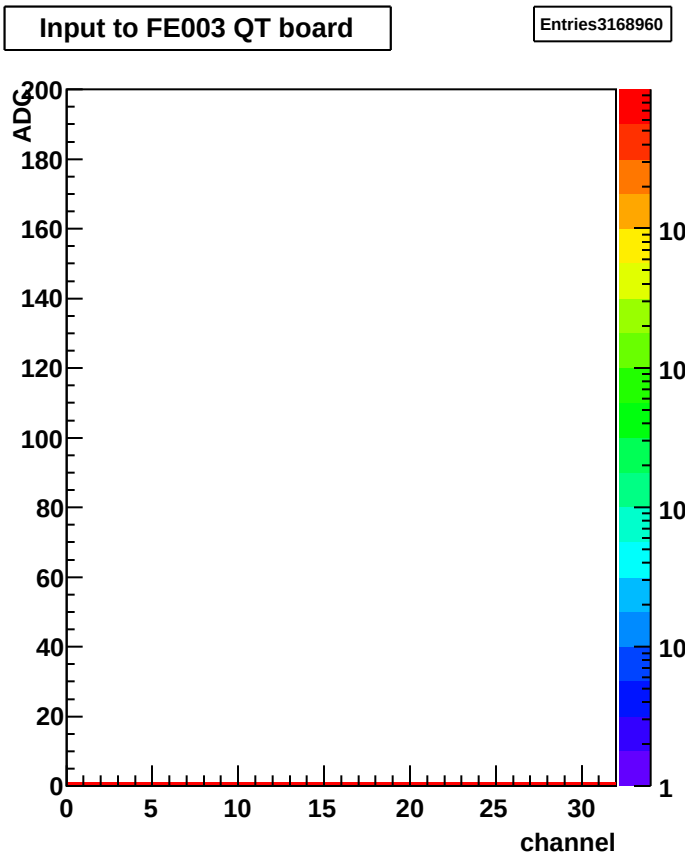

Input to FPD L3 DSM
Entries 1584480
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

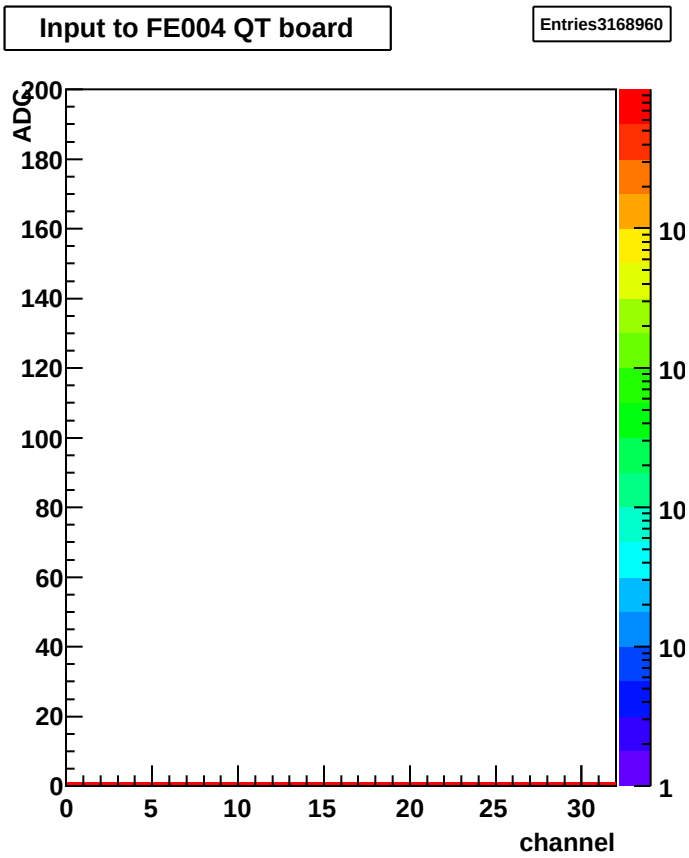

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

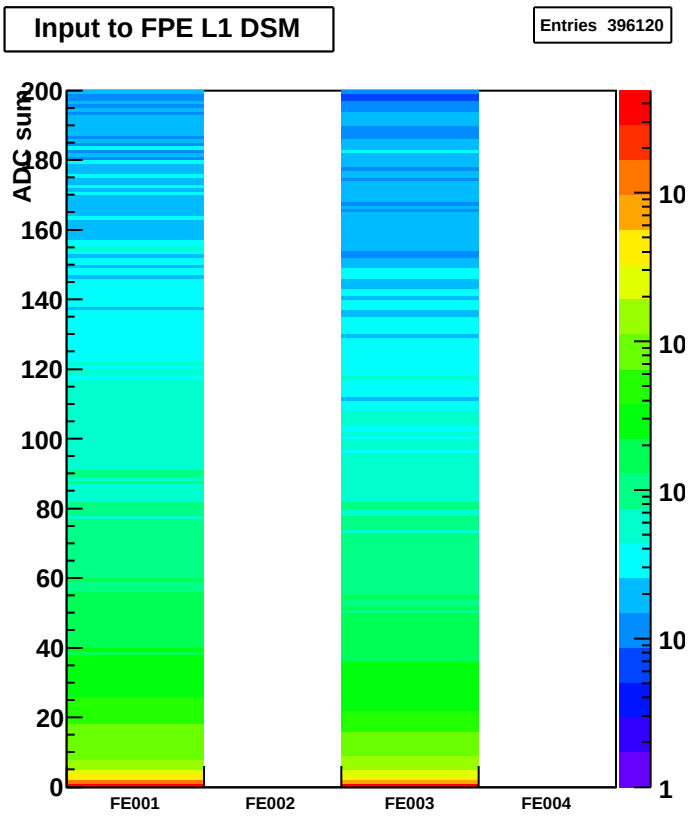

Input to FPD L2 DSM
Entries 396120
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

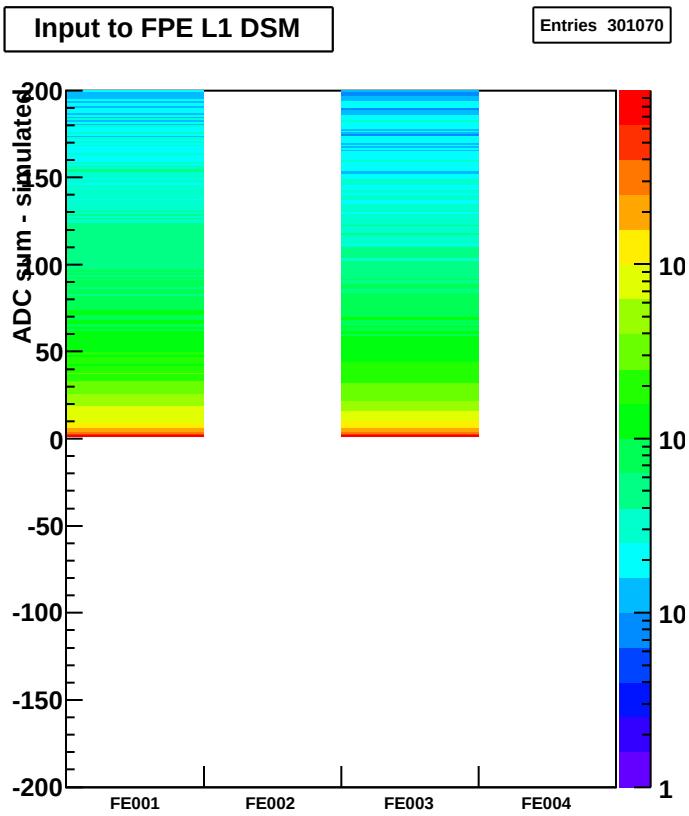

Input to FE001 QT board
Entries3168960
ADC
channel

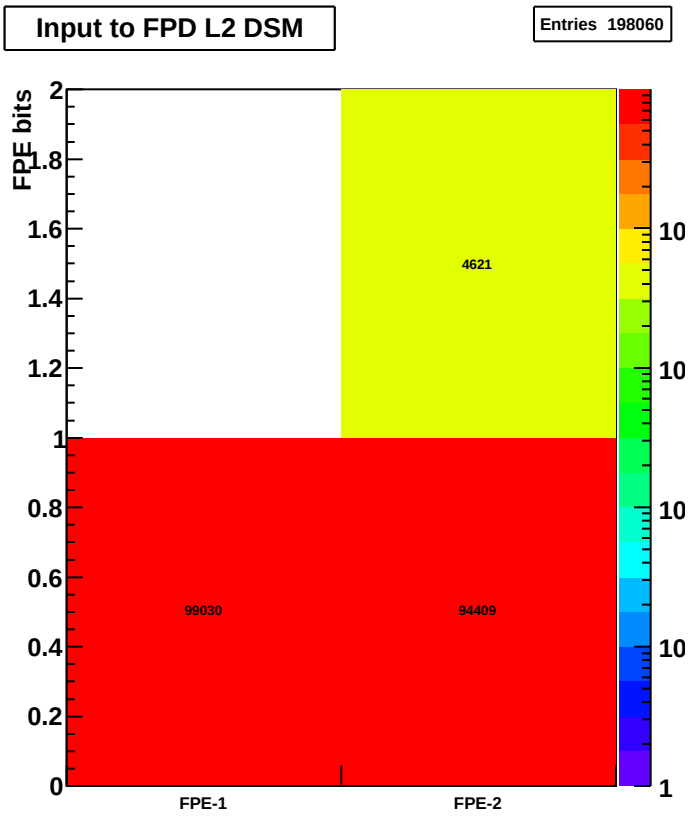

Input to FE002 QT board
Entries3168960
ADC
channel

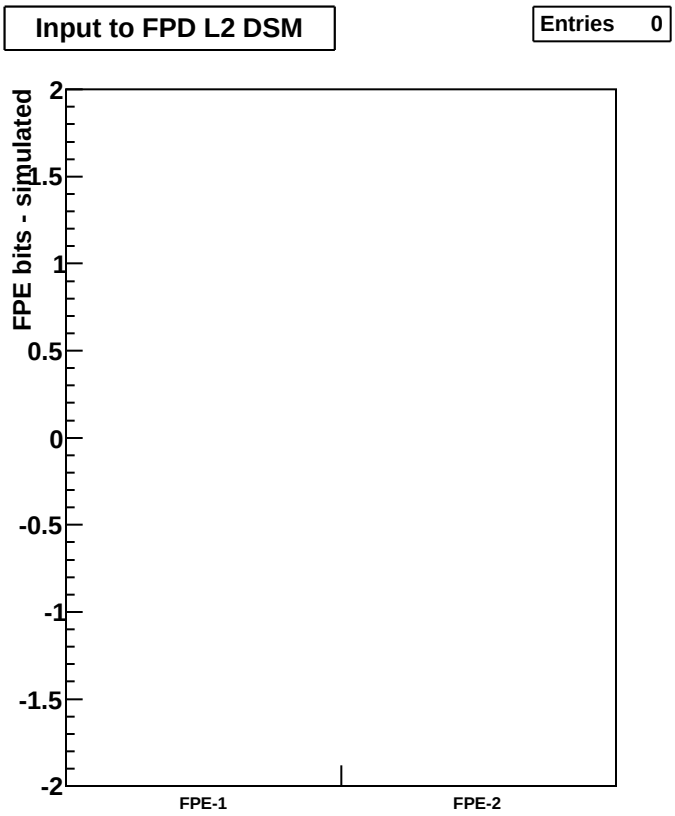

Input to FE003 QT board
Entries3168960
ADC
channel


Input to FE004 QT board
Entries3168960
ADC
channel


Input to FPE L1 DSM
Entries 396120
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 301070
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 198060
FPE bits
4621
99030
94409
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2