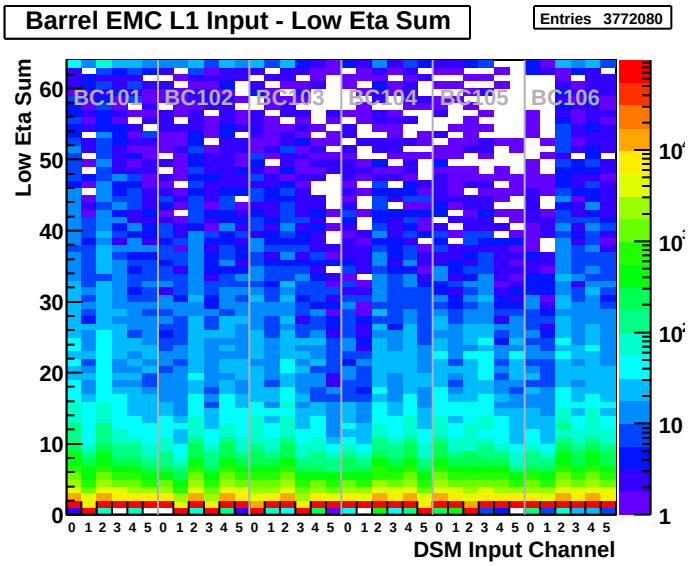
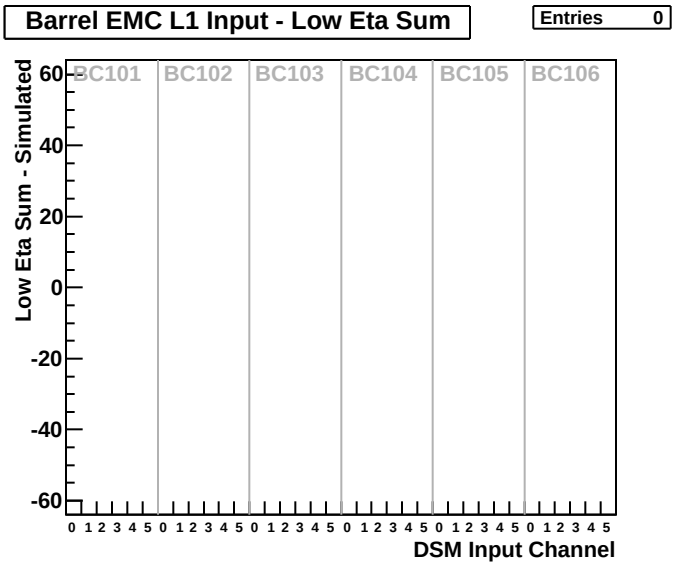
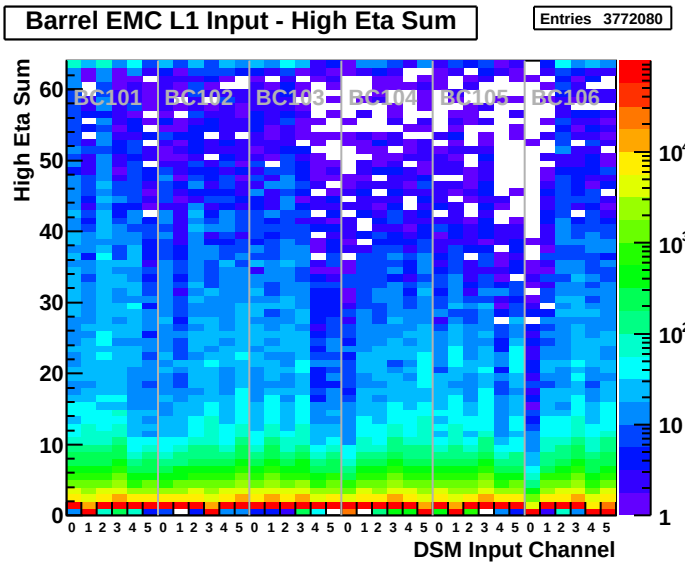
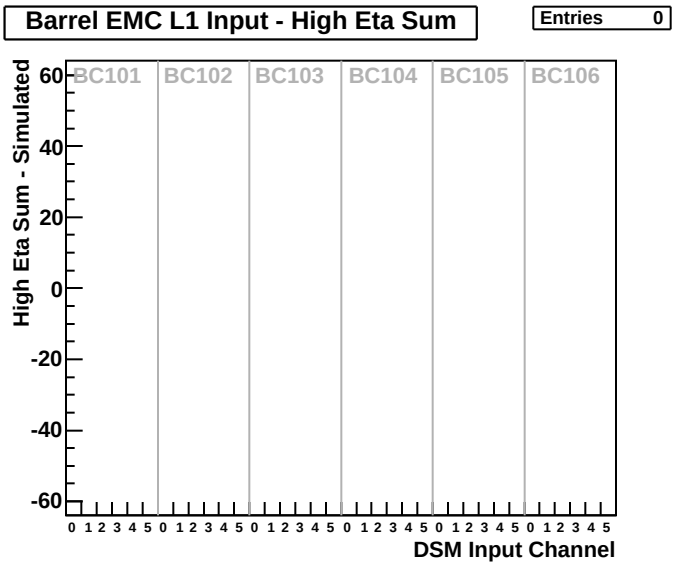
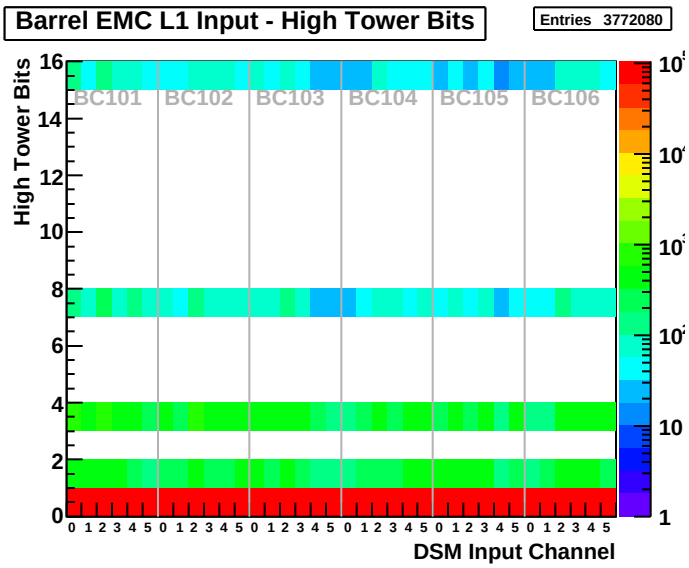
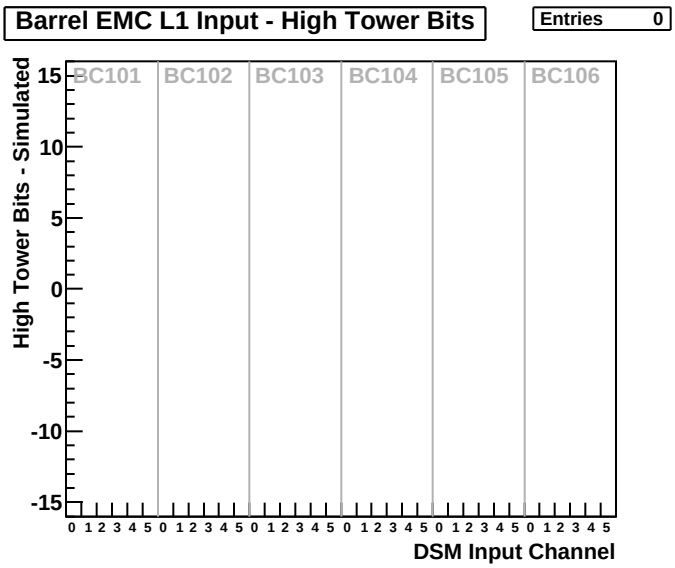

Barrel EMC L1 Input - Low Eta Sum
Entries 3772080
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

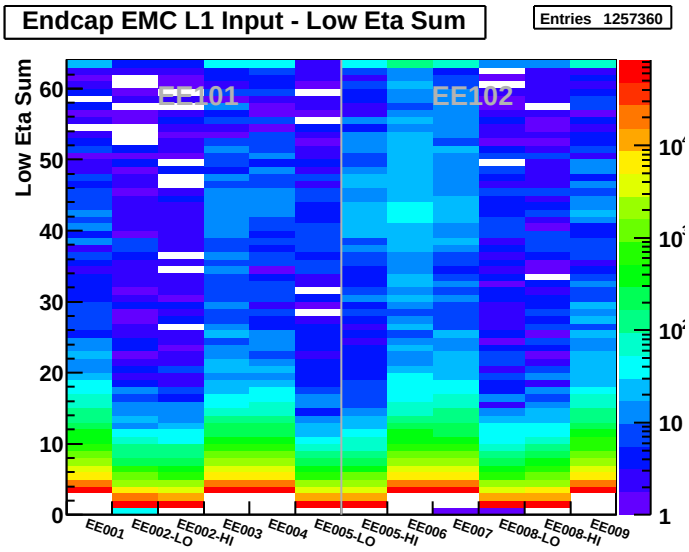

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

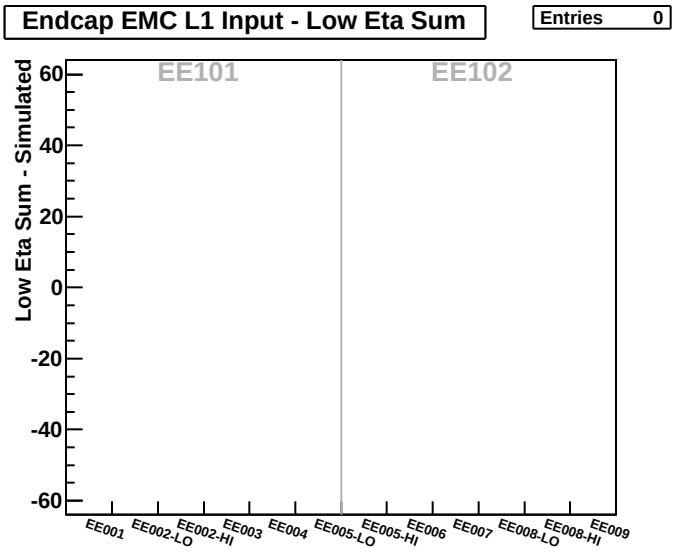

Barrel EMC L1 Input - High Eta Sum
Entries 3772080
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

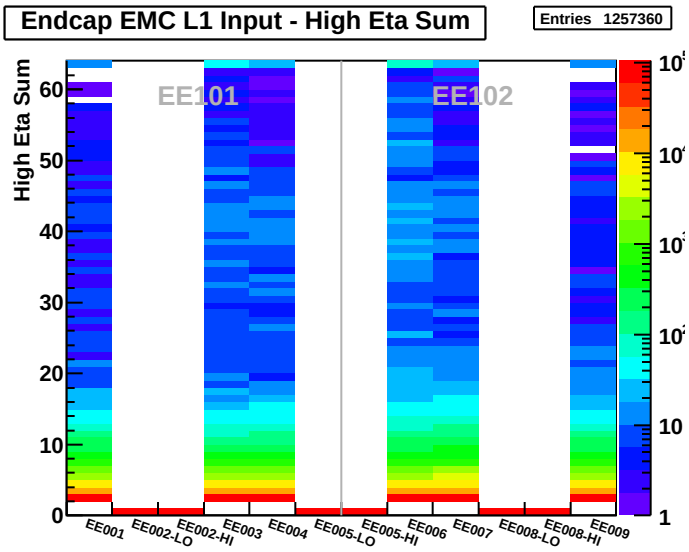

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

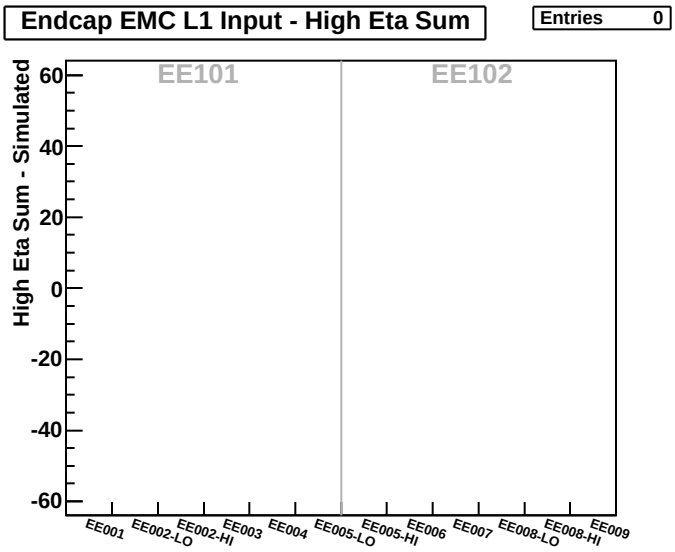

Barrel EMC L1 Input - High Tower Bits
Entries 3772080
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

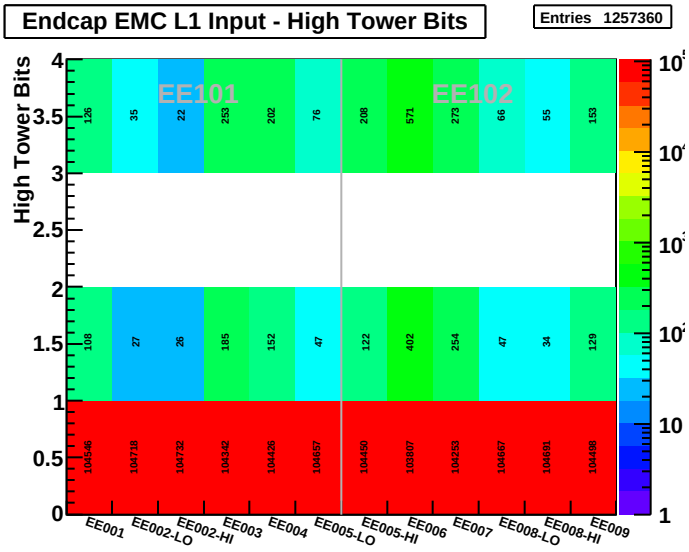

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

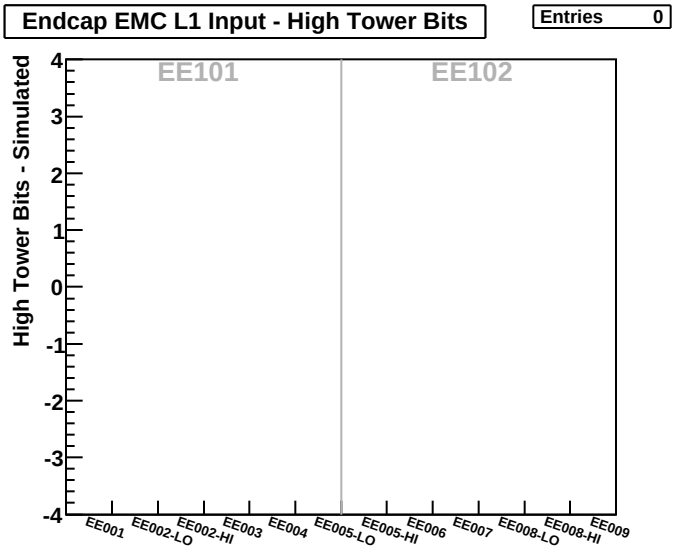

Endcap EMC L1 Input - Low Eta Sum
Entries 1257360
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

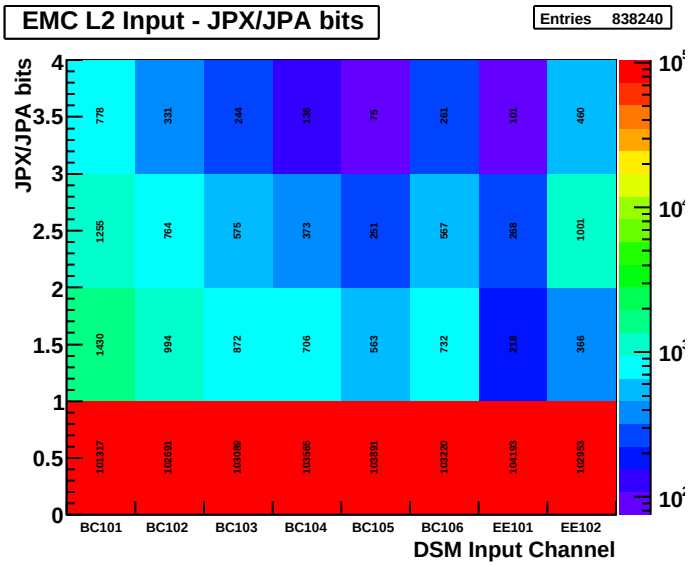

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

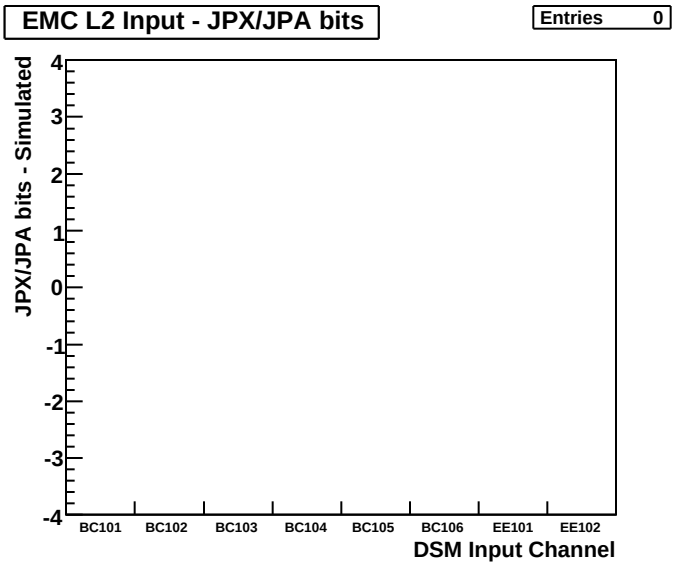

Endcap EMC L1 Input - High Eta Sum
Entries 1257360
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

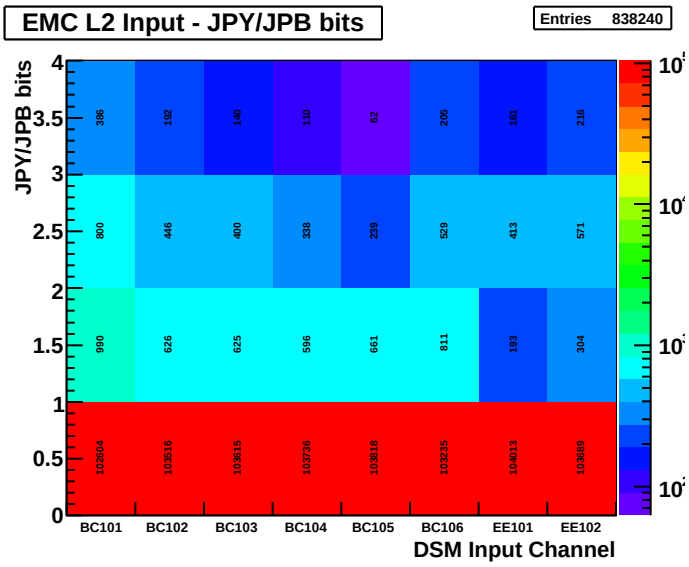

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

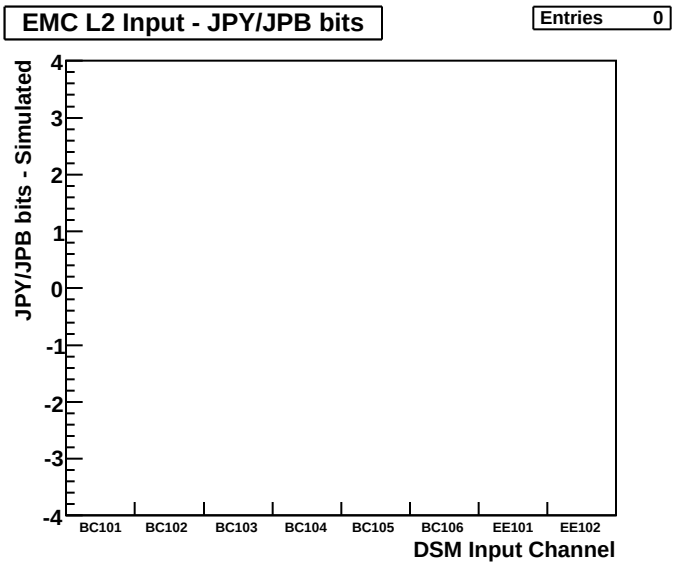

Endcap EMC L1 Input - High Tower Bits
Entries 1257360
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

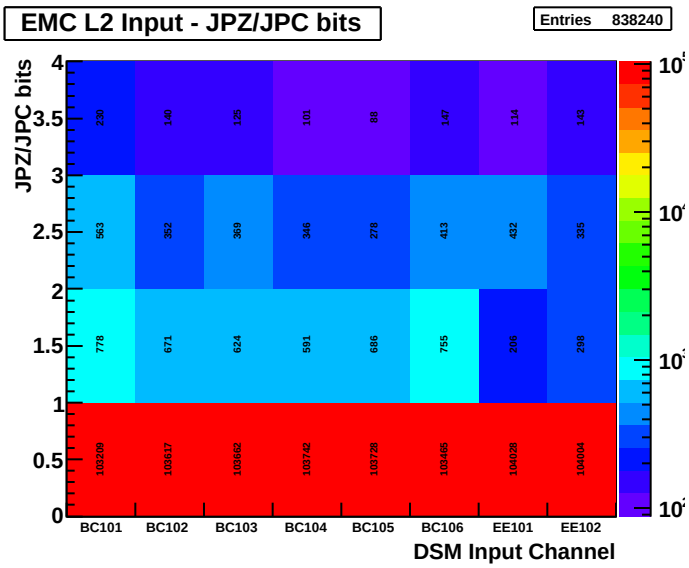

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

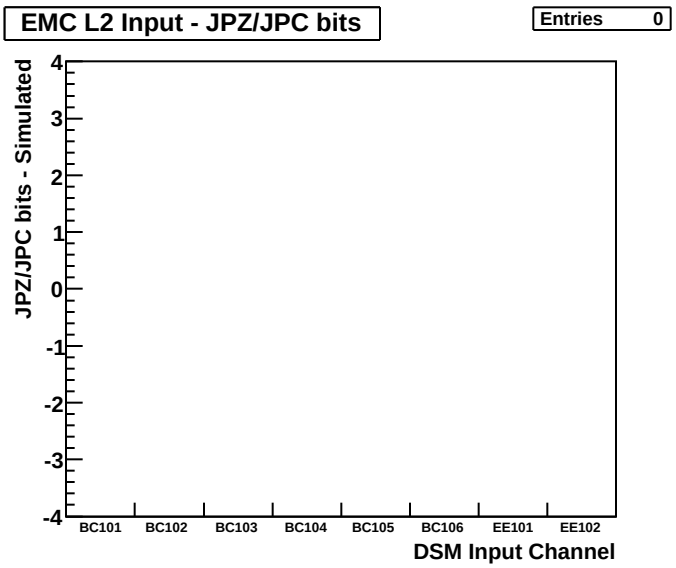

EMC L2 Input - JPX/JPA bits
Entries 838240
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
778 331 244 136 75 261 101 460
1255 764 575 373 251 567 288 1001
1430 994 872 706 563 732 218 366
101317 102691 103089 103565 103891 103220 104153 102953

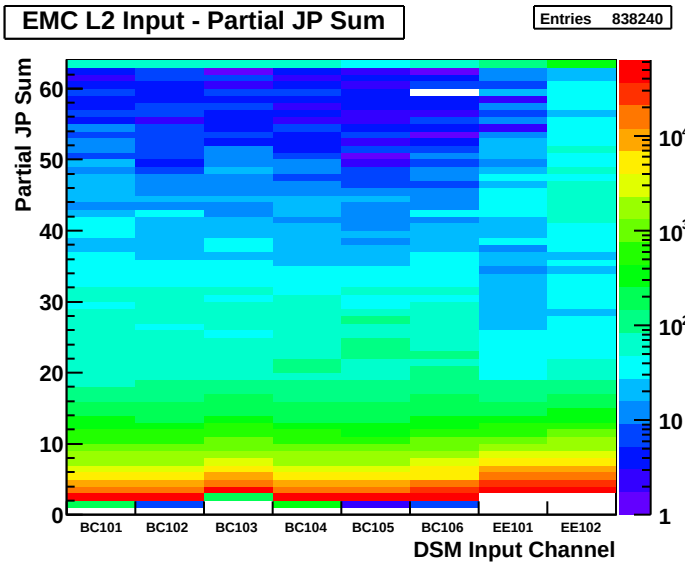

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

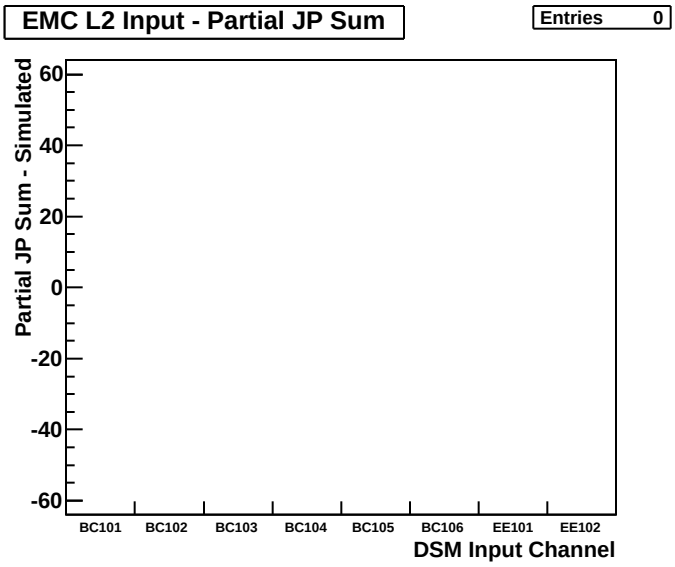

EMC L2 Input - JPY/JPB bits
Entries 838240
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
386 192 140 110 62 205 161 216
800 446 400 338 239 529 413 571
990 626 625 596 661 811 193 304
102604 103516 103615 103736 103818 103235 104013 103689

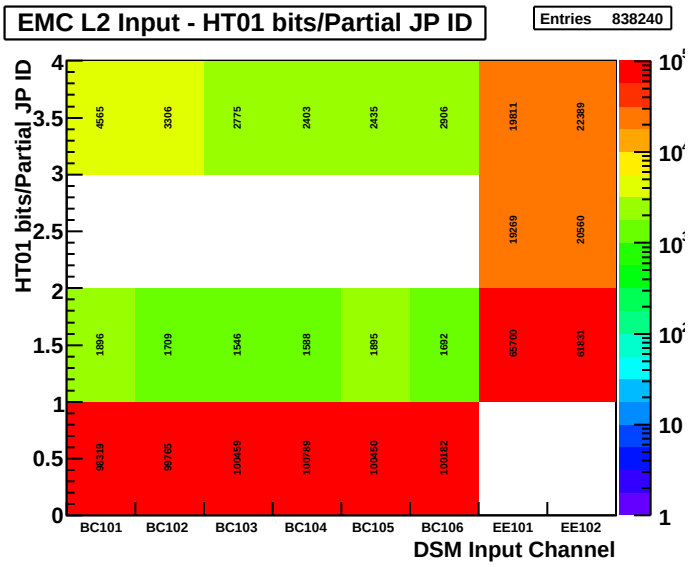

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

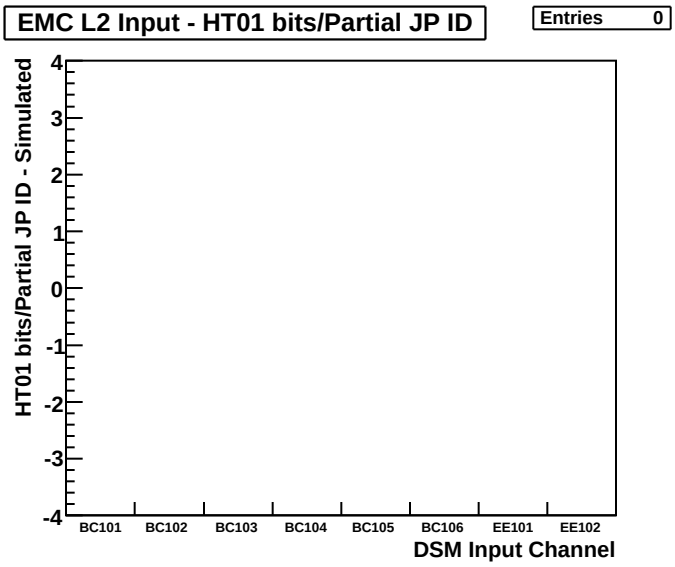

EMC L2 Input - JPZ/JPC bits
Entries 838240
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
230 140 125 101 88 147 114 143
563 352 369 346 278 413 432 335
778 671 624 591 686 755 206 298
103209 103617 103662 103742 103728 103465 104028 104004

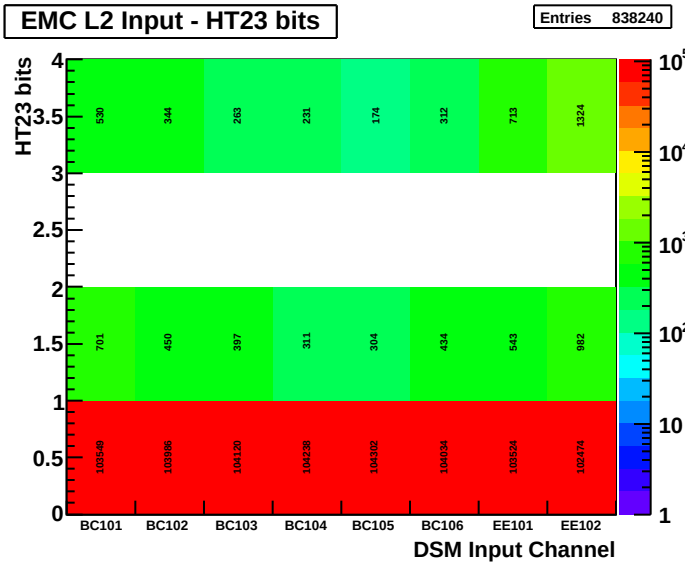

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

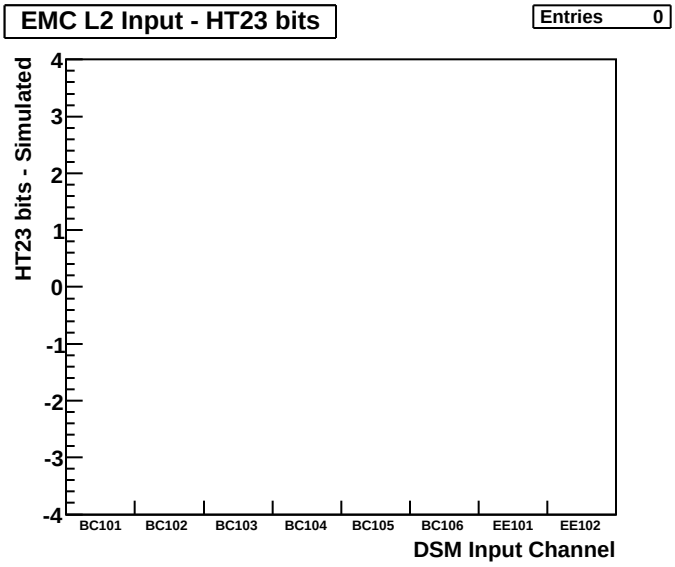

EMC L2 Input - Partial JP Sum
Entries 838240
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

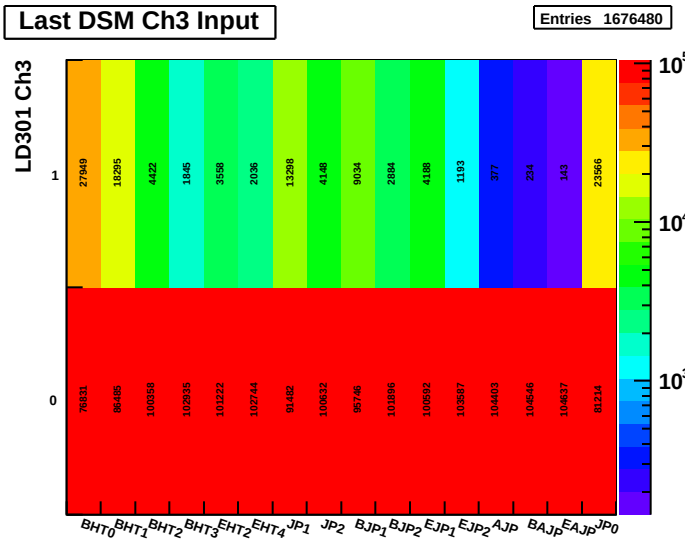

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

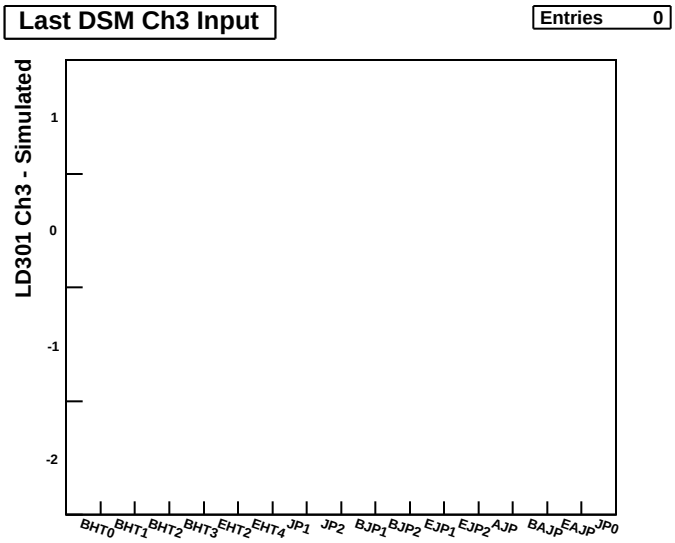

EMC L2 Input - HT01 bits/Partial JP ID
Entries 838240
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

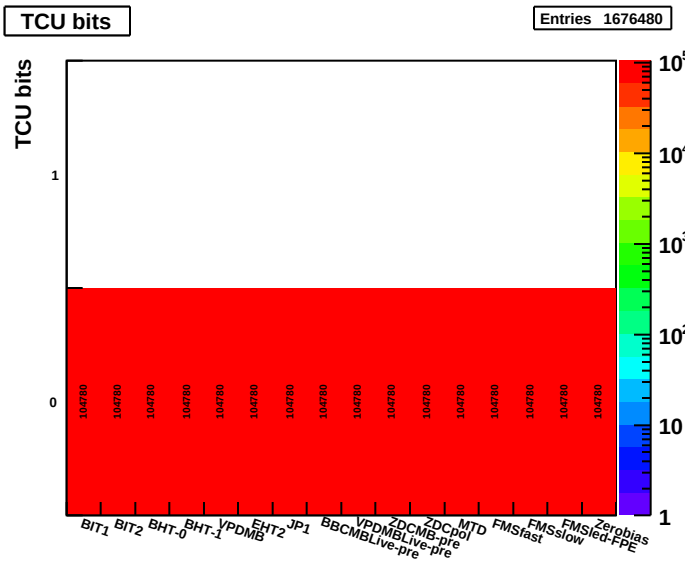

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

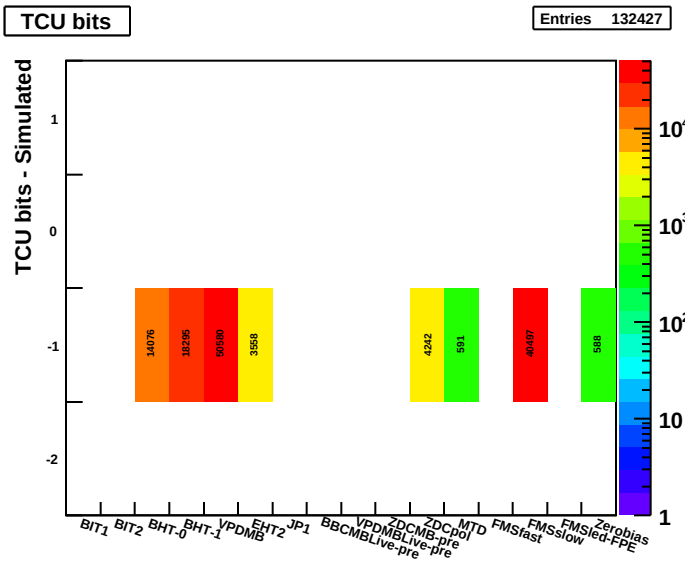

EMC L2 Input - HT23 bits
Entries 838240
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

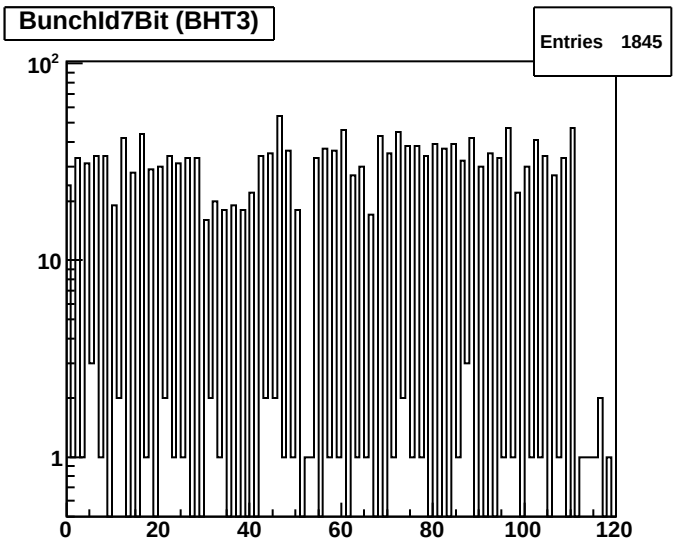

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

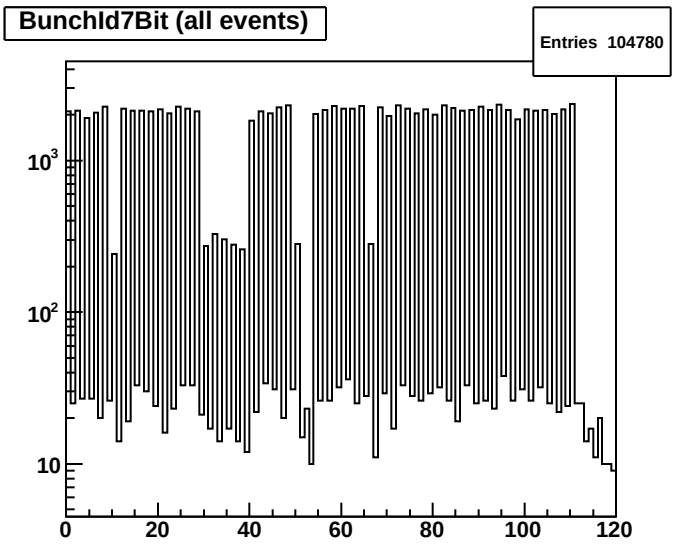

Last DSM Ch3 Input
Entries 1676480
LD301 Ch3
27949 18295 4422 1845 3558 2036 13298 4148 9034 2884 4188 1193 377 234 143 23566
76831 86485 100358 102935 101222 102744 91482 100632 95746 101896 100592 103587 104403 104546 104637 81214
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

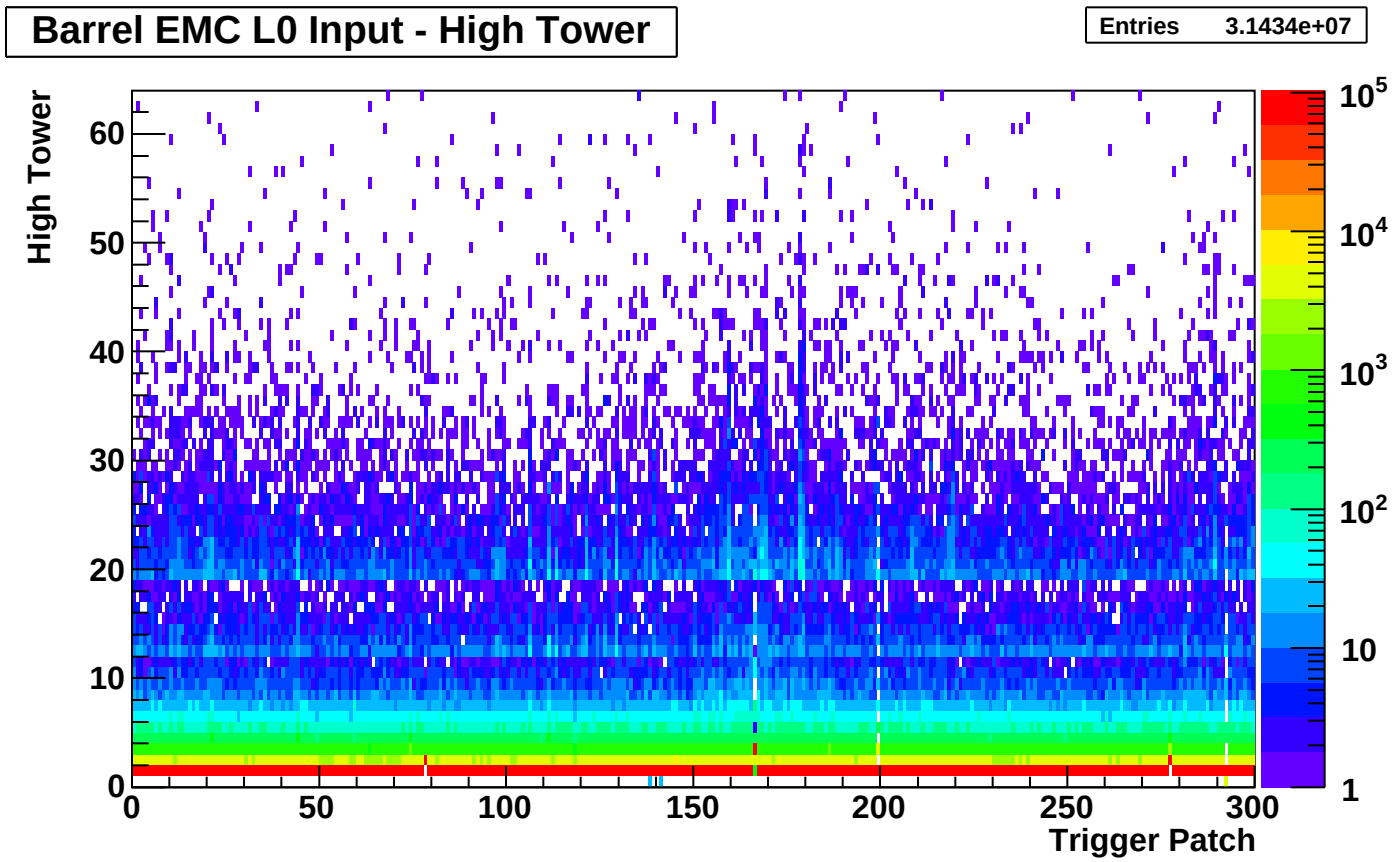

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

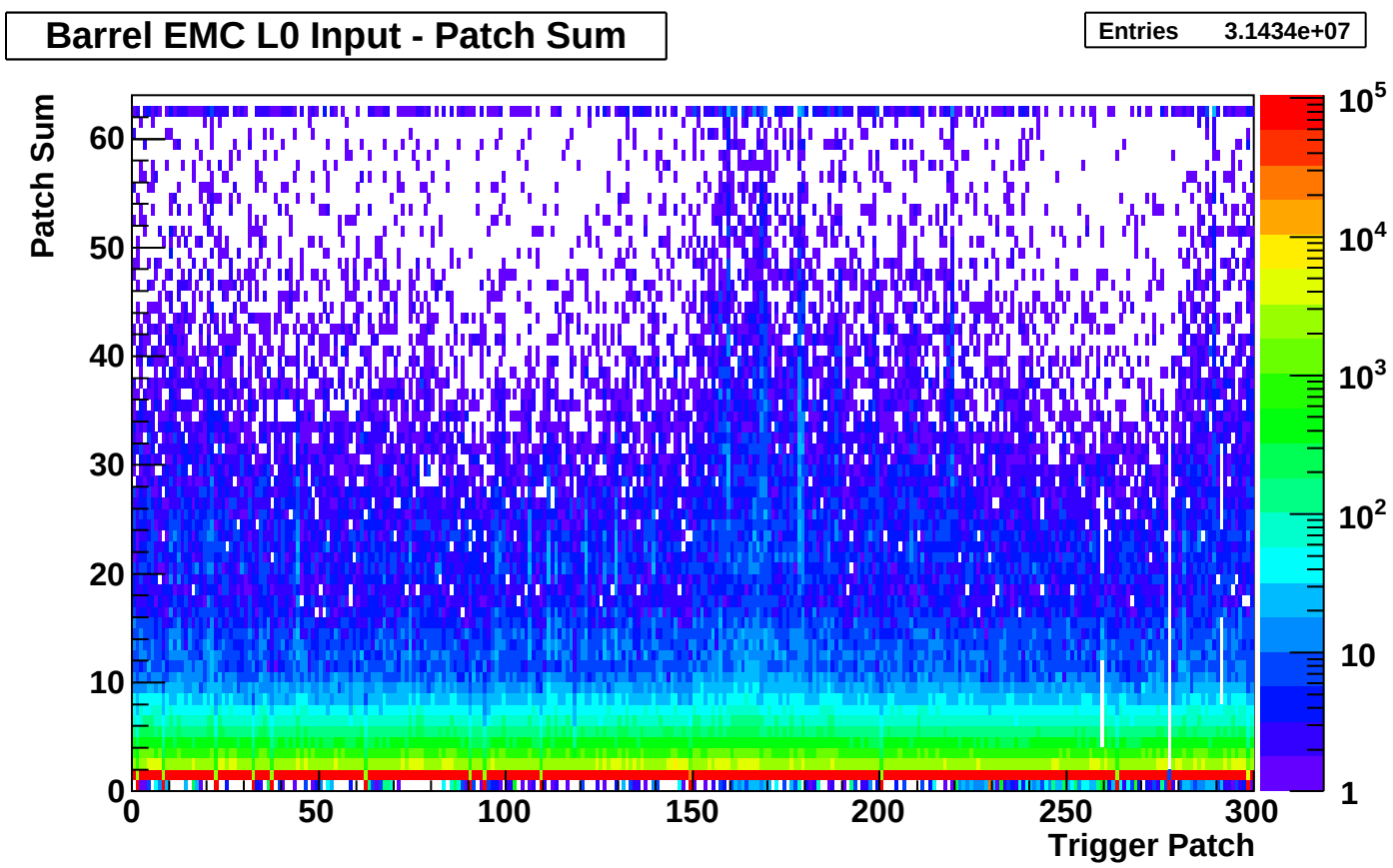

TCU bits
Entries 1676480
TCU bits
104780 104780 104780 104780 104780 104780 104780 104780 104780 104780 104780 104780 104780 104780 104780 104780
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

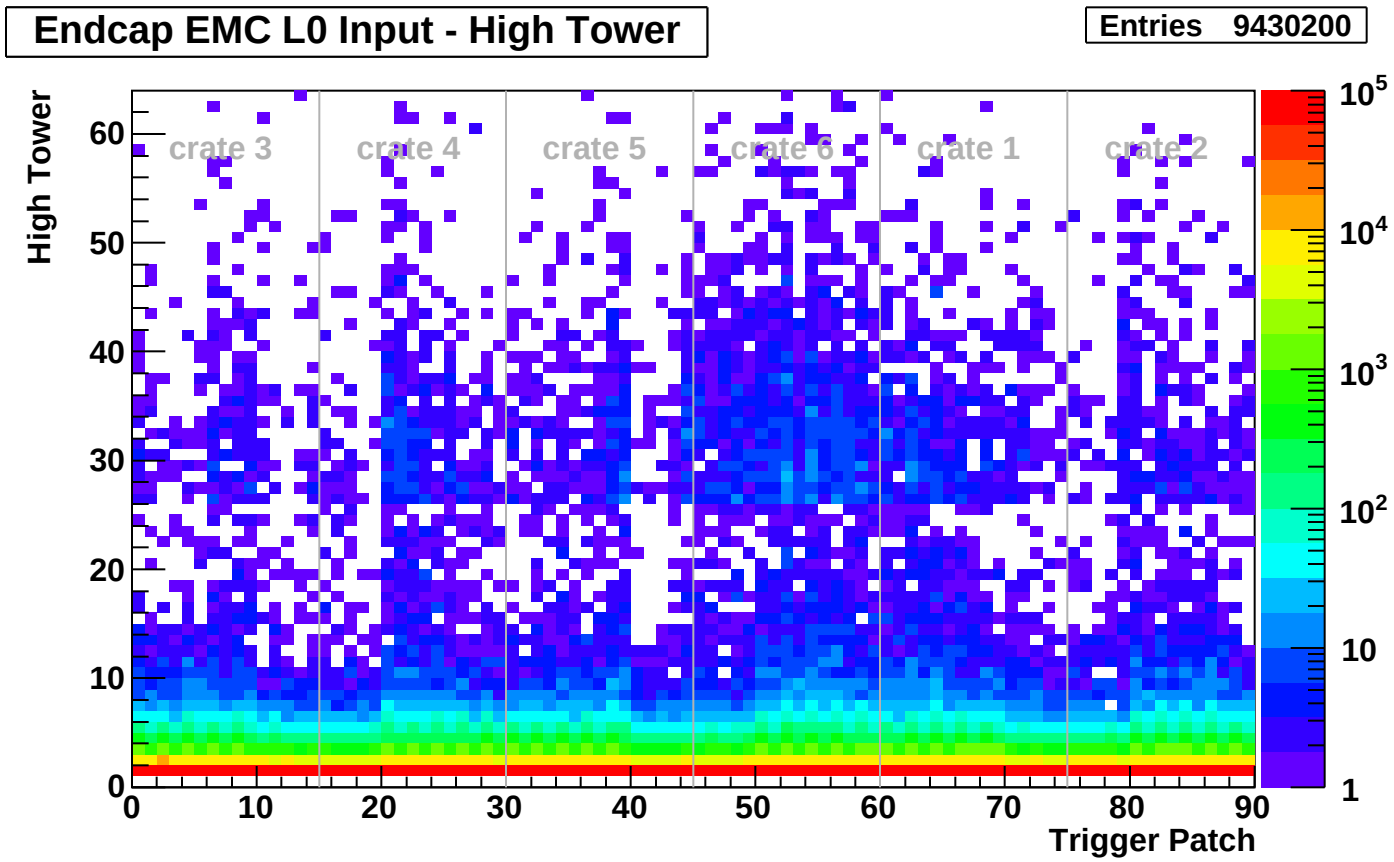

TCU bits
Entries 132427
TCU bits - Simulated
14076 18295 50580 3558 4242 591 40497 588
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

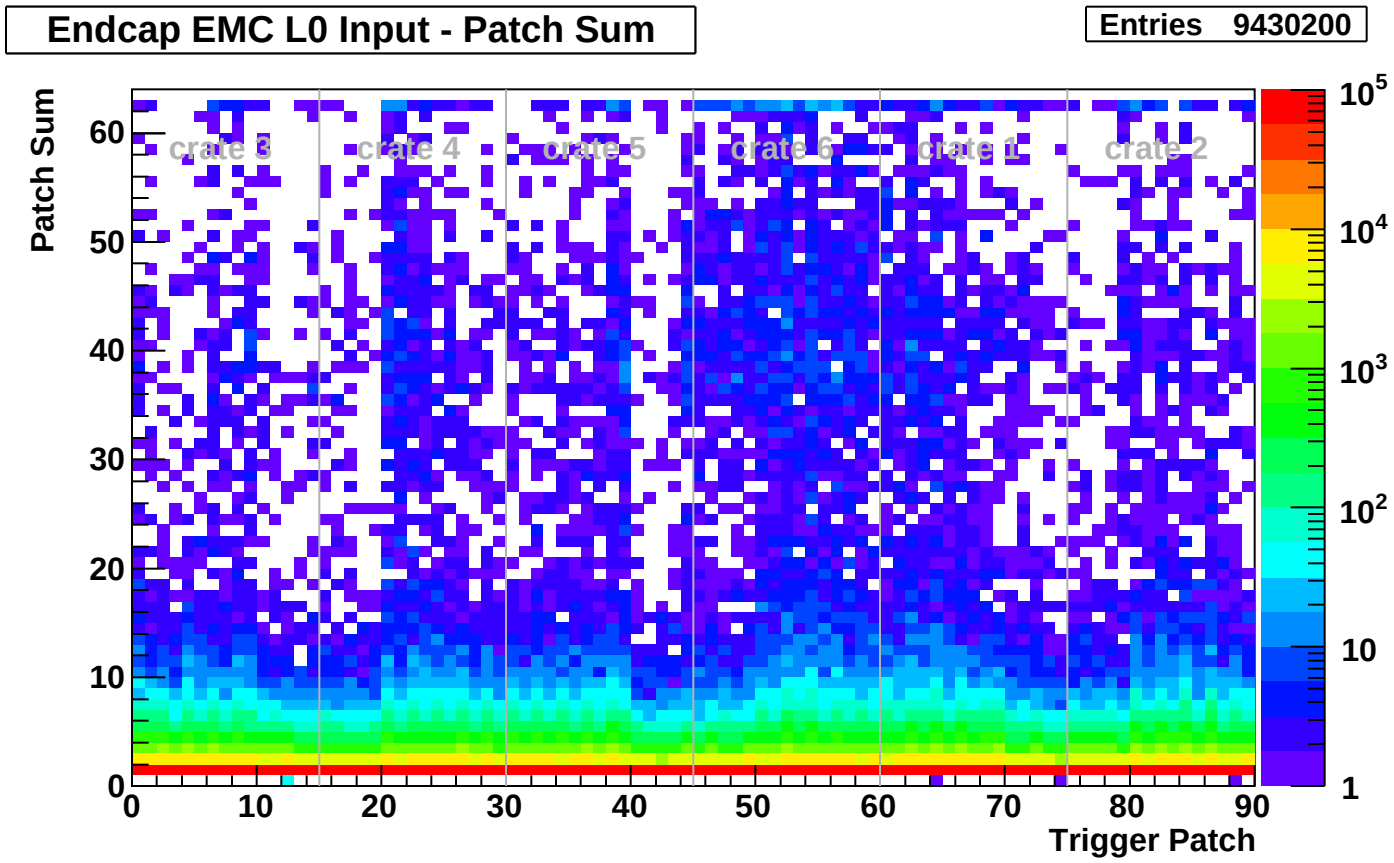

BunchId7Bit (BHT3)
Entries 1845

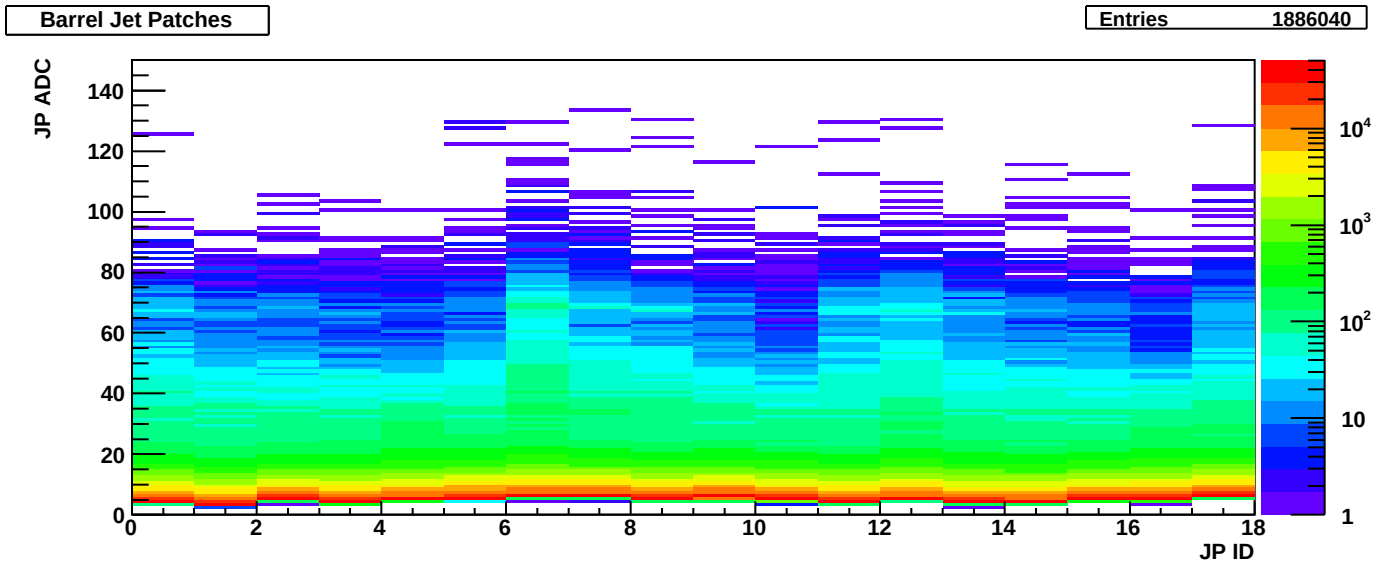

BunchId7Bit (all events)
Entries 104780

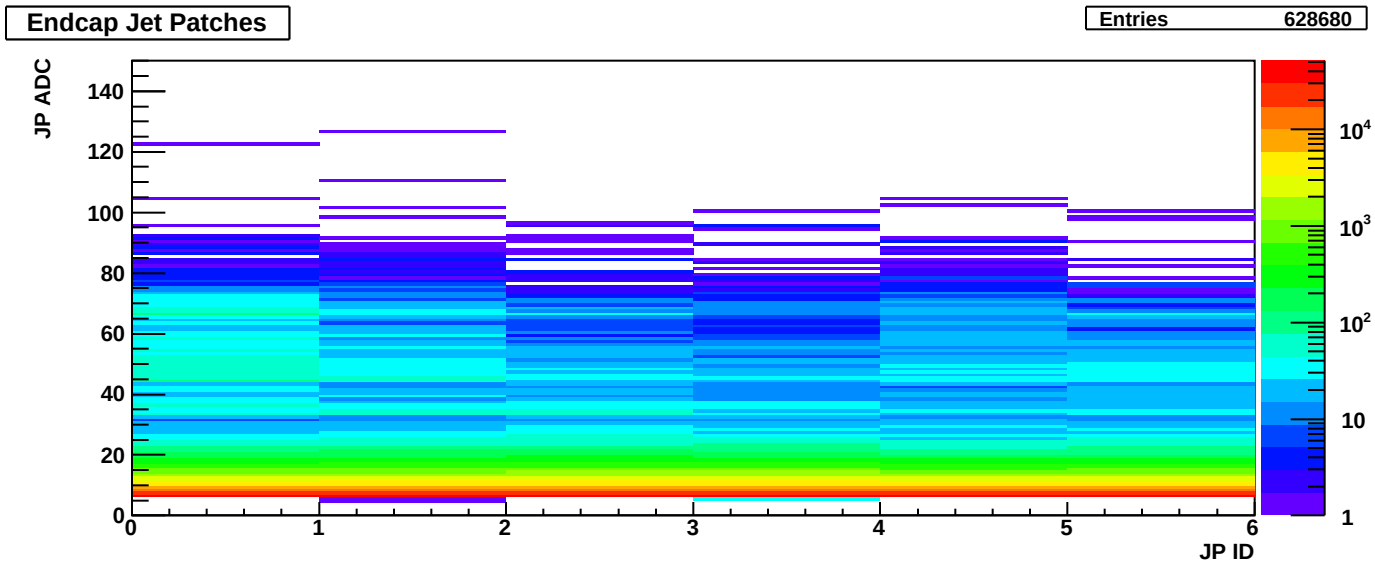

Barrel EMC L0 Input - High Tower
Entries 3.1434e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

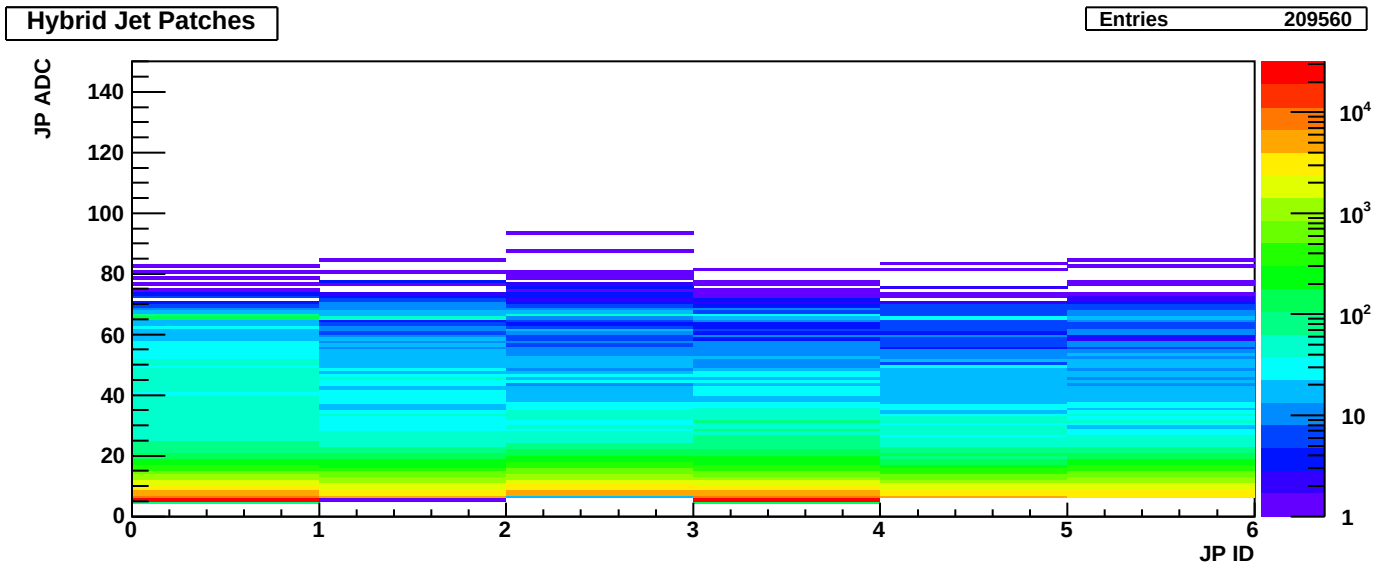

Barrel EMC L0 Input - Patch Sum
Entries 3.1434e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

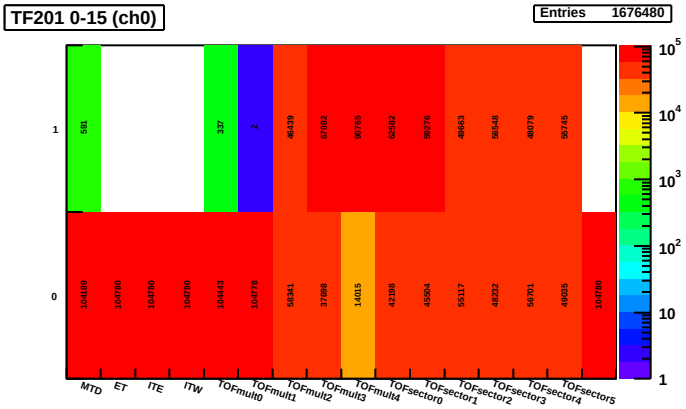

Endcap EMC L0 Input - High Tower
Entries 9430200
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

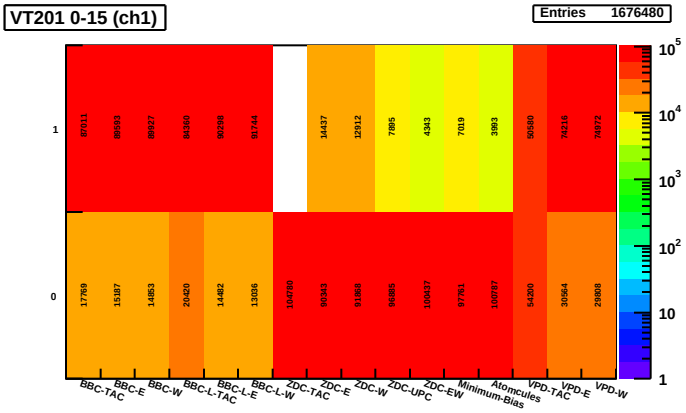

Endcap EMC L0 Input - Patch Sum
Entries 9430200
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

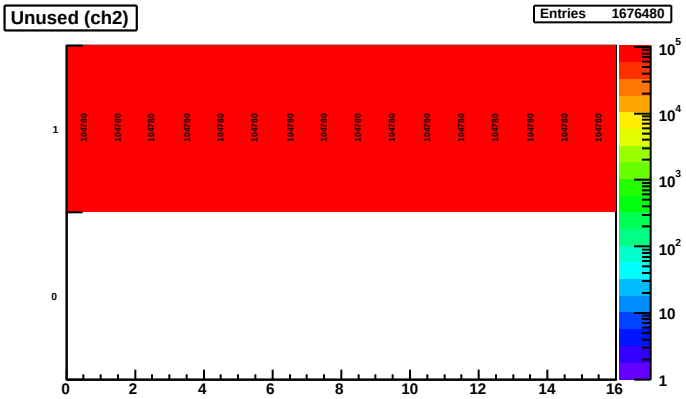

Barrel Jet Patches
Entries 1886040
JP ADC
JP ID

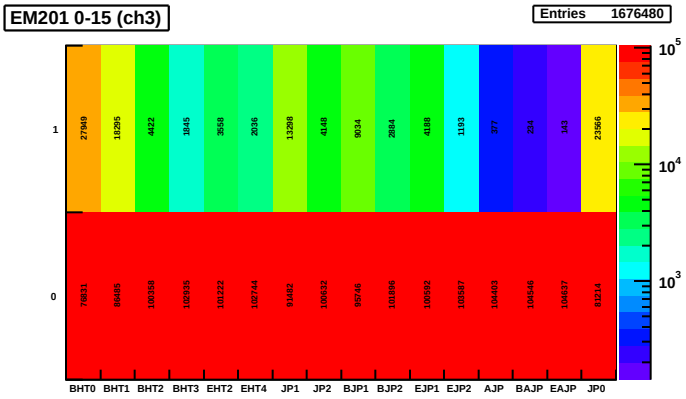

Endcap Jet Patches
Entries 628680
JP ADC
JP ID

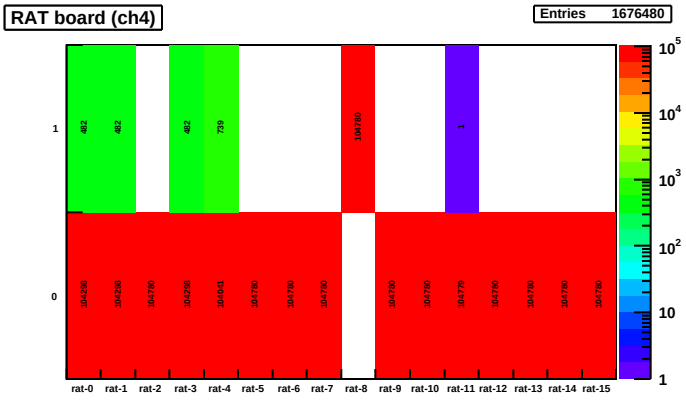

Hybrid Jet Patches
Entries 209560
JP ADC
JP ID

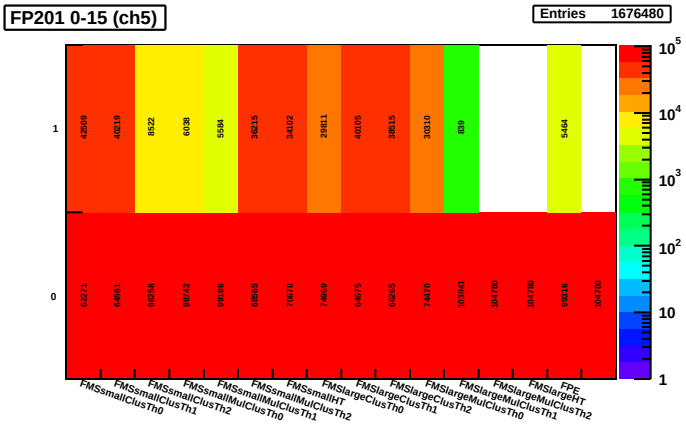

TF201 0-15 (ch0)
Entries 1676480
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

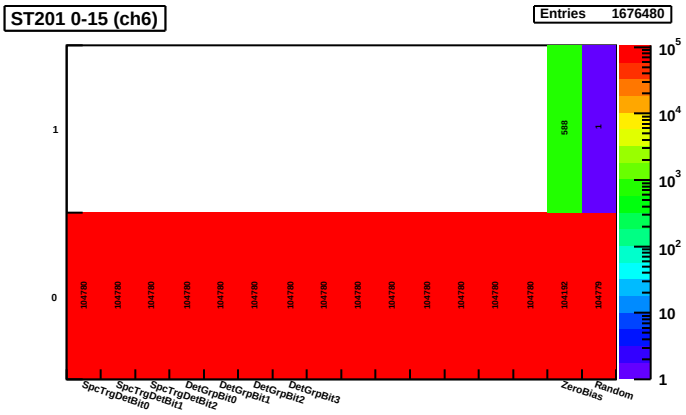

VT201 0-15 (ch1)
Entries 1676480
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

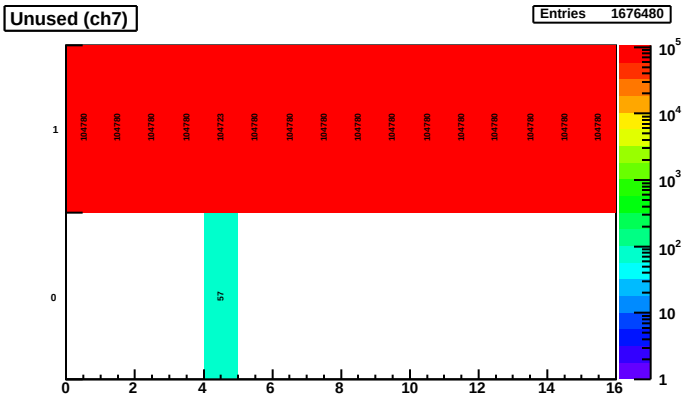

Unused (ch2)
Entries 1676480

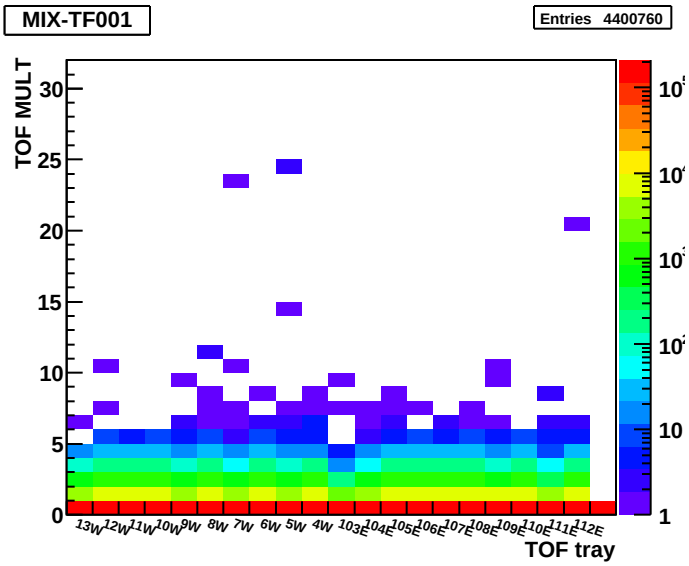

EM201 0-15 (ch3)
Entries 1676480
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

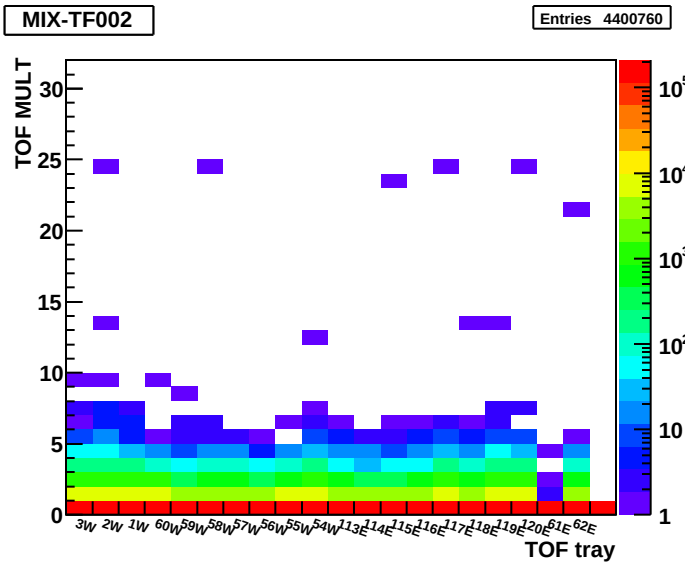

RAT board (ch4)
Entries 1676480
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

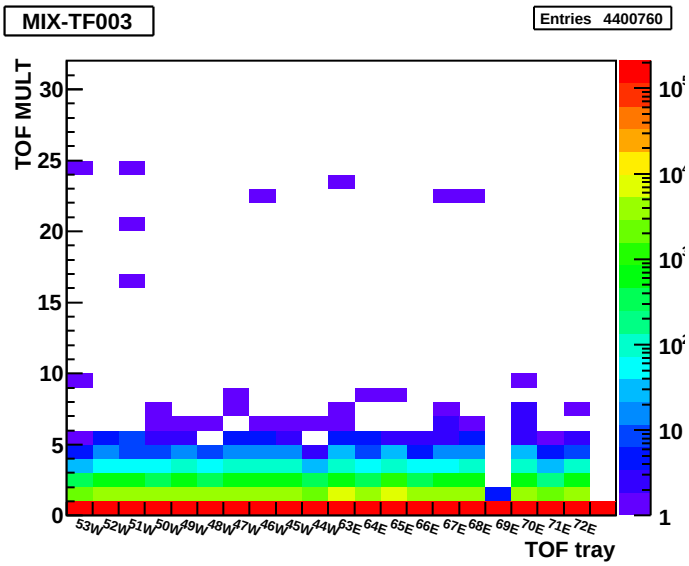

FP201 0-15 (ch5)
Entries 1676480
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

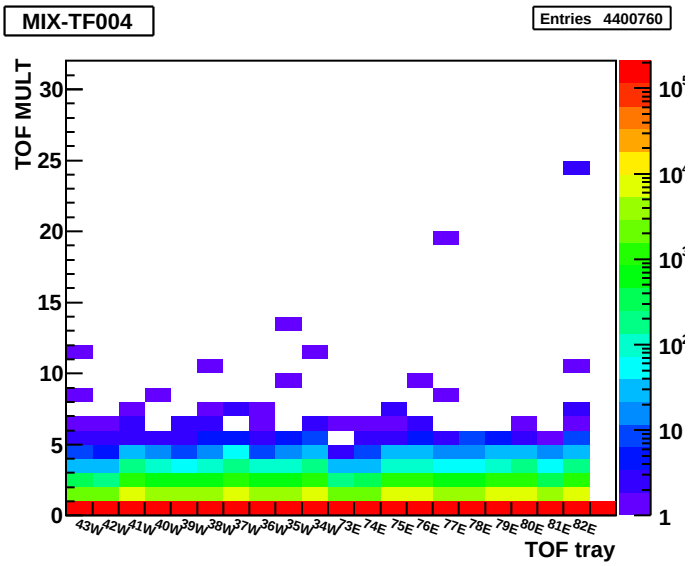

ST201 0-15 (ch6)
Entries 1676480
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

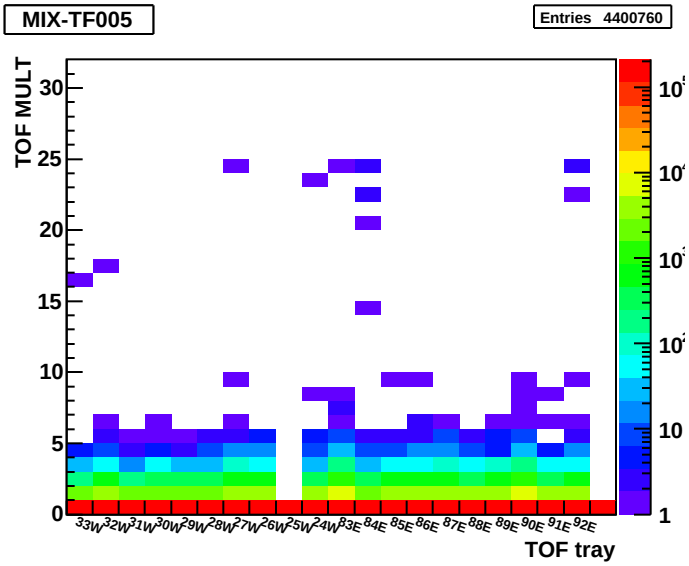

Unused (ch7)
Entries 1676480

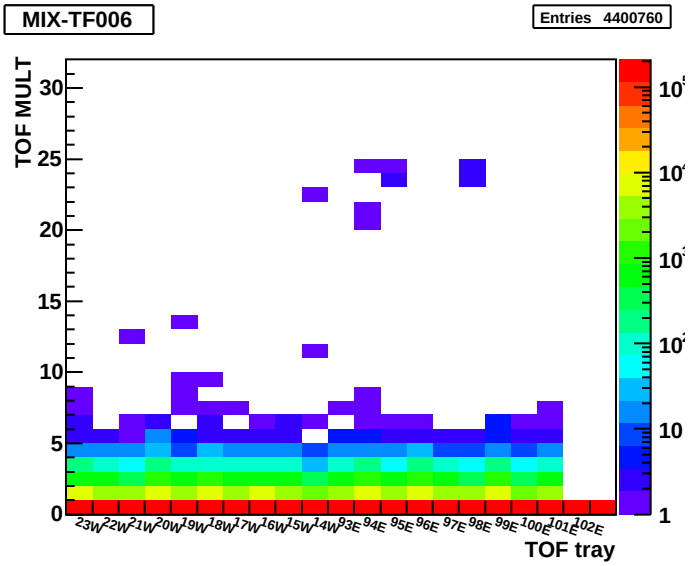

MIX-TF001
Entries 4400760
TOF MULT
TOF tray

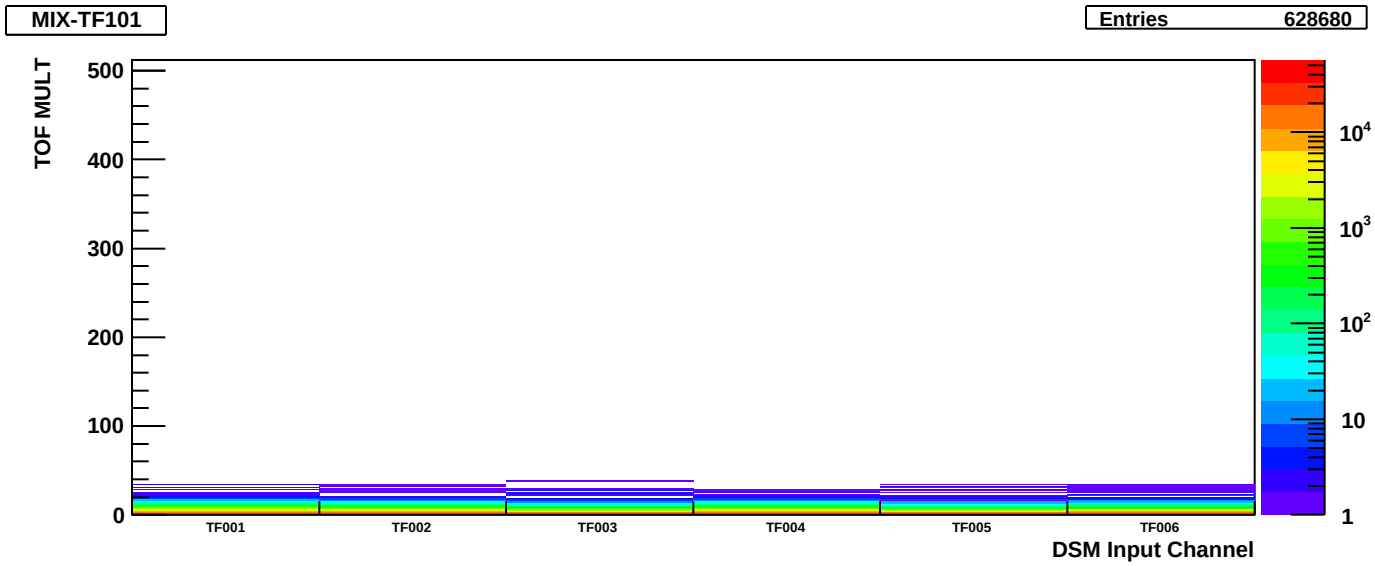

MIX-TF002
Entries 4400760
TOF MULT
TOF tray

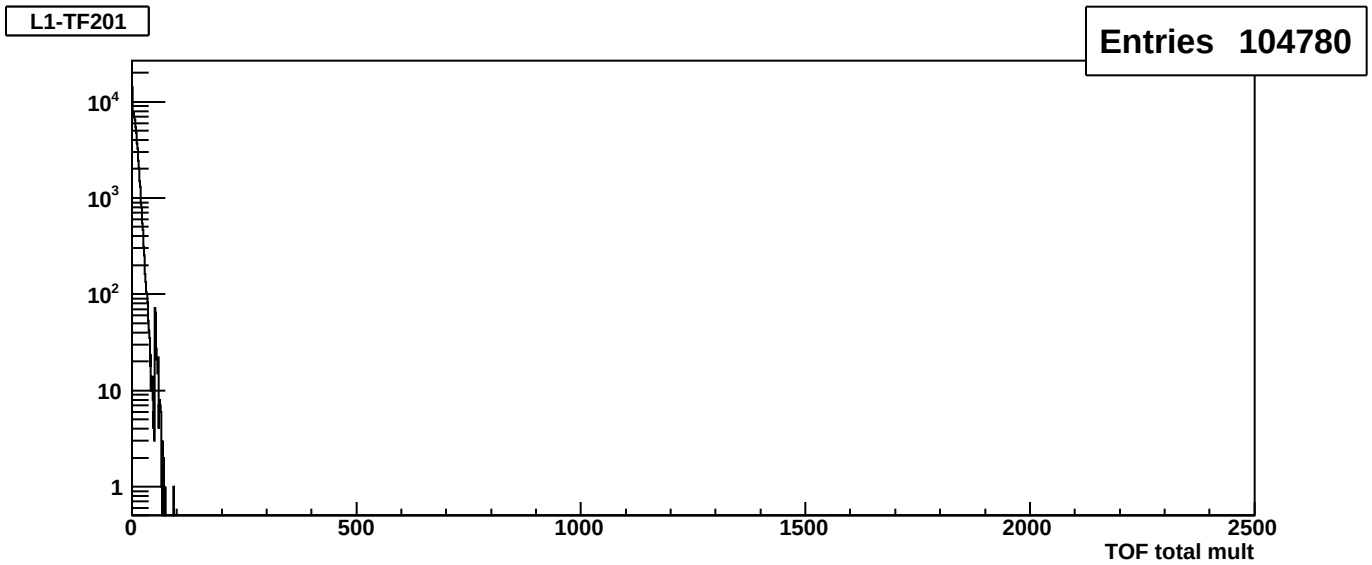

MIX-TF003
Entries 4400760
TOF MULT
TOF tray

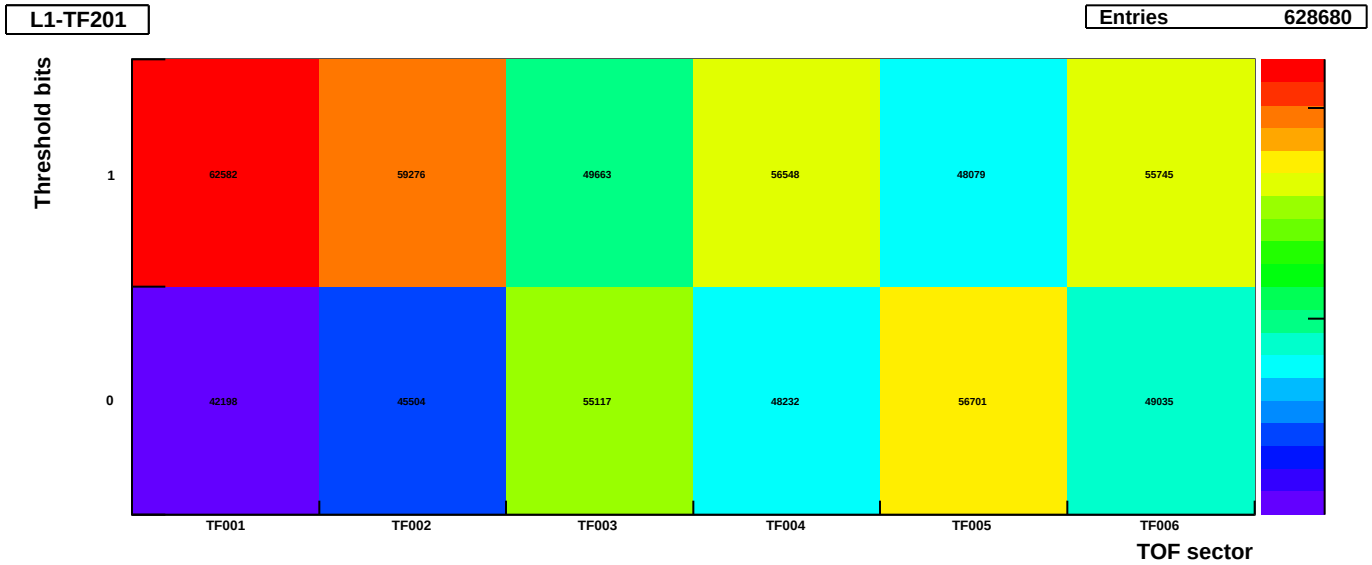

MIX-TF004
Entries 4400760
TOF MULT
TOF tray

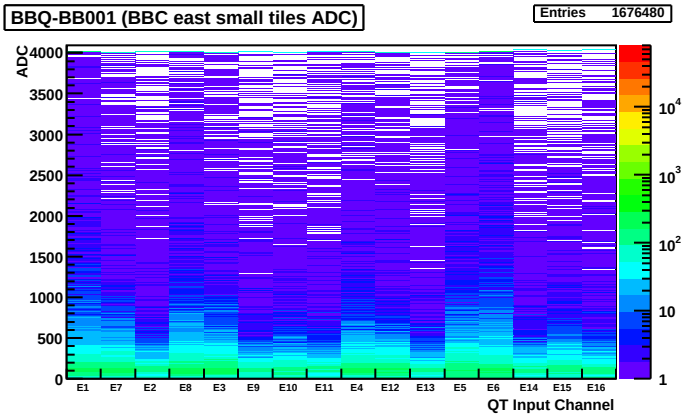

MIX-TF005
Entries 4400760
TOF MULT
TOF tray

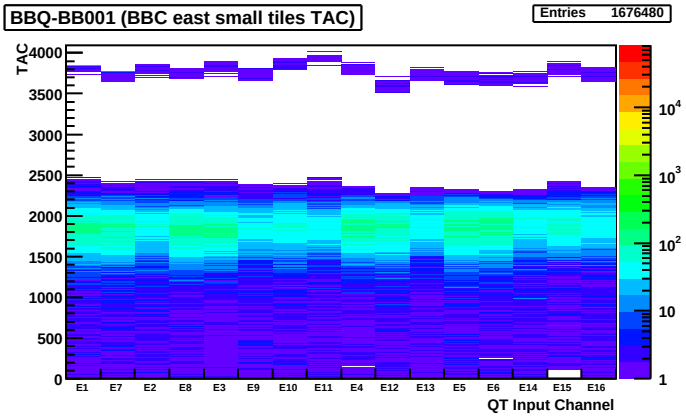

MIX-TF006
Entries 4400760
TOF MULT
TOF tray

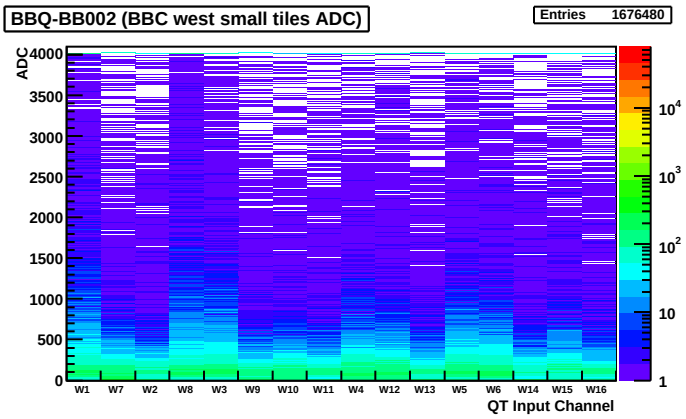

MIX-TF101
Entries 628680
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

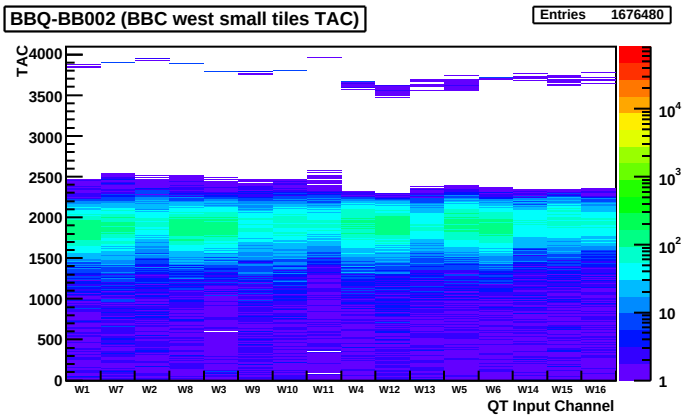

L1-TF201
Entries 104780
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

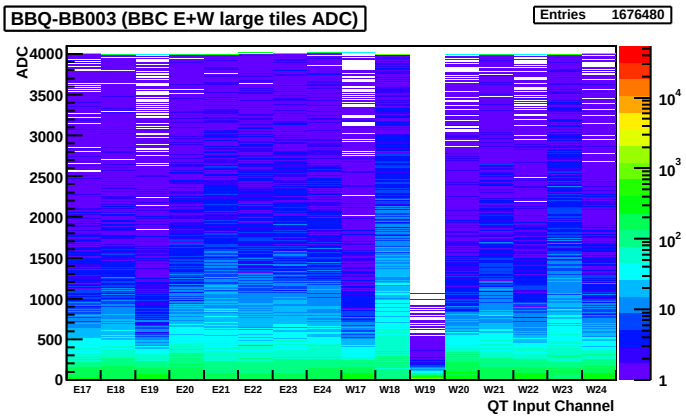

L1-TF201
Entries 628680
Threshold bits
1
0
62582
59276
49663
56548
48079
55745
42198
45504
55117
48232
56701
49035
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

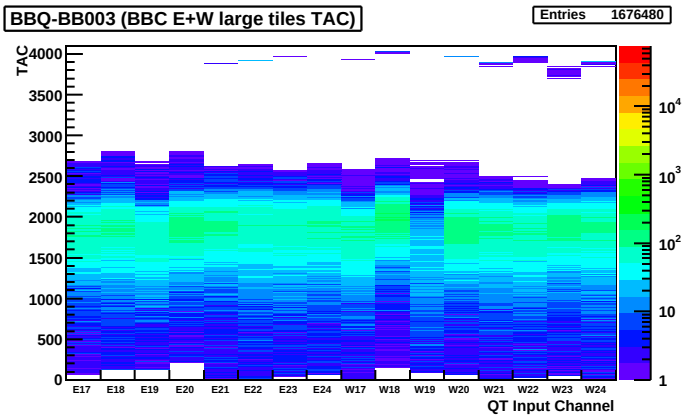

BBQ-BB001 (BBC east small tiles ADC)
Entries 1676480
ADC
QT Input Channel

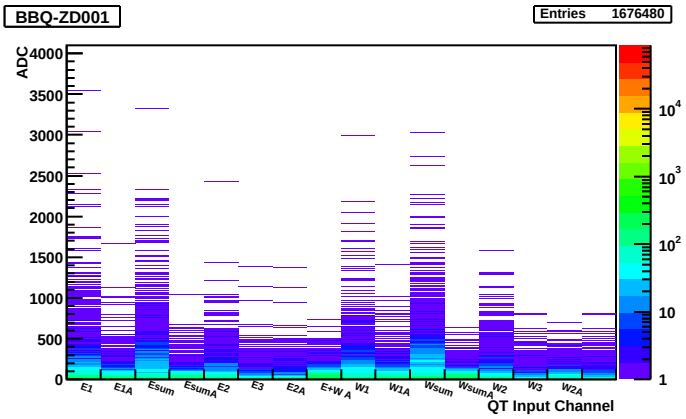

BBQ-BB001 (BBC east small tiles TAC)
Entries 1676480
TAC
QT Input Channel

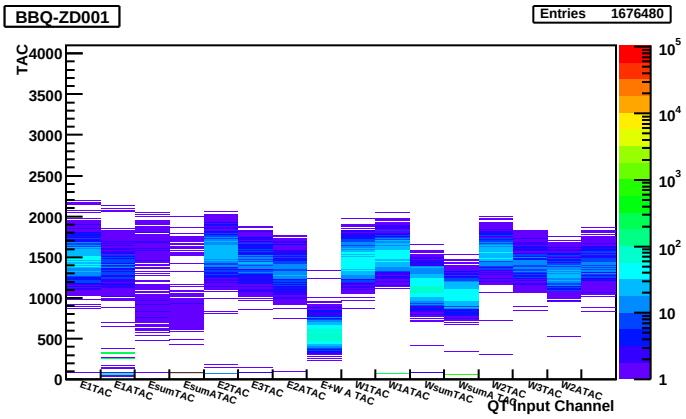

BBQ-BB002 (BBC west small tiles ADC)
Entries 1676480
ADC
QT Input Channel

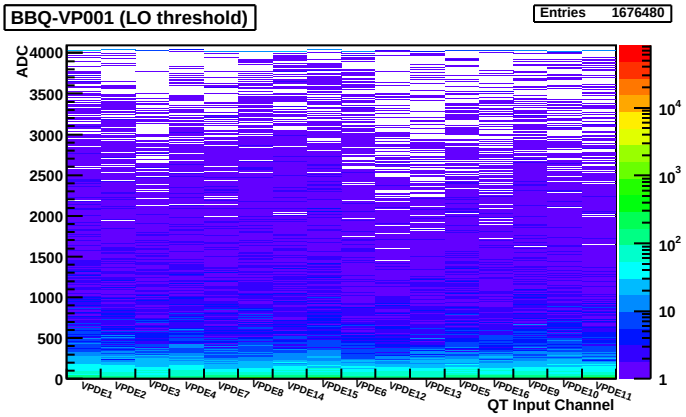

BBQ-BB002 (BBC west small tiles TAC)
Entries 1676480
TAC
QT Input Channel

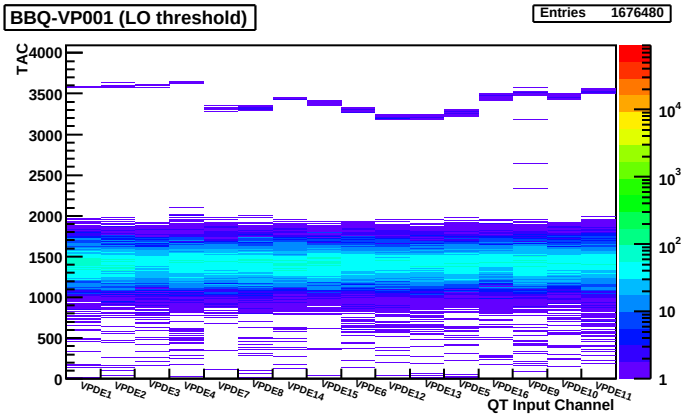

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1676480
ADC
QT Input Channel

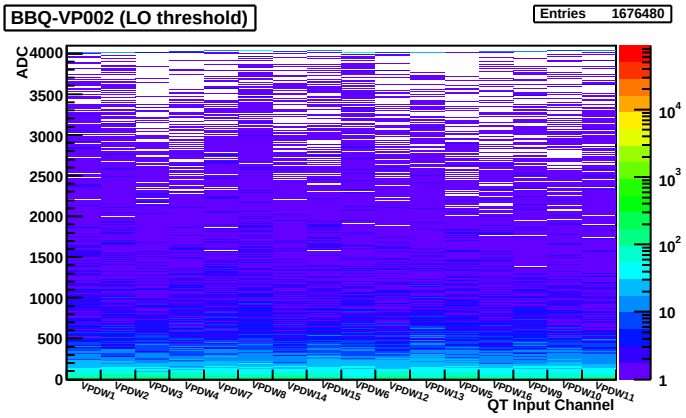

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1676480
TAC
QT Input Channel

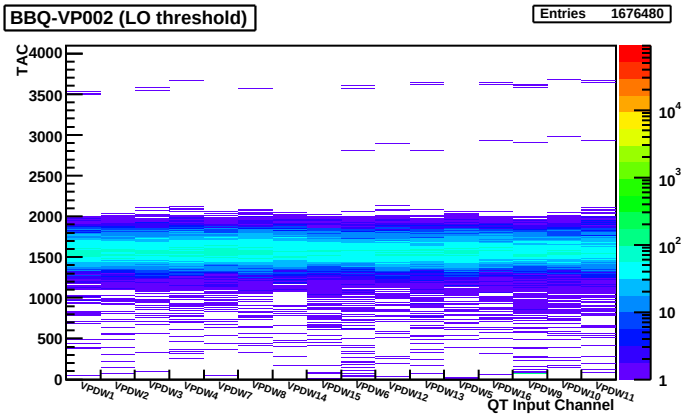

BBQ-ZD001
Entries 1676480
ADC
QT Input Channel

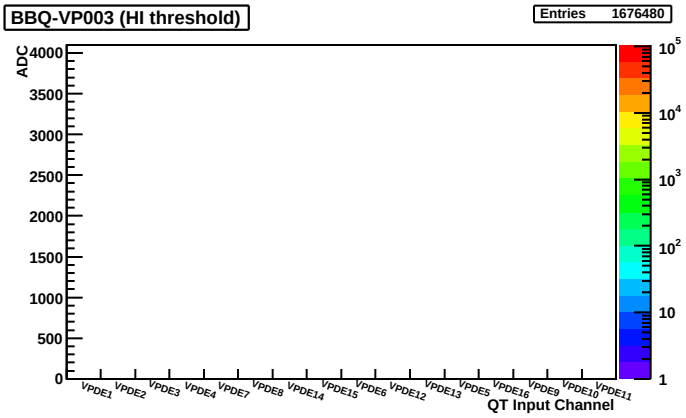

BBQ-ZD001
Entries 1676480
TAC
QT Input Channel

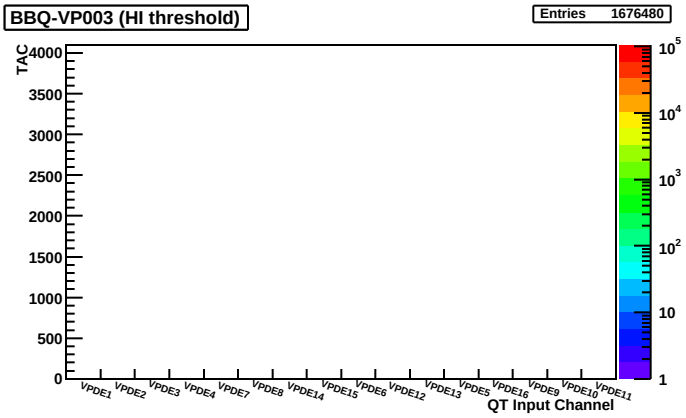

BBQ-VP001 (LO threshold)
Entries 1676480
ADC
QT Input Channel

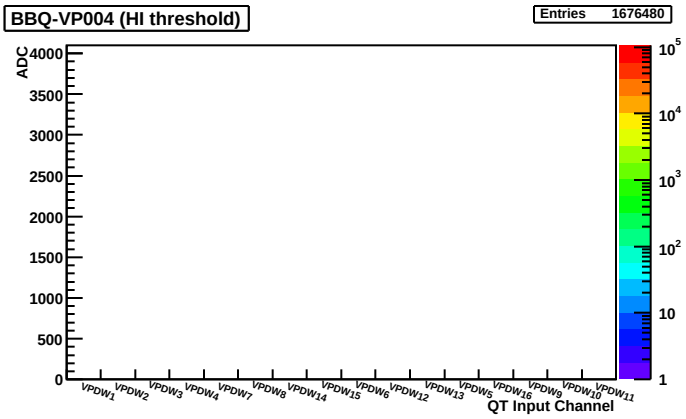

BBQ-VP001 (LO threshold)
Entries 1676480
TAC
QT Input Channel

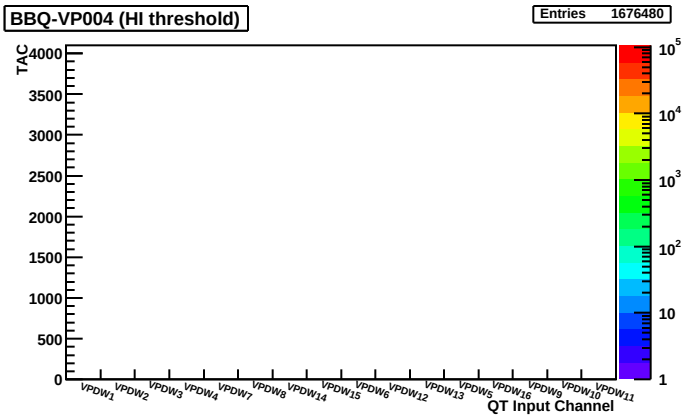

BBQ-VP002 (LO threshold)
Entries 1676480
ADC
QT Input Channel

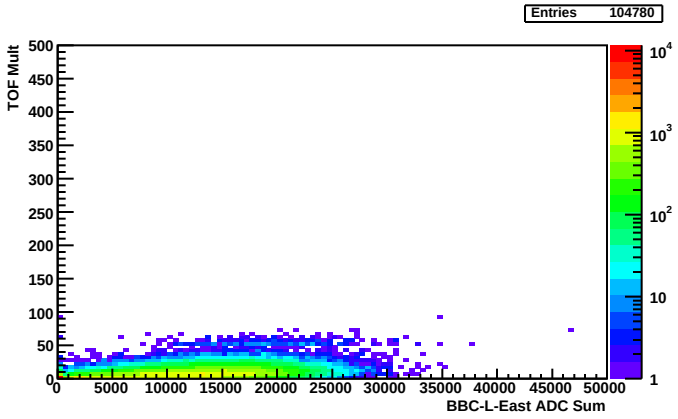

BBQ-VP002 (LO threshold)
Entries 1676480
TAC
QT Input Channel

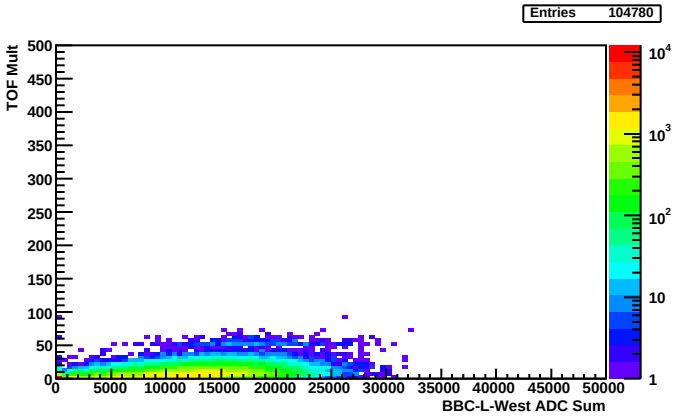

BBQ-VP003 (HI threshold)
Entries 1676480
ADC
QT Input Channel

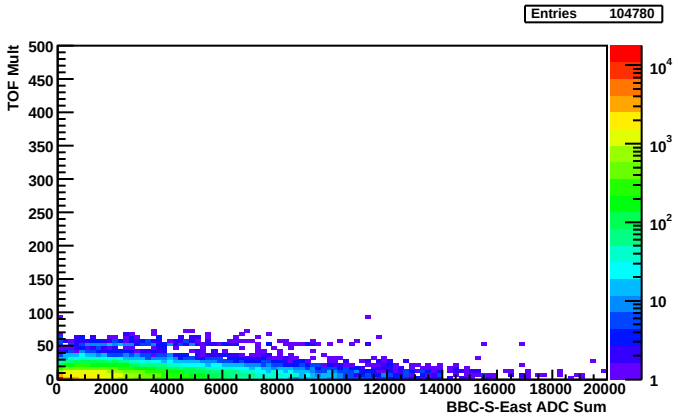

BBQ-VP003 (HI threshold)
Entries 1676480
TAC
QT Input Channel

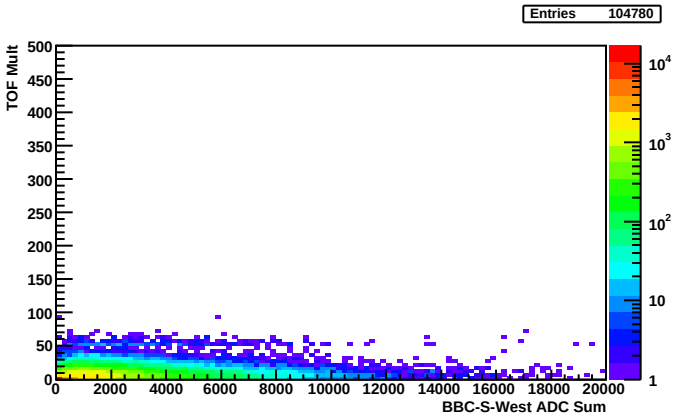

BBQ-VP004 (HI threshold)
Entries 1676480
ADC
QT Input Channel

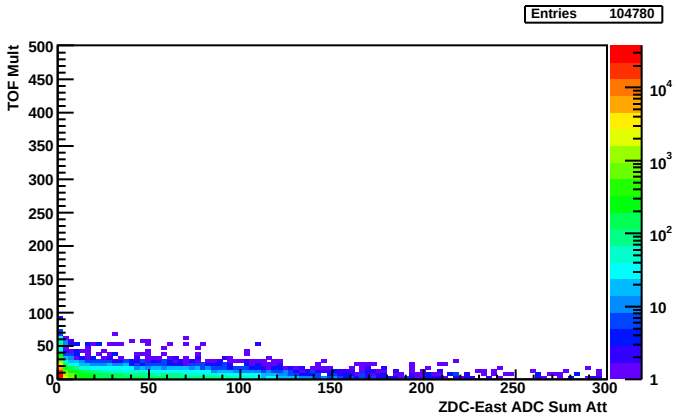

BBQ-VP004 (HI threshold)
Entries 1676480
TAC
QT Input Channel

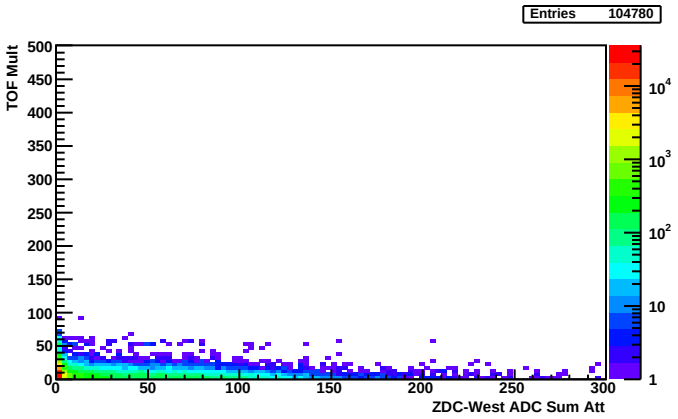

Entries 104780
TOF Mult
BBC-L-East ADC Sum

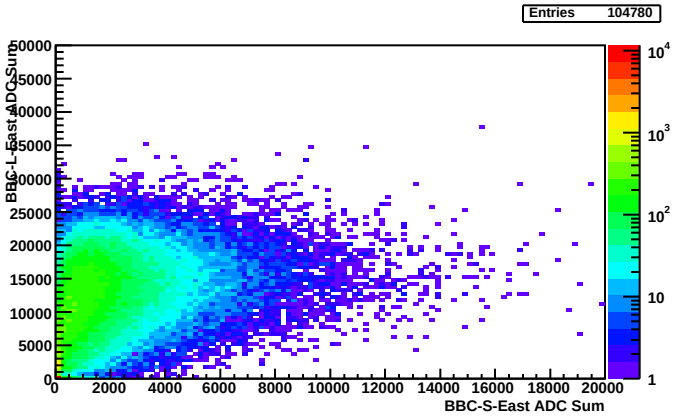

Entries 104780
TOF Mult
BBC-L-West ADC Sum

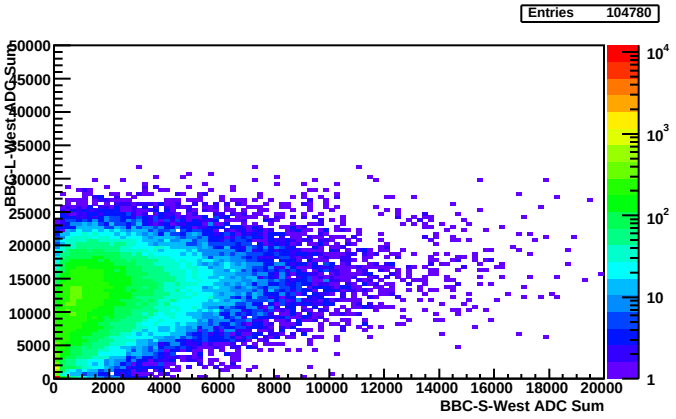

Entries 104780
TOF Mult
BBC-S-East ADC Sum

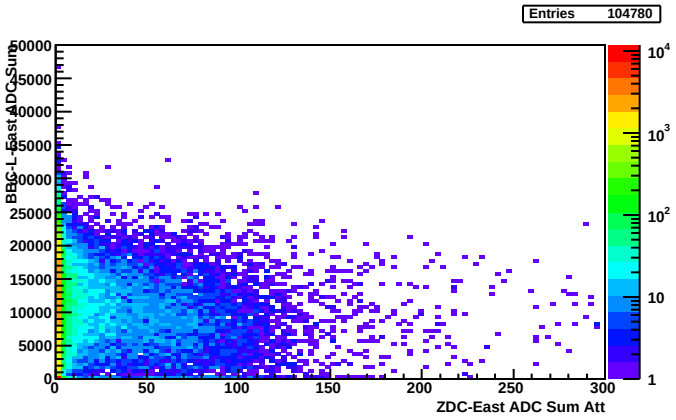

Entries 104780
TOF Mult
BBC-S-West ADC Sum

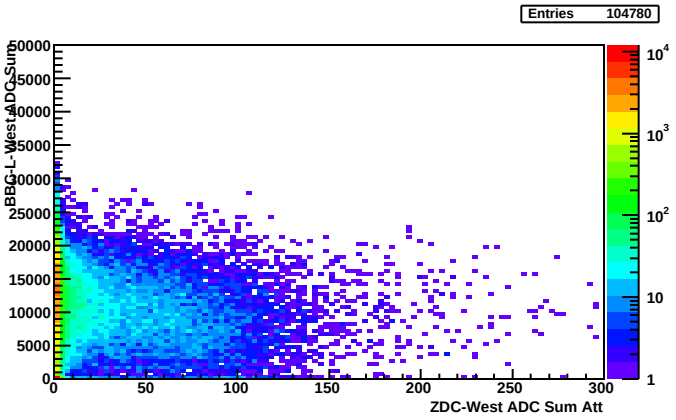

Entries 104780
TOF Mult
ZDC-East ADC Sum Att

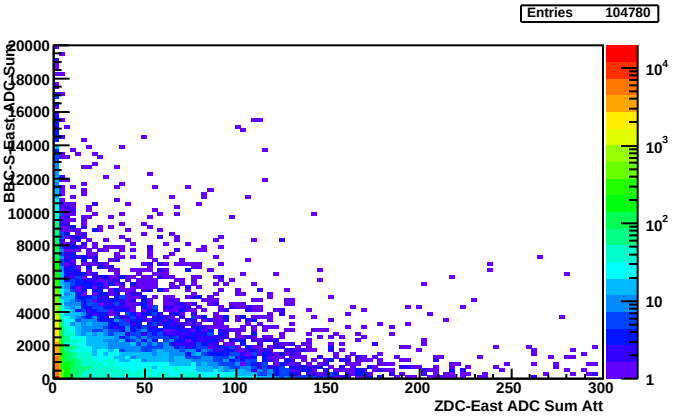

Entries 104780
TOF Mult
ZDC-West ADC Sum Att

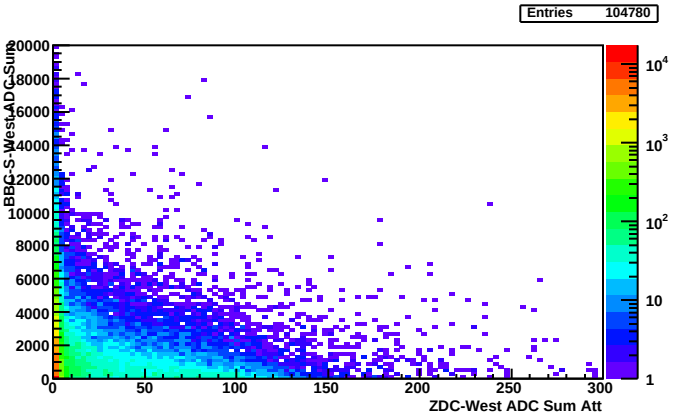

Entries 104780
BBC-L-East ADC Sum
BBC-S-East ADC Sum

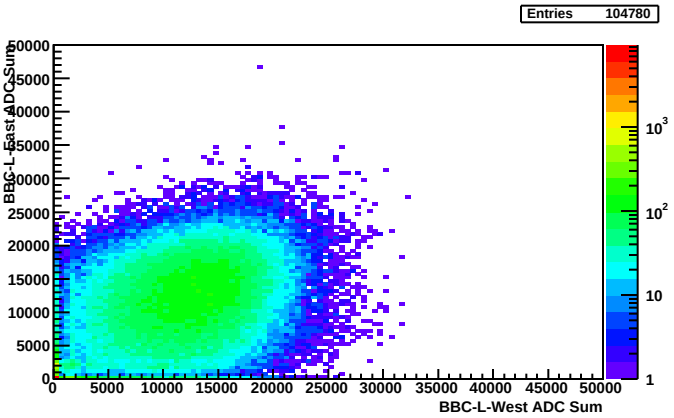

Entries 104780
BBC-L-West ADC Sum
BBC-S-West ADC Sum

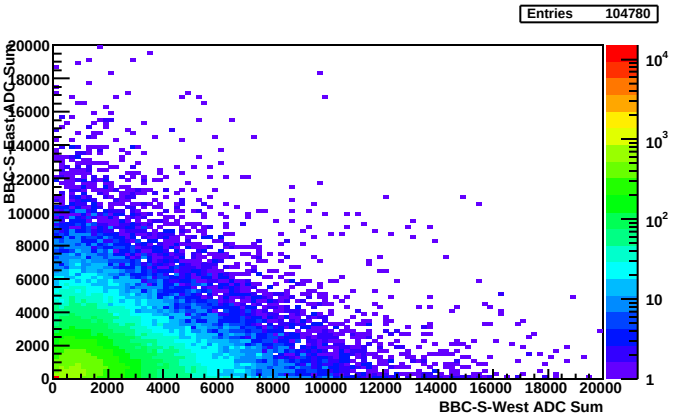

Entries 104780
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

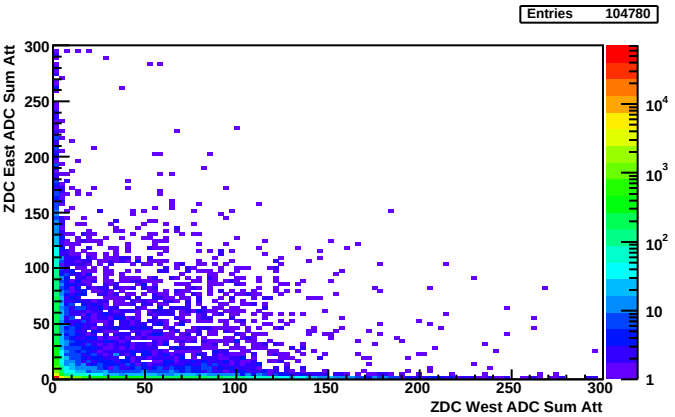

Entries 104780
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

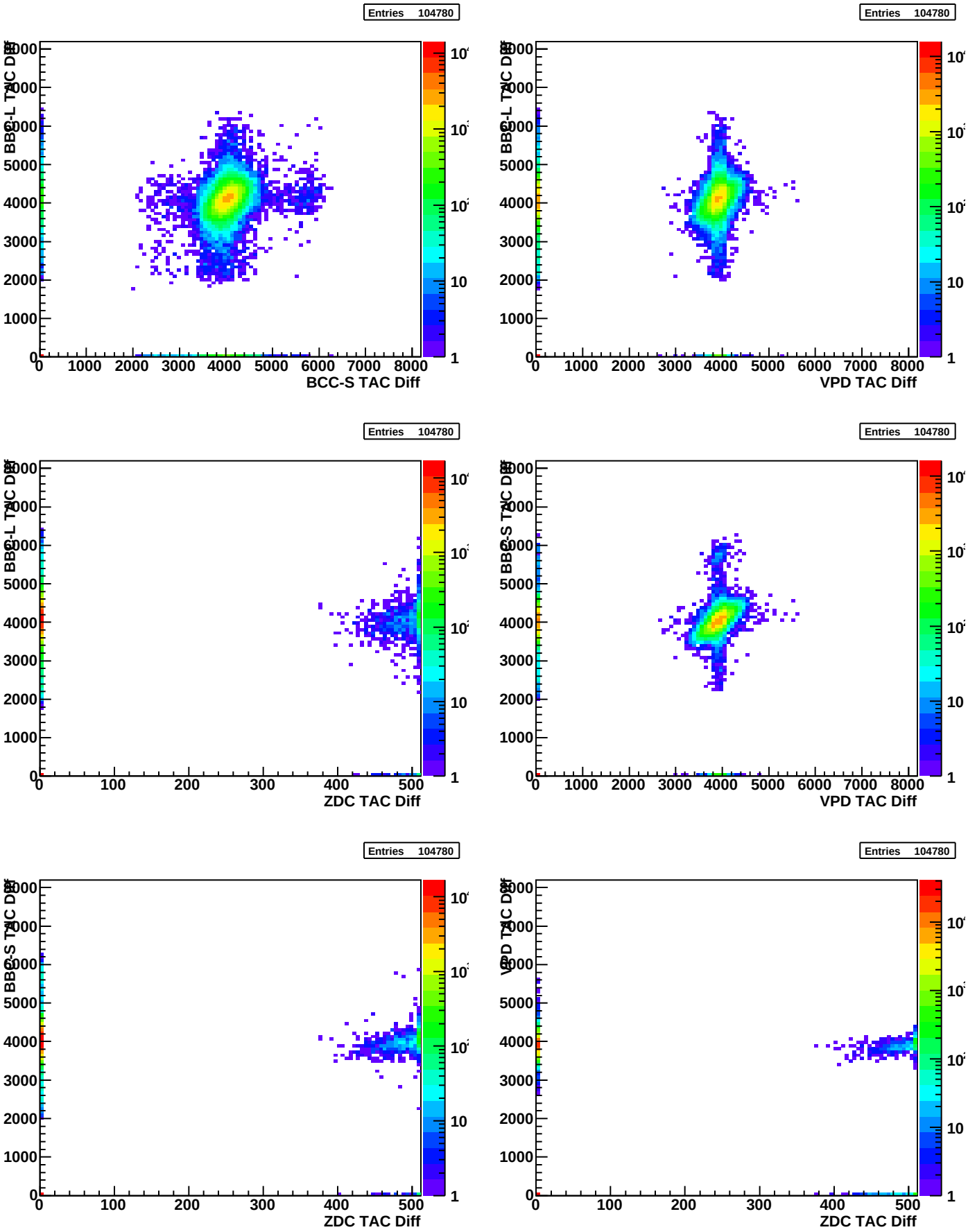

Entries 104780
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

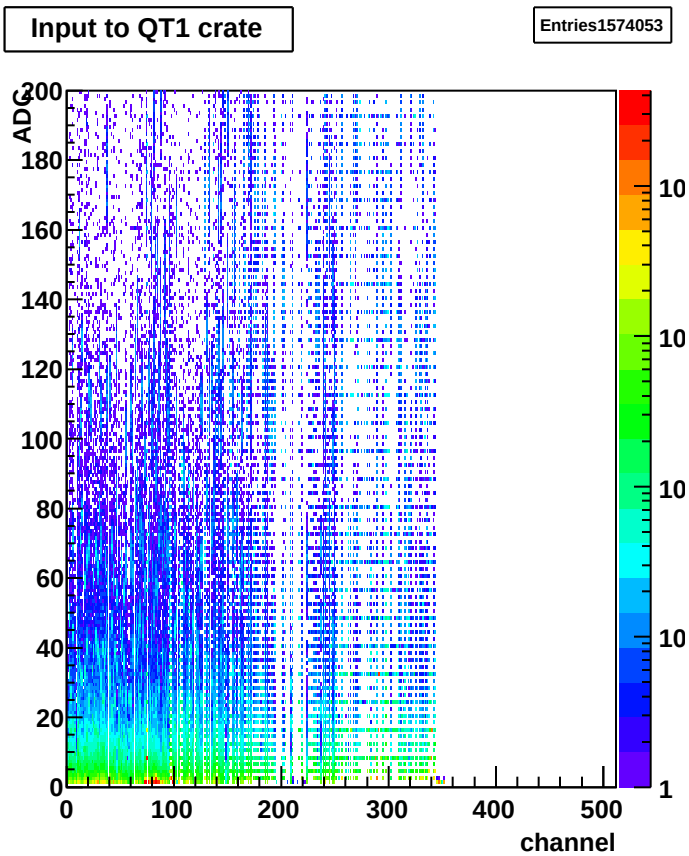

Entries 104780
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

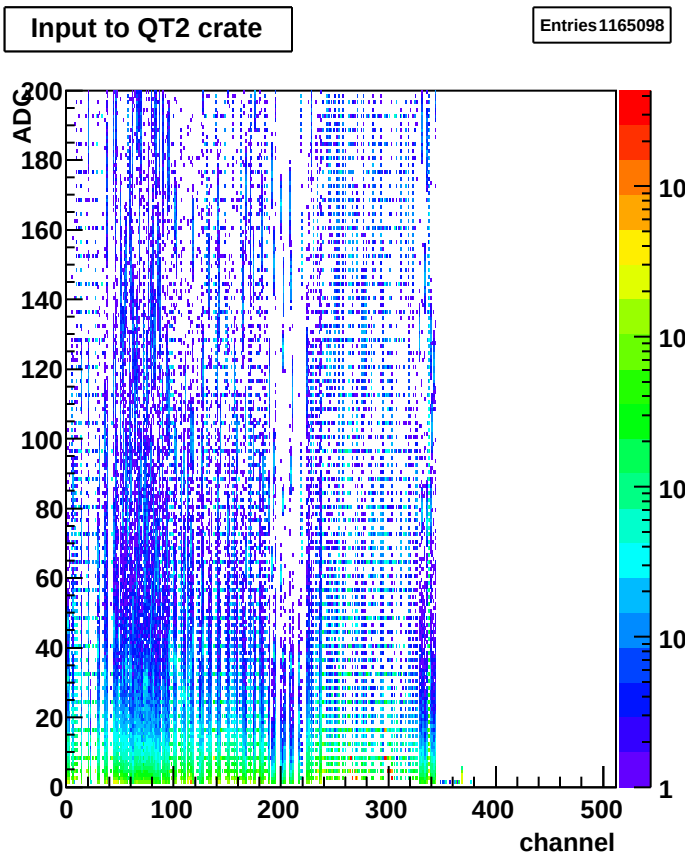

Entries 104780
BBC-L-East ADC Sum
BBC-L-West ADC Sum

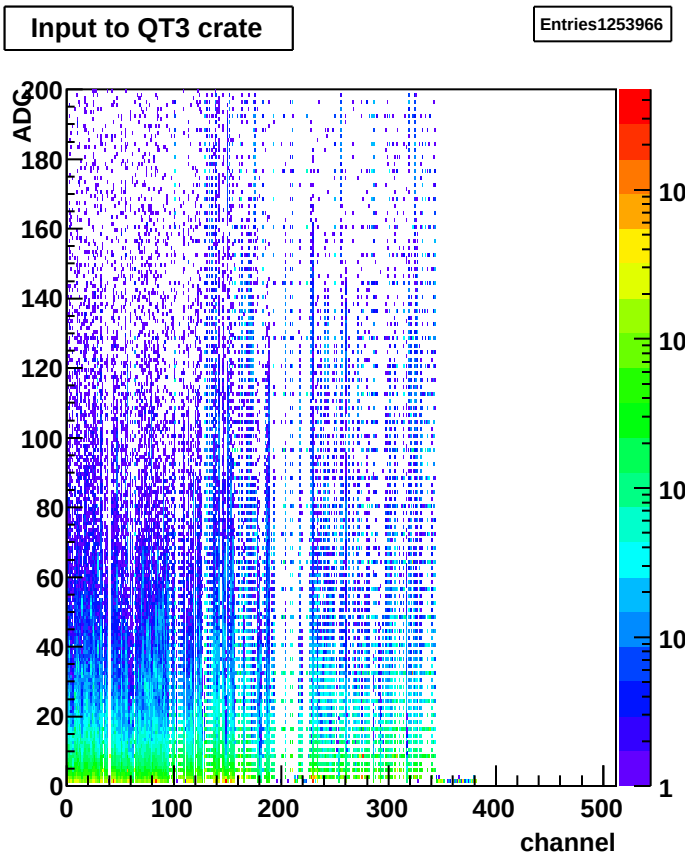

Entries 104780
BBC-S-East ADC Sum
BBC-S-West ADC Sum

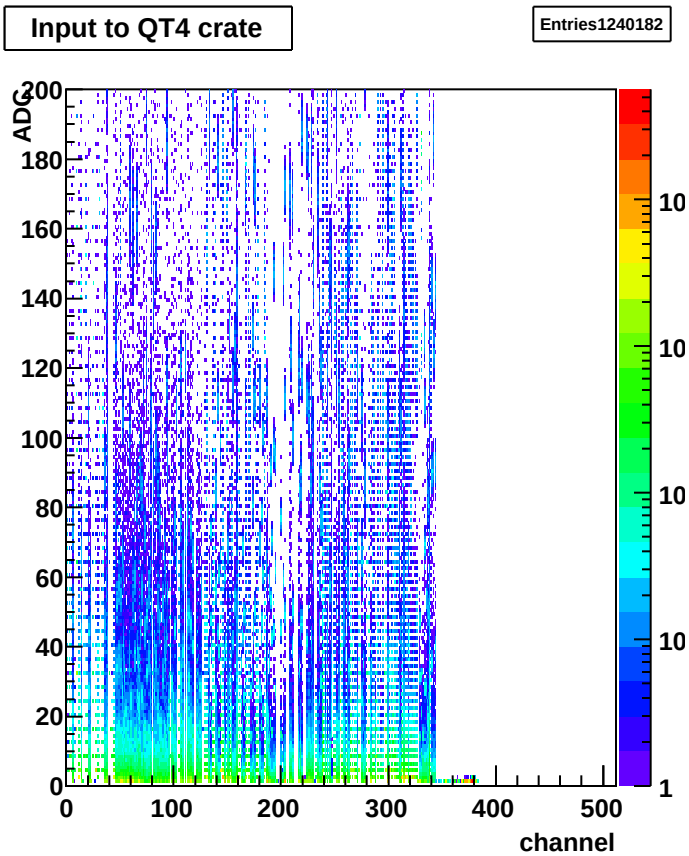

Entries 104780
ZDC East ADC Sum Att
ZDC West ADC Sum Att

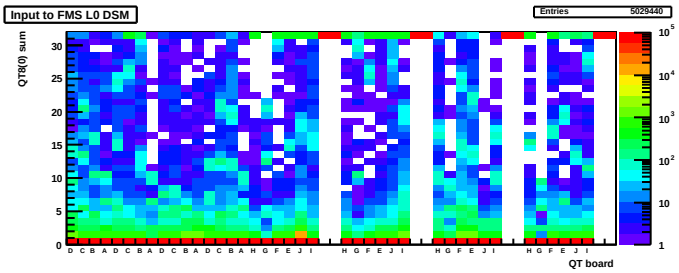

Entries 104780
BBC-L TAC Diff
BCC-S TAC Diff
Entries 104780
BBC-L TAC Diff
VPD TAC Diff
Entries 104780
BBC-L TAC Diff
ZDC TAC Diff
Entries 104780
BBC-S TAC Diff
VPD TAC Diff
Entries 104780
BBC-S TAC Diff
ZDC TAC Diff
Entries 104780
VPD TAC Diff
ZDC TAC Diff

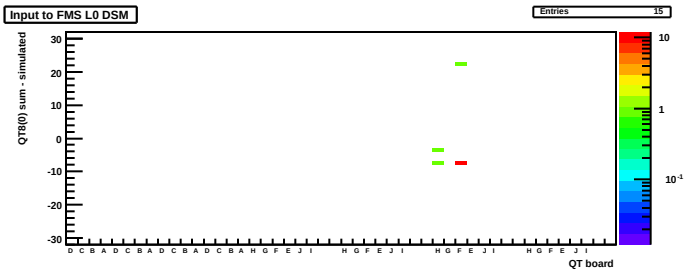

Input to QT1 crate
Entries1574053
ADC
channel

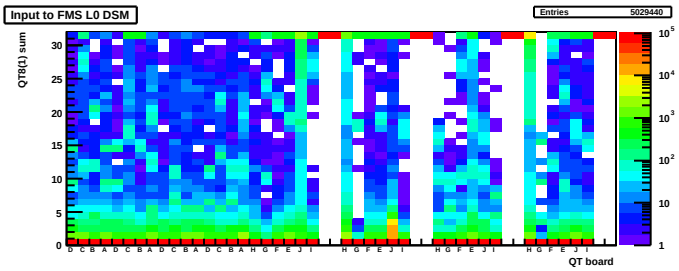

Input to QT2 crate
Entries1165098
ADC
channel

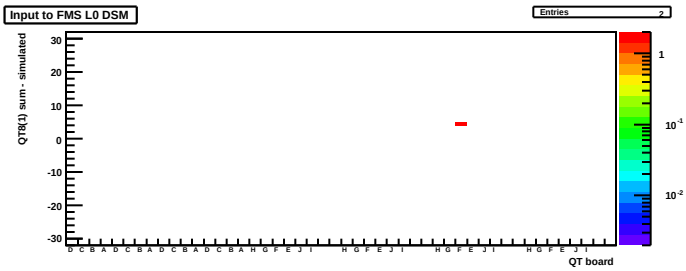

Input to QT3 crate
Entries1253966
ADC
channel

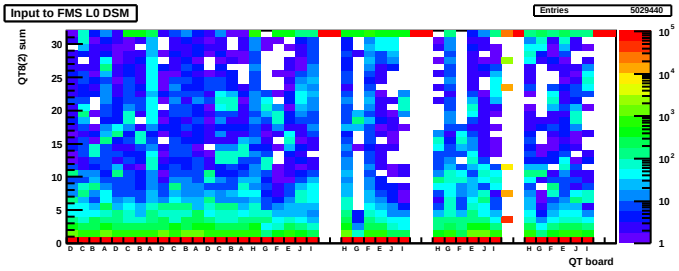

Input to QT4 crate
Entries1240182
ADC
channel

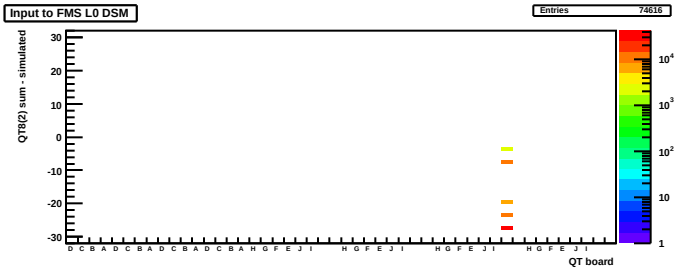

Input to FMS L0 DSM
Entries 5029440
QT8(0) sum
QT board

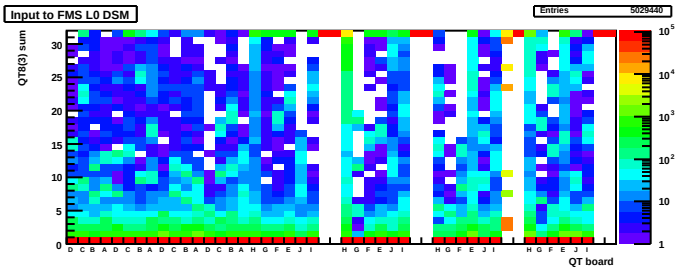

Input to FMS L0 DSM
Entries 15
QT8(0) sum - simulated
QT board

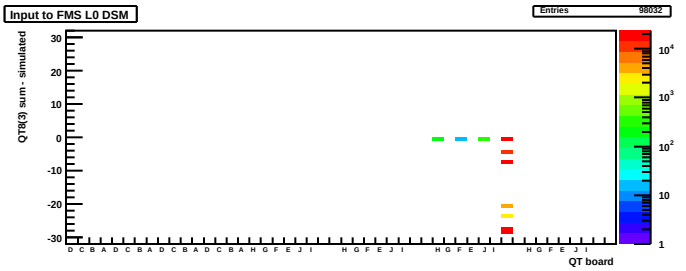

Input to FMS L0 DSM
Entries 5029440
QT8(1) sum
QT board

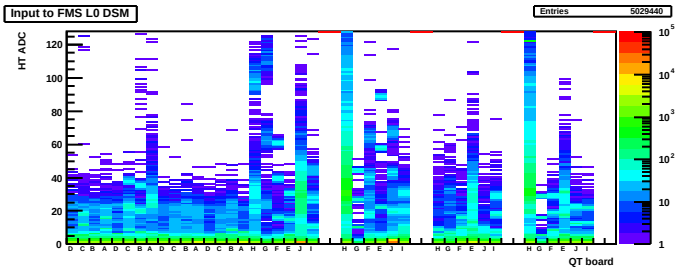

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

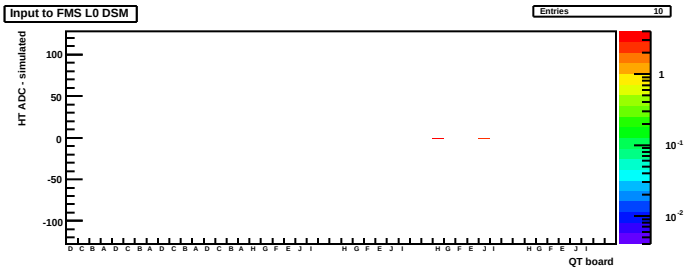

Input to FMS L0 DSM
Entries 5029440
QT8(2) sum
QT board

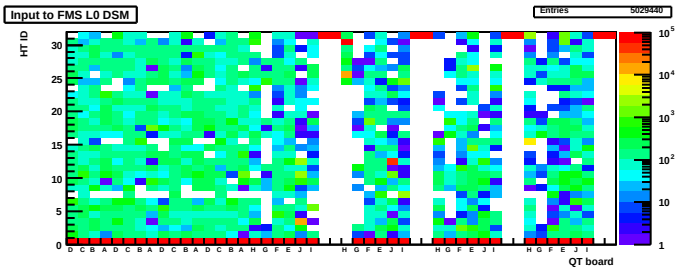

Input to FMS L0 DSM
Entries 74616
QT8(2) sum - simulated
QT board

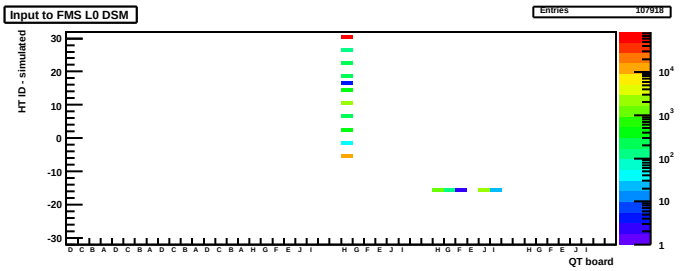

Input to FMS L0 DSM
Entries 5029440
QT8(3) sum
QT board

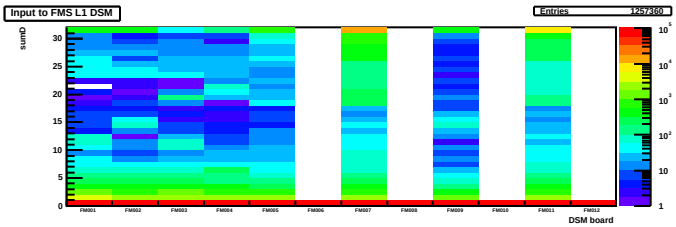

Input to FMS L0 DSM
Entries 98032
QT8(3) sum - simulated
QT board

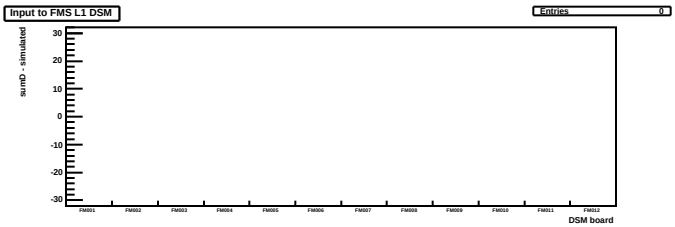

Input to FMS L0 DSM
Entries 5029440
HT ADC
QT board

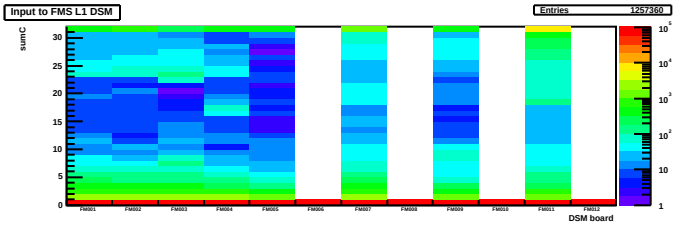

Input to FMS L0 DSM
Entries 10
HT ADC - simulated
QT board

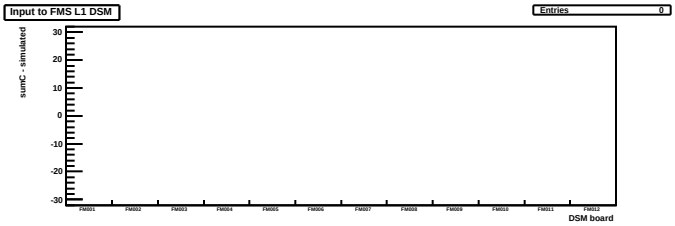

Input to FMS L0 DSM
Entries 5029440
HT ID
QT board

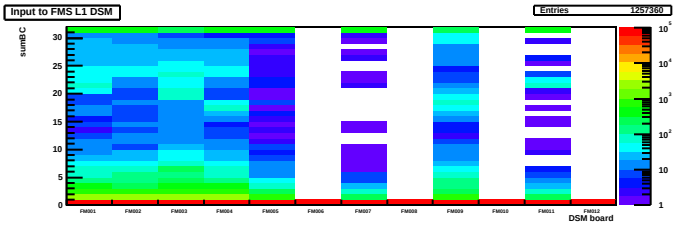

Input to FMS L0 DSM
Entries 107918
HT ID - simulated
QT board

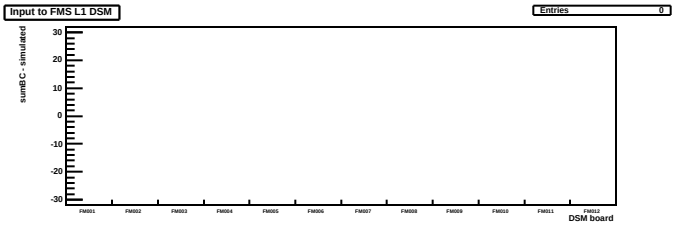

Input to FMS L1 DSM
Entries 1257360
sumD
DSM board

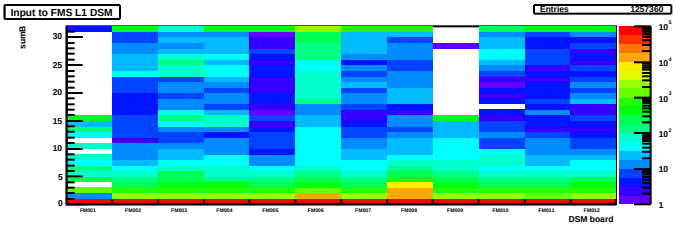

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

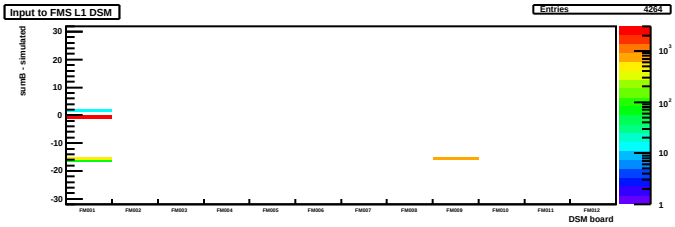

Input to FMS L1 DSM
Entries 1257360
sumC
DSM board

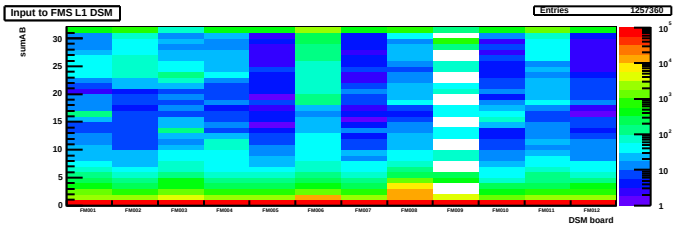

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

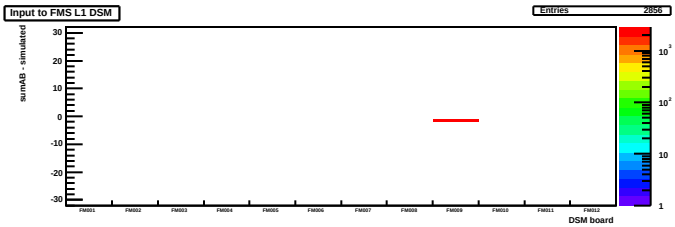

Input to FMS L1 DSM
Entries 1257360
sumBC
DSM board

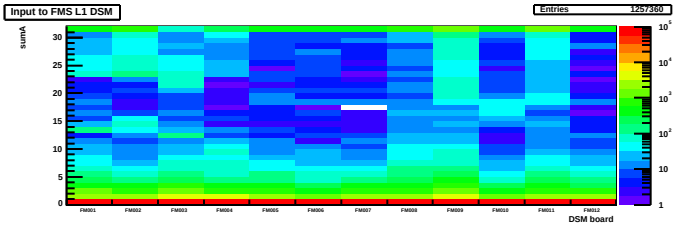

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

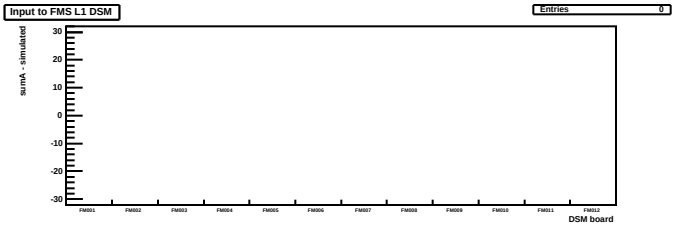

Input to FMS L1 DSM
Entries 1257360
sumB
DSM board

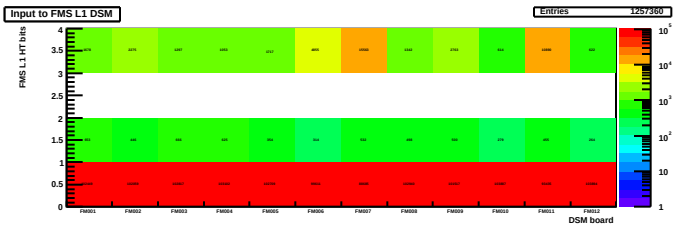

Input to FMS L1 DSM
Entries 4264
sumB - simulated
DSM board

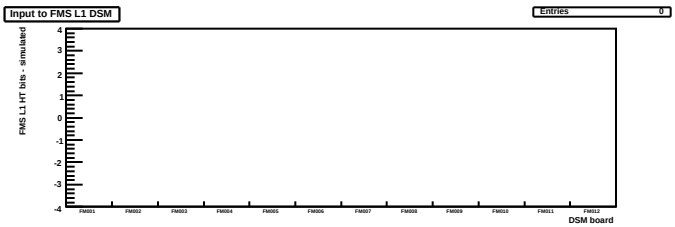

Input to FMS L1 DSM
Entries 1257360
sumAB
DSM board

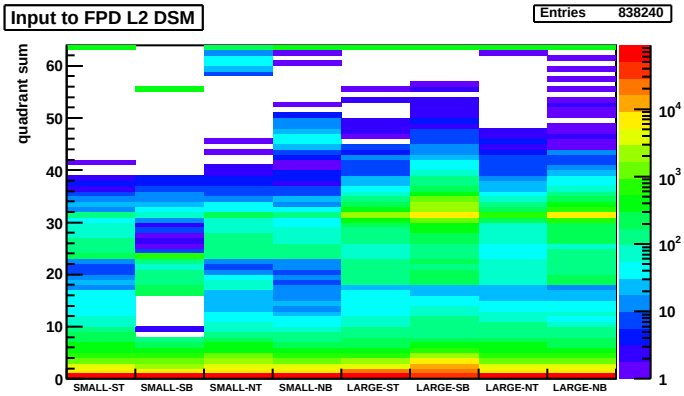

Input to FMS L1 DSM
Entries 2856
sumAB - simulated
DSM board

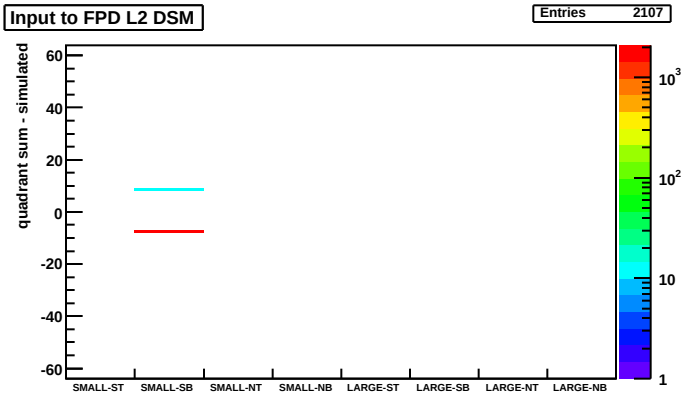

Input to FMS L1 DSM
Entries 1257360
sumA
DSM board

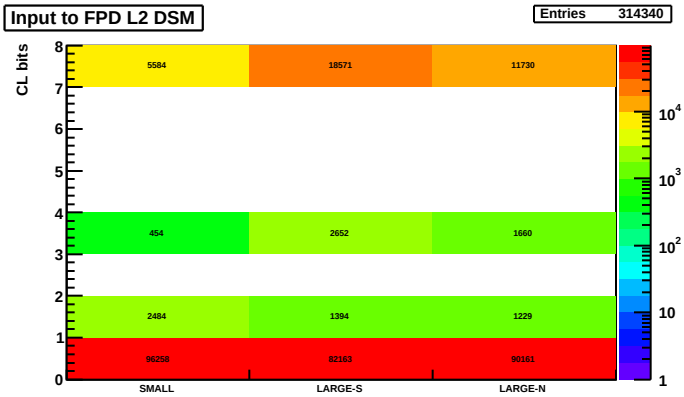

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

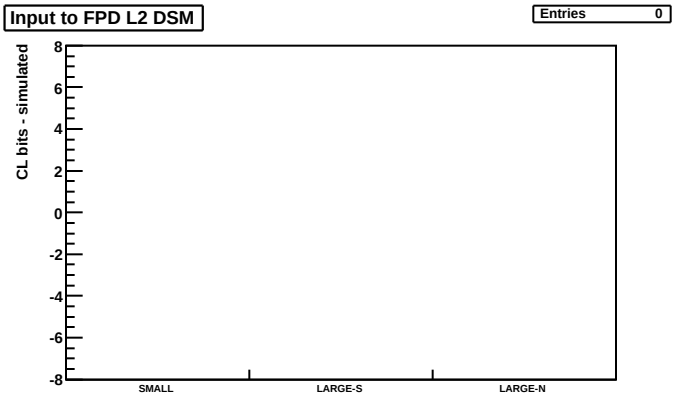

Input to FMS L1 DSM
Entries 1257360
FMS L1 HT bits
DSM board

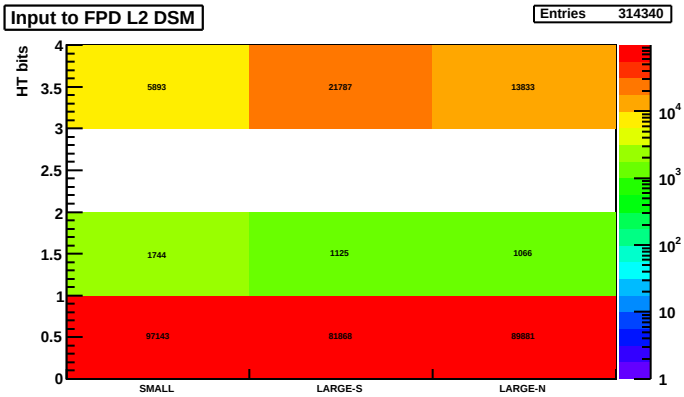

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

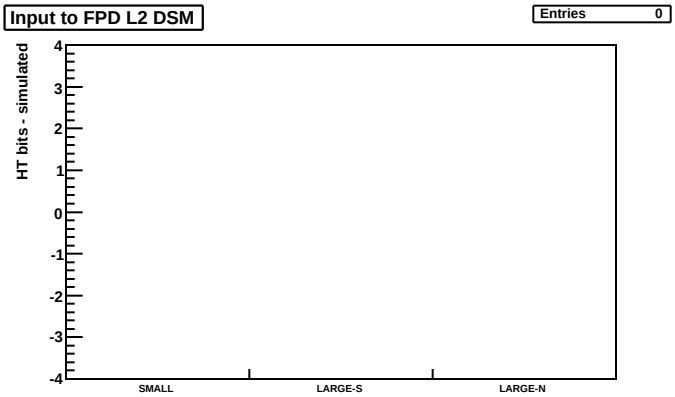

Input to FPD L2 DSM
Entries 838240
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

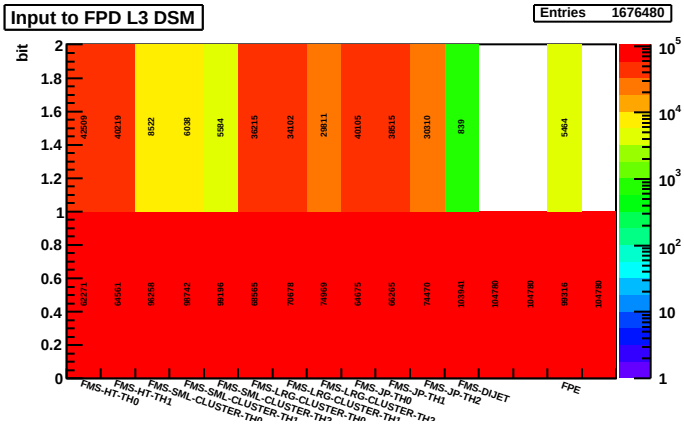

Input to FPD L2 DSM
Entries 2107
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

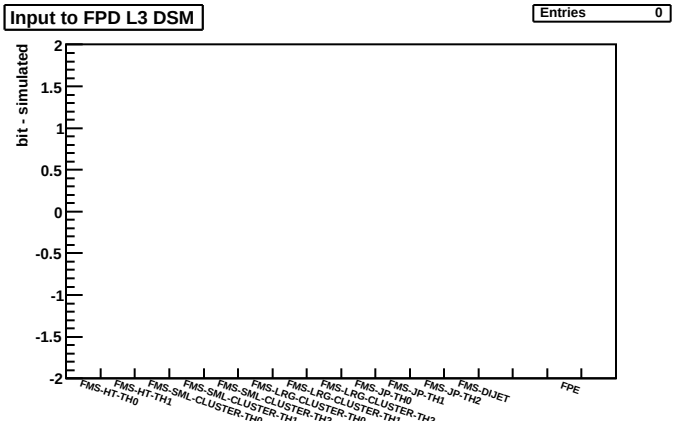

Input to FPD L2 DSM
Entries 314340
CL bits
5584
18571
11730
454
2652
1660
2484
1394
1229
96258
82163
90161
SMALL
LARGE-S
LARGE-N

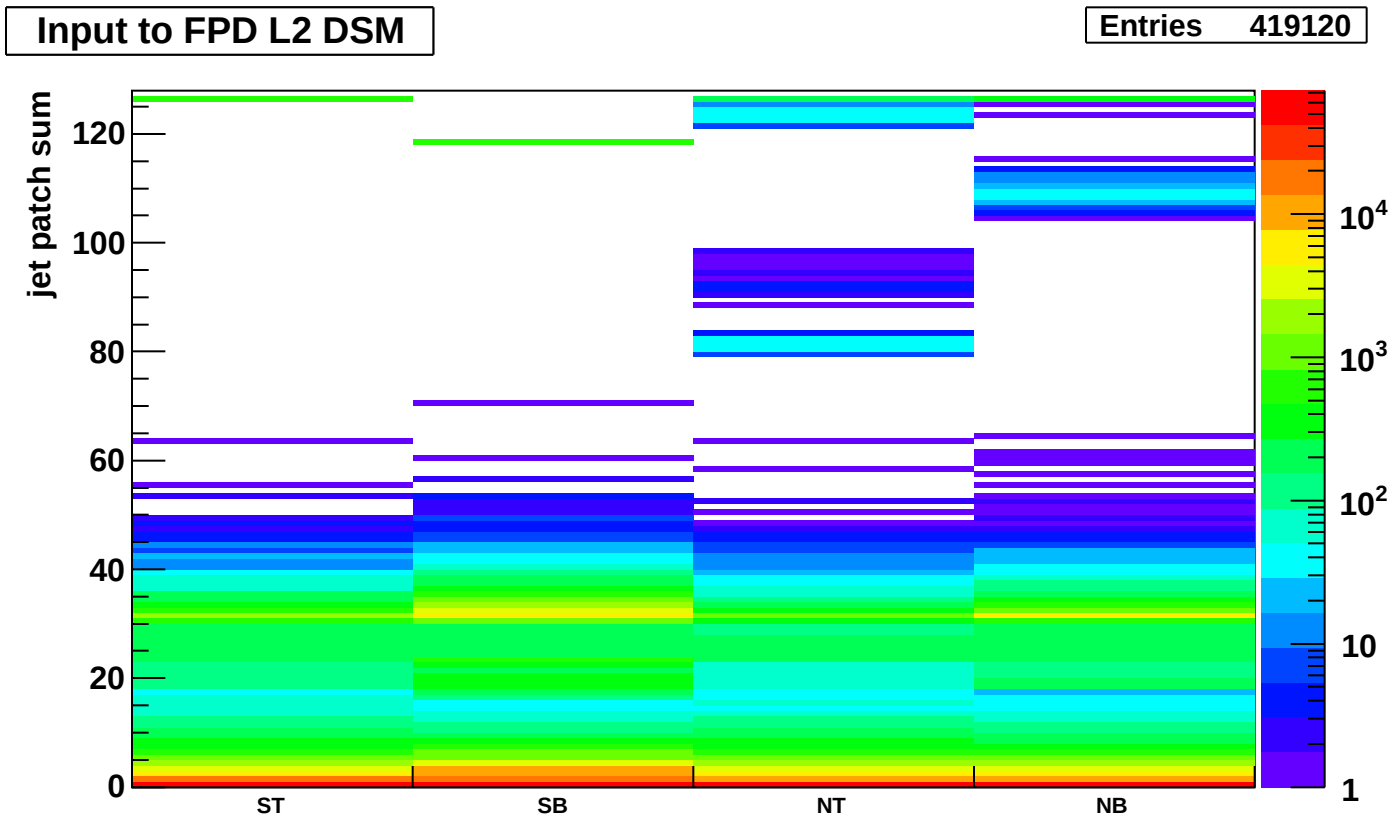

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

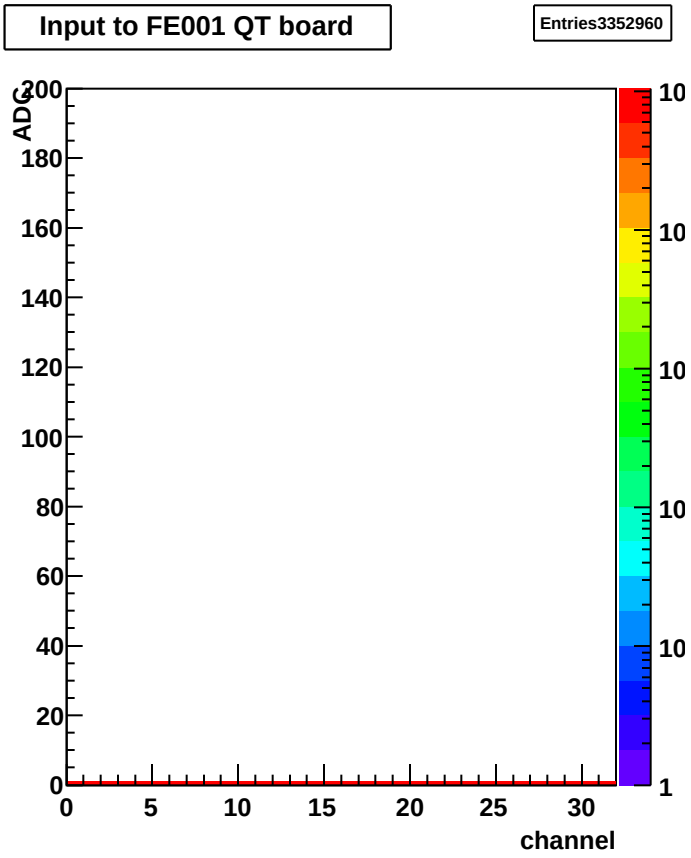

Input to FPD L2 DSM
Entries 314340
HT bits
5893
21787
13833
1744
1125
1066
97143
81868
89881
SMALL
LARGE-S
LARGE-N

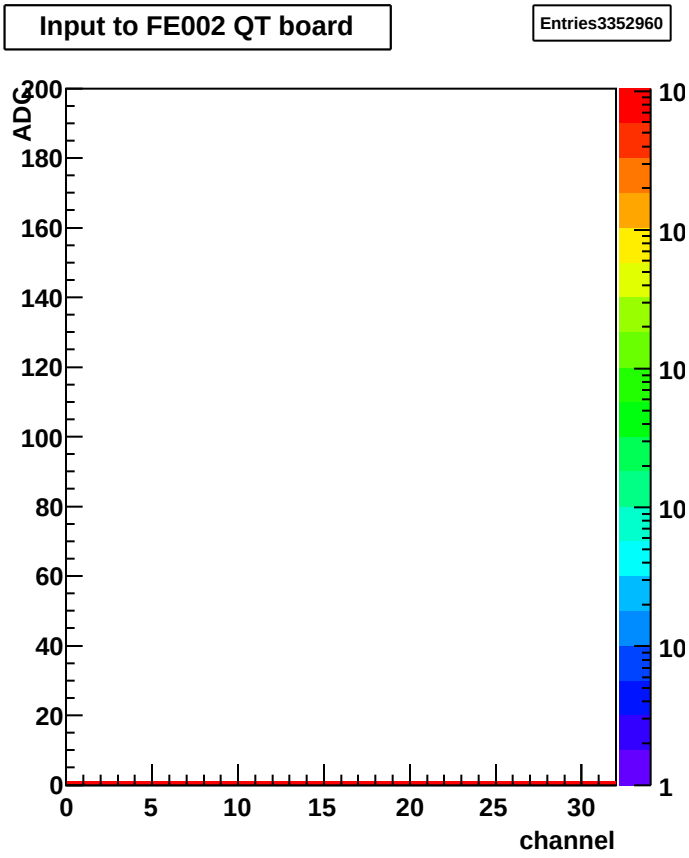

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

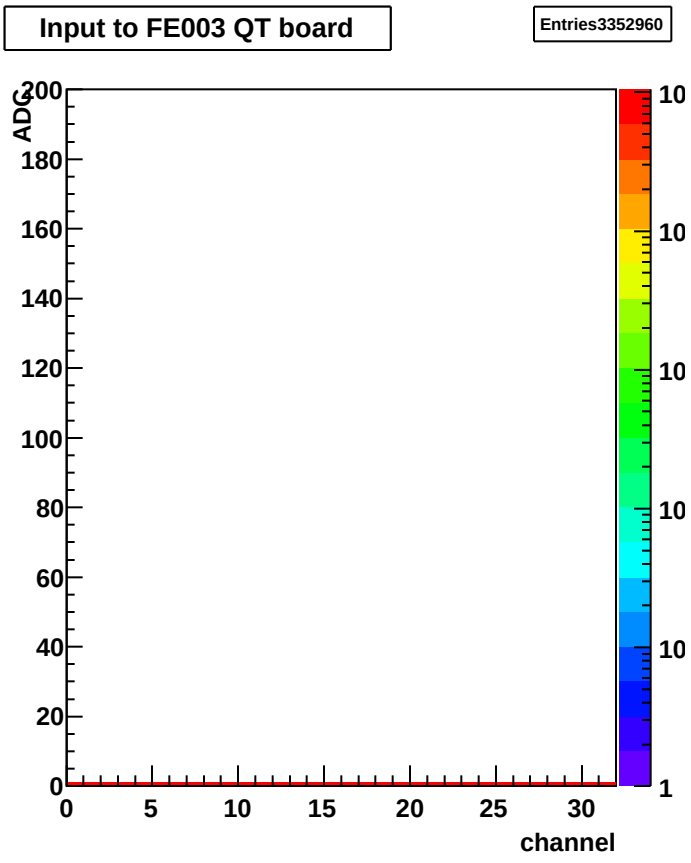

Input to FPD L3 DSM
Entries 1676480
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

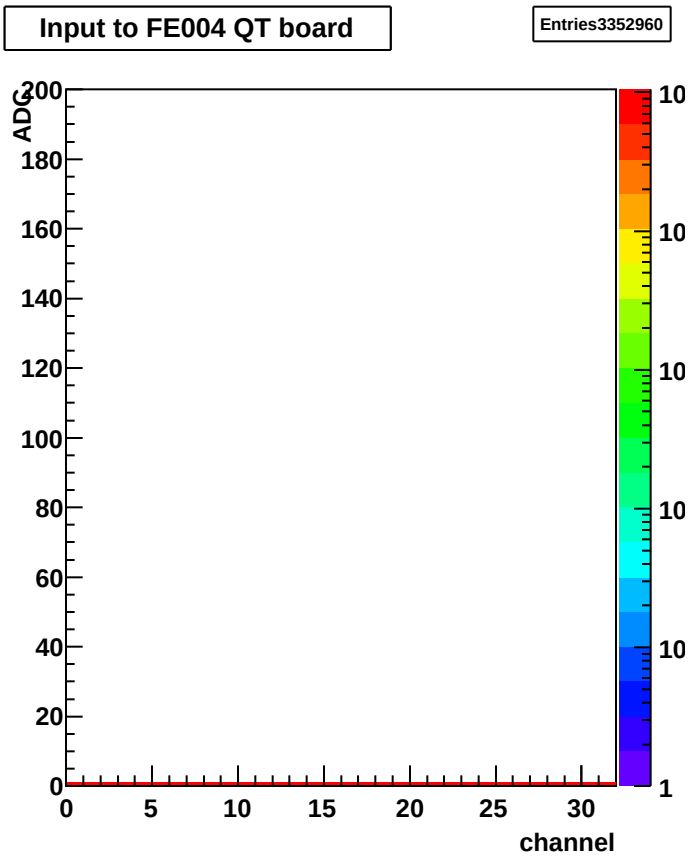

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

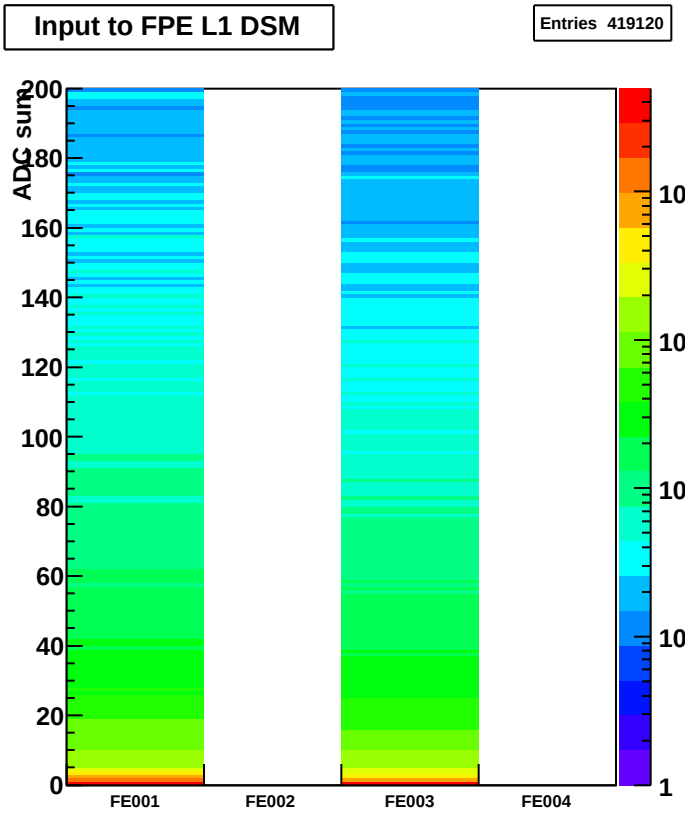

Input to FPD L2 DSM
Entries 419120
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

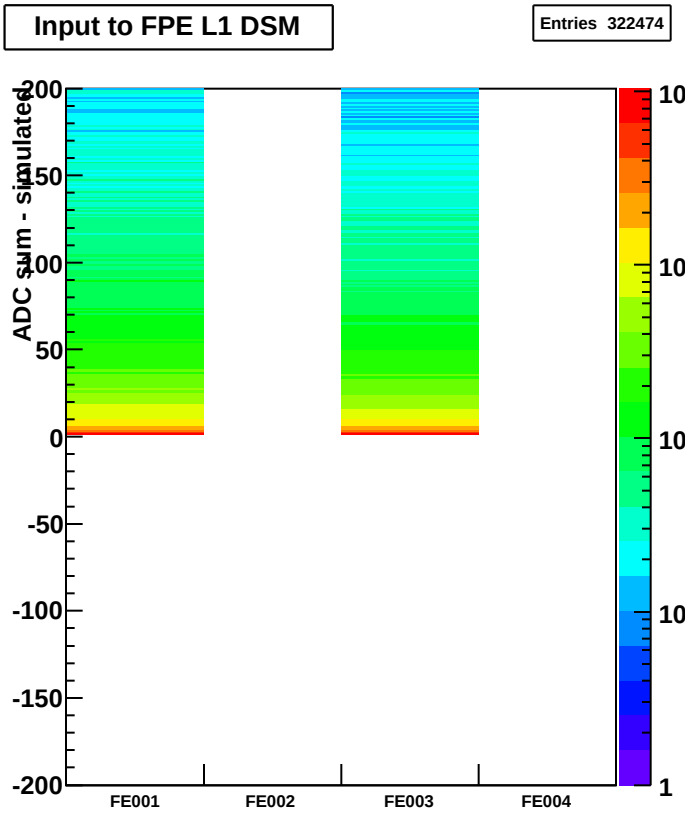

Input to FE001 QT board
Entries3352960
ADC
channel

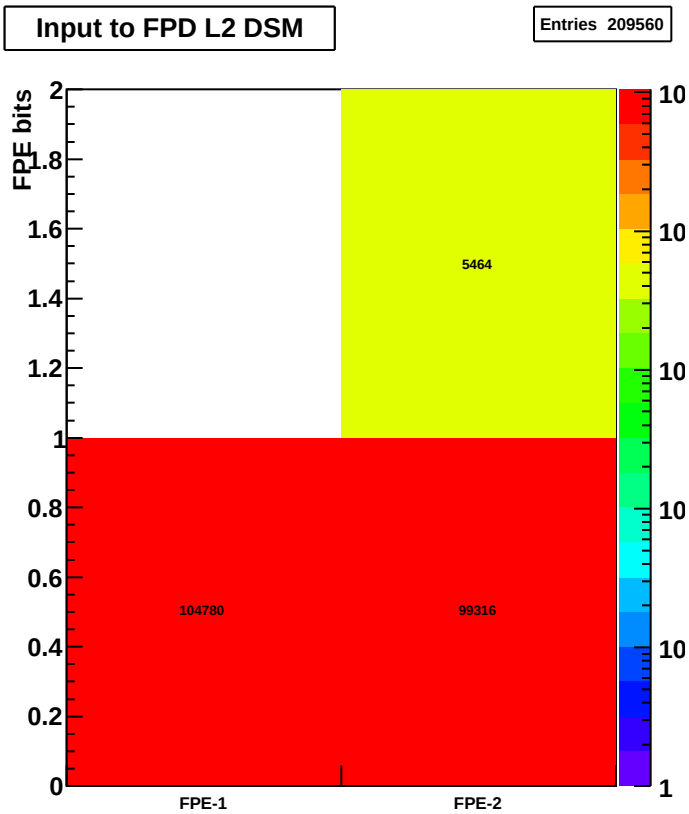

Input to FE002 QT board
Entries3352960
ADC
channel

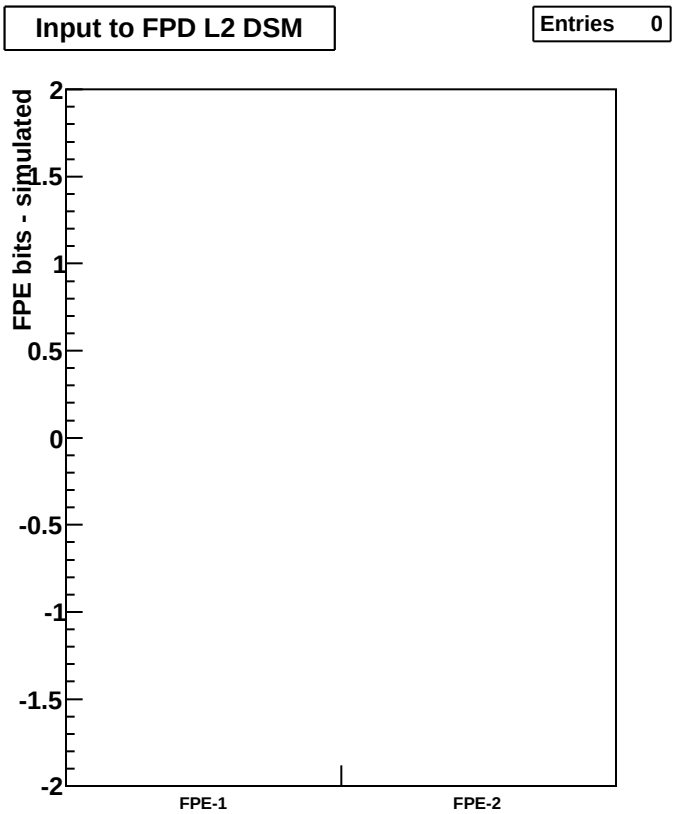

Input to FE003 QT board
Entries3352960
ADC
channel


Input to FE004 QT board
Entries3352960
ADC
channel


Input to FPE L1 DSM
Entries 419120
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 322474
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 209560
FPE bits
5464
104780
99316
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2