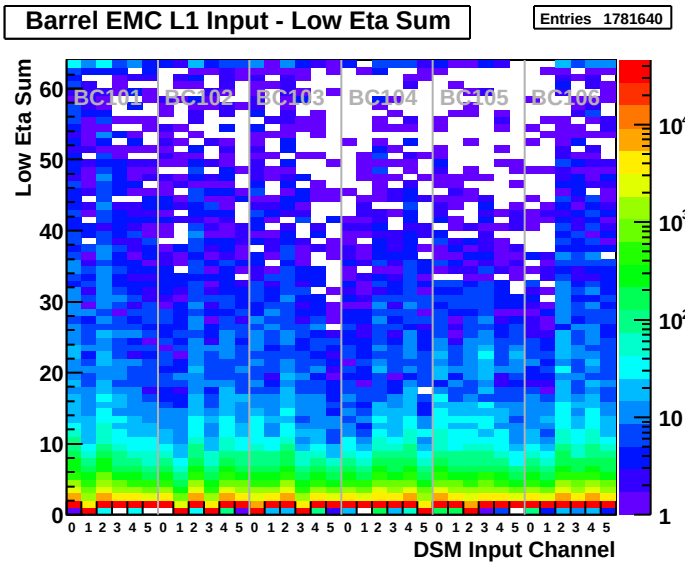
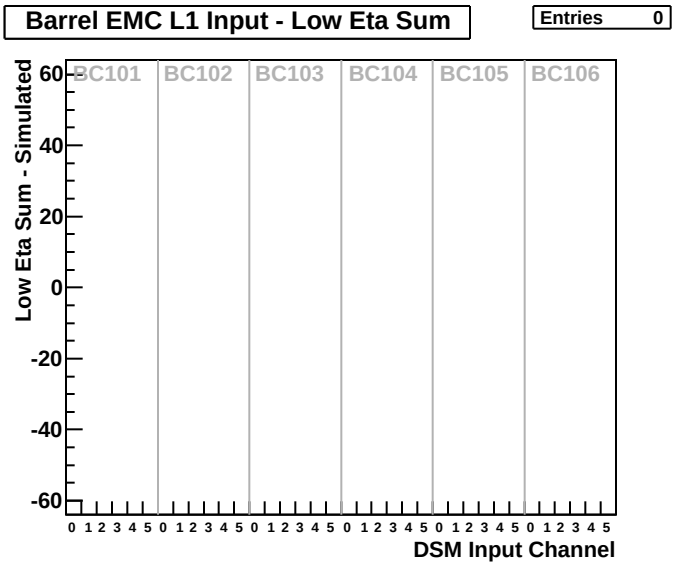
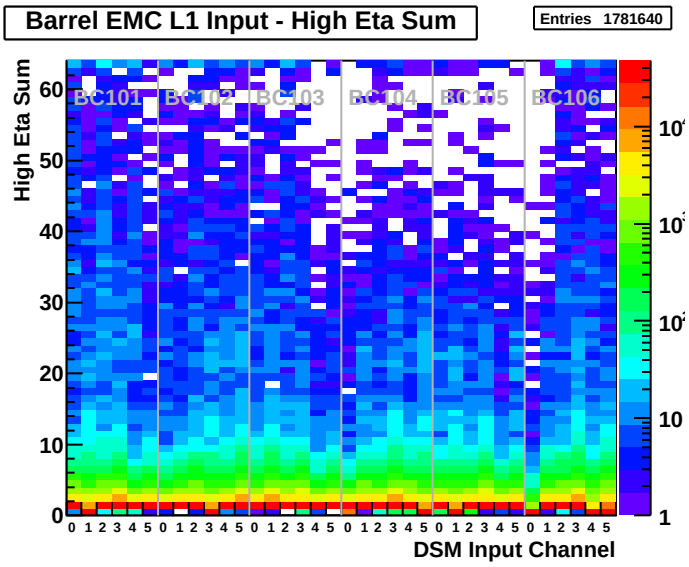
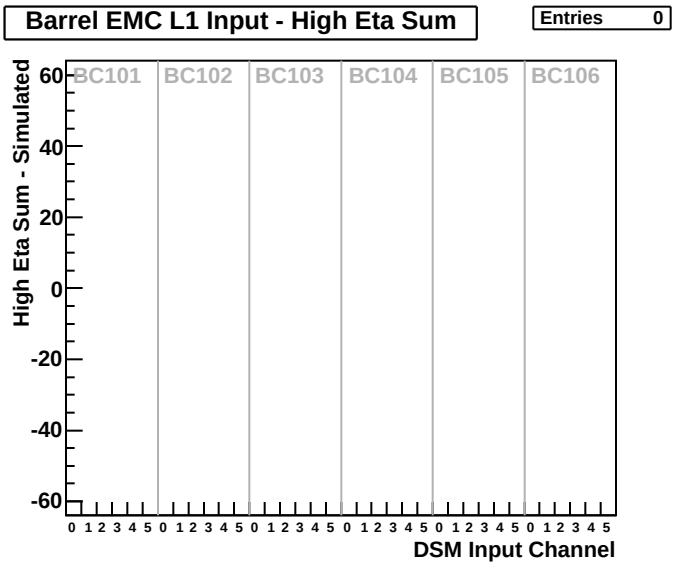
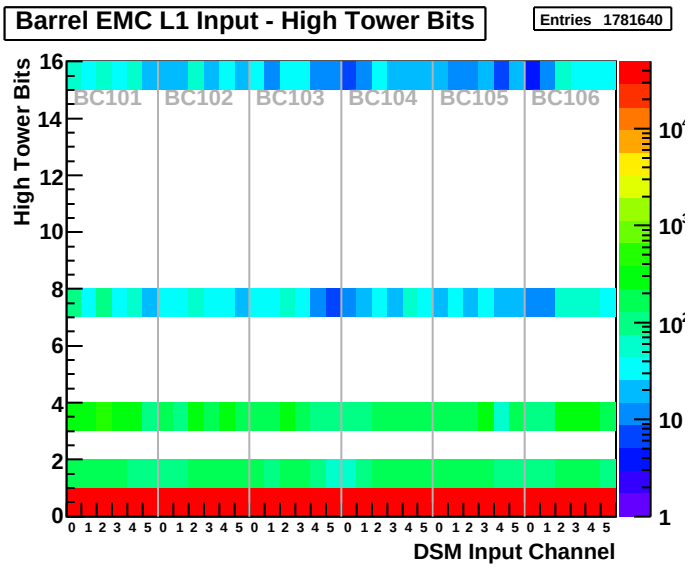
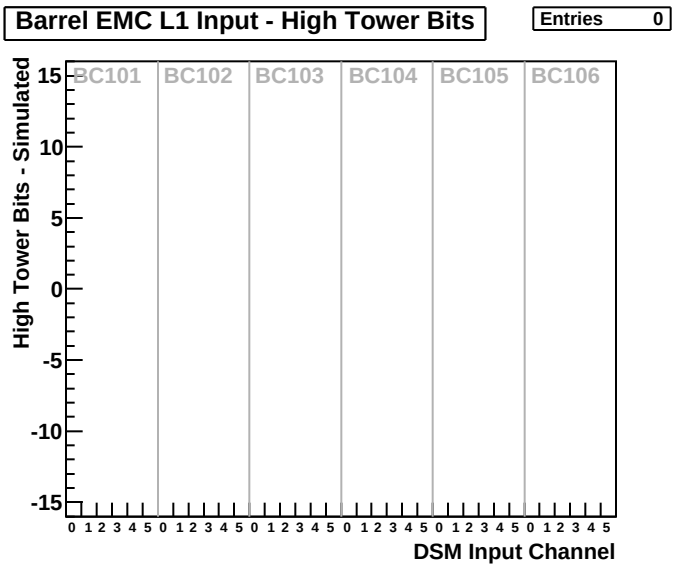

Barrel EMC L1 Input - Low Eta Sum
Entries 1781640
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

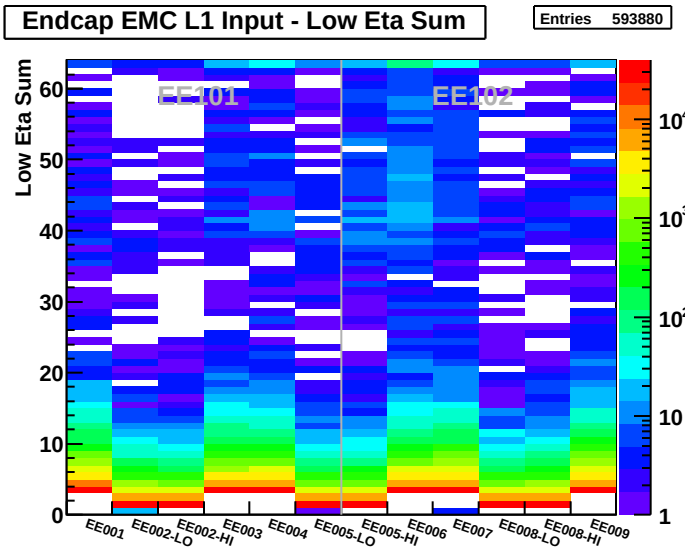

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

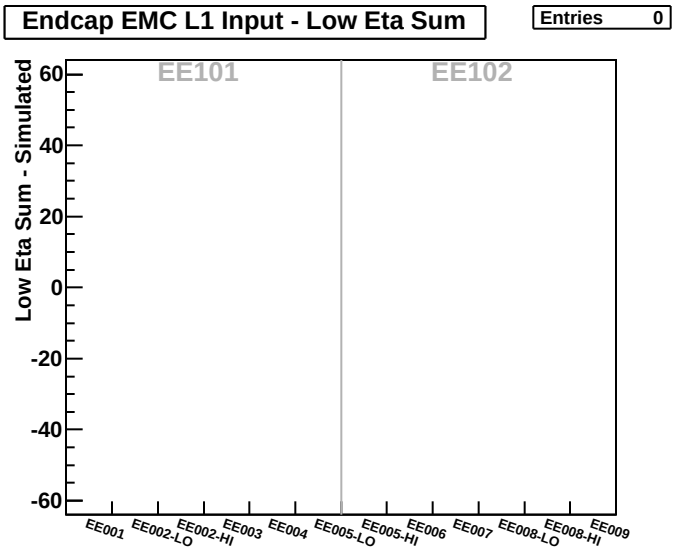

Barrel EMC L1 Input - High Eta Sum
Entries 1781640
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

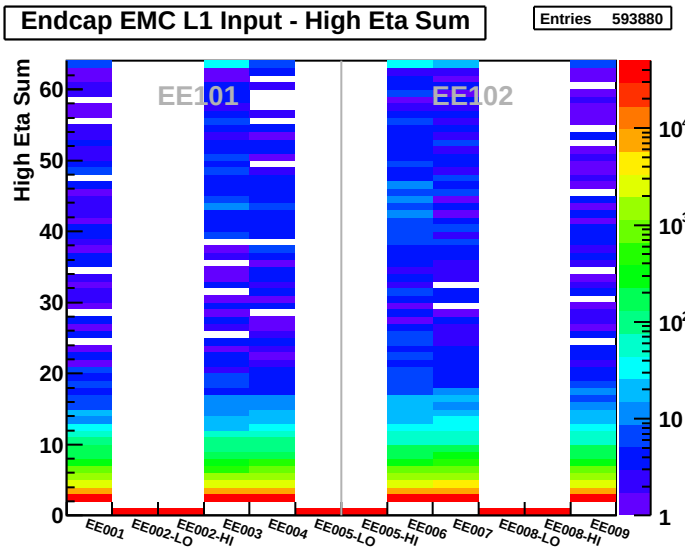

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

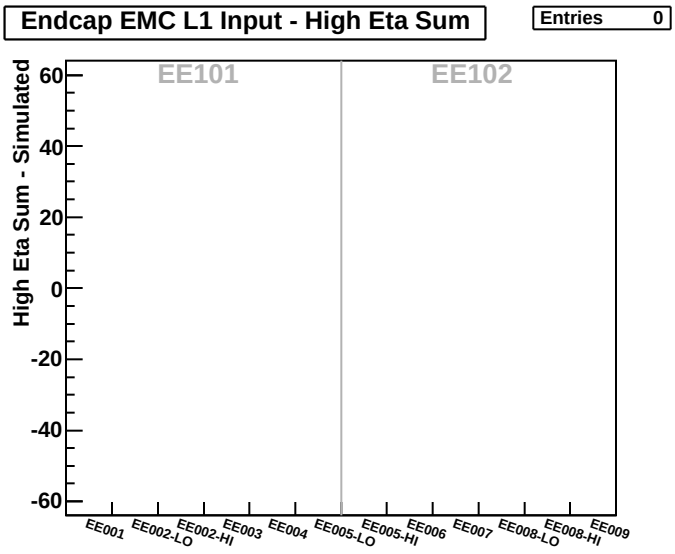

Barrel EMC L1 Input - High Tower Bits
Entries 1781640
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

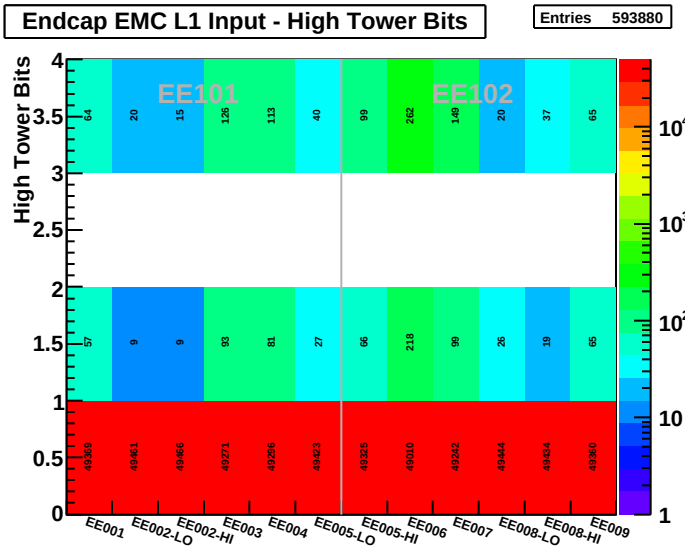

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

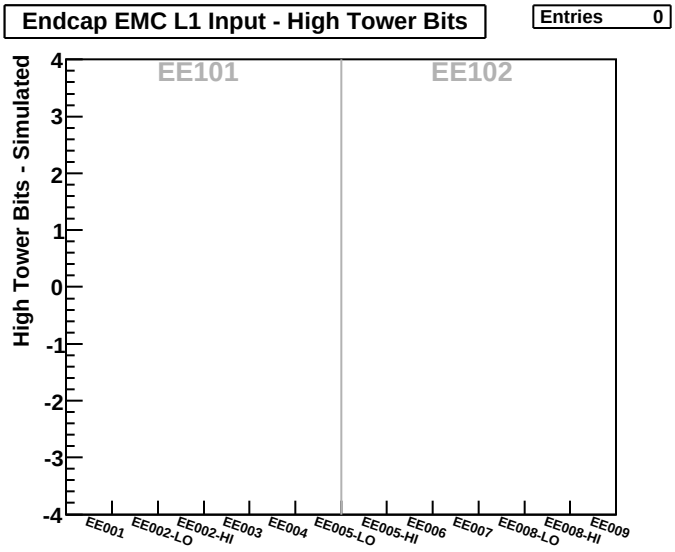

Endcap EMC L1 Input - Low Eta Sum
Entries 593880
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

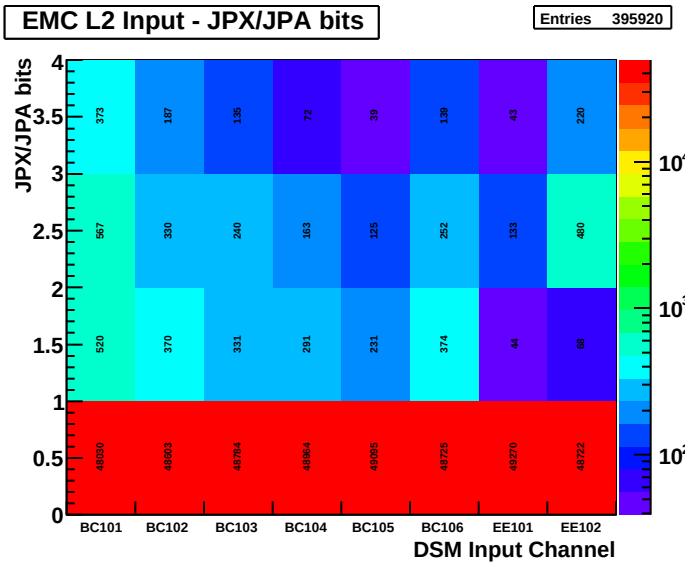

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

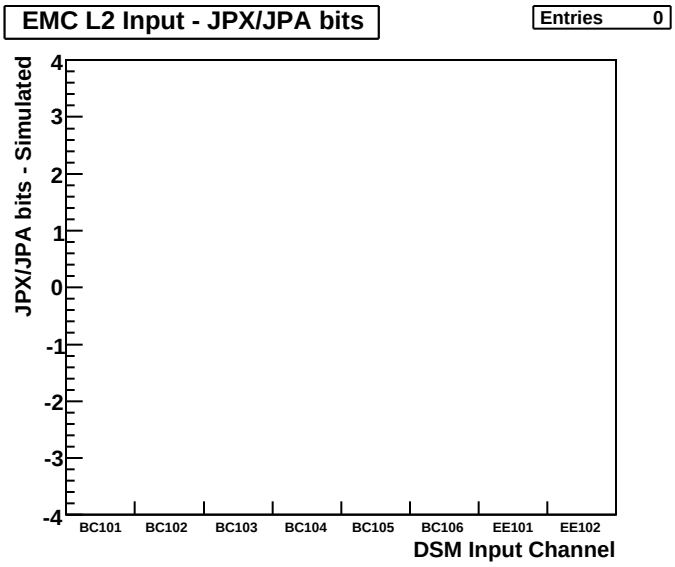

Endcap EMC L1 Input - High Eta Sum
Entries 593880
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

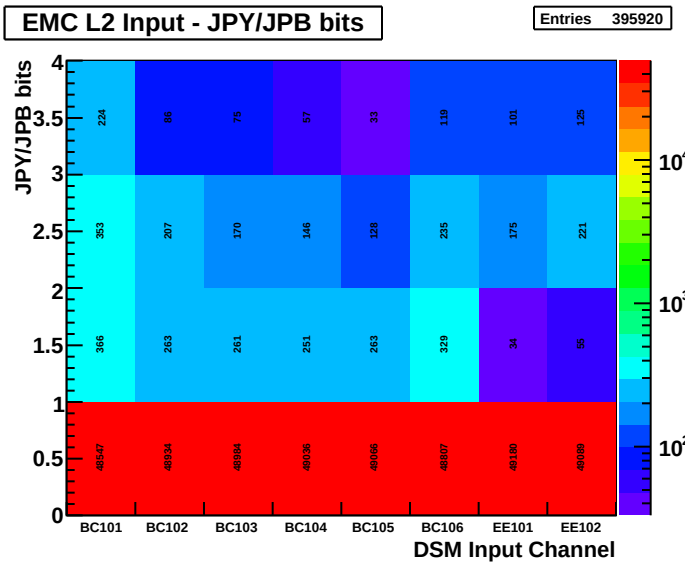

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

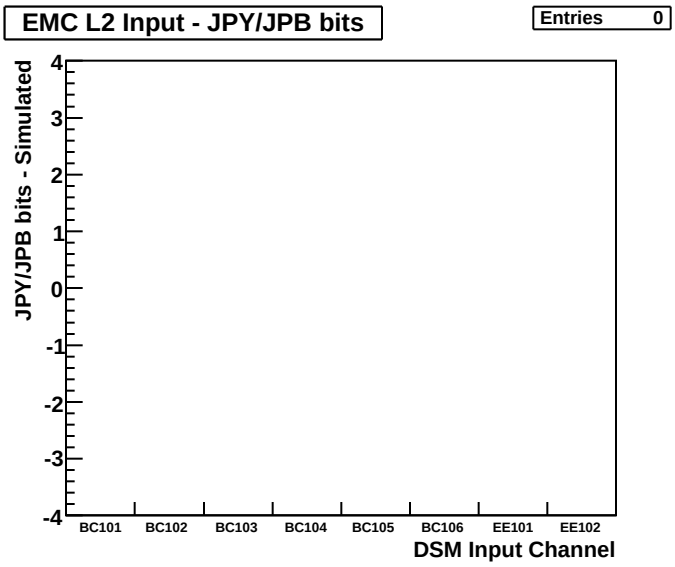

Endcap EMC L1 Input - High Tower Bits
Entries 593880
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

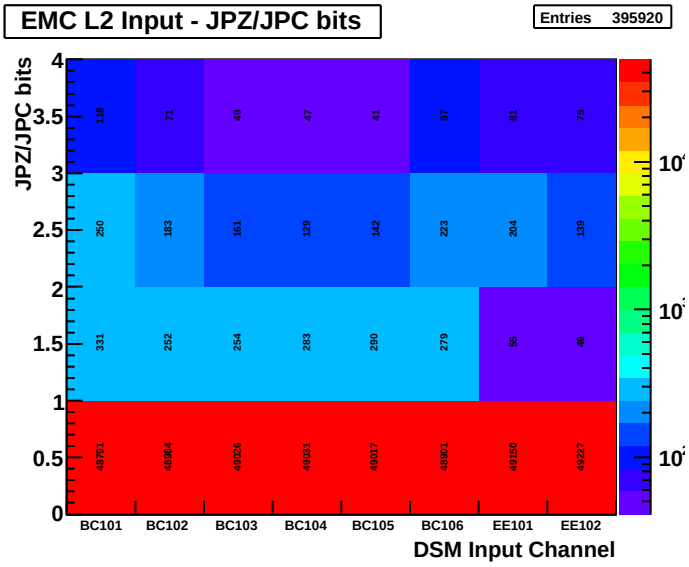

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

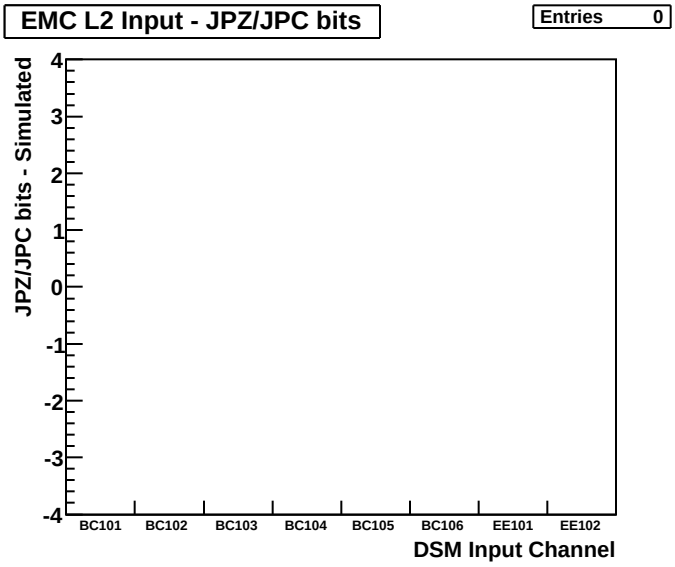

EMC L2 Input - JPX/JPA bits
Entries 395920
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
373 187 135 72 39 139 43 220
567 330 240 163 125 252 133 480
520 370 331 291 231 374 44 68
48030 48603 48784 48964 49095 49725 49270 48722

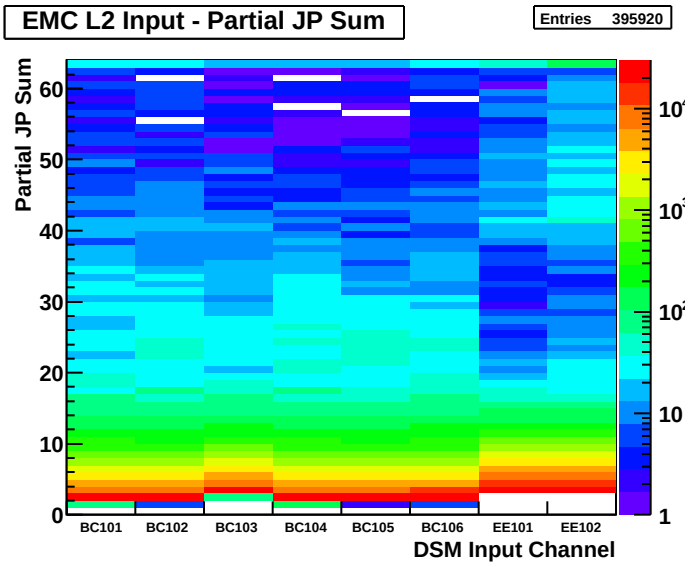

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

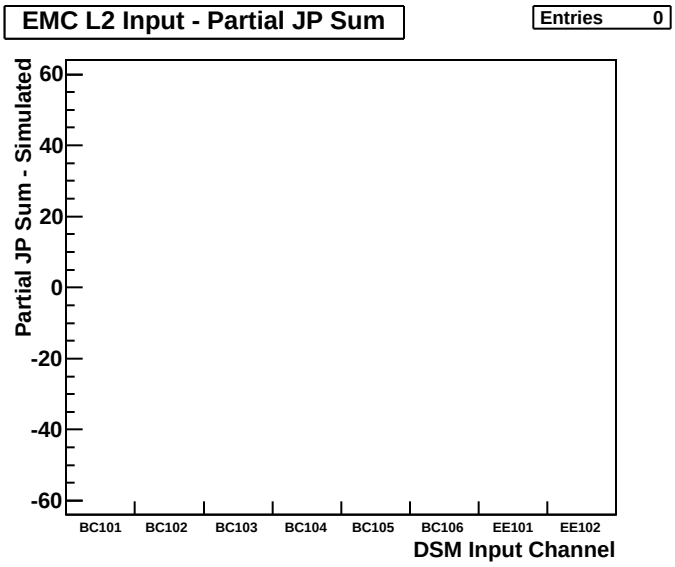

EMC L2 Input - JPY/JPB bits
Entries 395920
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
224 86 75 57 33 119 101 125
353 207 170 146 128 235 175 221
366 263 261 251 263 329 34 55
48547 48934 48984 49036 49066 48807 49180 49089

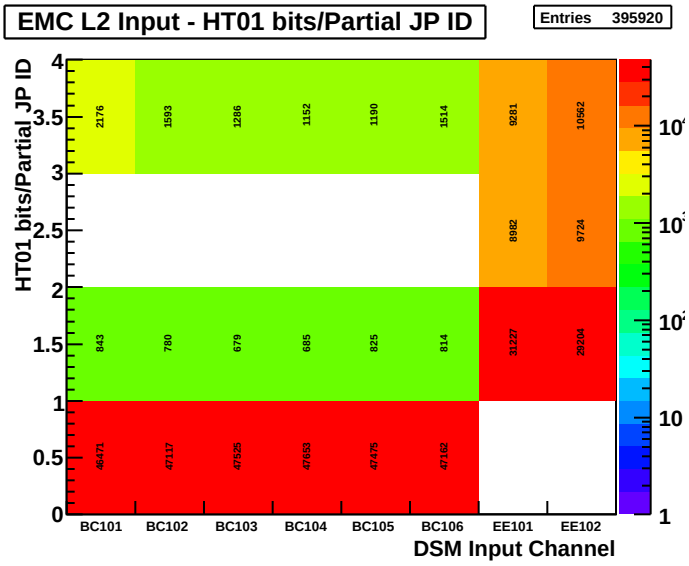

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

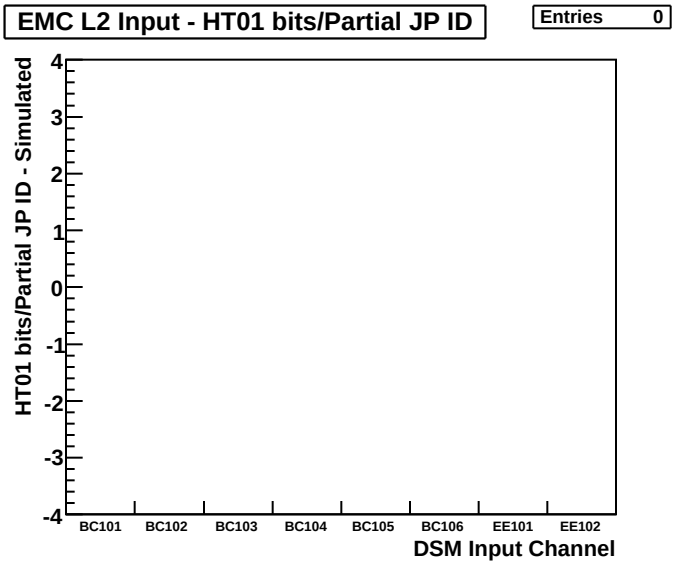

EMC L2 Input - JPZ/JPC bits
Entries 395920
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
118 71 49 47 41 87 81 75
250 183 161 129 142 223 204 139
331 252 254 283 290 279 55 49
48791 48884 49026 49031 49017 48901 49150 49227

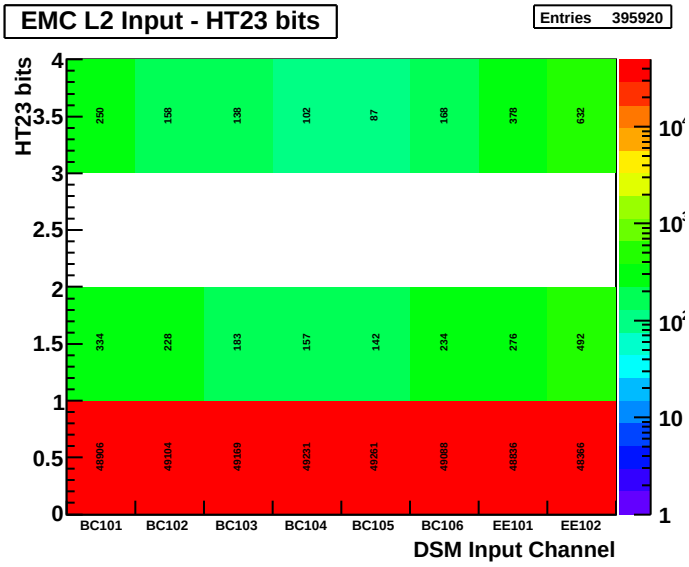

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

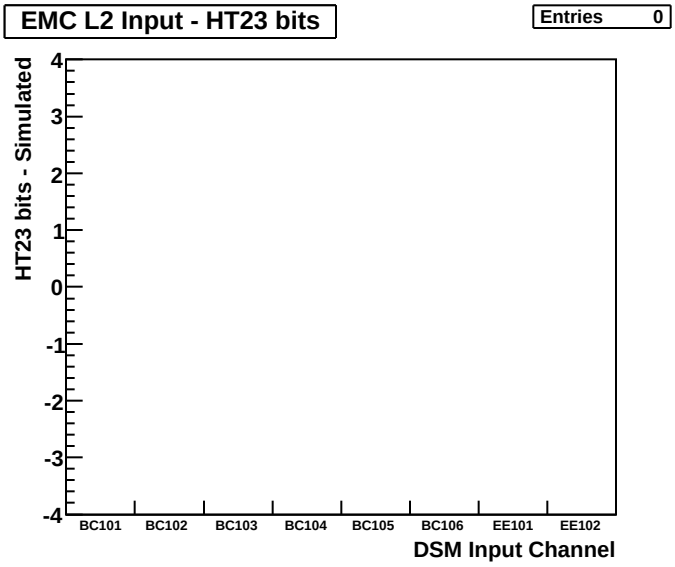

EMC L2 Input - Partial JP Sum
Entries 395920
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

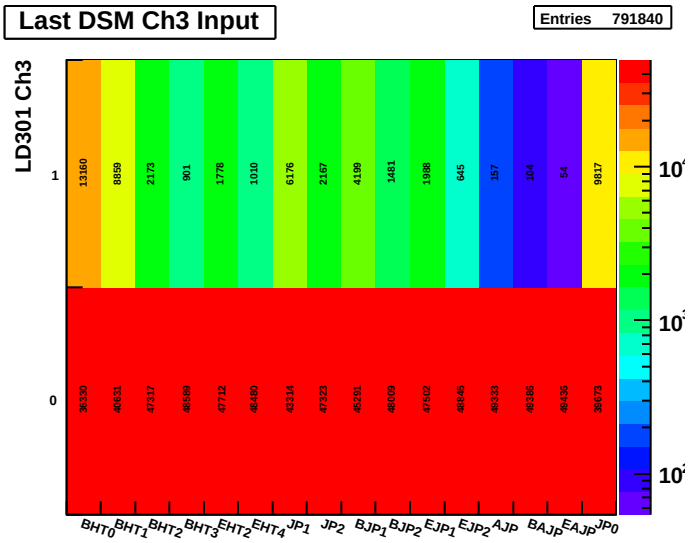

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

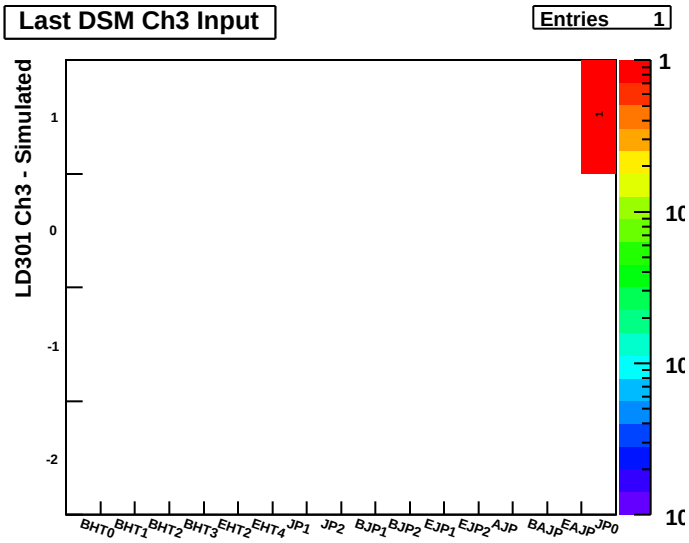

EMC L2 Input - HT01 bits/Partial JP ID
Entries 395920
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

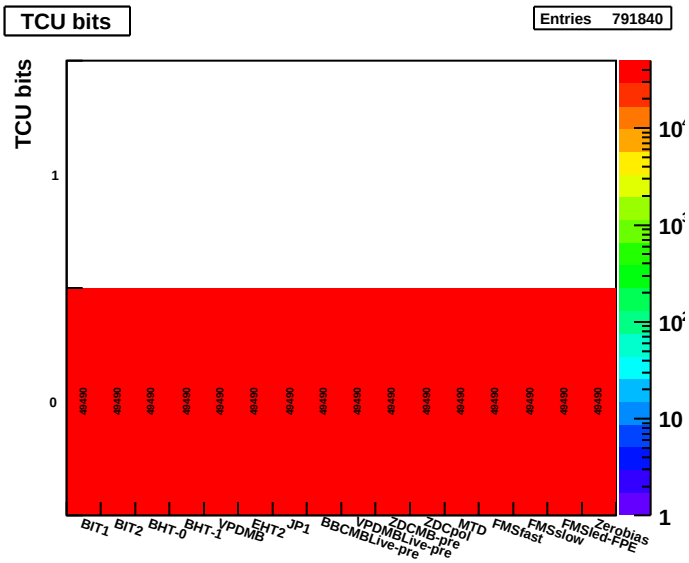

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

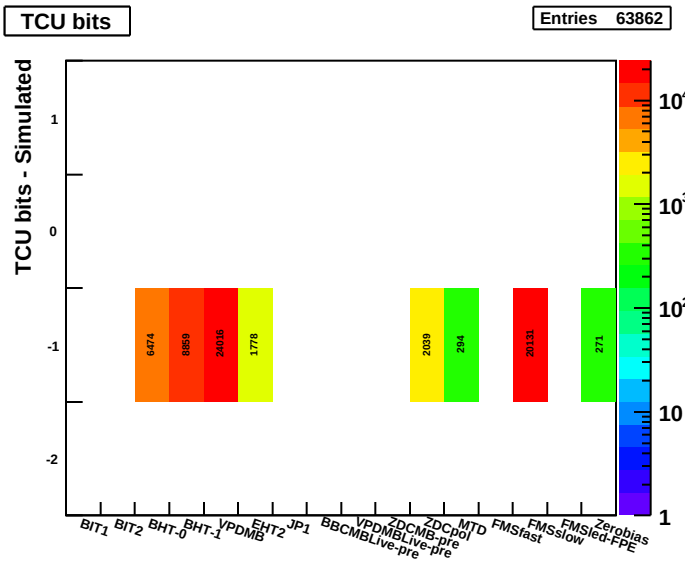

EMC L2 Input - HT23 bits
Entries 395920
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

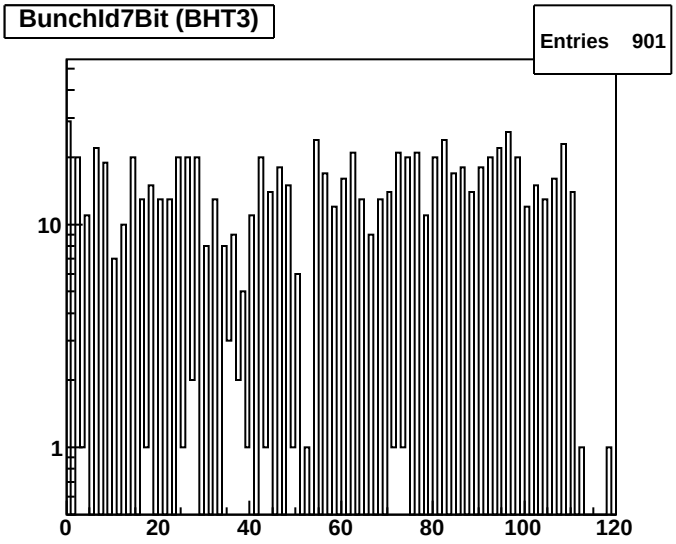

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

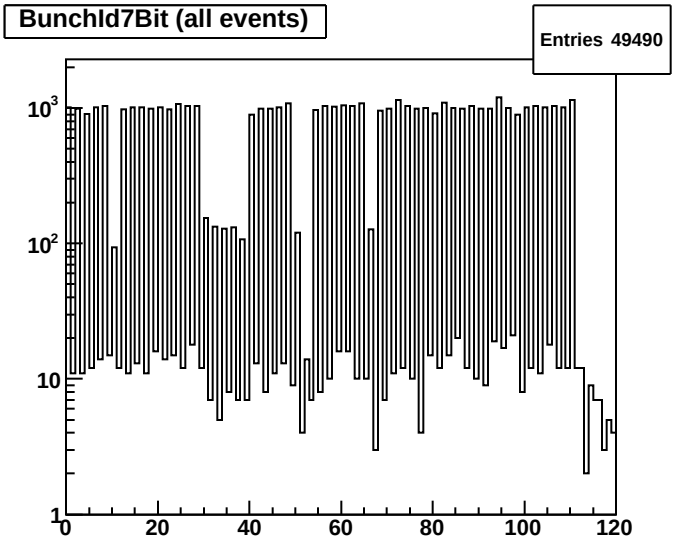

Last DSM Ch3 Input
Entries 791840
LD301 Ch3
13160 8859 2173 901 1778 1010 6176 2167 4199 1481 1988 645 157 104 54 9817
36330 40631 47317 49589 47712 48460 43314 47323 45291 49009 47502 48845 49333 49386 49436 39673
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

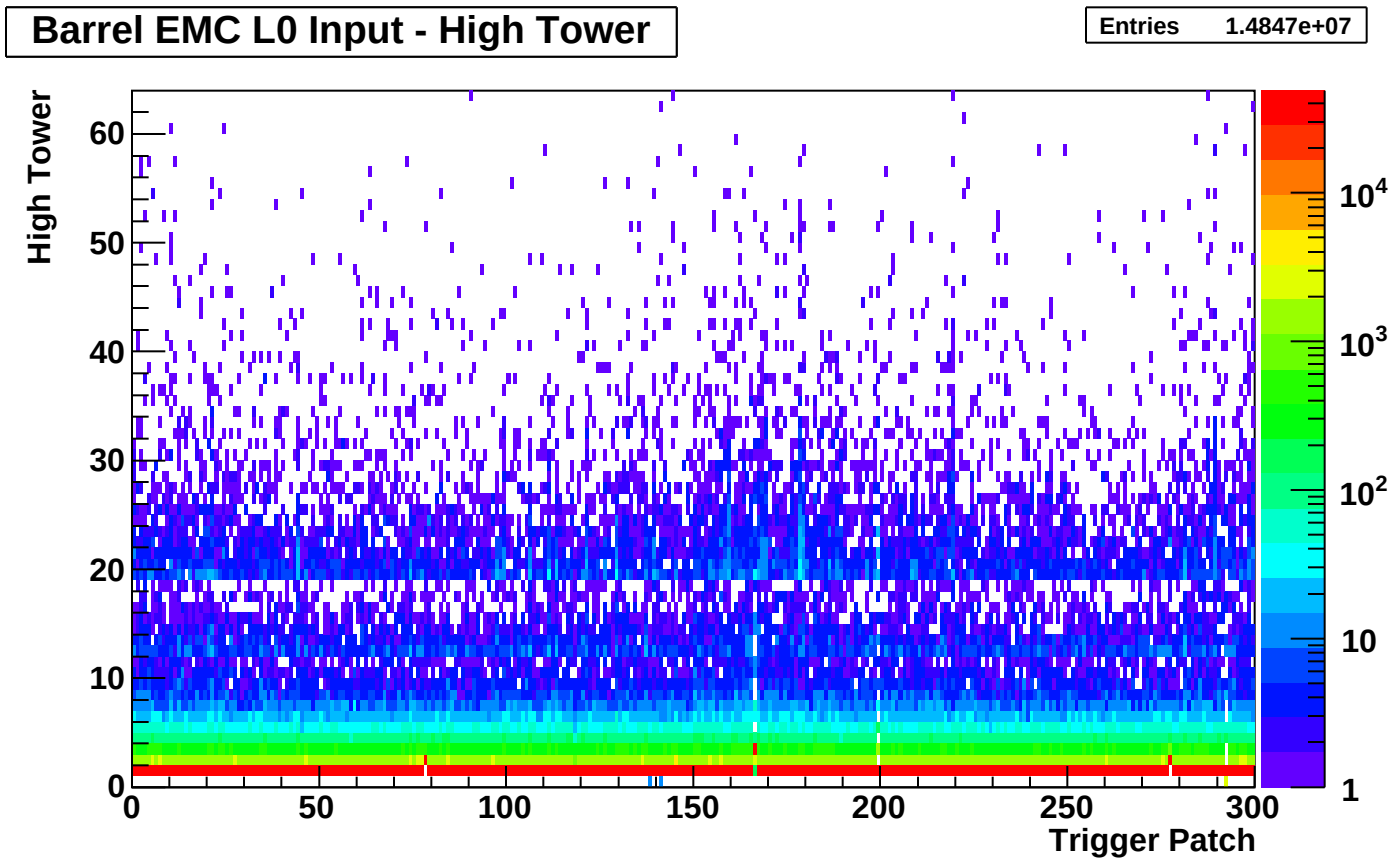

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

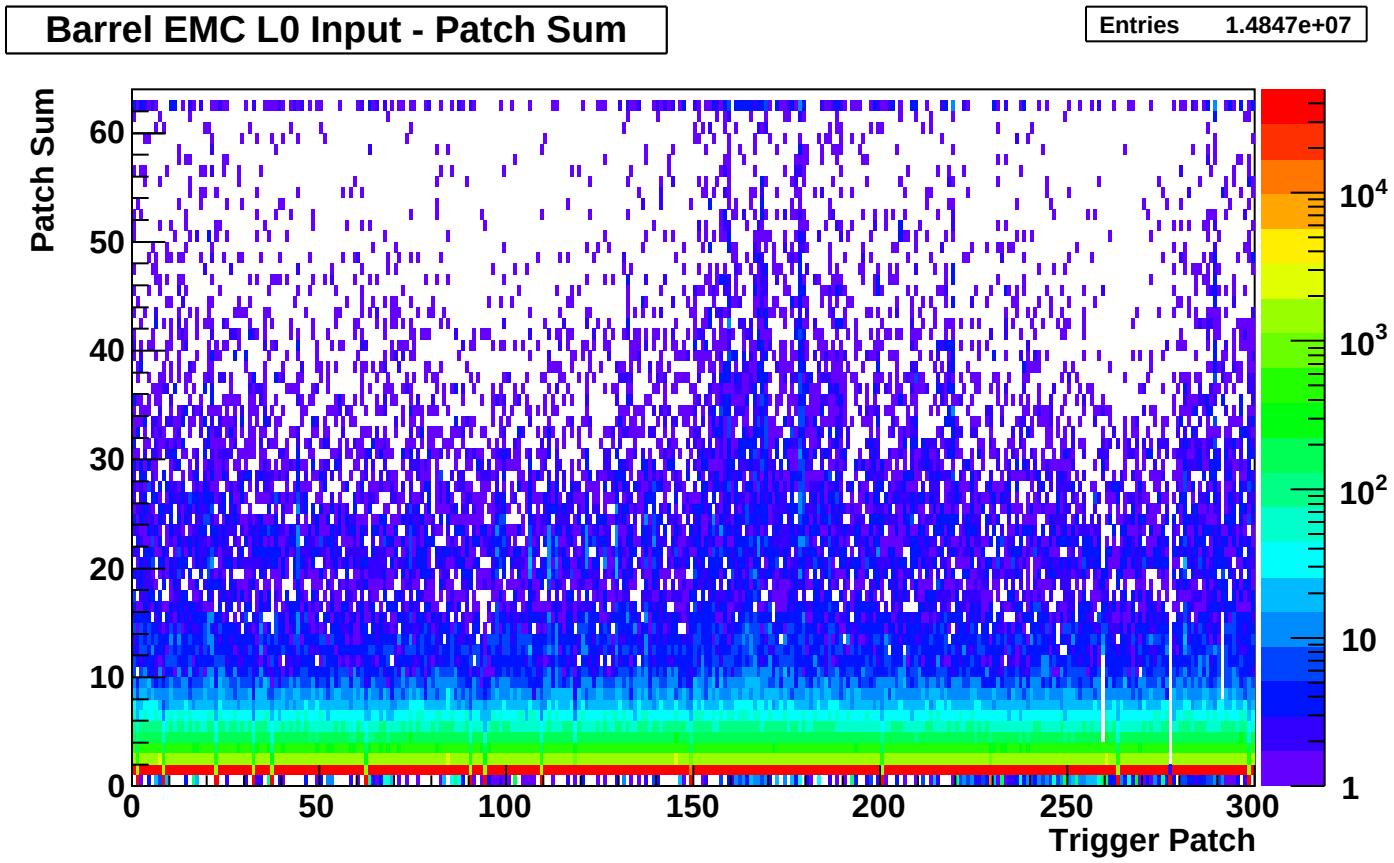

TCU bits
Entries 791840
TCU bits
49490 49490 49490 49490 49490 49490 49490 49490 49490 49490 49490 49490 49490 49490 49490 49490
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

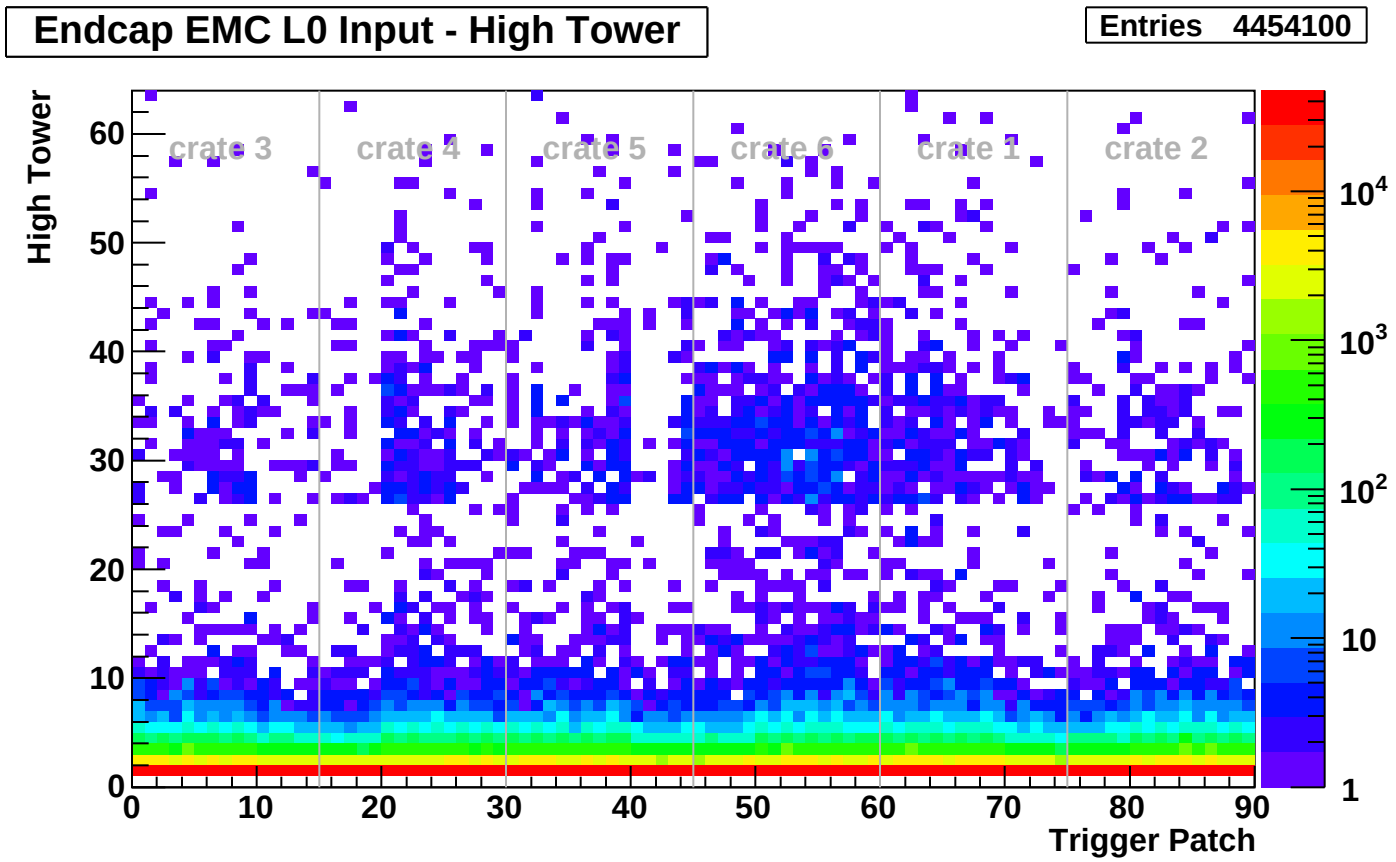

TCU bits
Entries 63862
TCU bits - Simulated
6474 8859 24016 1778 2039 294 20131 271
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

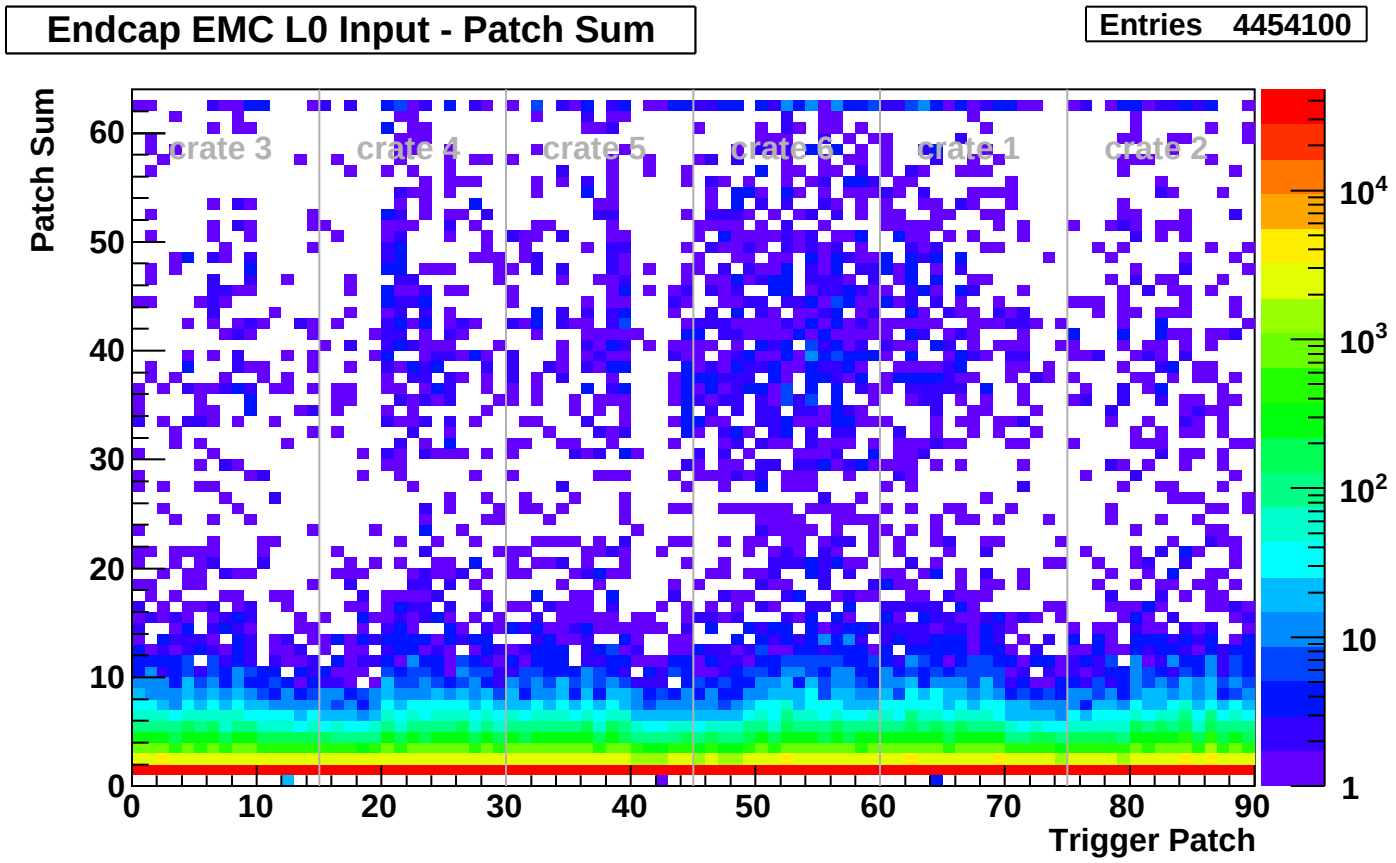

BunchId7Bit (BHT3)
Entries 901

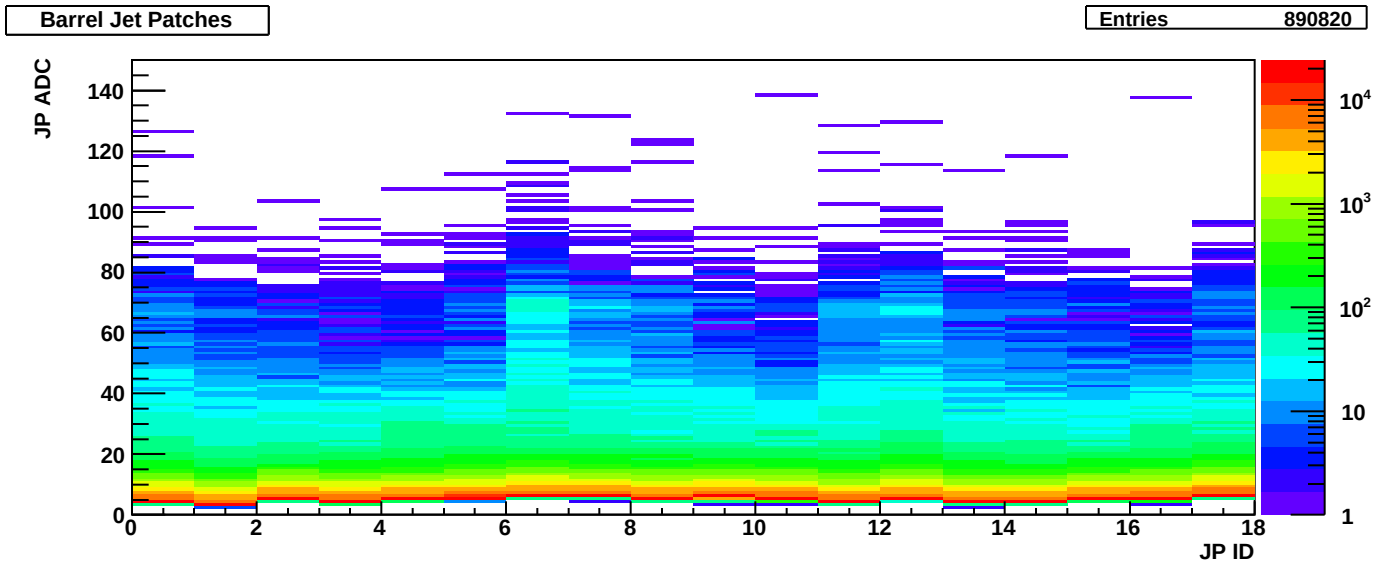

BunchId7Bit (all events)
Entries 49490

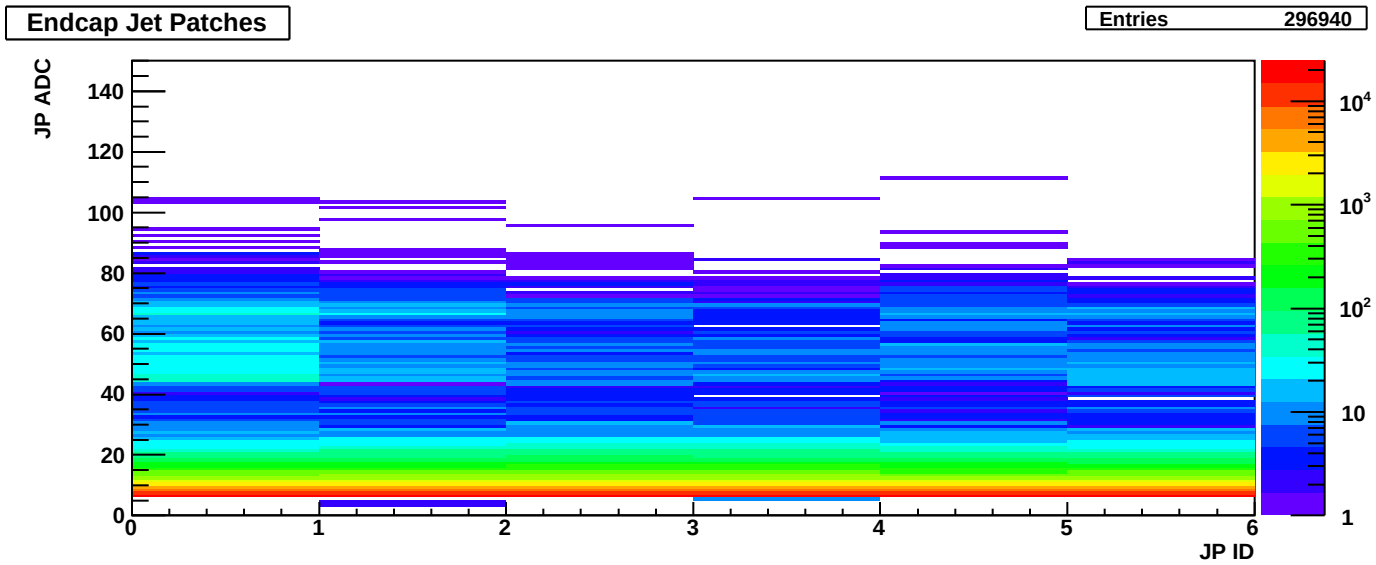

Barrel EMC L0 Input - High Tower
Entries 1.4847e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

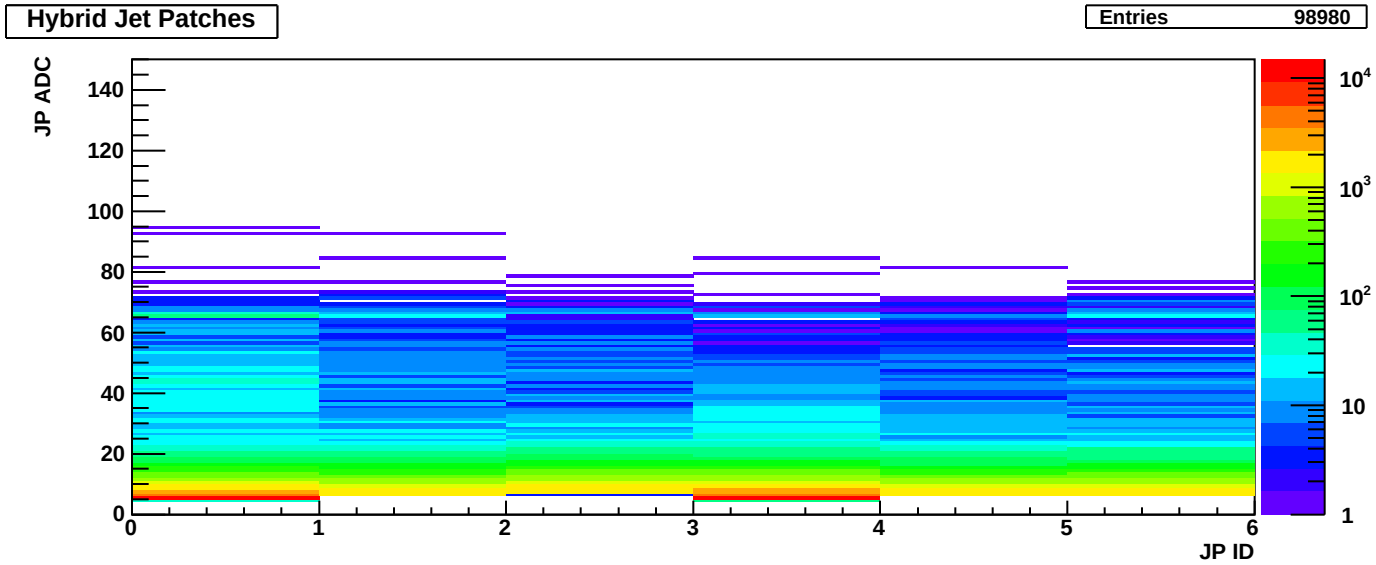

Barrel EMC L0 Input - Patch Sum
Entries 1.4847e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

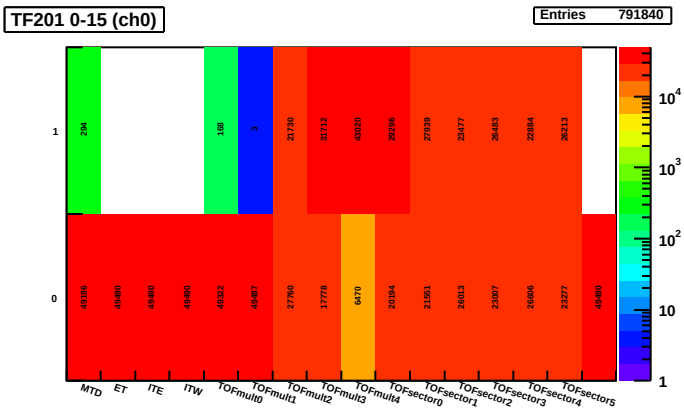

Endcap EMC L0 Input - High Tower
Entries 4454100
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

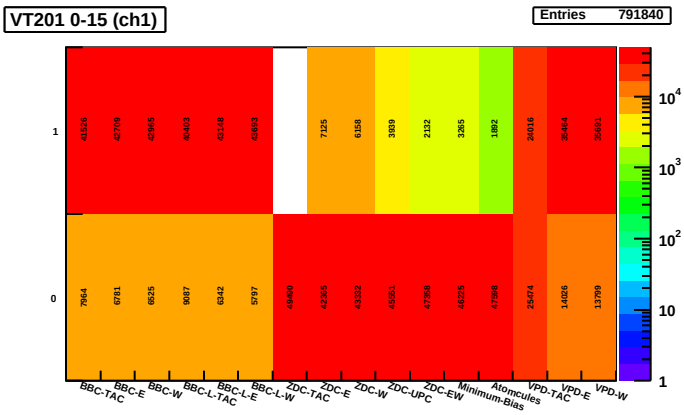

Endcap EMC L0 Input - Patch Sum
Entries 4454100
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

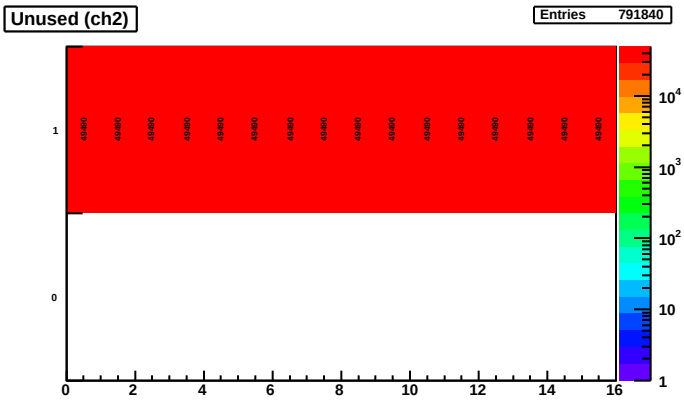

Barrel Jet Patches
Entries 890820
JP ADC
JP ID

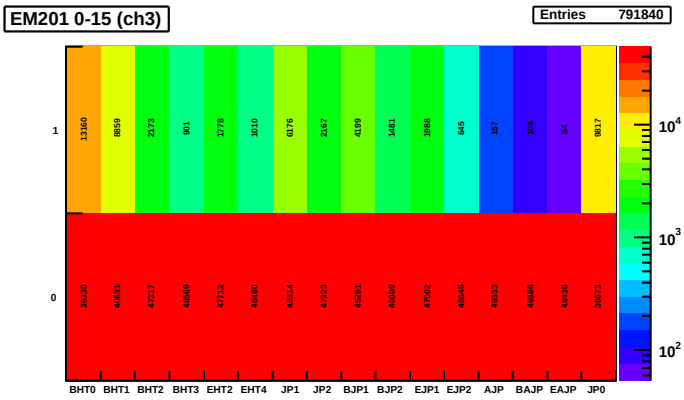

Endcap Jet Patches
Entries 296940
JP ADC
JP ID

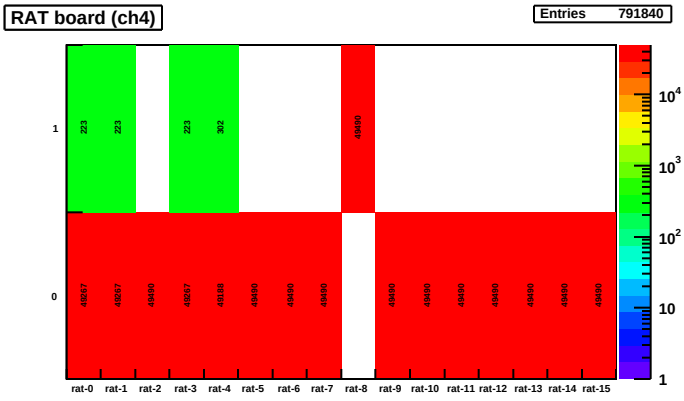

Hybrid Jet Patches
Entries 98980
JP ADC
JP ID

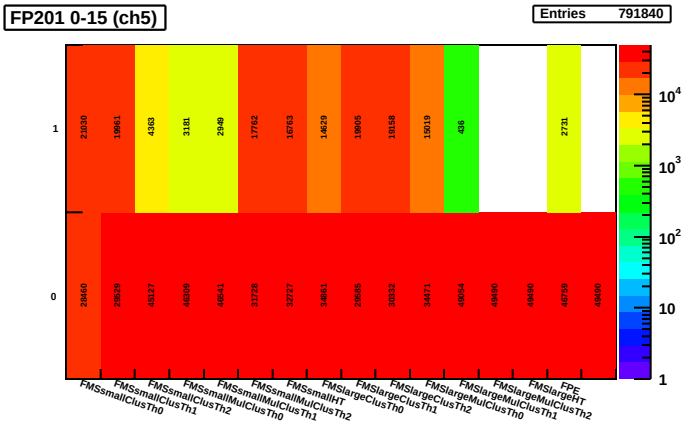

TF201 0-15 (ch0)
Entries 791840
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

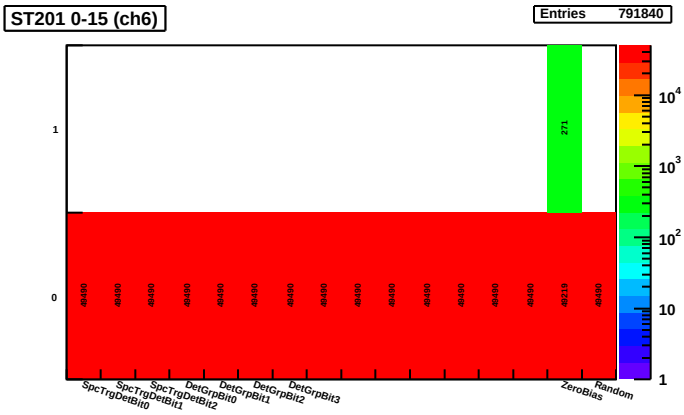

VT201 0-15 (ch1)
Entries 791840
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

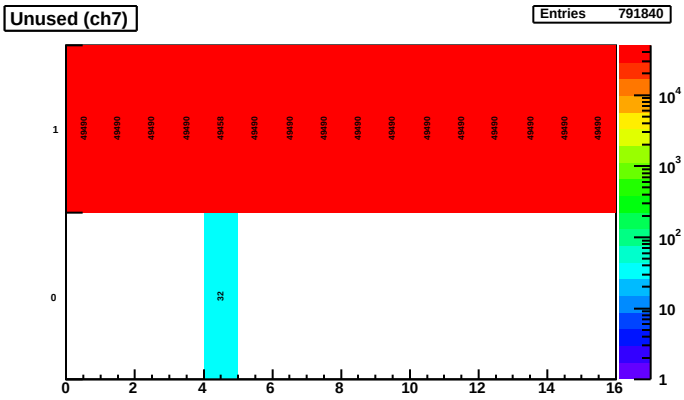

Unused (ch2)
Entries 791840

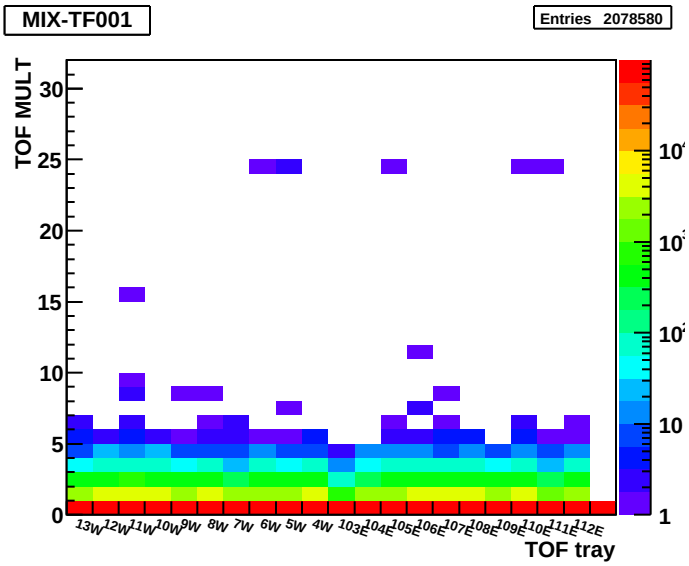

EM201 0-15 (ch3)
Entries 791840
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

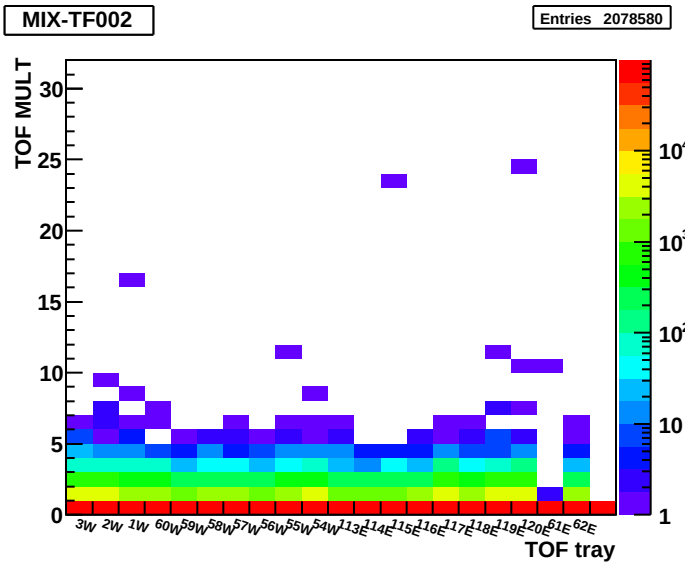

RAT board (ch4)
Entries 791840
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

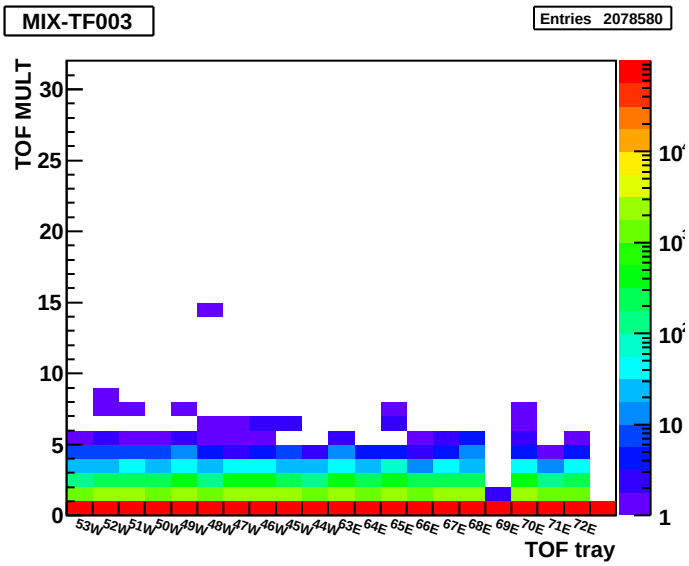

FP201 0-15 (ch5)
Entries 791840
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

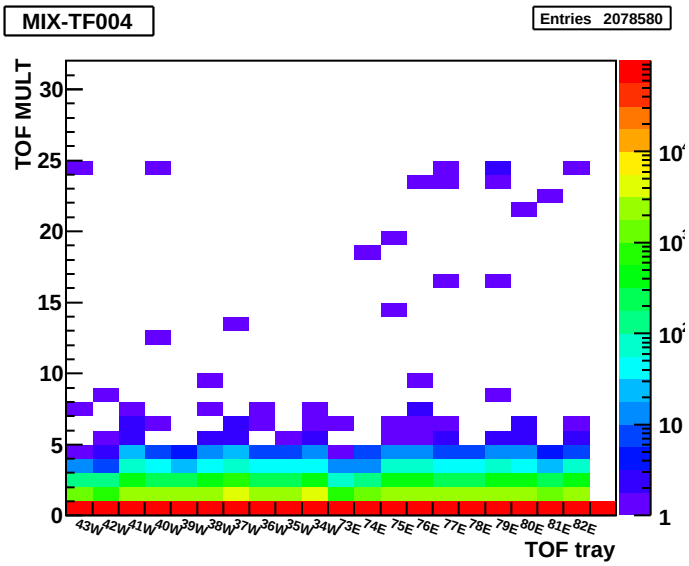

ST201 0-15 (ch6)
Entries 791840
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

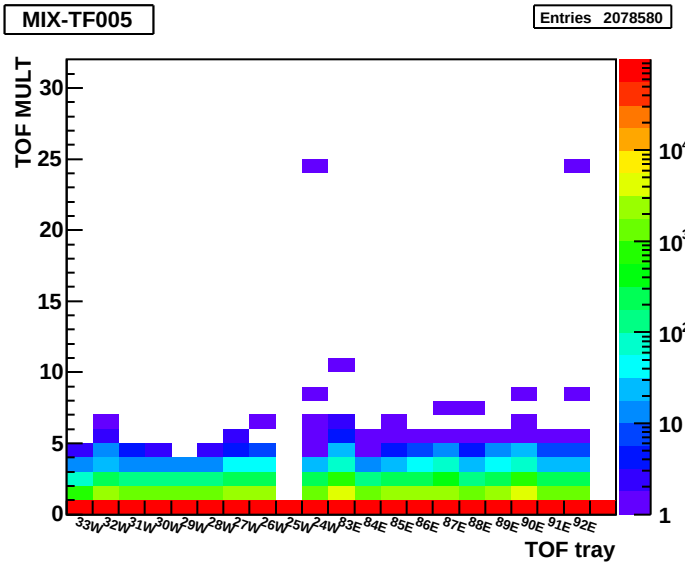

Unused (ch7)
Entries 791840

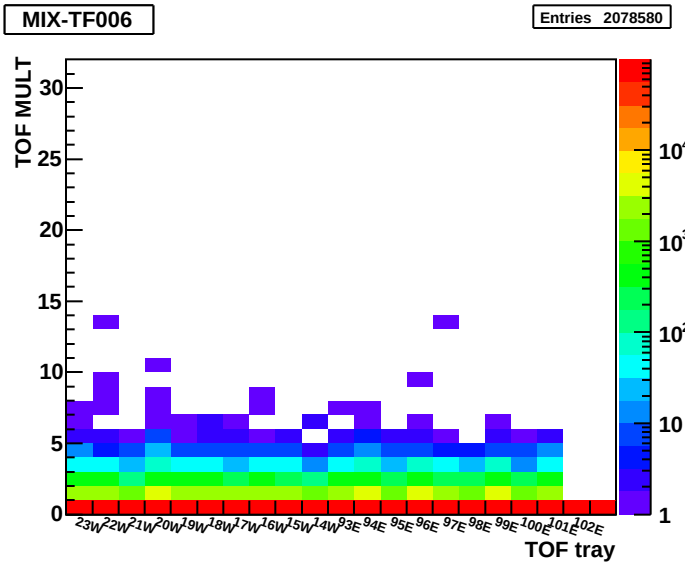

MIX-TF001
Entries 2078580
TOF MULT
TOF tray

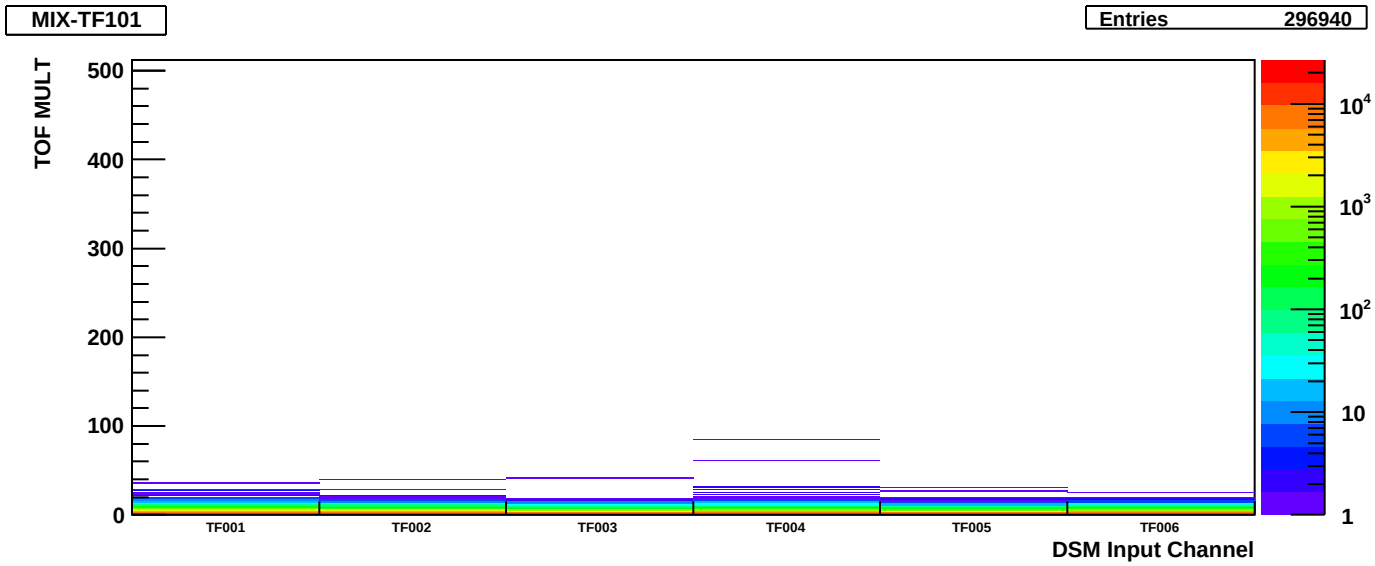

MIX-TF002
Entries 2078580
TOF MULT
TOF tray

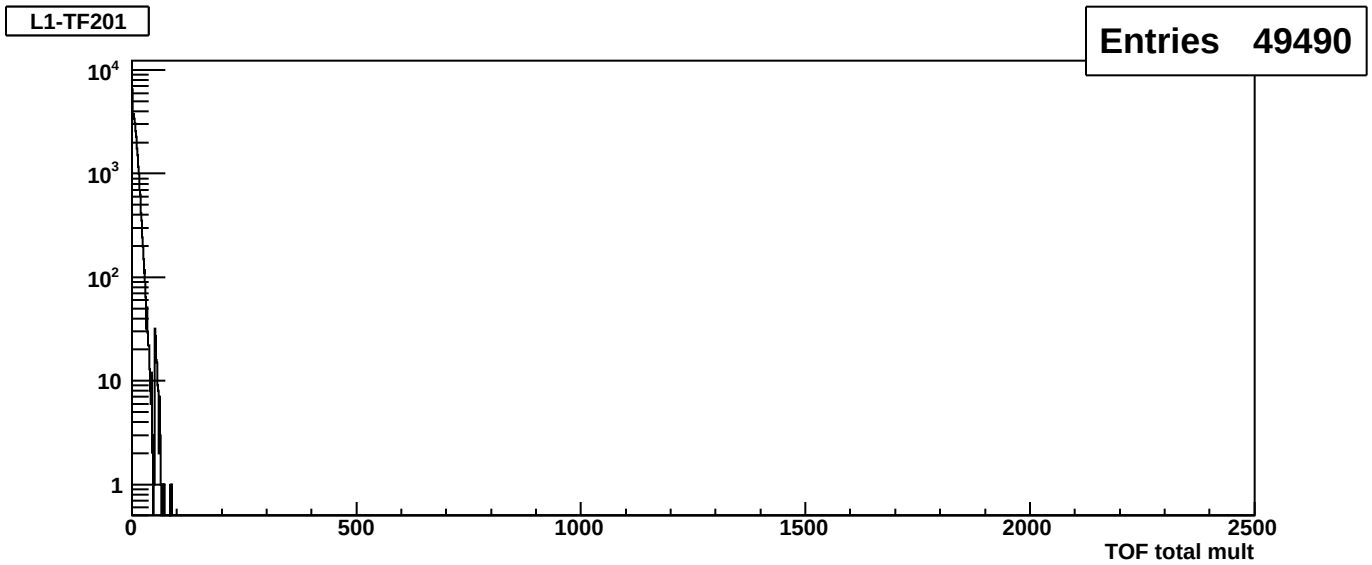

MIX-TF003
Entries 2078580
TOF MULT
TOF tray

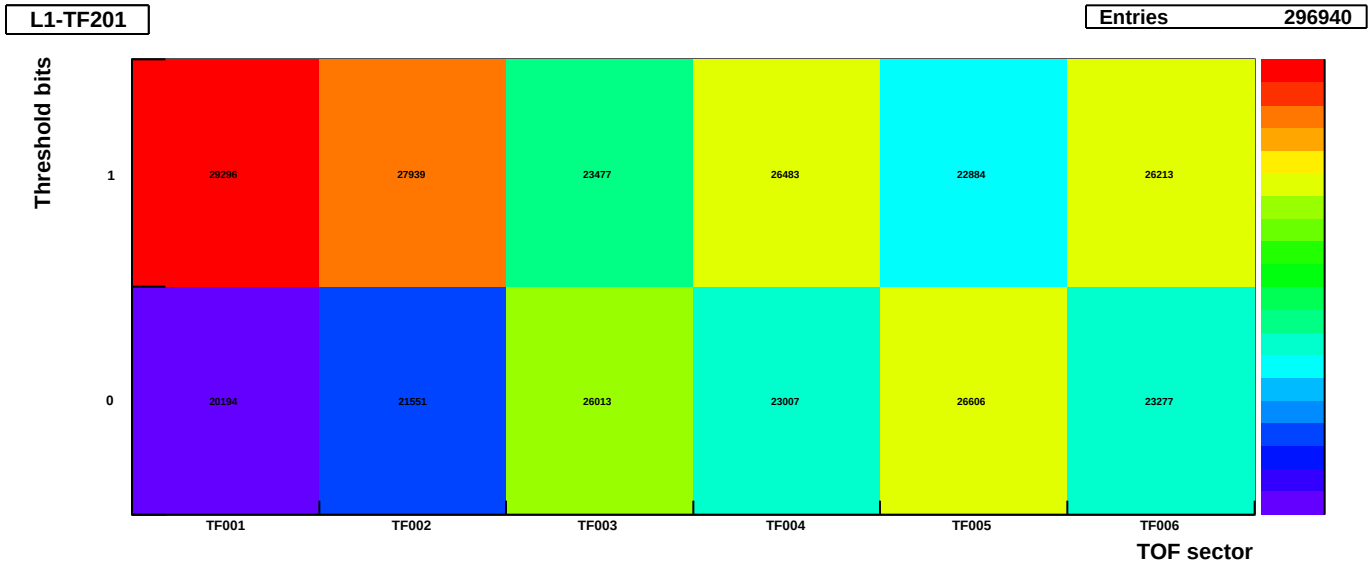

MIX-TF004
Entries 2078580
TOF MULT
TOF tray

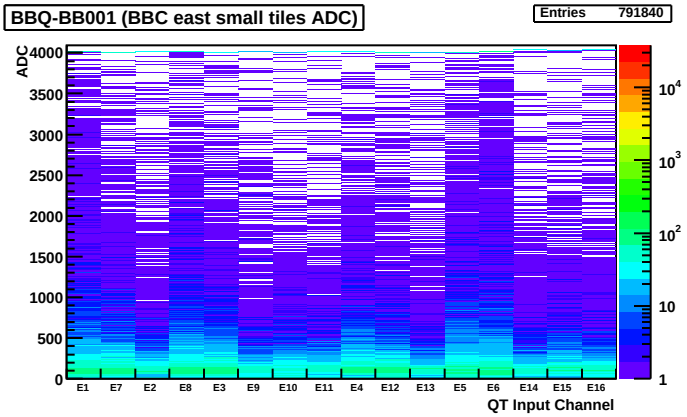

MIX-TF005
Entries 2078580
TOF MULT
TOF tray

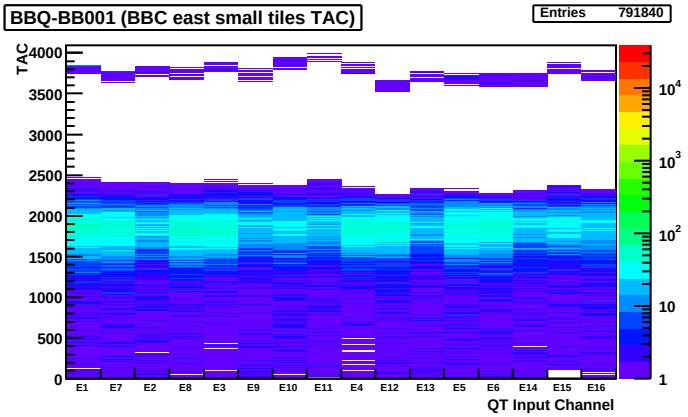

MIX-TF006
Entries 2078580
TOF MULT
TOF tray

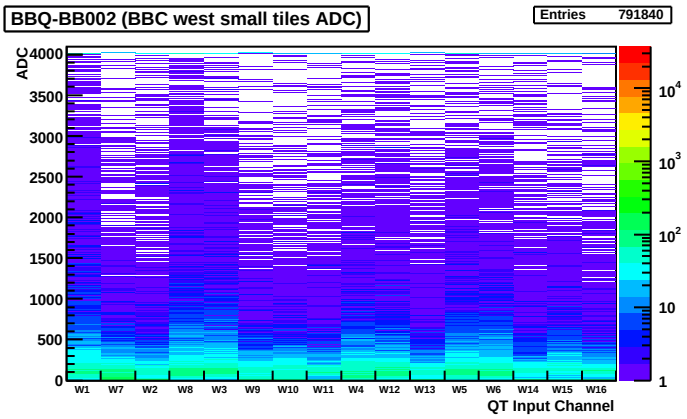

MIX-TF101
Entries 296940
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

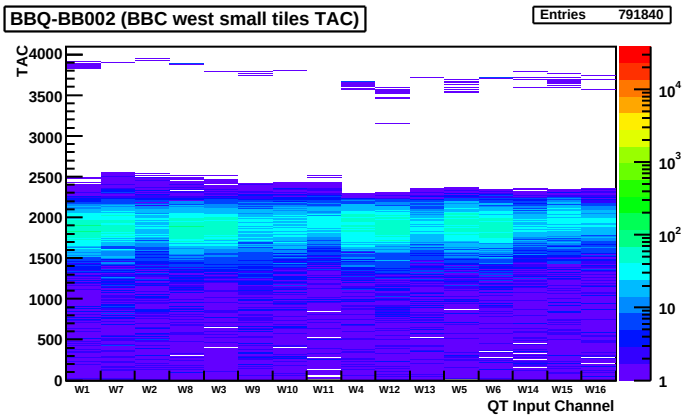

L1-TF201
Entries 49490
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

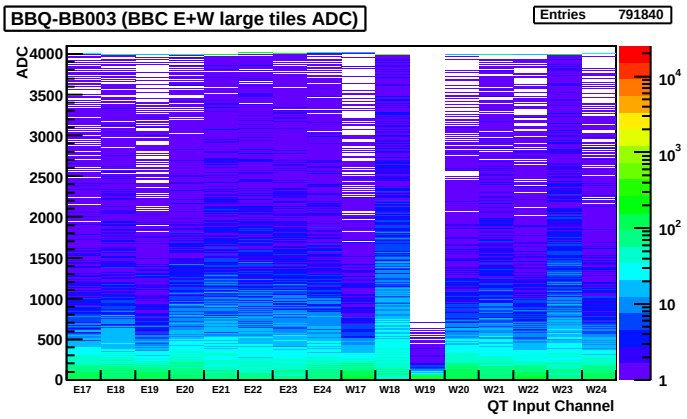

L1-TF201
Entries 296940
Threshold bits
1
0
29296
27939
23477
26483
22884
26213
20194
21551
26013
23007
26606
23277
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

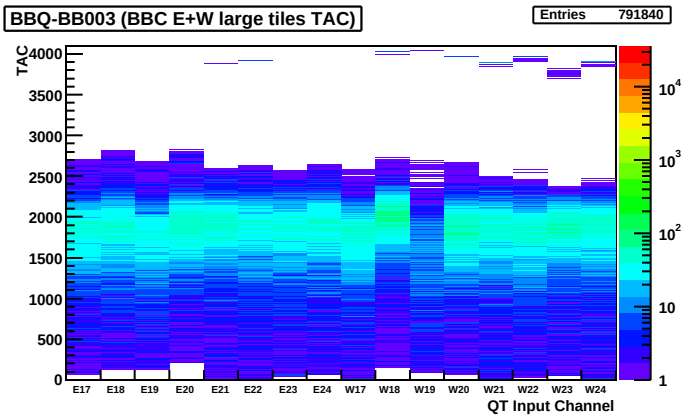

BBQ-BB001 (BBC east small tiles ADC)
Entries 791840
ADC
QT Input Channel

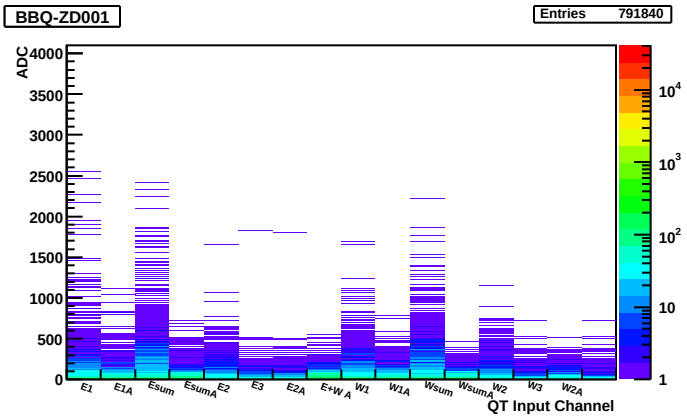

BBQ-BB001 (BBC east small tiles TAC)
Entries 791840
TAC
QT Input Channel

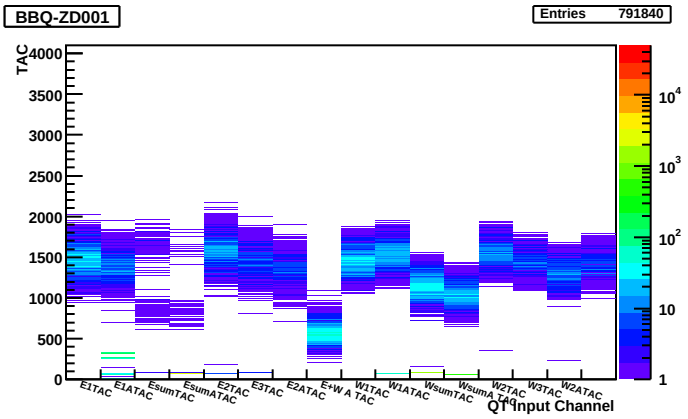

BBQ-BB002 (BBC west small tiles ADC)
Entries 791840
ADC
QT Input Channel

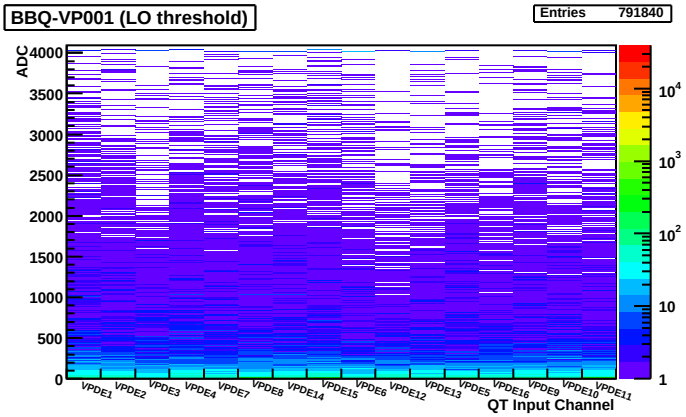

BBQ-BB002 (BBC west small tiles TAC)
Entries 791840
TAC
QT Input Channel

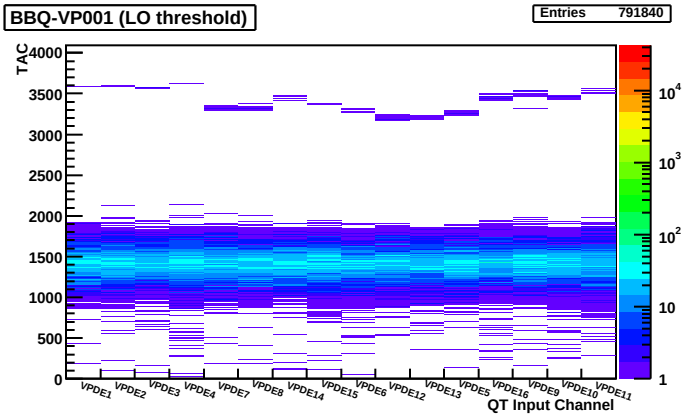

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 791840
ADC
QT Input Channel

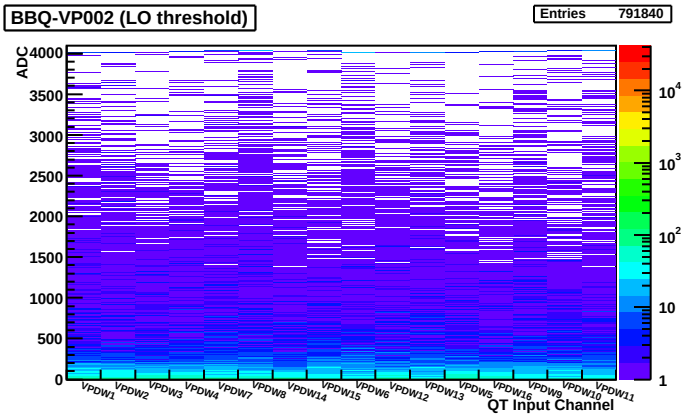

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 791840
TAC
QT Input Channel

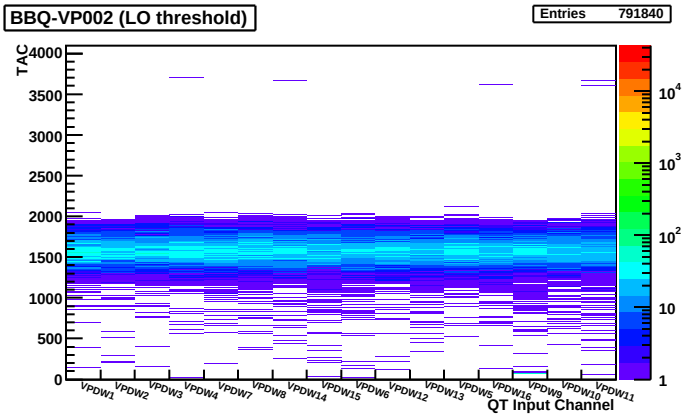

BBQ-ZD001
Entries 791840
ADC
QT Input Channel

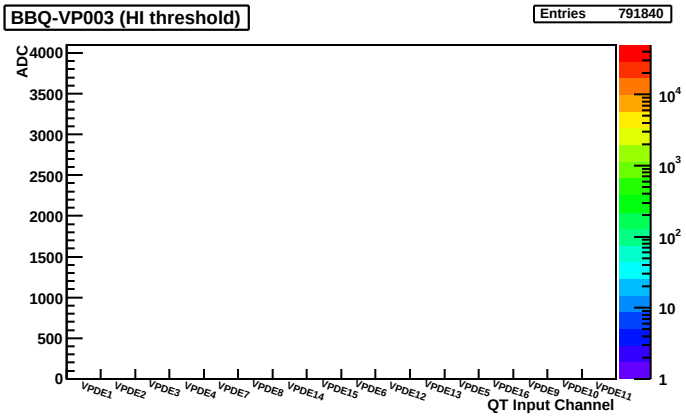

BBQ-ZD001
Entries 791840
TAC
QT Input Channel

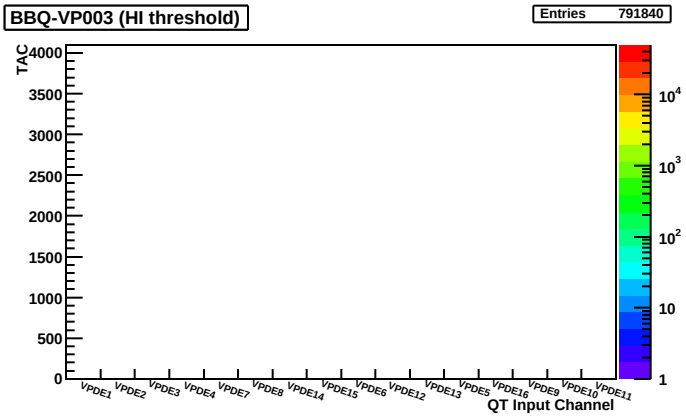

BBQ-VP001 (LO threshold)
Entries 791840
ADC
QT Input Channel

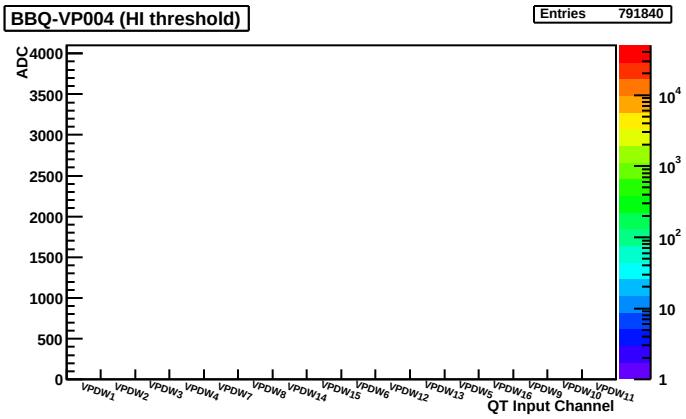

BBQ-VP001 (LO threshold)
Entries 791840
TAC
QT Input Channel

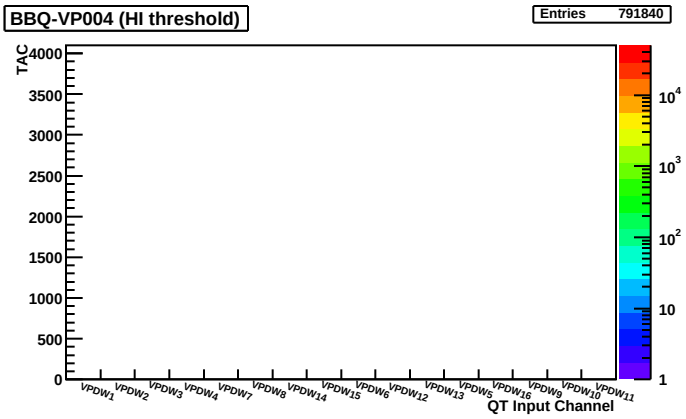

BBQ-VP002 (LO threshold)
Entries 791840
ADC
QT Input Channel

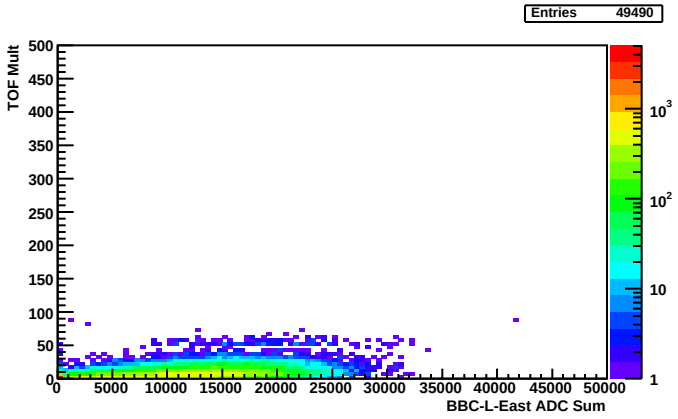

BBQ-VP002 (LO threshold)
Entries 791840
TAC
QT Input Channel

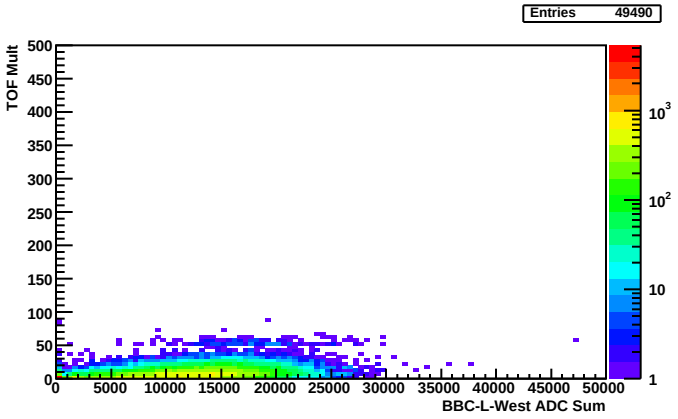

BBQ-VP003 (HI threshold)
Entries 791840
ADC
QT Input Channel

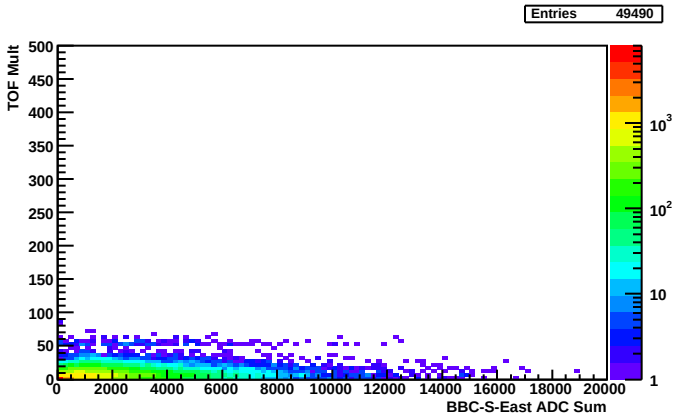

BBQ-VP003 (HI threshold)
Entries 791840
TAC
QT Input Channel

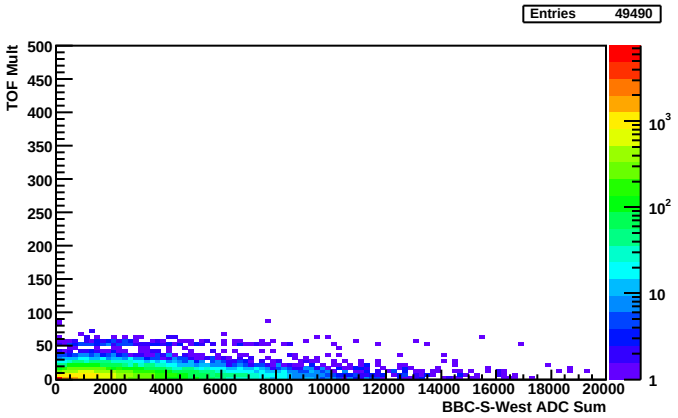

BBQ-VP004 (HI threshold)
Entries 791840
ADC
QT Input Channel

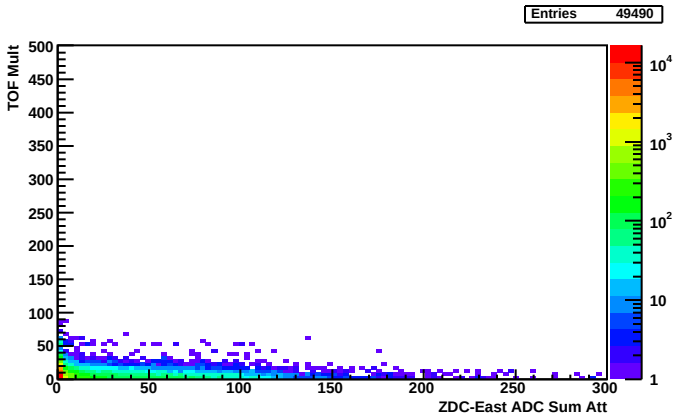

BBQ-VP004 (HI threshold)
Entries 791840
TAC
QT Input Channel

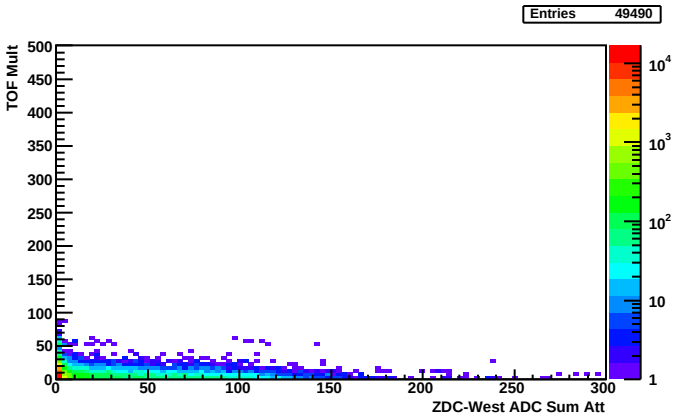

Entries 49490
TOF Mult
BBC-L-East ADC Sum

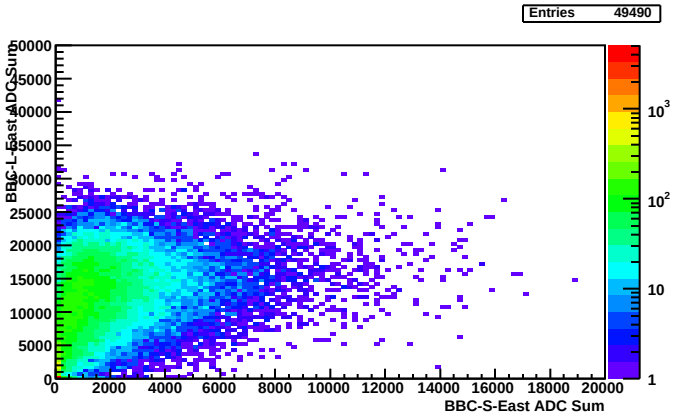

Entries 49490
TOF Mult
BBC-L-West ADC Sum

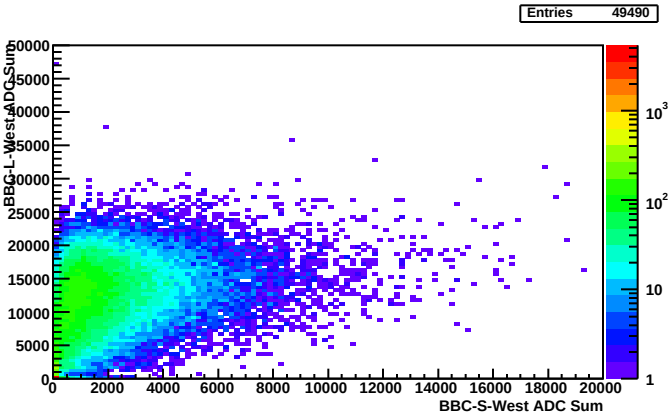

Entries 49490
TOF Mult
BBC-S-East ADC Sum

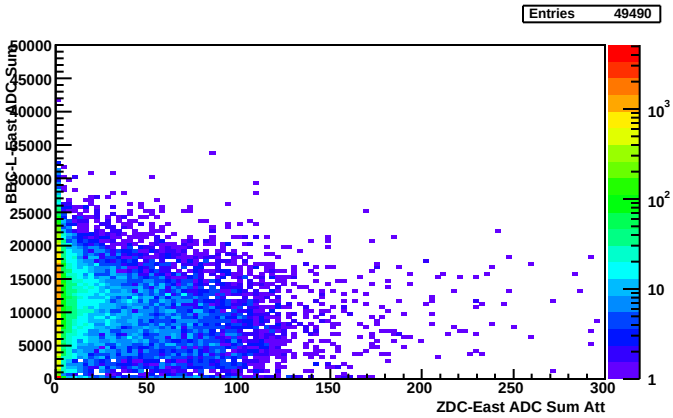

Entries 49490
TOF Mult
BBC-S-West ADC Sum

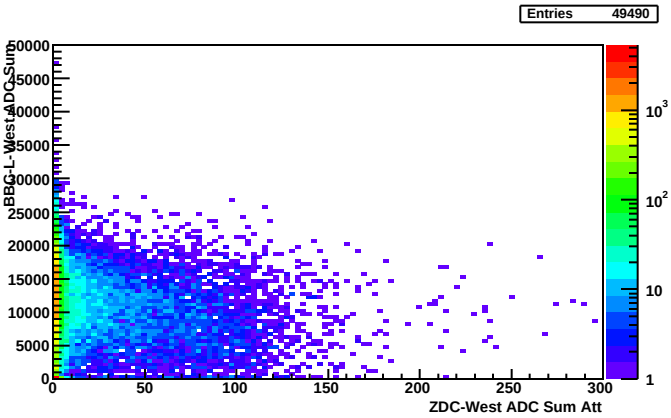

Entries 49490
TOF Mult
ZDC-East ADC Sum Att

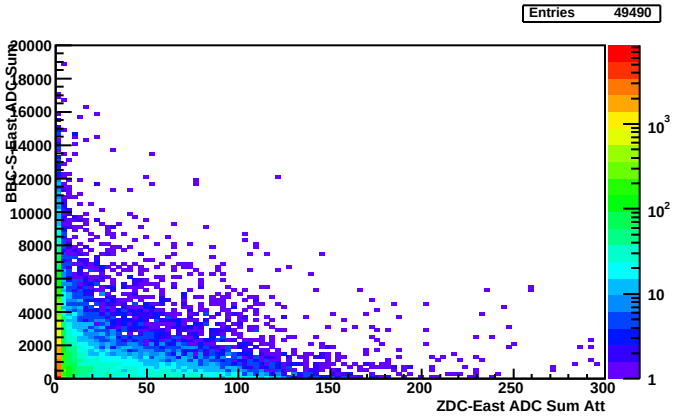

Entries 49490
TOF Mult
ZDC-West ADC Sum Att

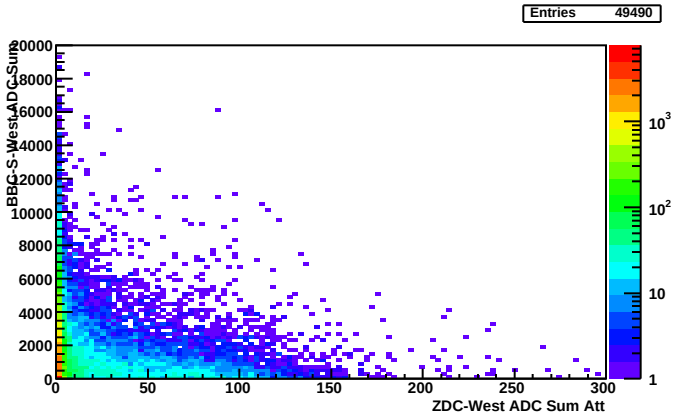

Entries 49490
BBC-L-East ADC Sum
BBC-S-East ADC Sum

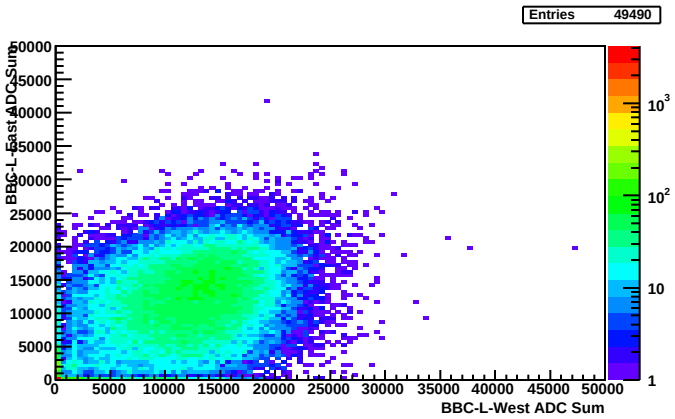

Entries 49490
BBC-L-West ADC Sum
BBC-S-West ADC Sum

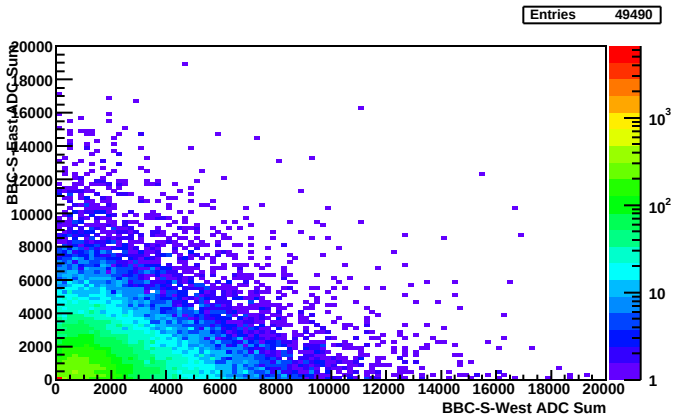

Entries 49490
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

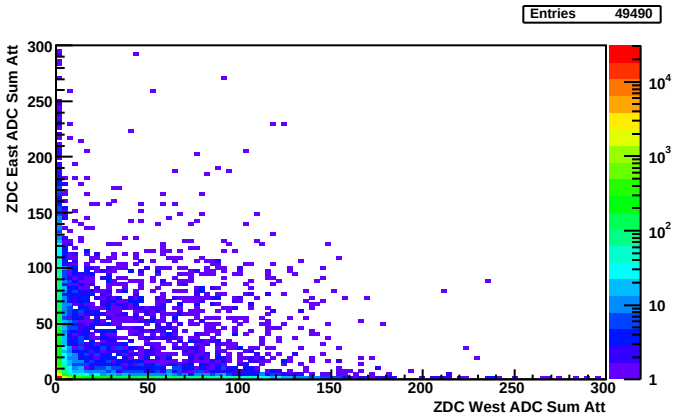

Entries 49490
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

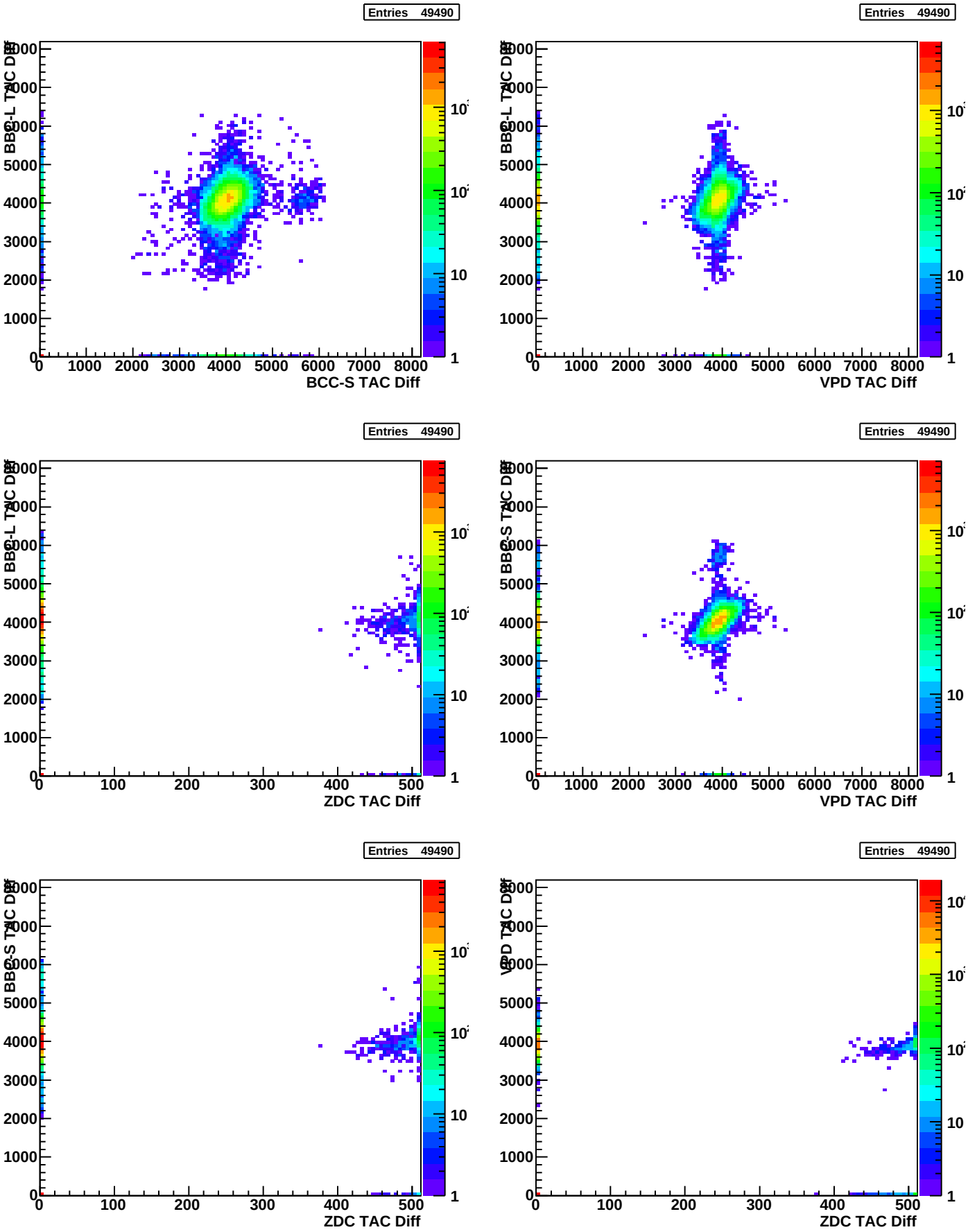

Entries 49490
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

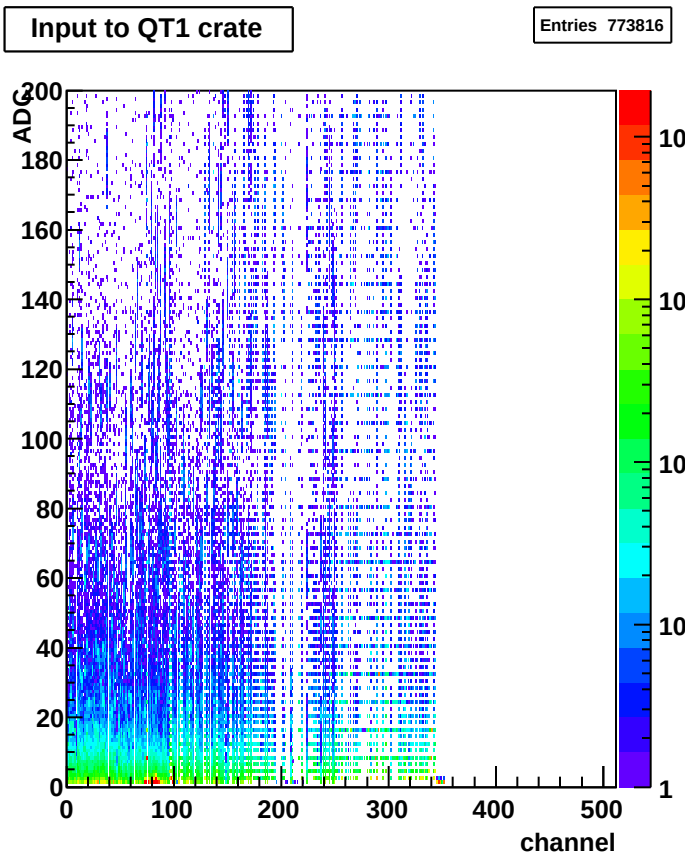

Entries 49490
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

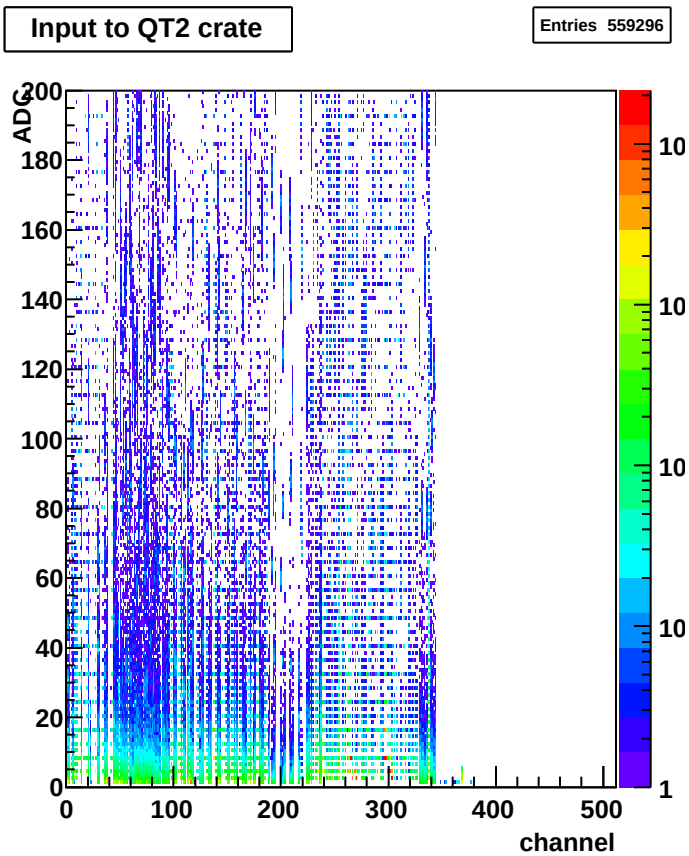

Entries 49490
BBC-L-East ADC Sum
BBC-L-West ADC Sum

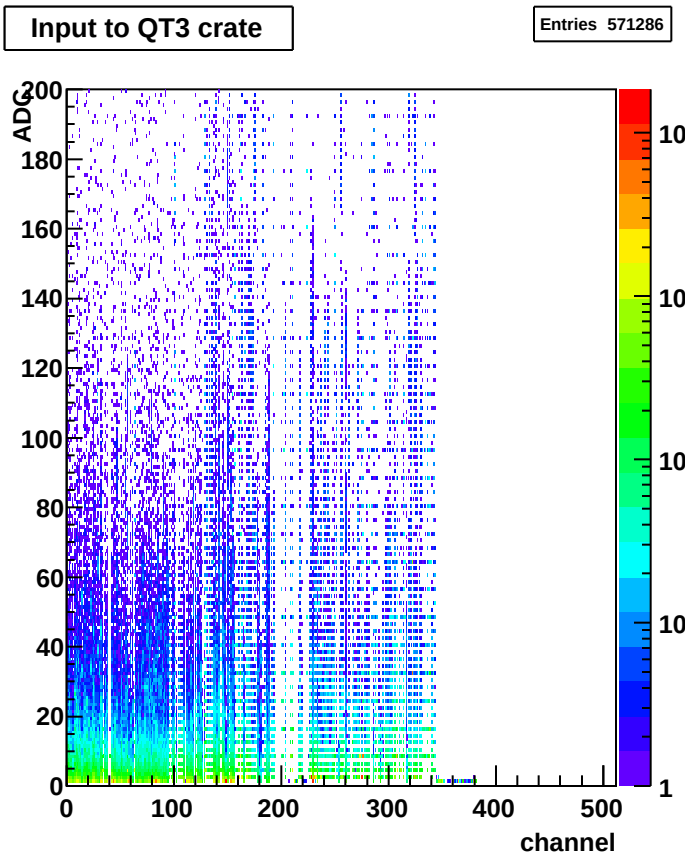

Entries 49490
BBC-S-East ADC Sum
BBC-S-West ADC Sum

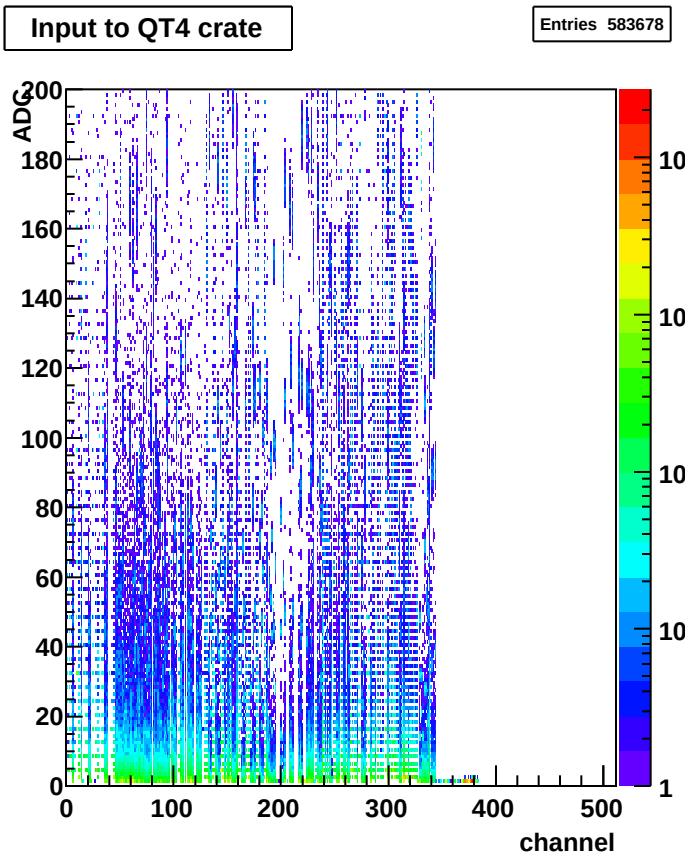

Entries 49490
ZDC East ADC Sum Att
ZDC West ADC Sum Att

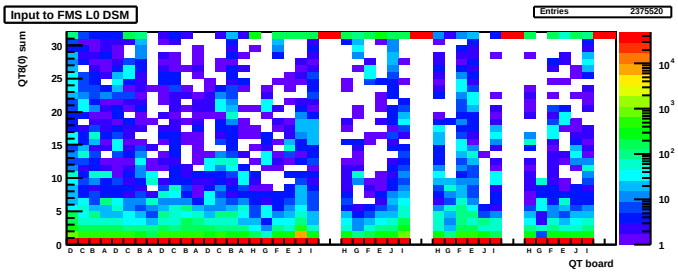

Entries 49490
BBC-L TAC Diff
BCC-S TAC Diff
Entries 49490
BBC-L TAC Diff
VPD TAC Diff
Entries 49490
BBC-L TAC Diff
ZDC TAC Diff
Entries 49490
BBC-S TAC Diff
VPD TAC Diff
Entries 49490
BBC-S TAC Diff
ZDC TAC Diff
Entries 49490
VPD TAC Diff
ZDC TAC Diff

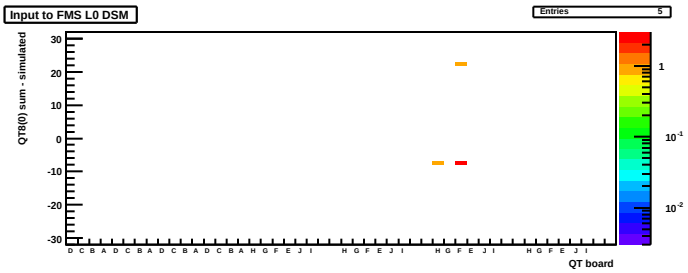

Input to QT1 crate
Entries 773816
ADC
channel

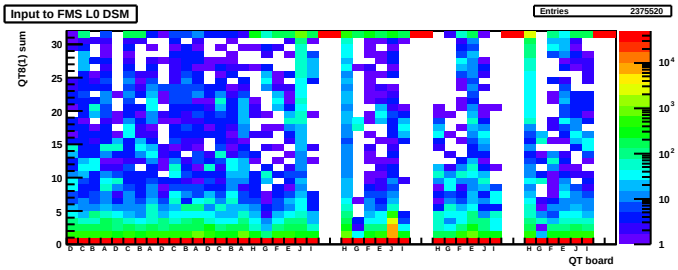

Input to QT2 crate
Entries 559296
ADC
channel

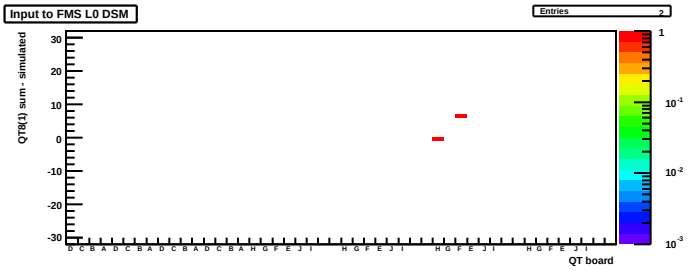

Input to QT3 crate
Entries 571286
ADC
channel

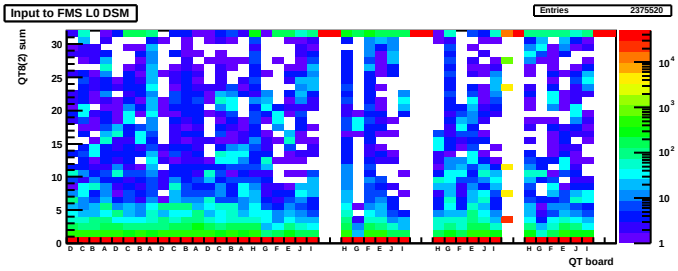

Input to QT4 crate
Entries 583678
ADC
channel

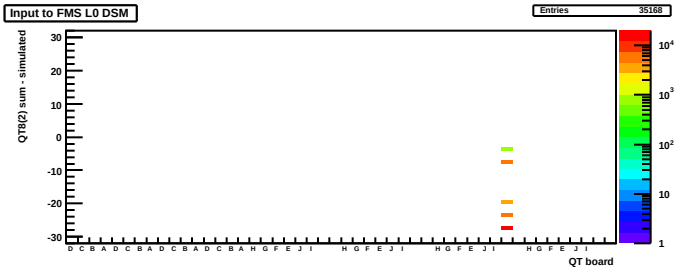

Input to FMS L0 DSM
Entries 2375520
QT8(0) sum
QT board

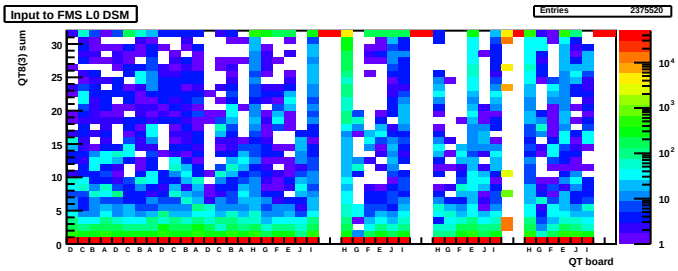

Input to FMS L0 DSM
Entries 5
QT8(0) sum - simulated
QT board

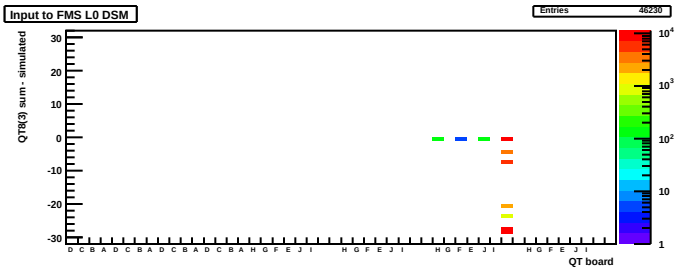

Input to FMS L0 DSM
Entries 2375520
QT8(1) sum
QT board

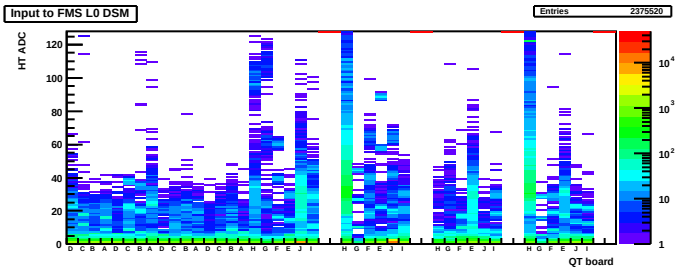

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

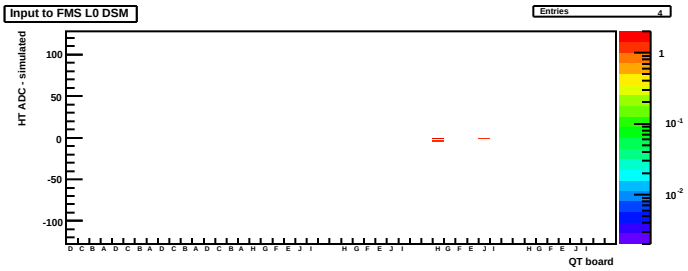

Input to FMS L0 DSM
Entries 2375520
QT8(2) sum
QT board

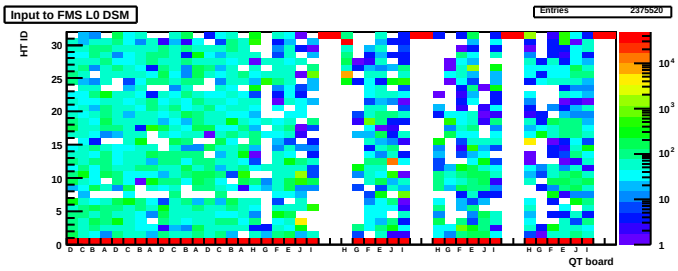

Input to FMS L0 DSM
Entries 35168
QT8(2) sum - simulated
QT board

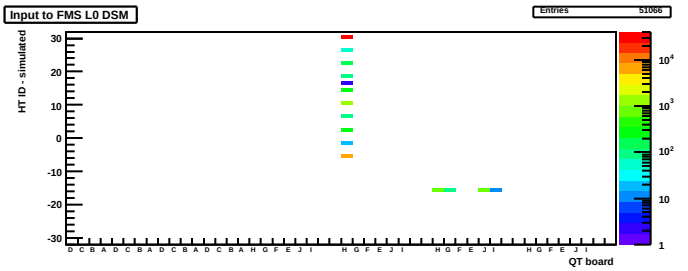

Input to FMS L0 DSM
Entries 2375520
QT8(3) sum
QT board

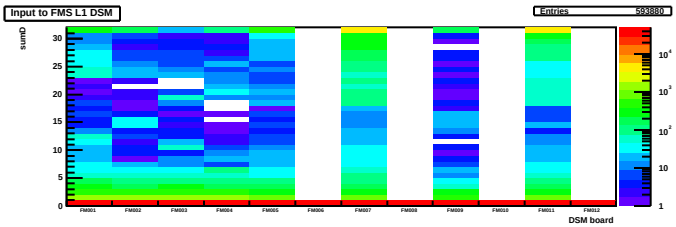

Input to FMS L0 DSM
Entries 46230
QT8(3) sum - simulated
QT board

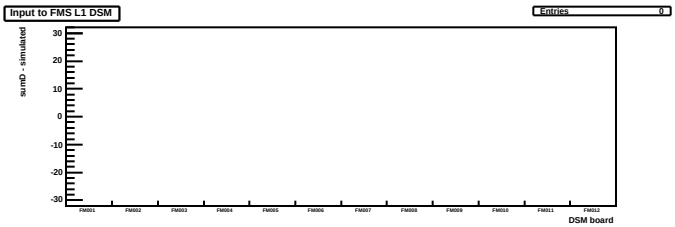

Input to FMS L0 DSM
Entries 2375520
HT ADC
QT board

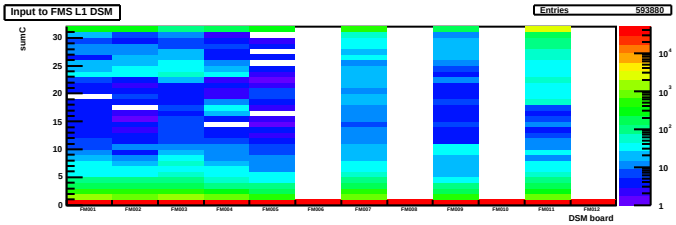

Input to FMS L0 DSM
Entries 4
HT ADC - simulated
QT board

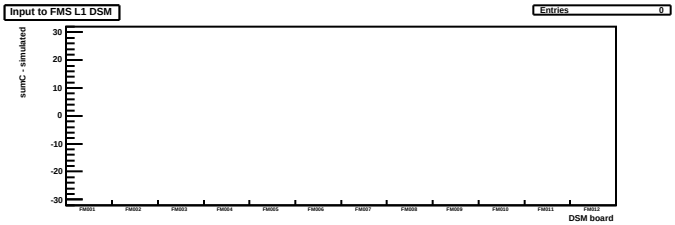

Input to FMS L0 DSM
Entries 2375520
HT ID
QT board

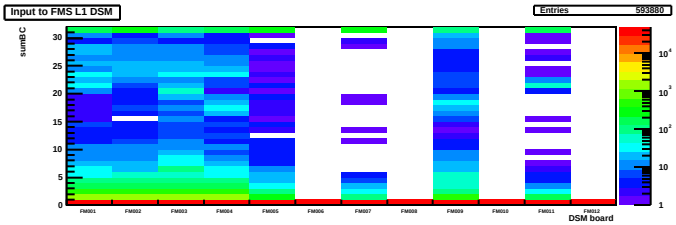

Input to FMS L0 DSM
Entries 51066
HT ID - simulated
QT board

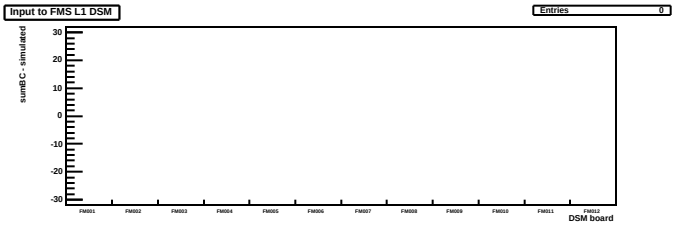

Input to FMS L1 DSM
Entries 593880
sumD
DSM board

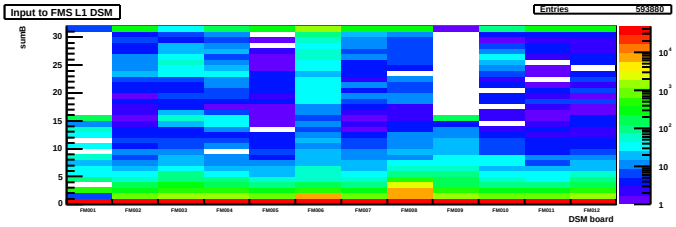

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

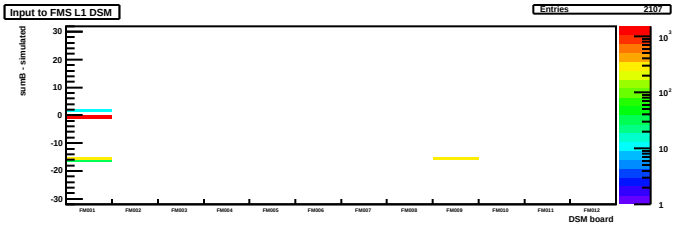

Input to FMS L1 DSM
Entries 593880
sumC
DSM board

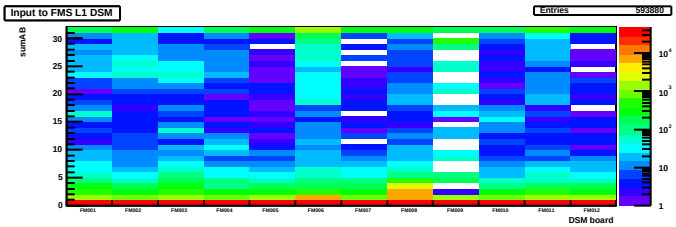

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

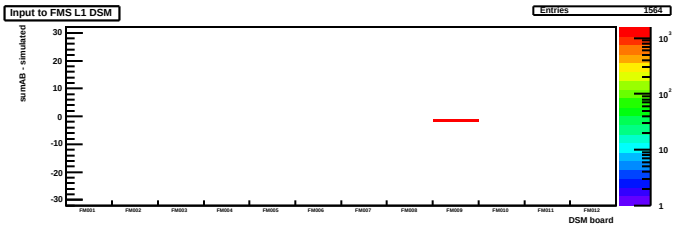

Input to FMS L1 DSM
Entries 593880
sumBC
DSM board

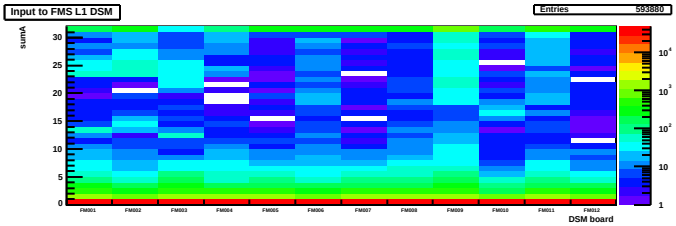

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

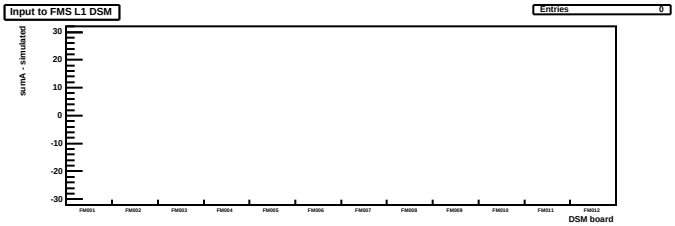

Input to FMS L1 DSM
Entries 593880
sumB
DSM board

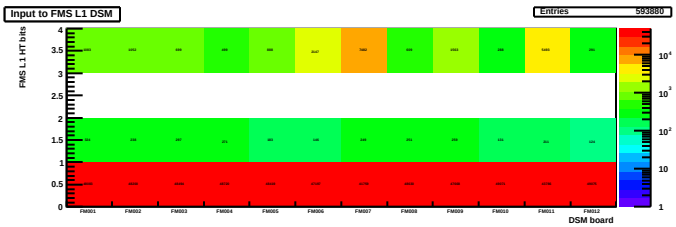

Input to FMS L1 DSM
Entries 2107
sumB - simulated
DSM board

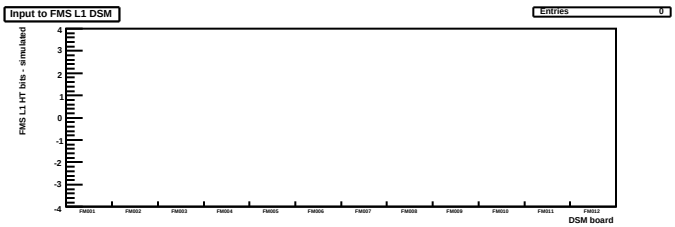

Input to FMS L1 DSM
Entries 593880
sumAB
DSM board

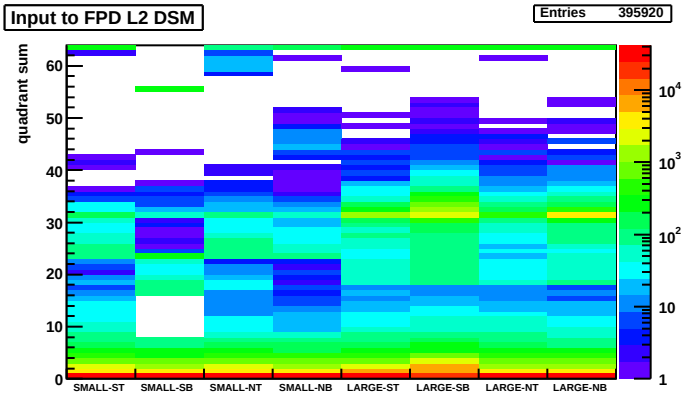

Input to FMS L1 DSM
Entries 1564
sumAB - simulated
DSM board

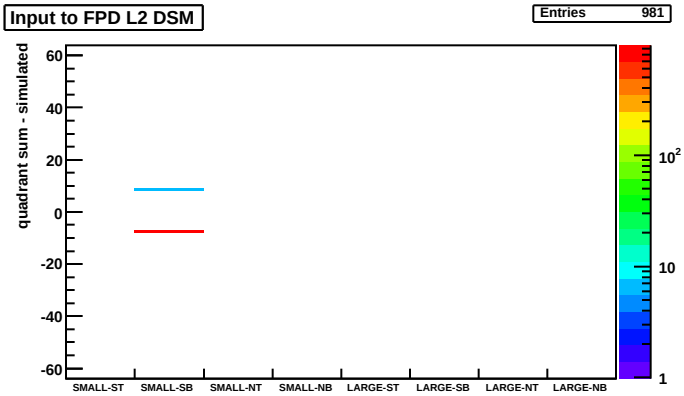

Input to FMS L1 DSM
Entries 593880
sumA
DSM board

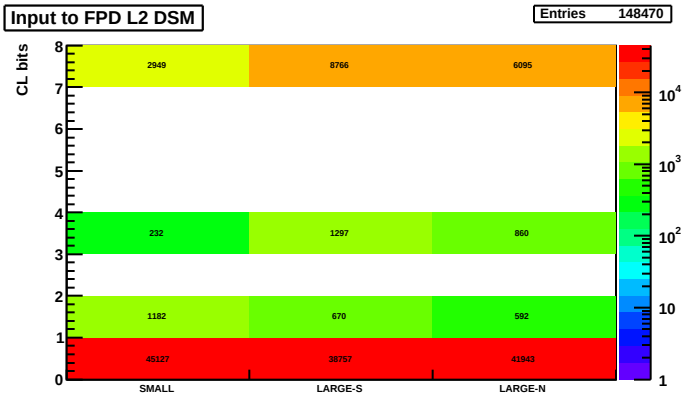

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

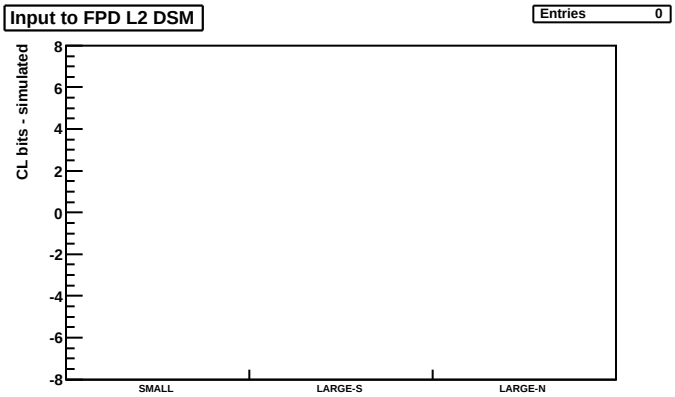

Input to FMS L1 DSM
Entries 593880
FMS L1 HT bits
DSM board

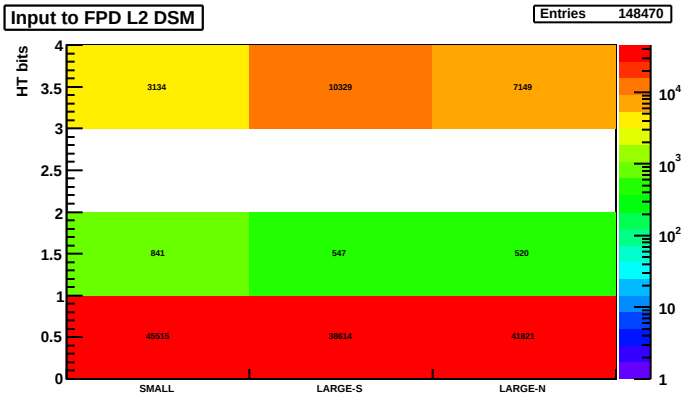

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

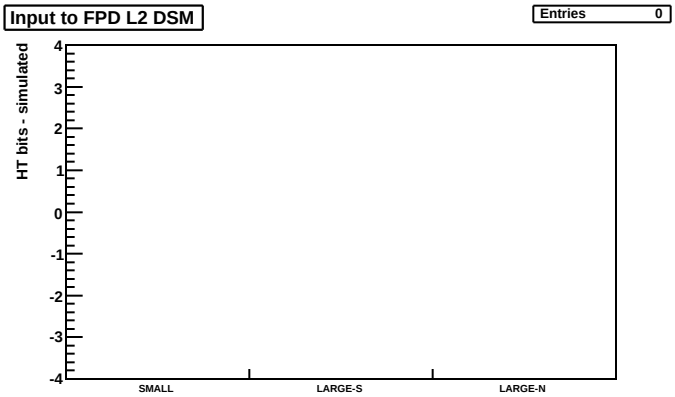

Input to FPD L2 DSM
Entries 395920
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

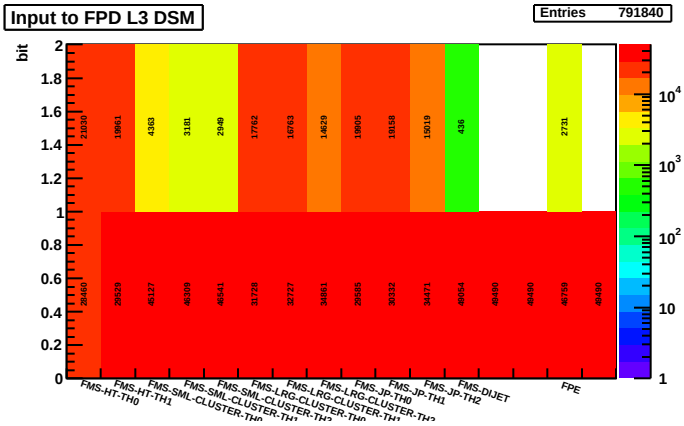

Input to FPD L2 DSM
Entries 981
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

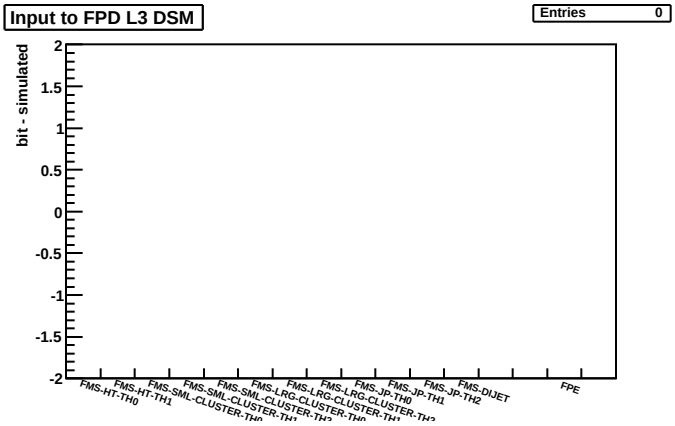

Input to FPD L2 DSM
Entries 148470
CL bits
2949
8766
6095
232
1297
860
1182
670
592
45127
38757
41943
SMALL
LARGE-S
LARGE-N

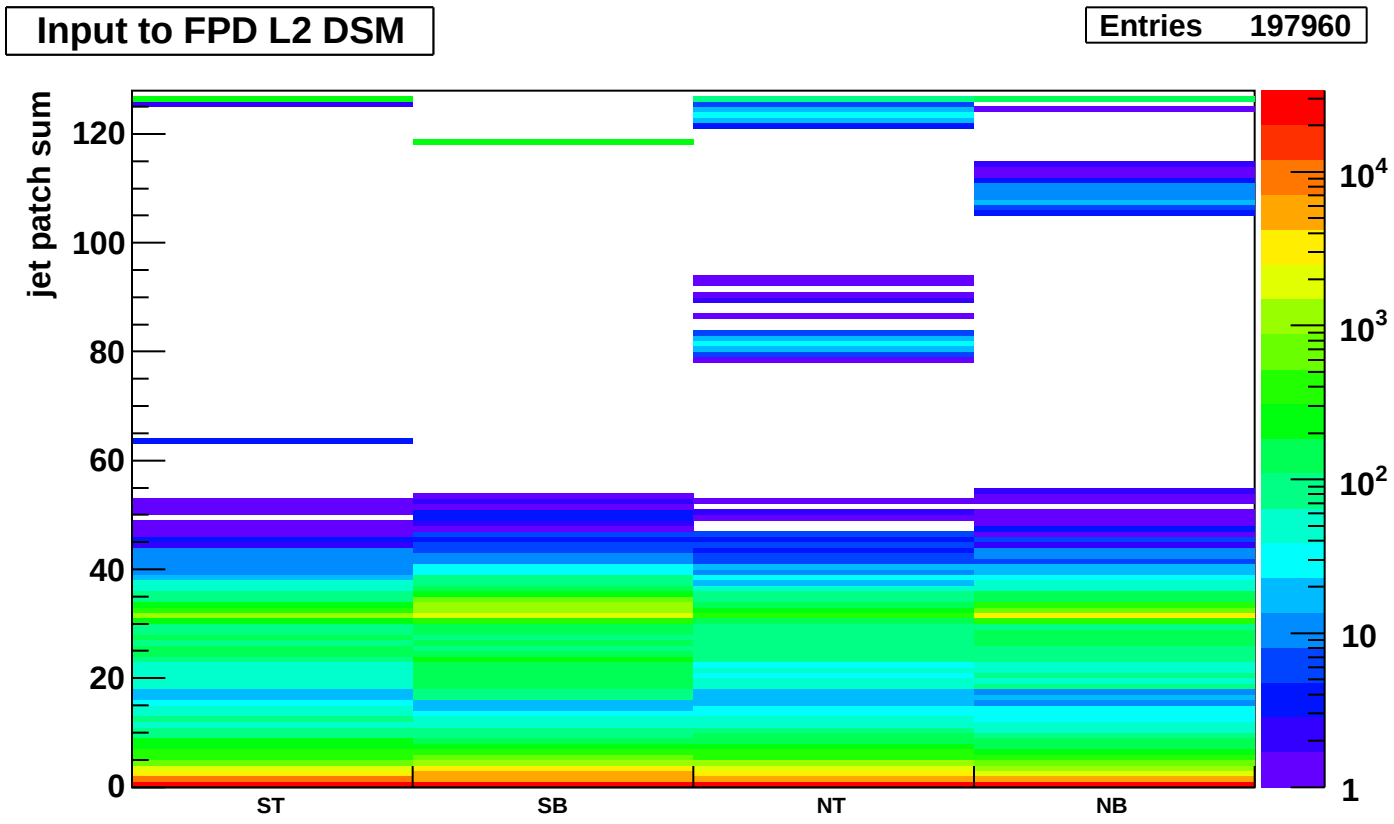

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

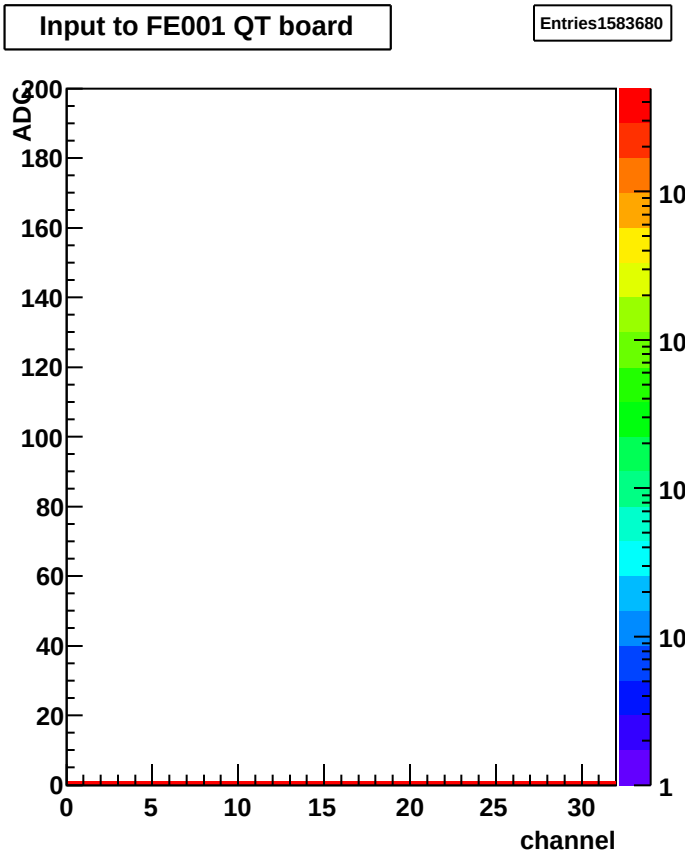

Input to FPD L2 DSM
Entries 148470
HT bits
3134
10329
7149
841
547
520
45515
38614
41821
SMALL
LARGE-S
LARGE-N

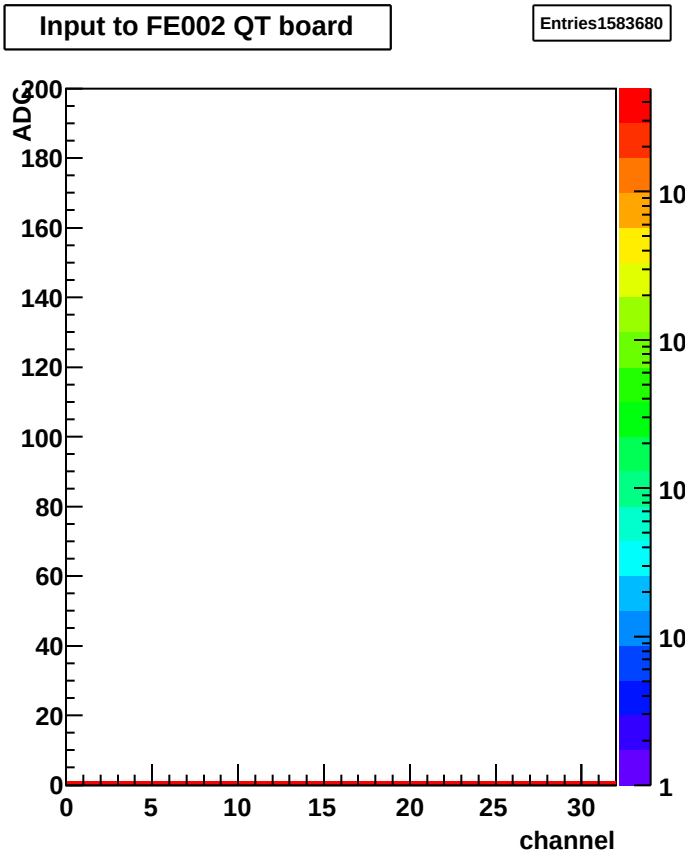

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

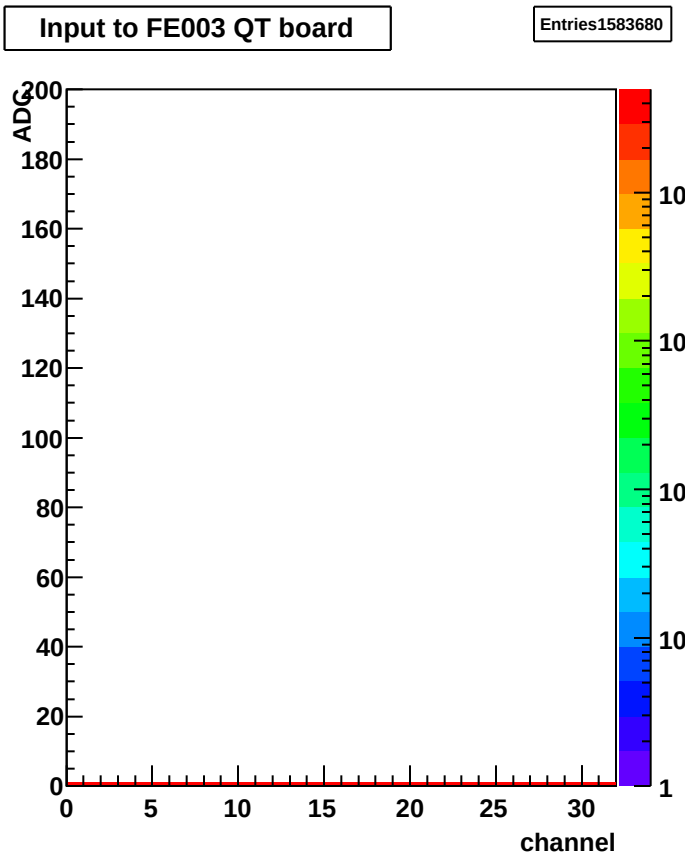

Input to FPD L3 DSM
Entries 791840
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

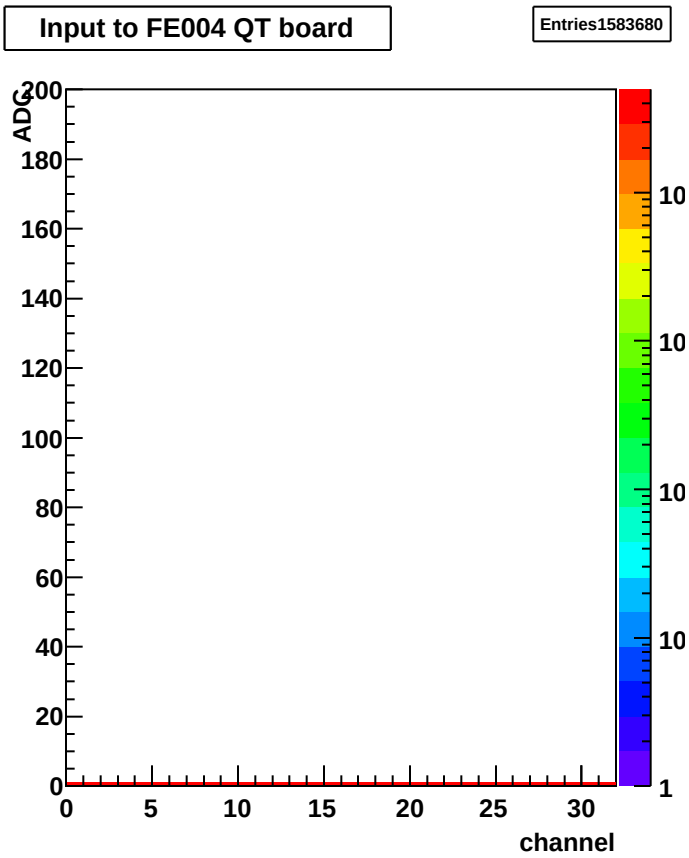

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

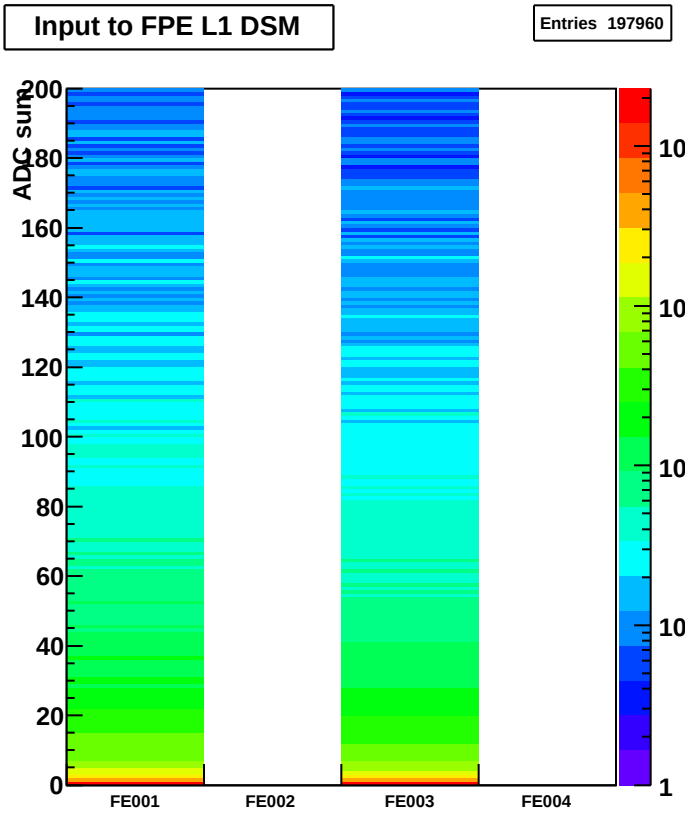

Input to FPD L2 DSM
Entries 197960
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

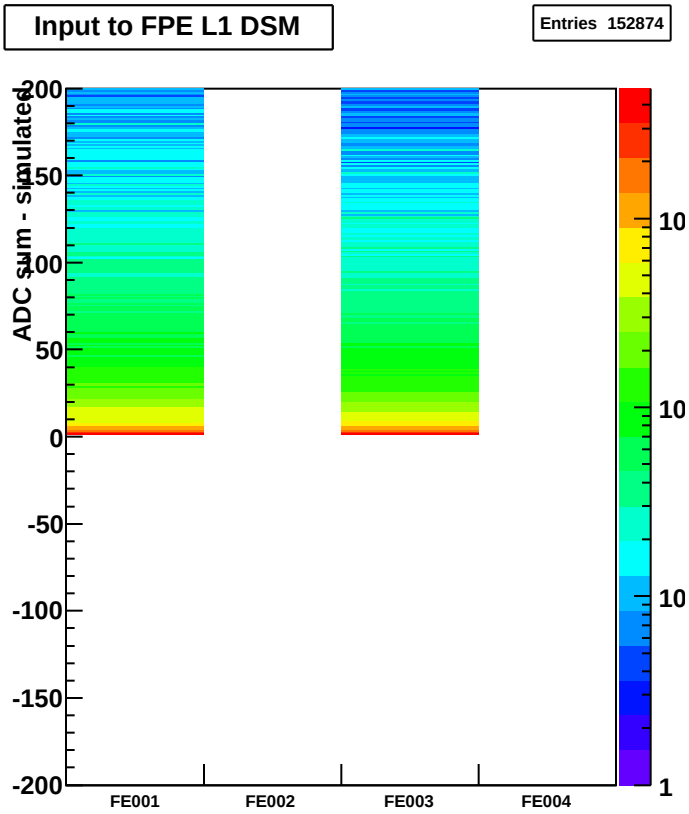

Input to FE001 QT board
Entries1583680
ADC
channel

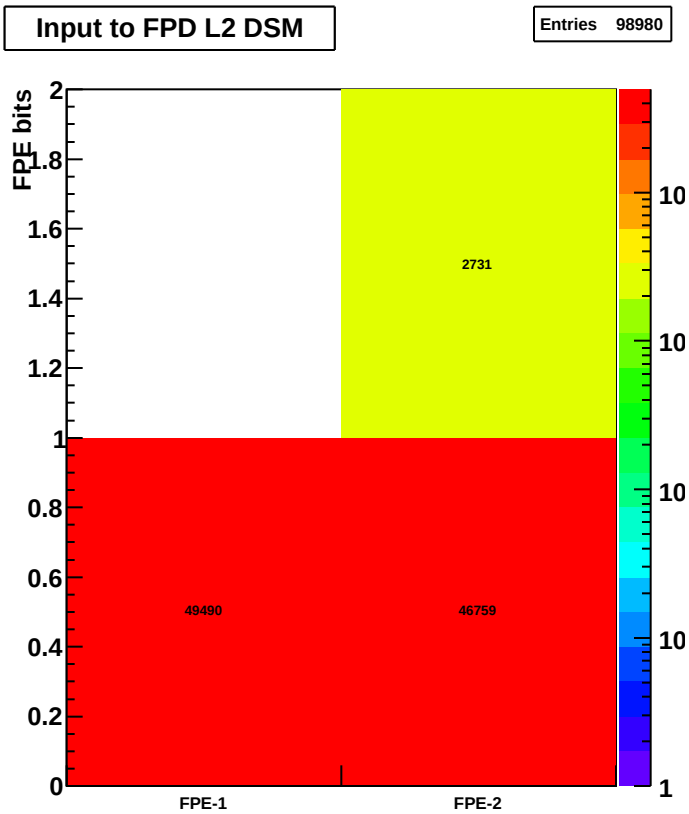

Input to FE002 QT board
Entries1583680
ADC
channel

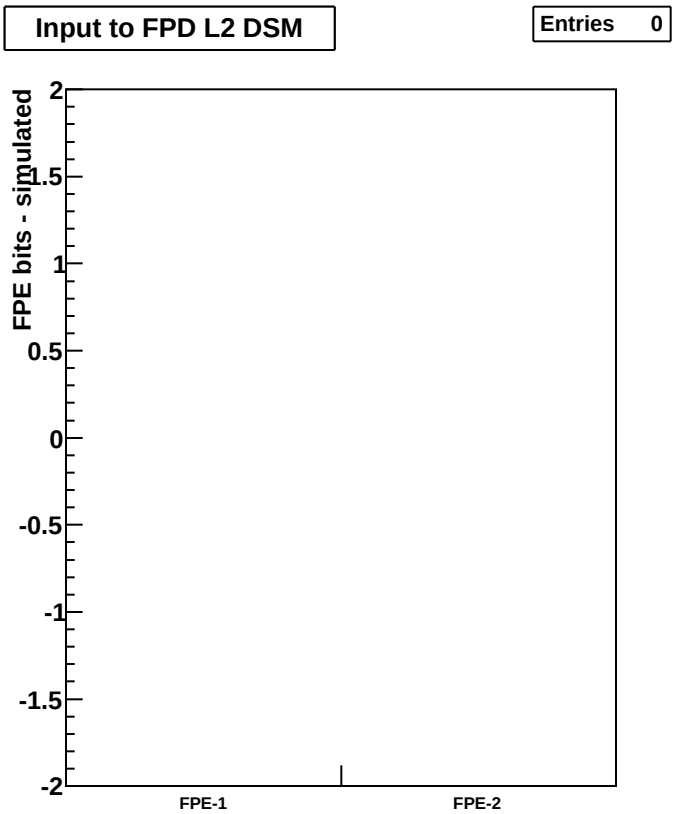

Input to FE003 QT board
Entries1583680
ADC
channel


Input to FE004 QT board
Entries1583680
ADC
channel


Input to FPE L1 DSM
Entries 197960
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 152874
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 98980
FPE bits
2731
49490
46759
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2