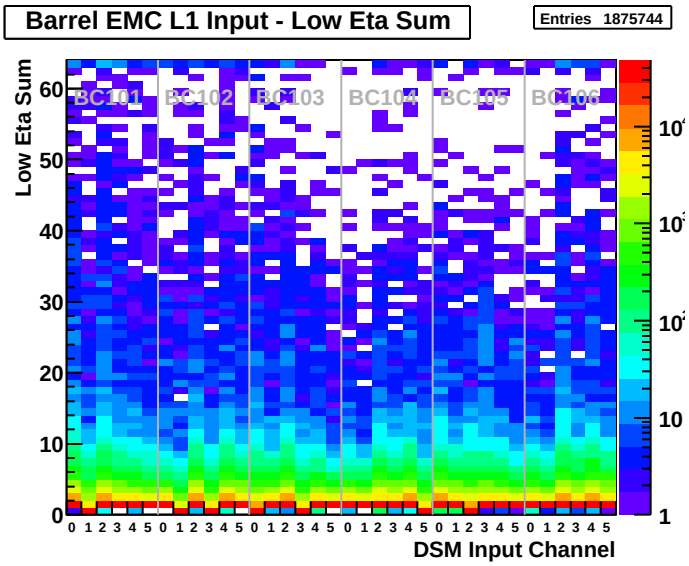
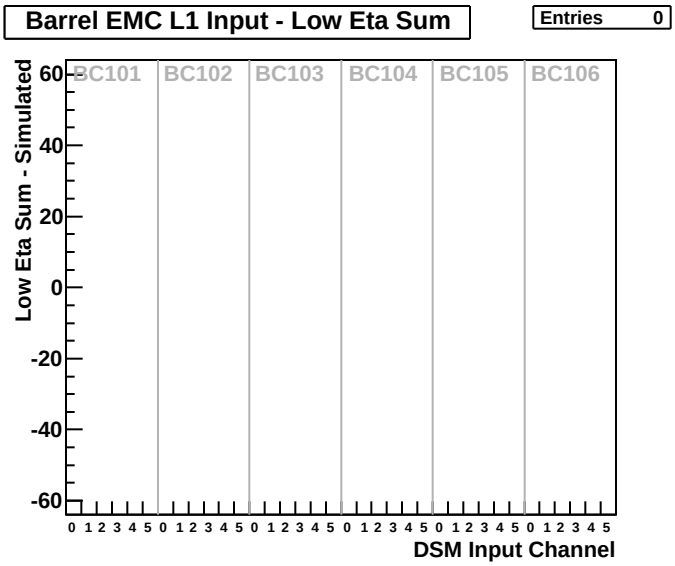
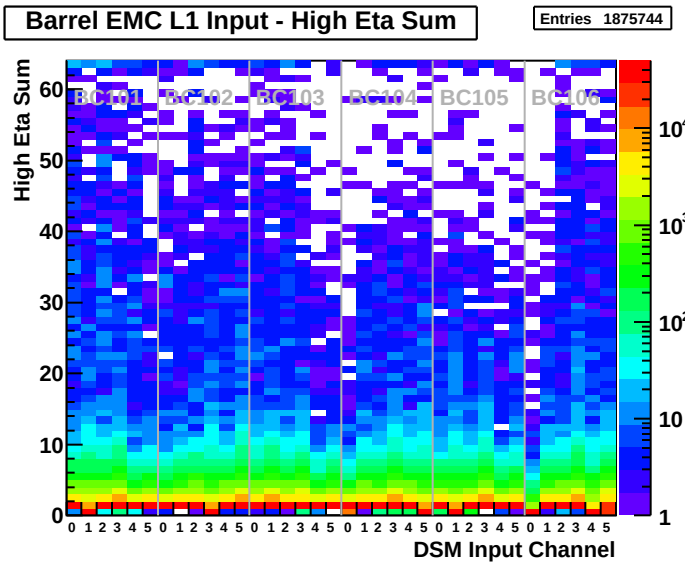
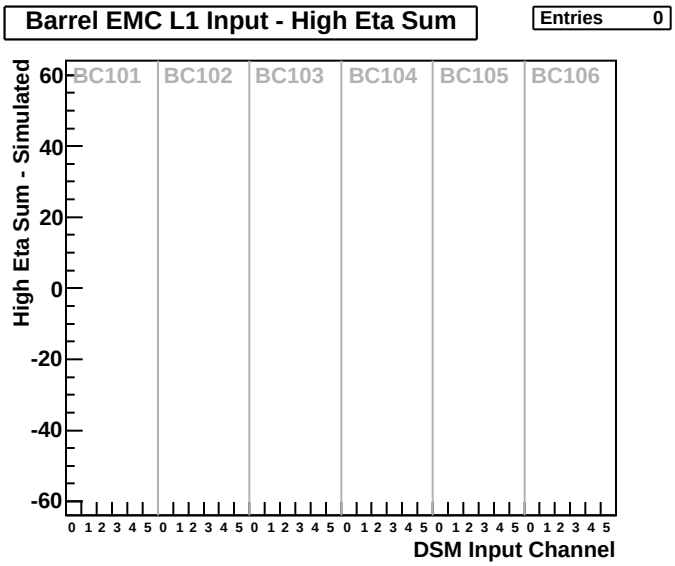
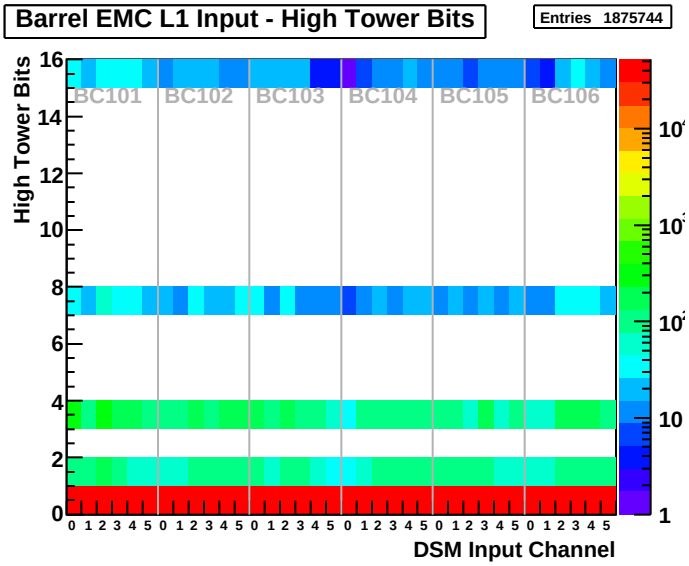
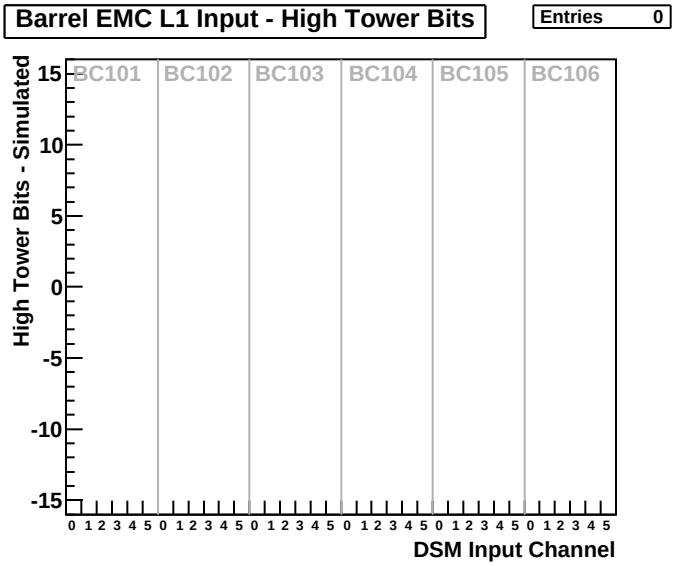

Barrel EMC L1 Input - Low Eta Sum
Entries 1875744
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

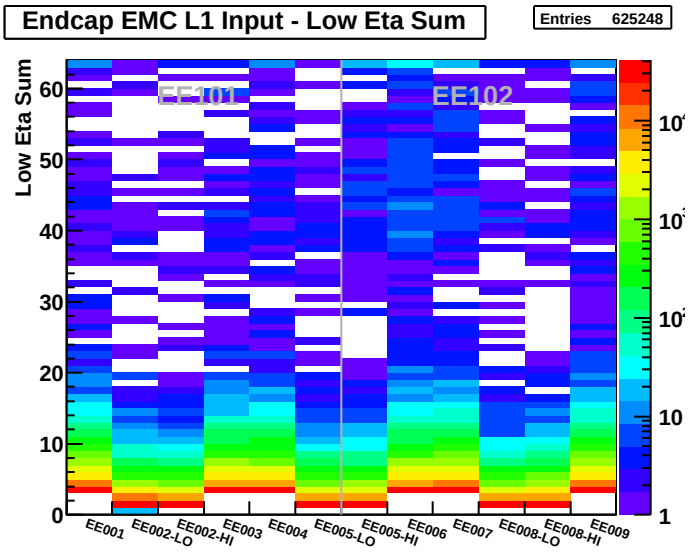

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

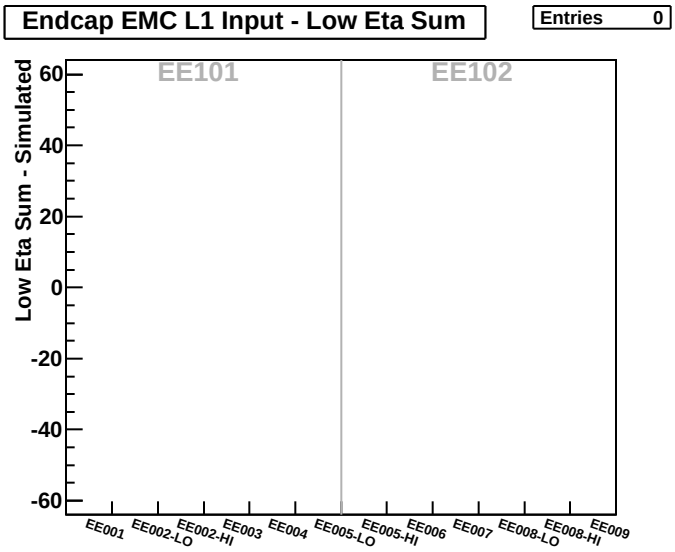

Barrel EMC L1 Input - High Eta Sum
Entries 1875744
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

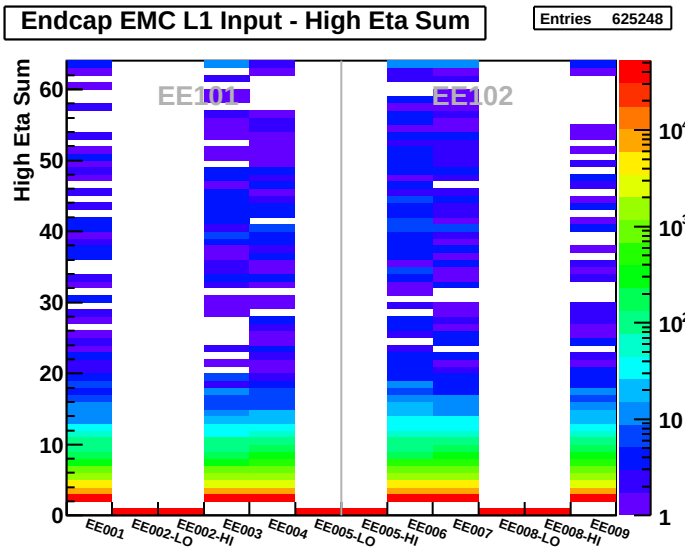

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

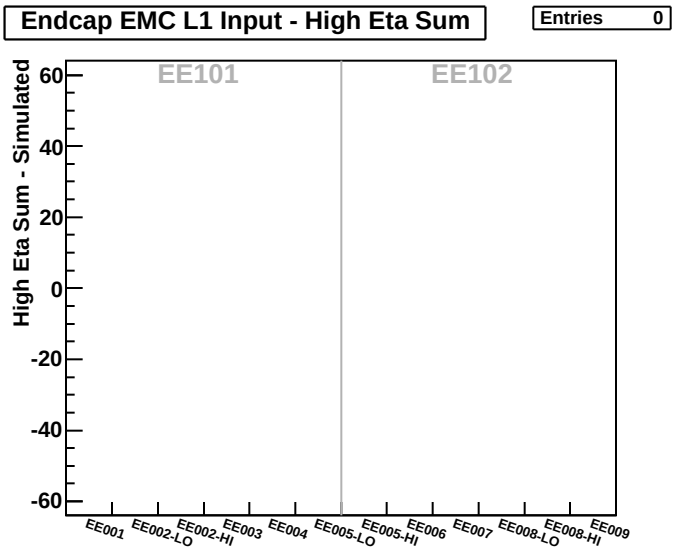

Barrel EMC L1 Input - High Tower Bits
Entries 1875744
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

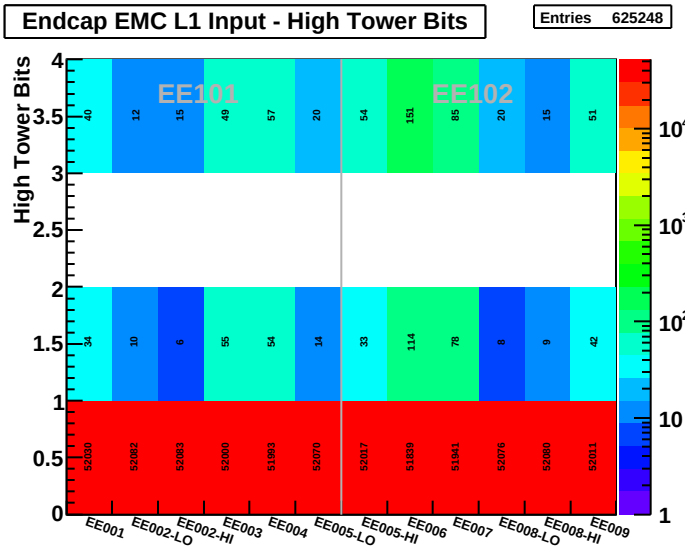

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

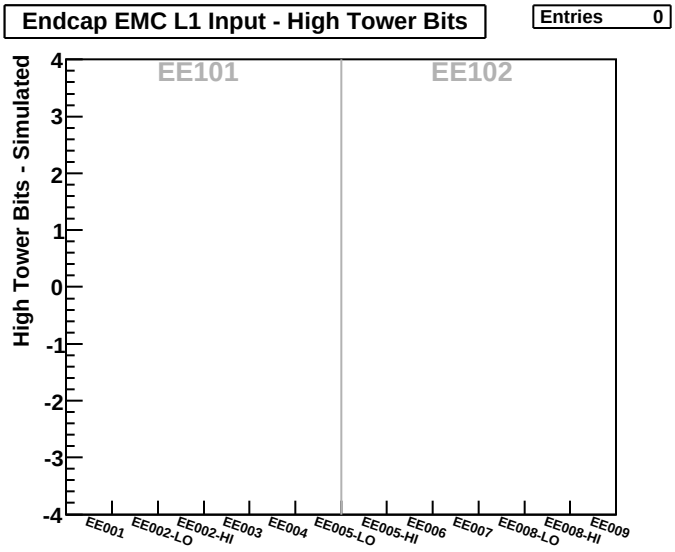

Endcap EMC L1 Input - Low Eta Sum
Entries 625248
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

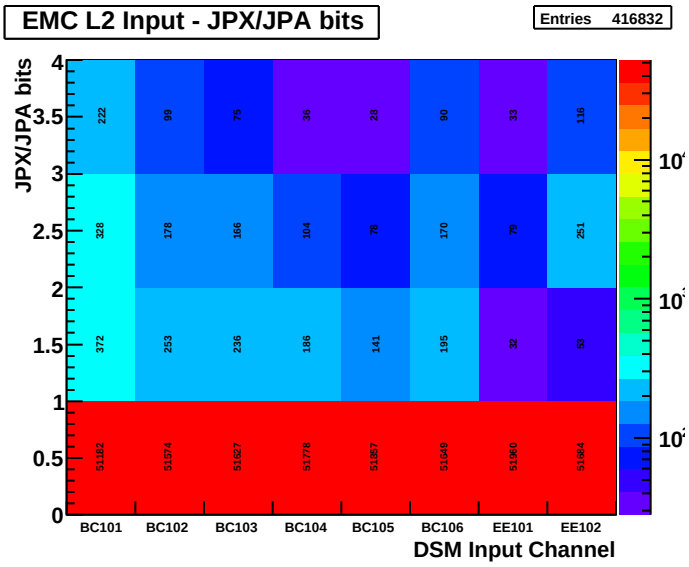

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

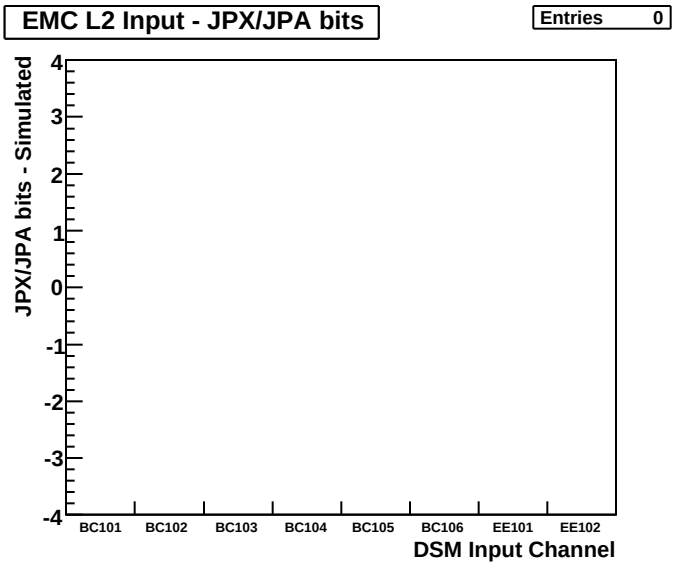

Endcap EMC L1 Input - High Eta Sum
Entries 625248
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

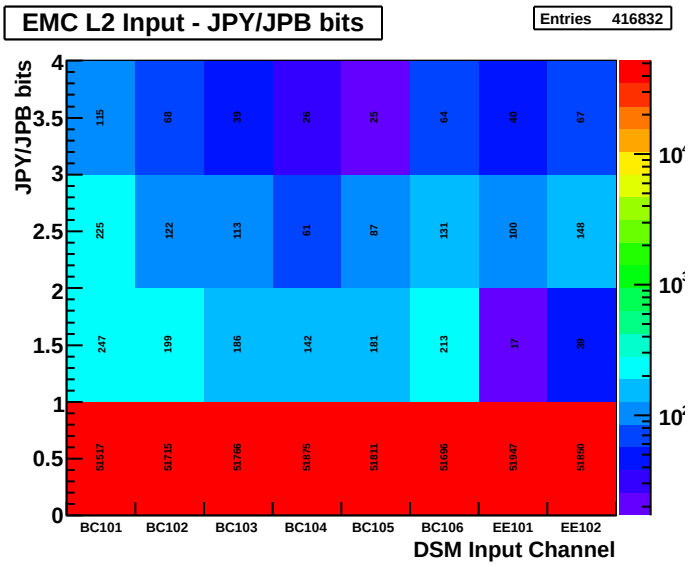

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

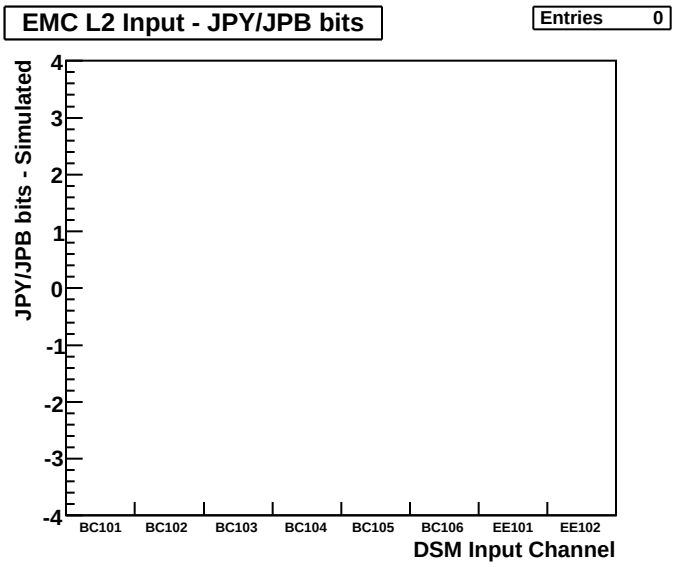

Endcap EMC L1 Input - High Tower Bits
Entries 625248
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

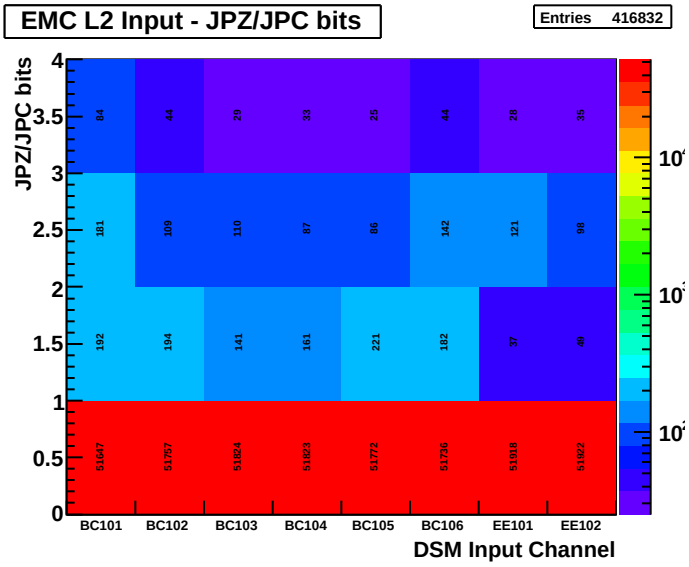

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

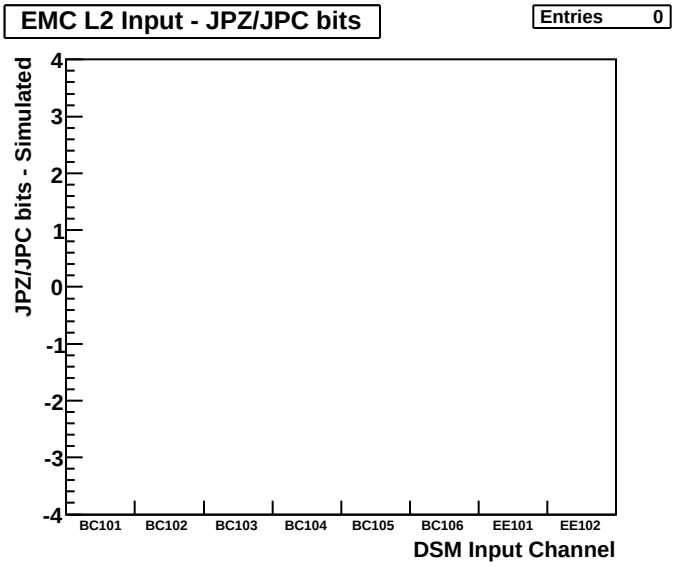

EMC L2 Input - JPX/JPA bits
Entries 416832
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
222 99 75 36 28 90 33 116
328 178 166 104 78 170 79 251
372 253 236 186 141 195 32 53
51182 51574 51627 51778 51857 51649 51960 51884

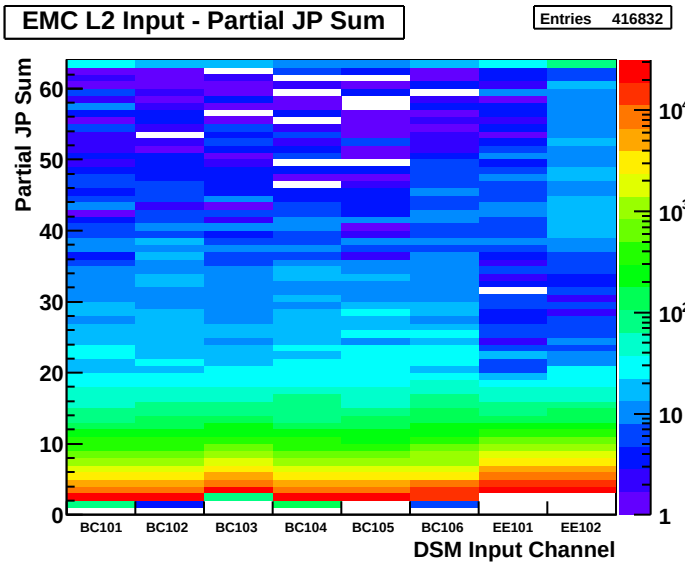

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

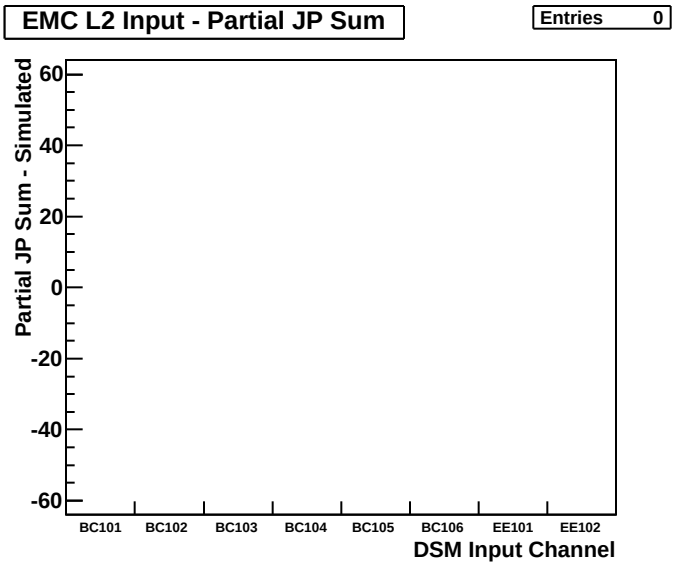

EMC L2 Input - JPY/JPB bits
Entries 416832
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
115 68 39 26 25 64 40 67
225 122 113 61 87 131 100 148
247 199 186 142 181 213 17 38
51517 51715 51766 51875 51811 51696 51947 51950

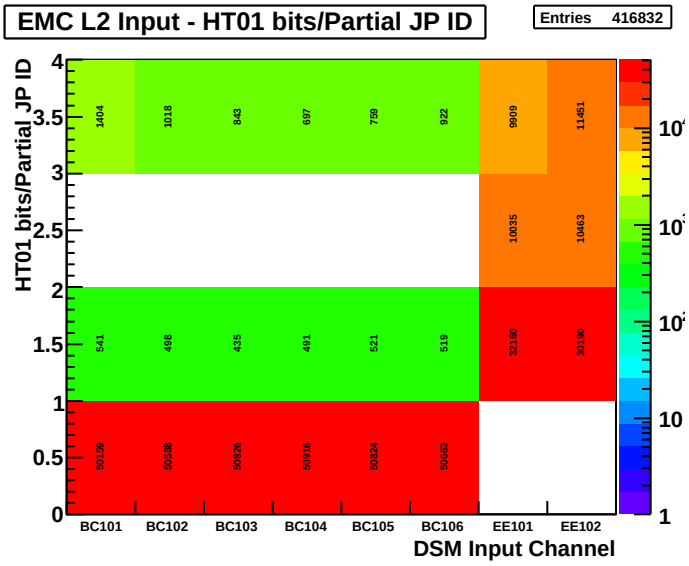

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

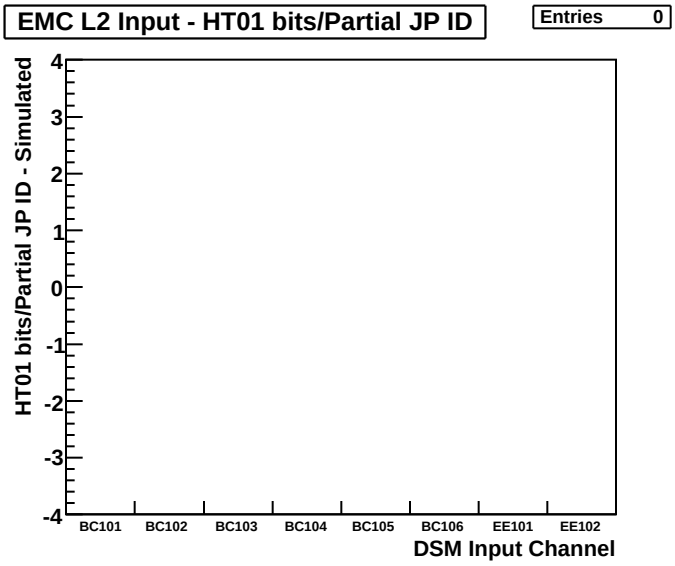

EMC L2 Input - JPZ/JPC bits
Entries 416832
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
84 44 29 33 25 44 28 35
181 109 110 87 86 142 121 98
192 194 141 161 221 182 37 49
51647 51757 51824 51823 51772 51736 51918 51922

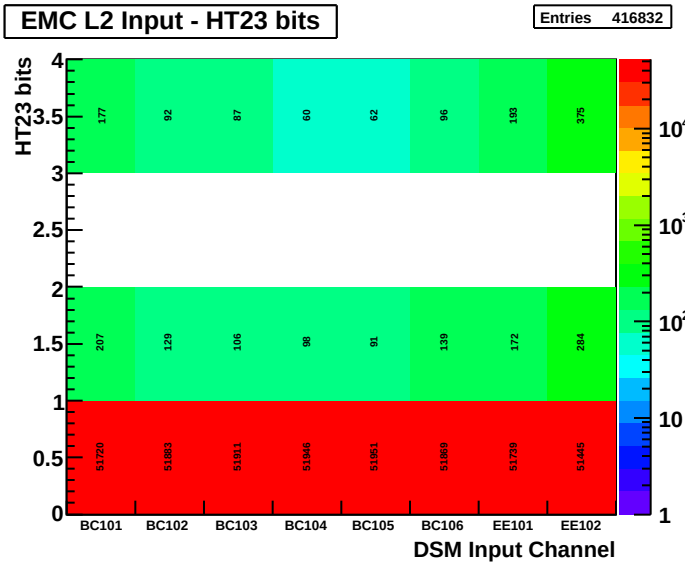

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

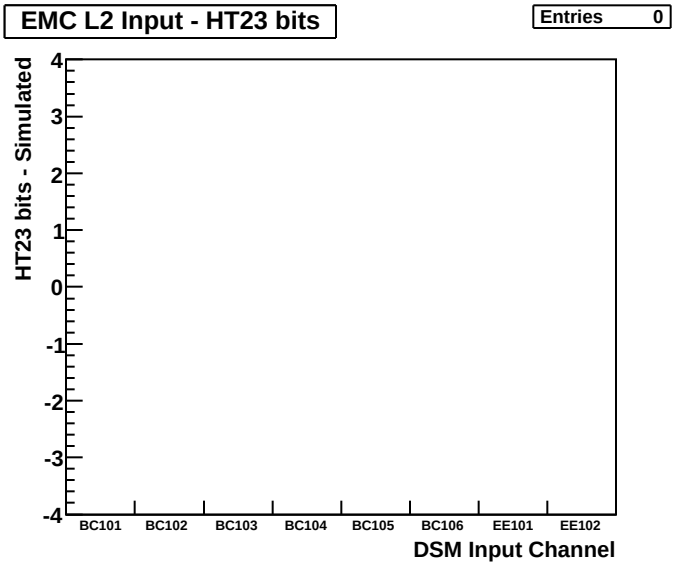

EMC L2 Input - Partial JP Sum
Entries 416832
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

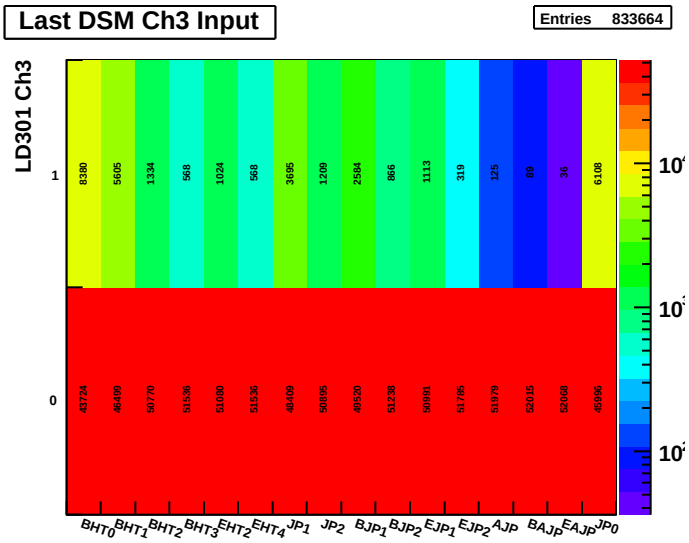

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

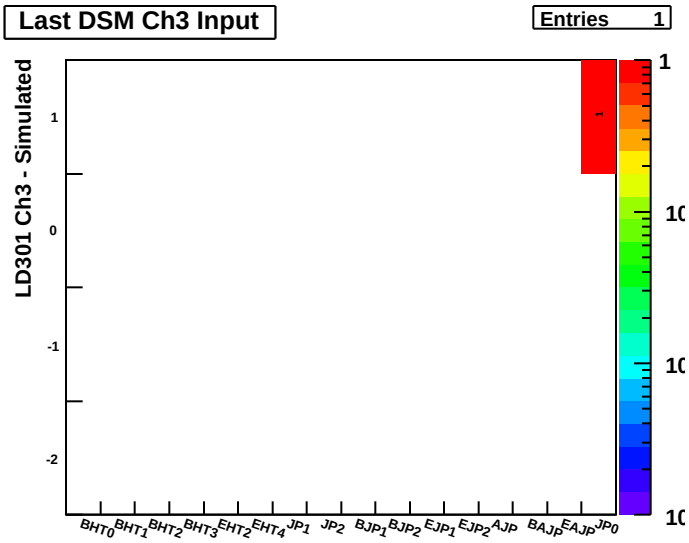

EMC L2 Input - HT01 bits/Partial JP ID
Entries 416832
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

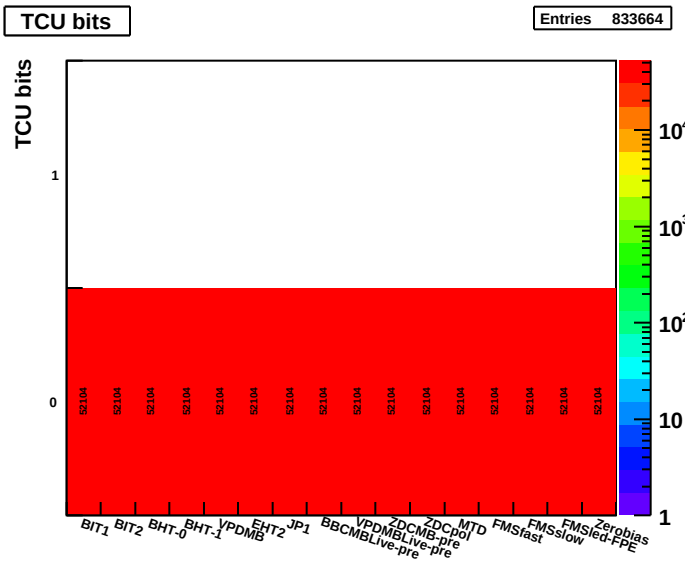

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

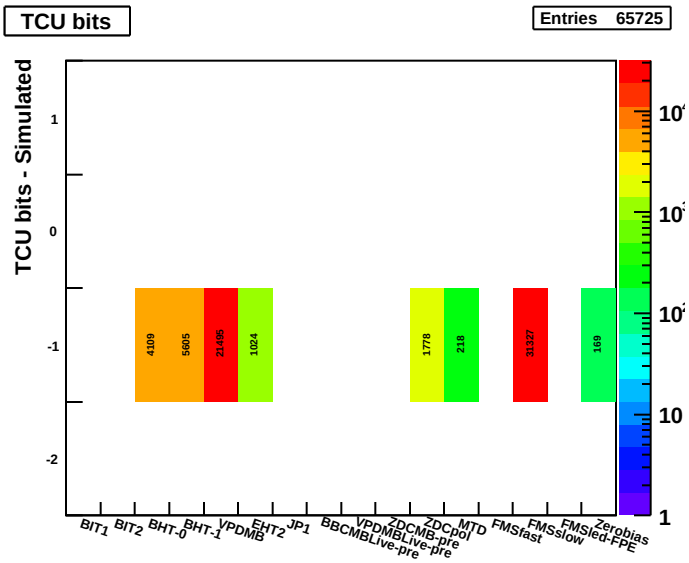

EMC L2 Input - HT23 bits
Entries 416832
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

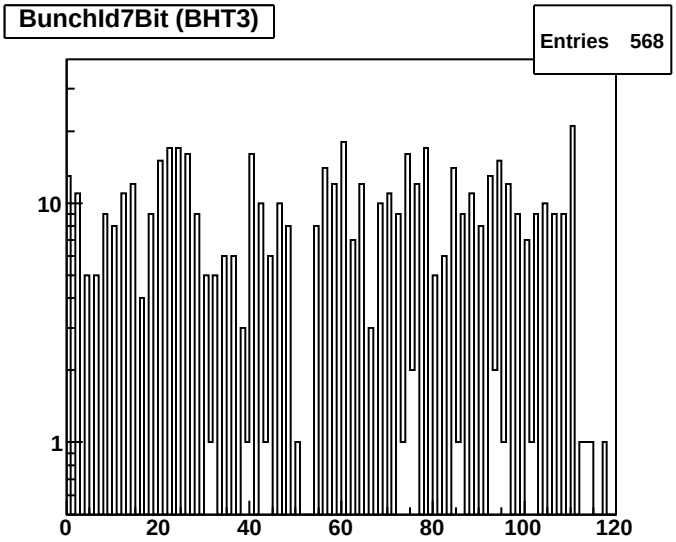

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

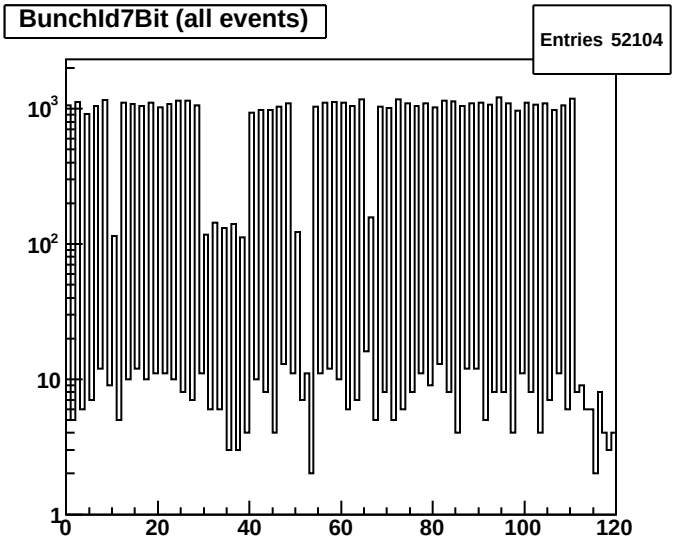

Last DSM Ch3 Input
Entries 833664
LD301 Ch3
8380 5605 1334 568 1024 568 3695 1209 2584 866 1113 319 125 89 36 6108
43724 46499 50770 51536 51080 51536 49409 50695 49520 51238 50991 51785 51979 52015 52068 45996
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

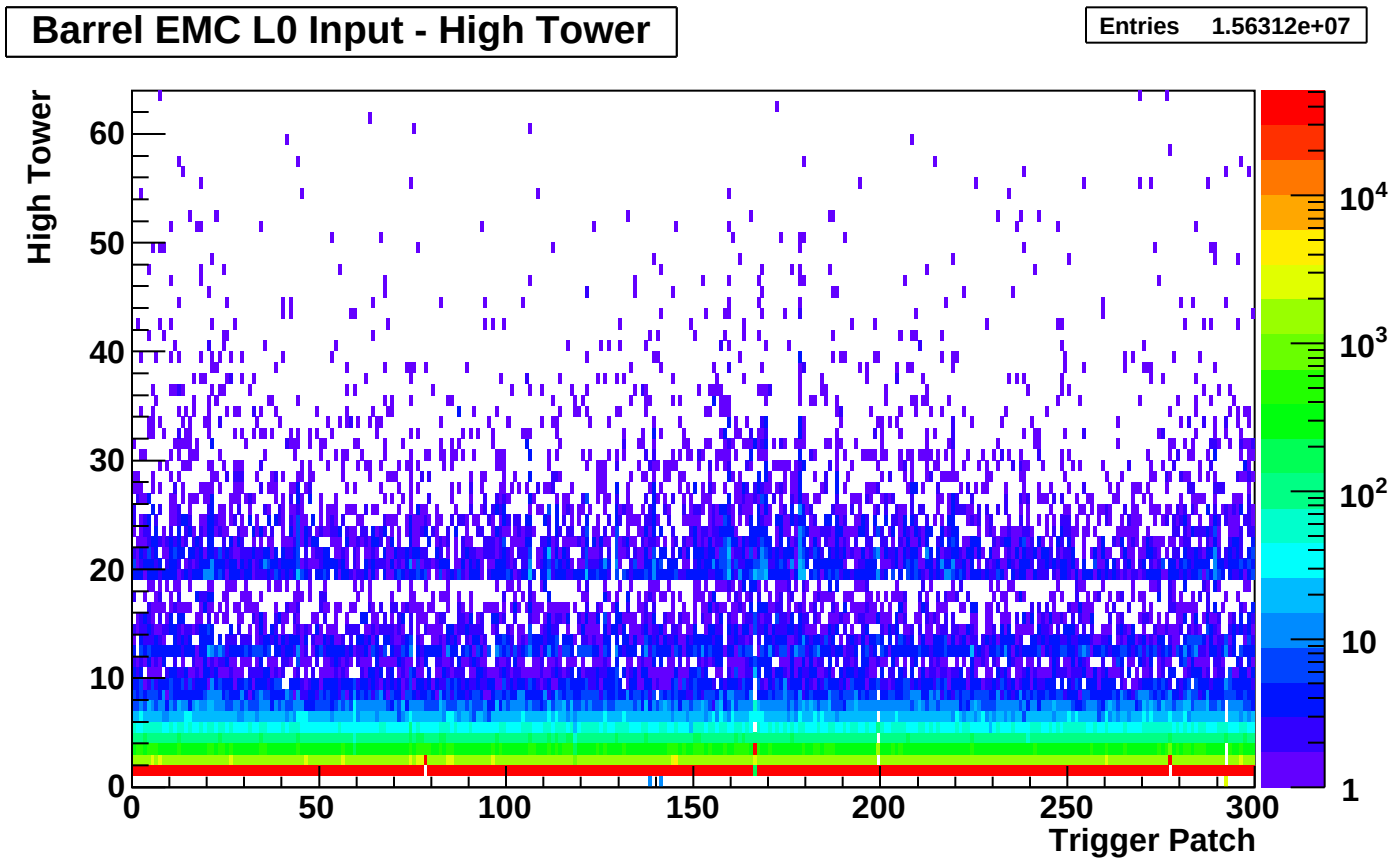

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

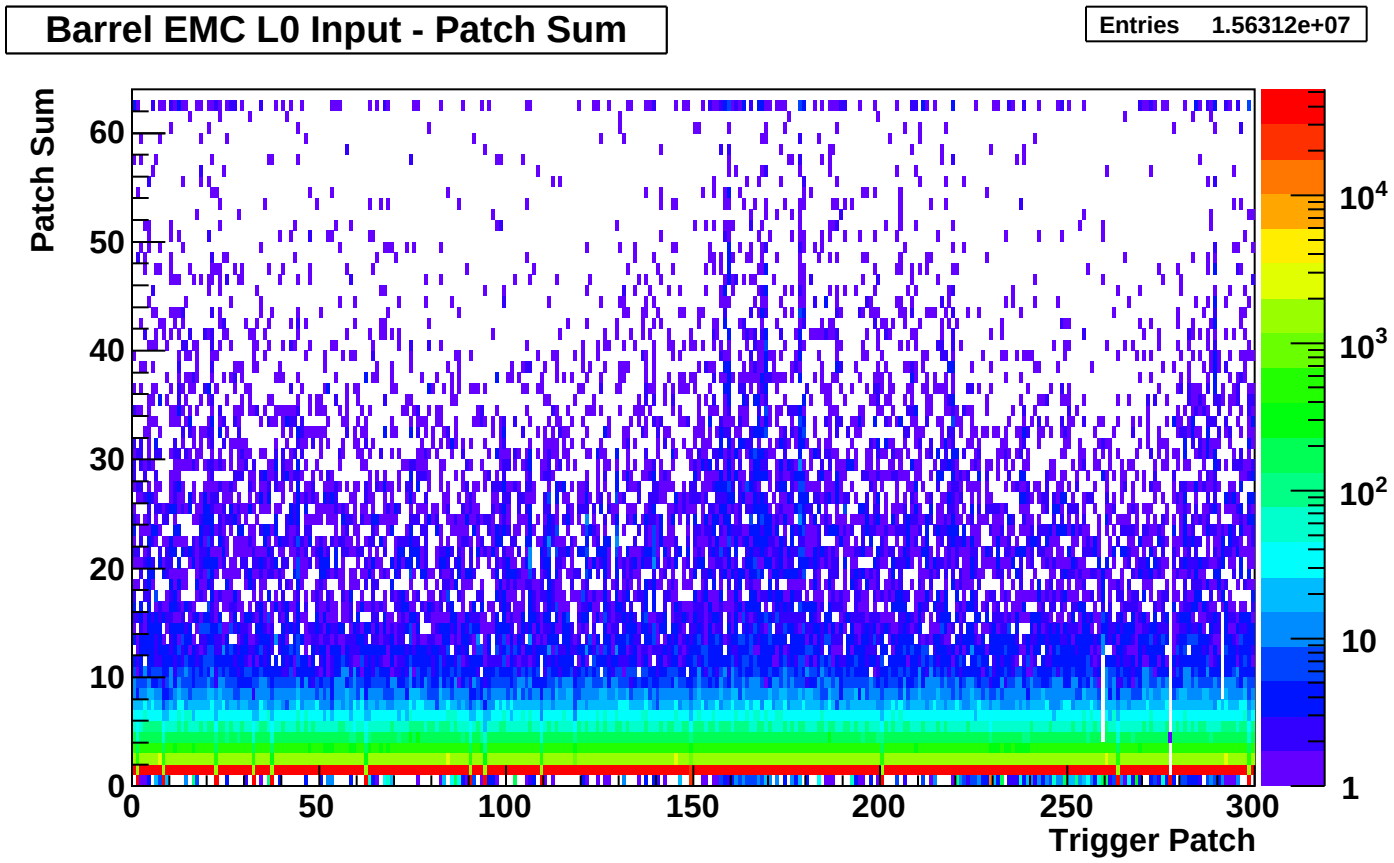

TCU bits
Entries 833664
TCU bits
52104
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

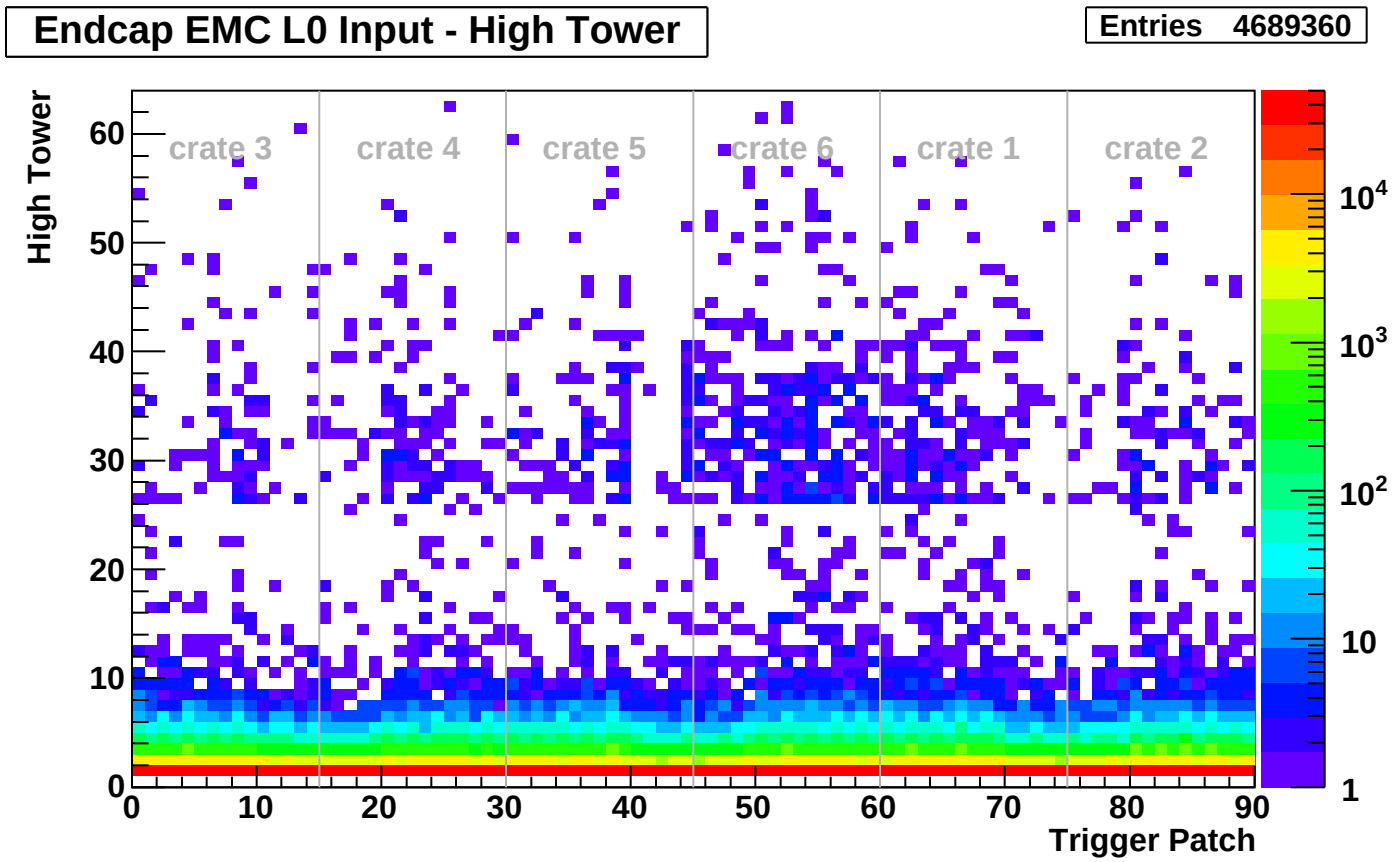

TCU bits
Entries 65725
TCU bits - Simulated
4109 5605 21495 1024 1778 218 31327 169
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

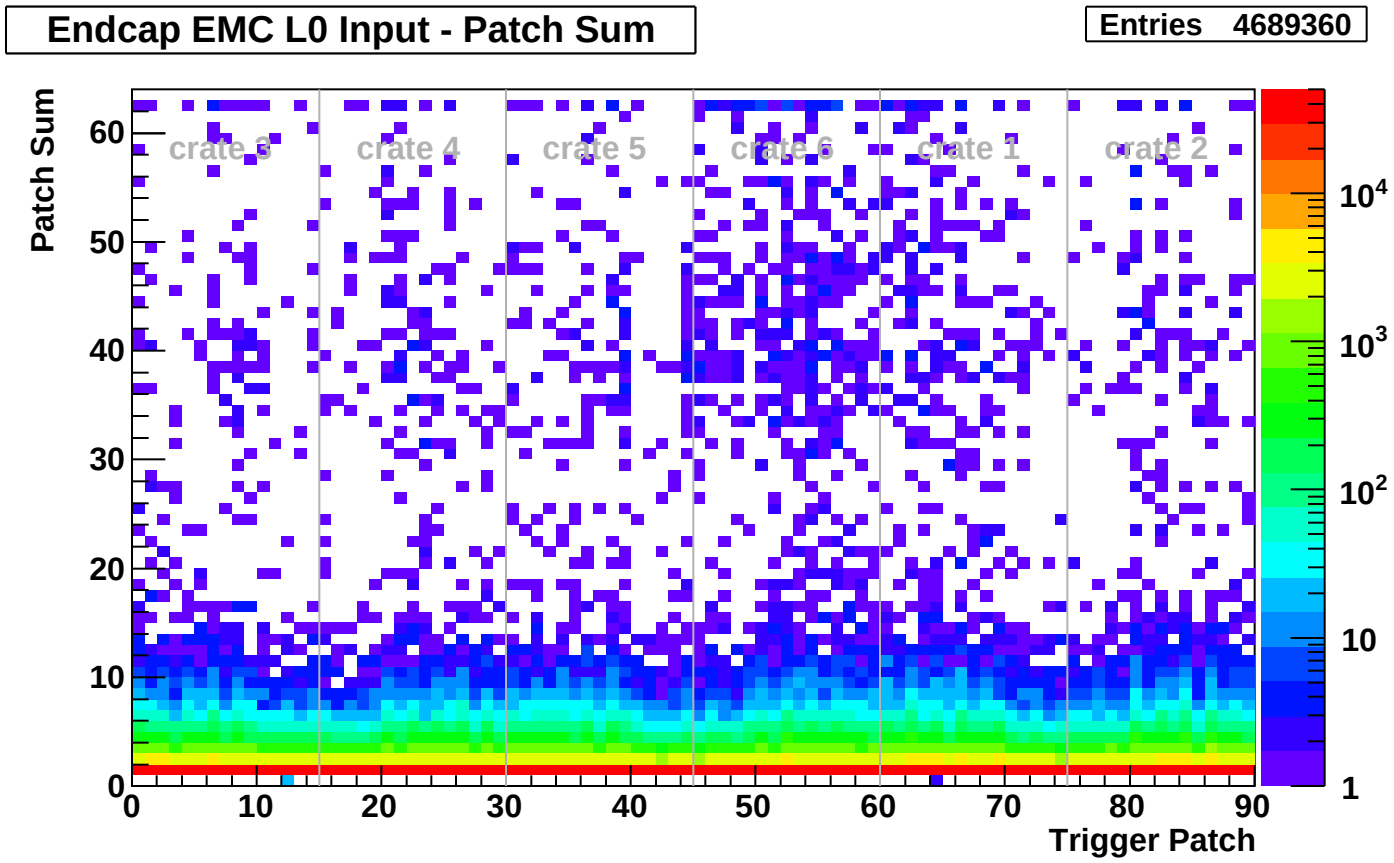

BunchId7Bit (BHT3)
Entries 568

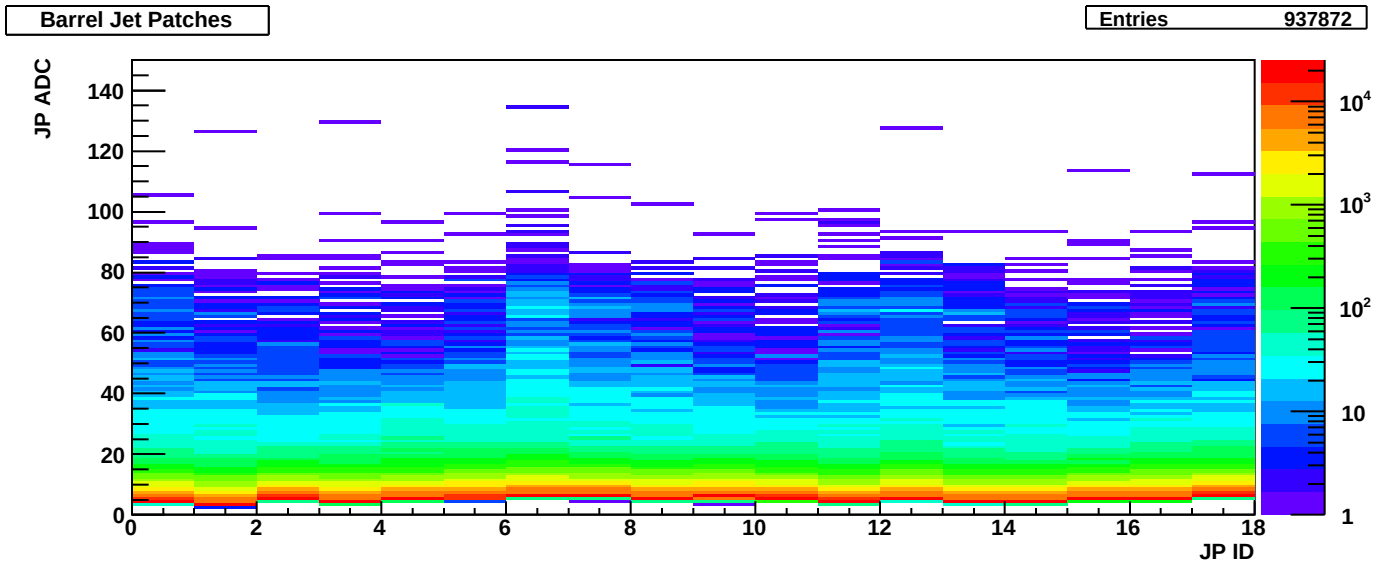

BunchId7Bit (all events)
Entries 52104

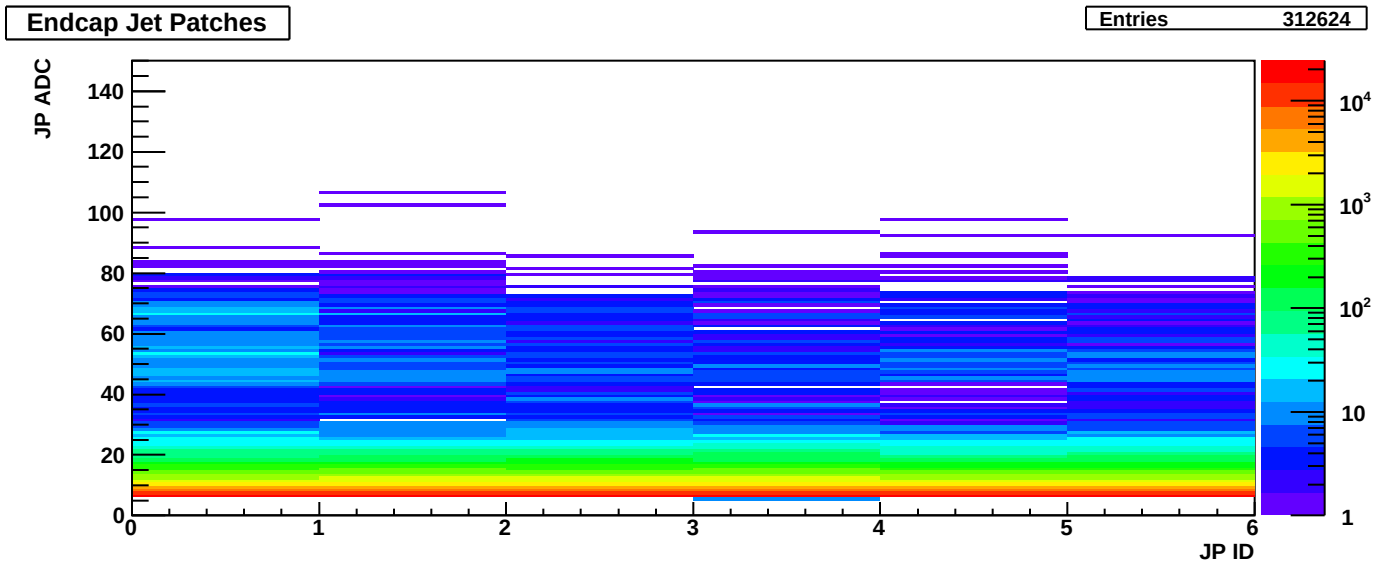

Barrel EMC L0 Input - High Tower
Entries 1.56312e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

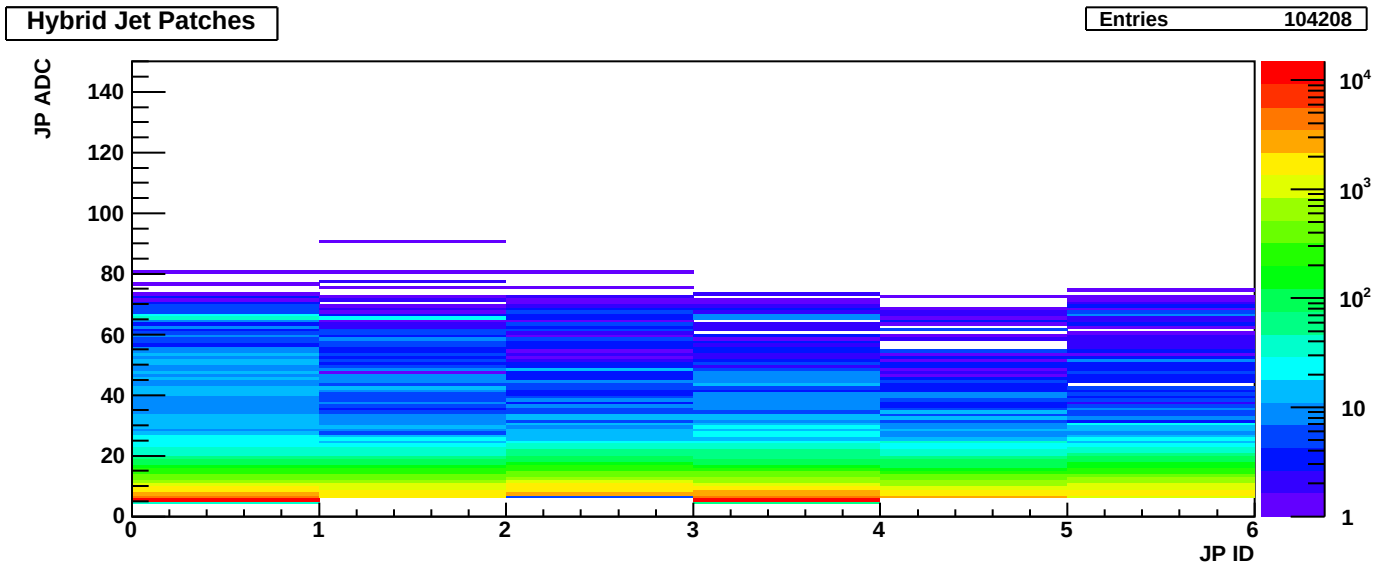

Barrel EMC L0 Input - Patch Sum
Entries 1.56312e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

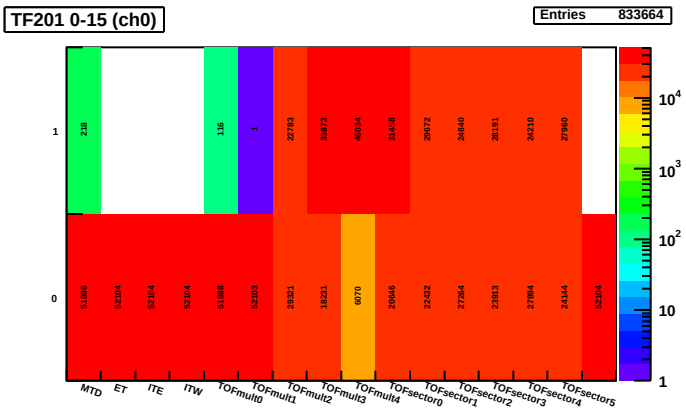

Endcap EMC L0 Input - High Tower
Entries 4689360
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

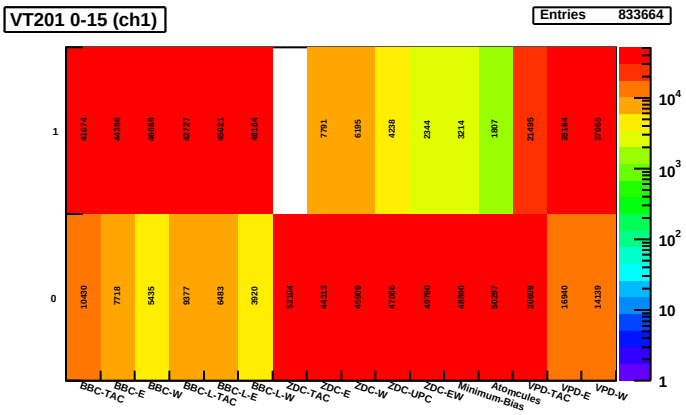

Endcap EMC L0 Input - Patch Sum
Entries 4689360
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

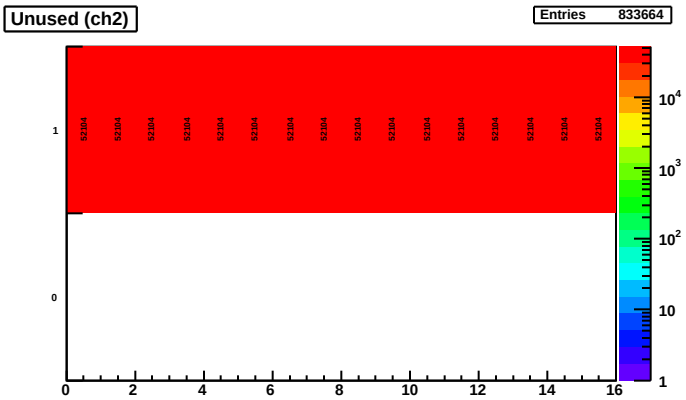

Barrel Jet Patches
Entries 937872
JP ADC
JP ID

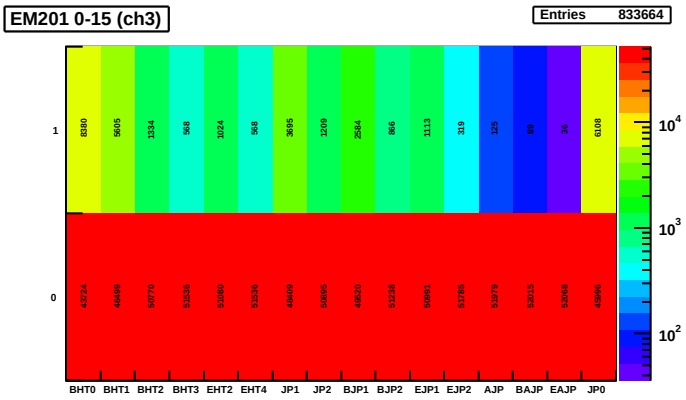

Endcap Jet Patches
Entries 312624
JP ADC
JP ID

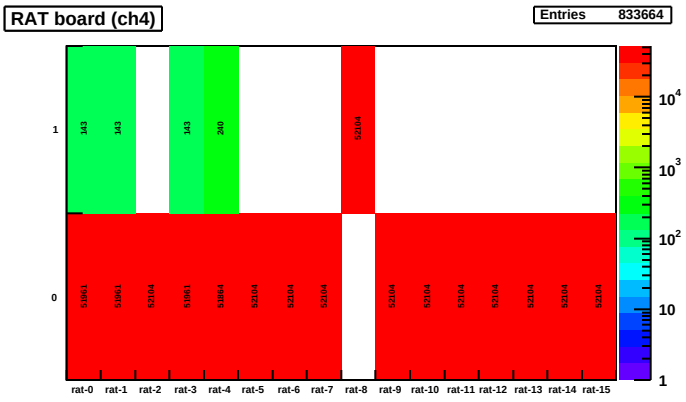

Hybrid Jet Patches
Entries 104208
JP ADC
JP ID

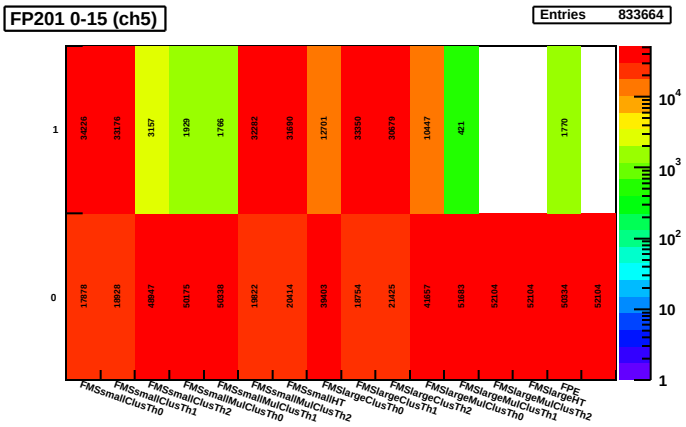

TF201 0-15 (ch0)
Entries 833664
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

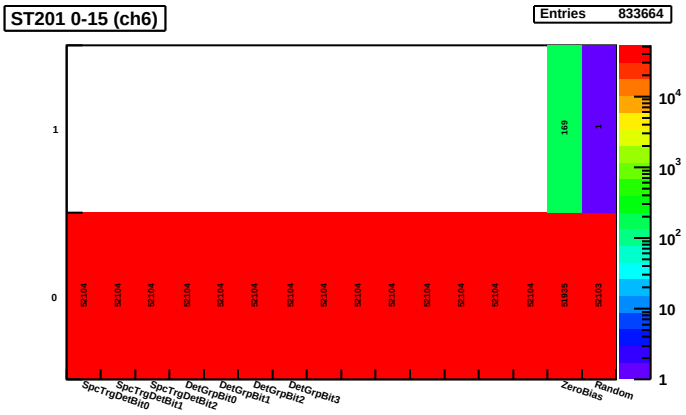

VT201 0-15 (ch1)
Entries 833664
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

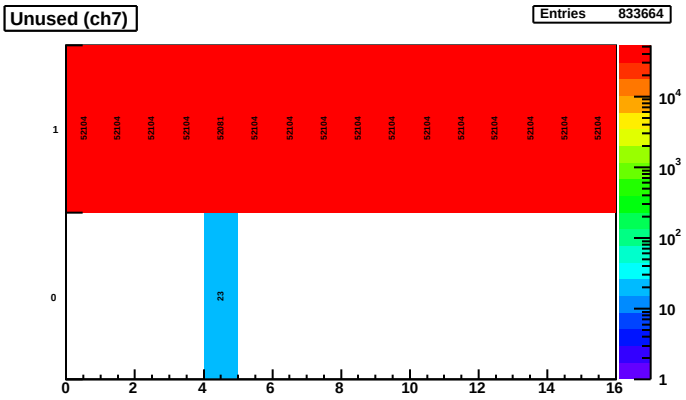

Unused (ch2)
Entries 833664

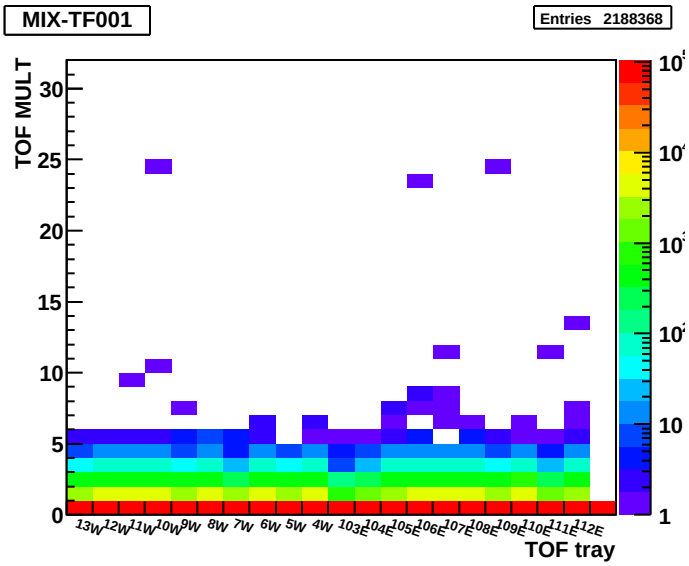

EM201 0-15 (ch3)
Entries 833664
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

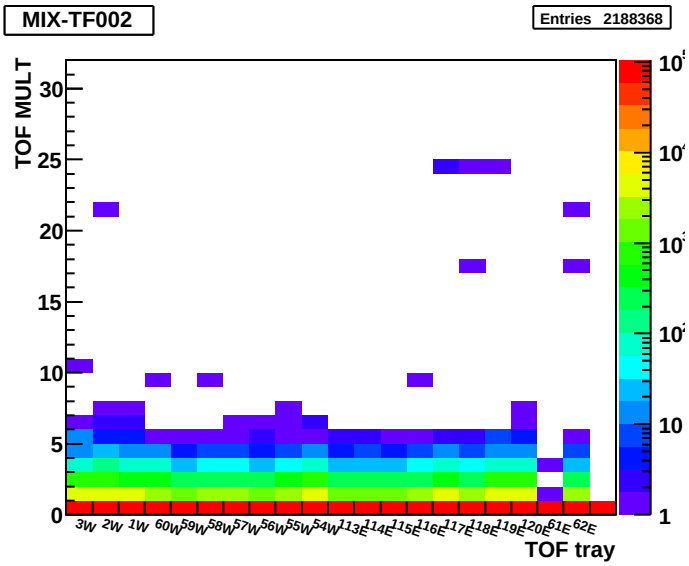

RAT board (ch4)
Entries 833664
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

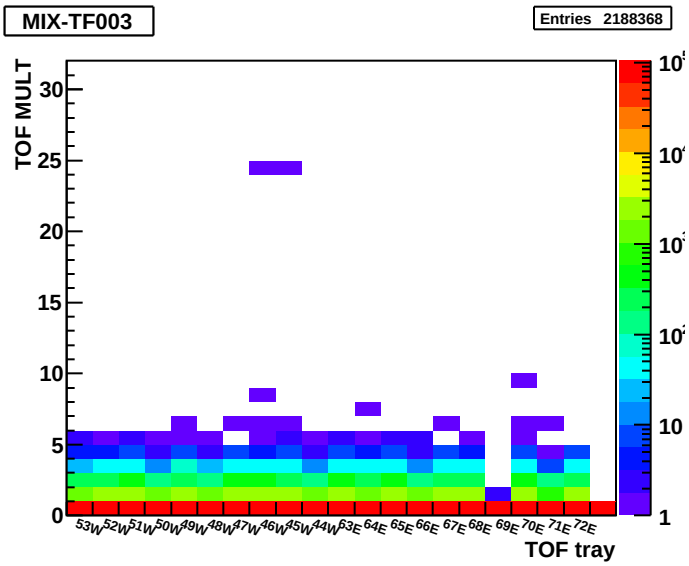

FP201 0-15 (ch5)
Entries 833664
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

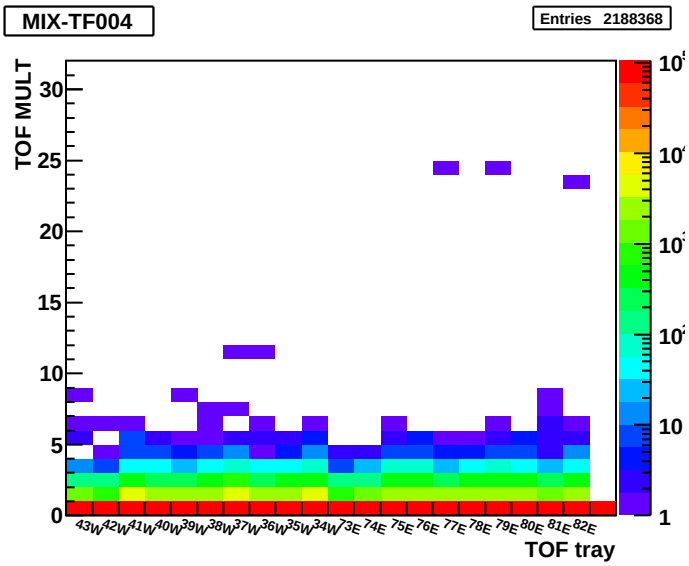

ST201 0-15 (ch6)
Entries 833664
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

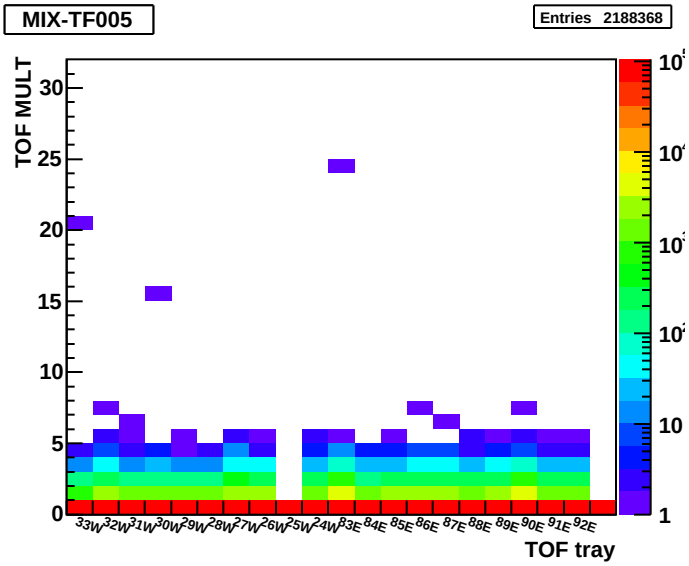

Unused (ch7)
Entries 833664

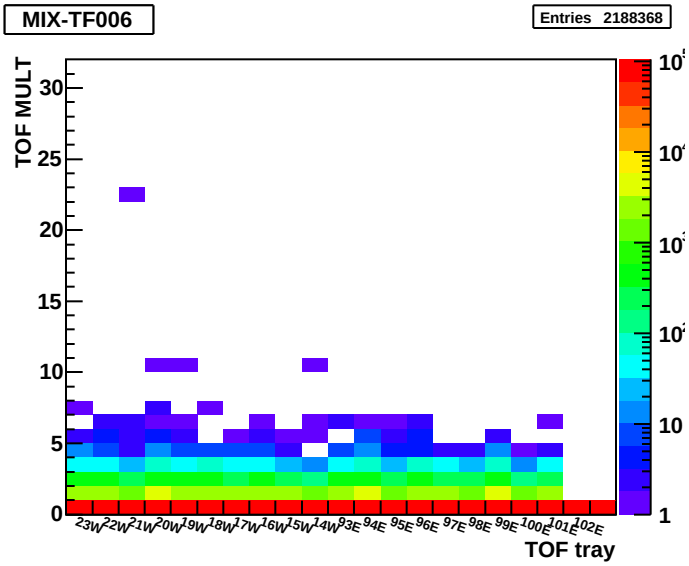

MIX-TF001
Entries 2188368
TOF MULT
TOF tray

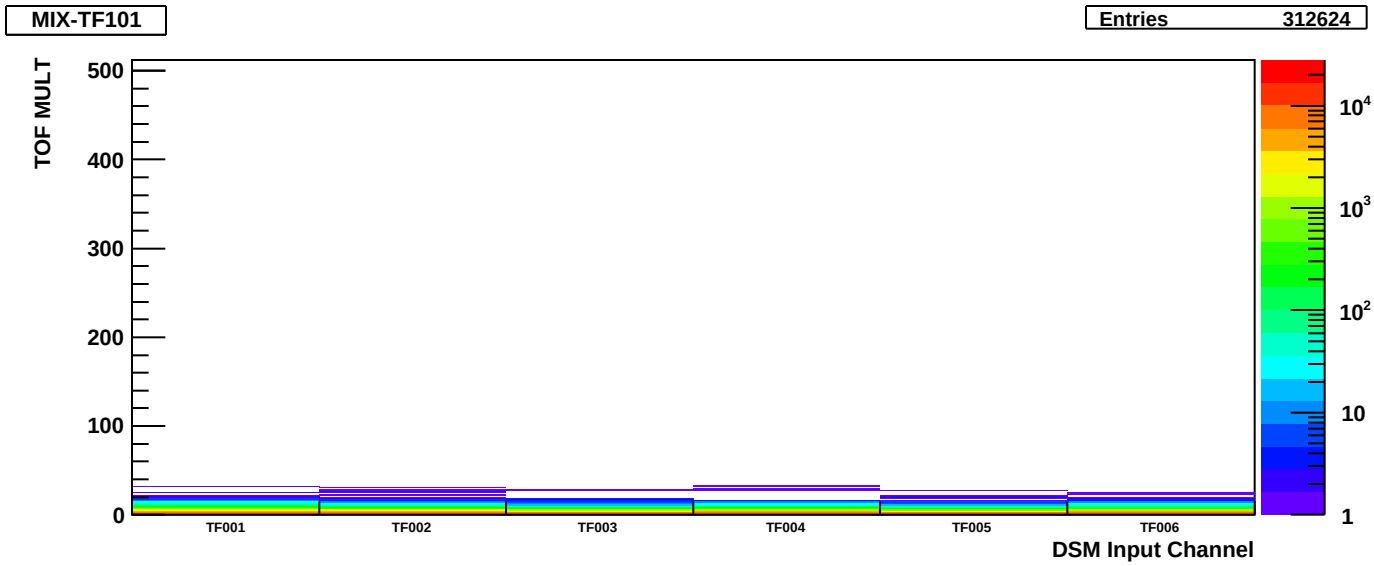

MIX-TF002
Entries 2188368
TOF MULT
TOF tray

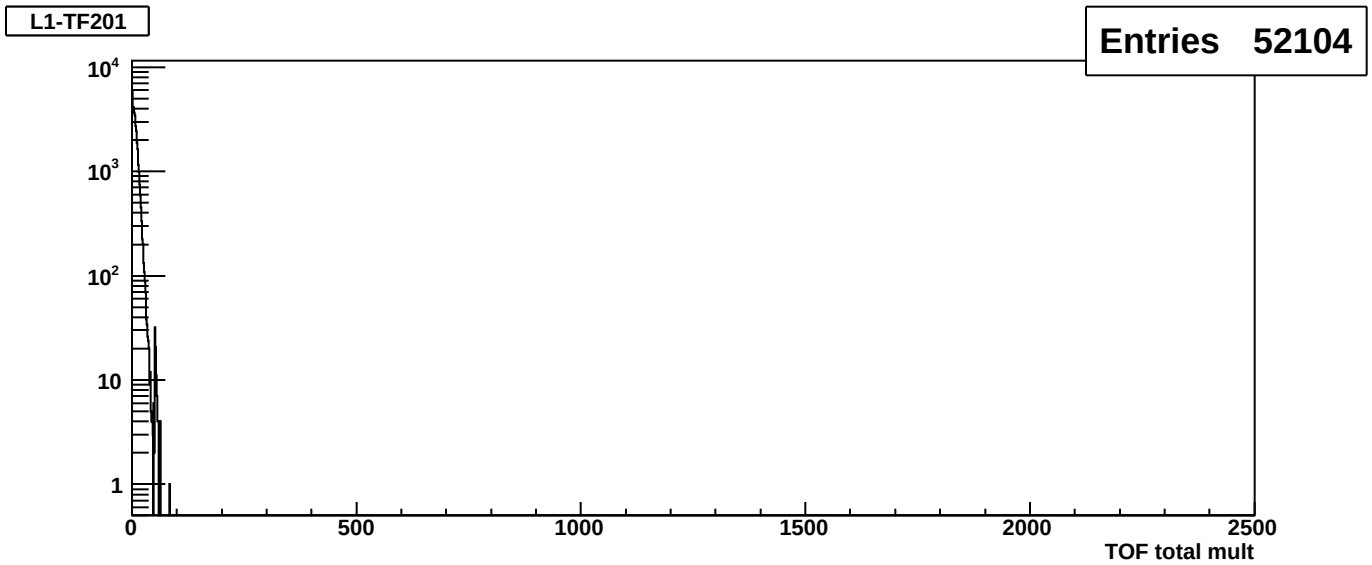

MIX-TF003
Entries 2188368
TOF MULT
TOF tray

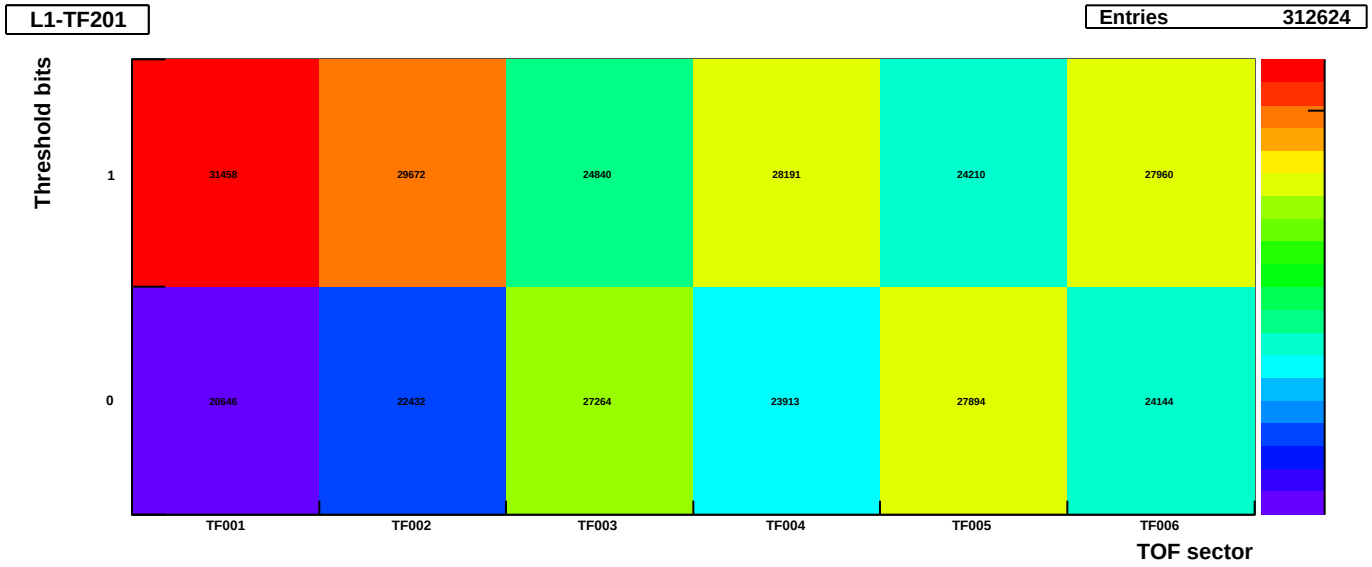

MIX-TF004
Entries 2188368
TOF MULT
TOF tray

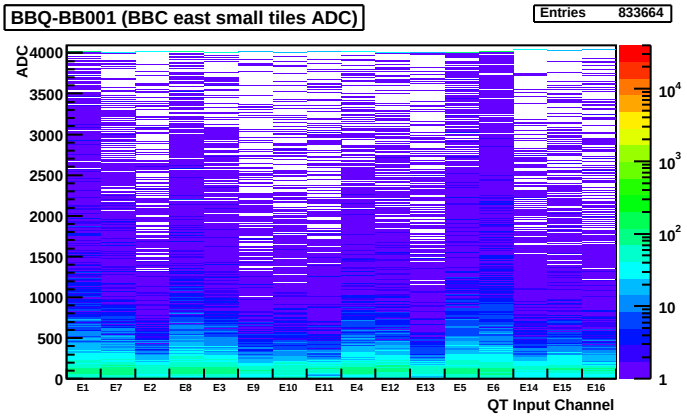

MIX-TF005
Entries 2188368
TOF MULT
TOF tray

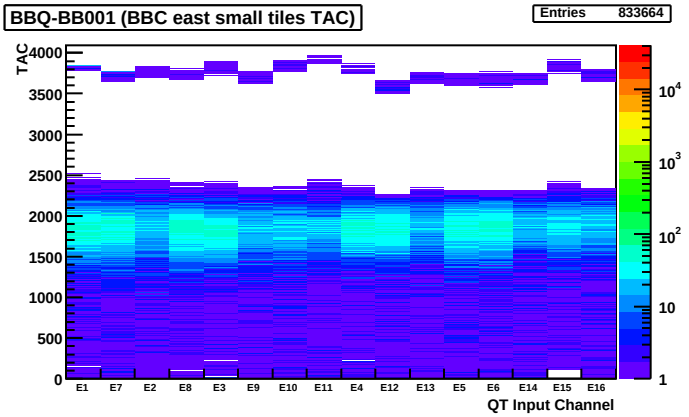

MIX-TF006
Entries 2188368
TOF MULT
TOF tray

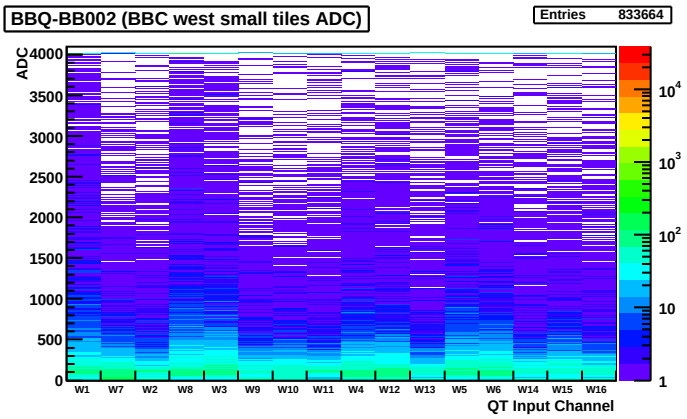

MIX-TF101
Entries 312624
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

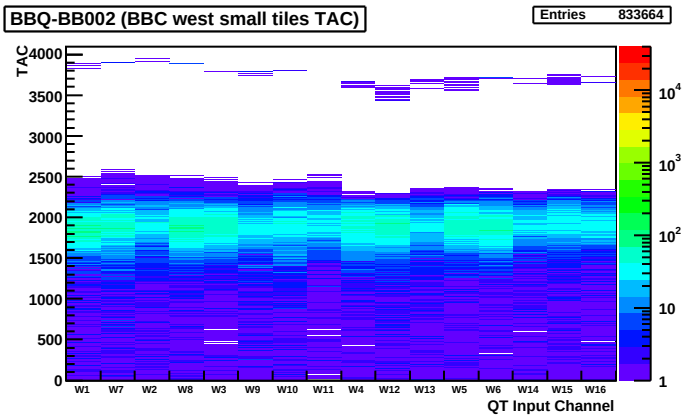

L1-TF201
Entries 52104
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

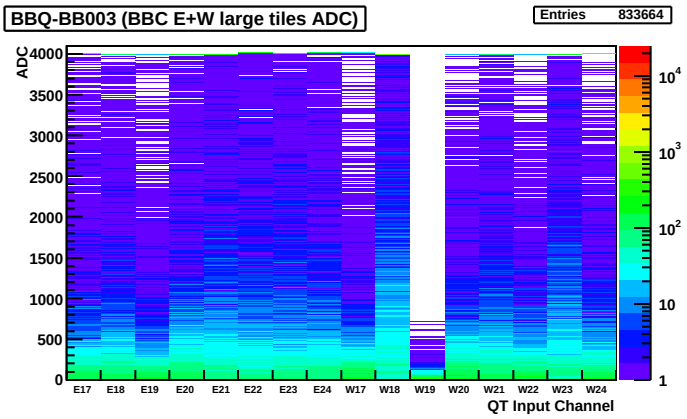

L1-TF201
Entries 312624
Threshold bits
1
0
31458
29672
24840
28191
24210
27960
20646
22432
27264
23913
27894
24144
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

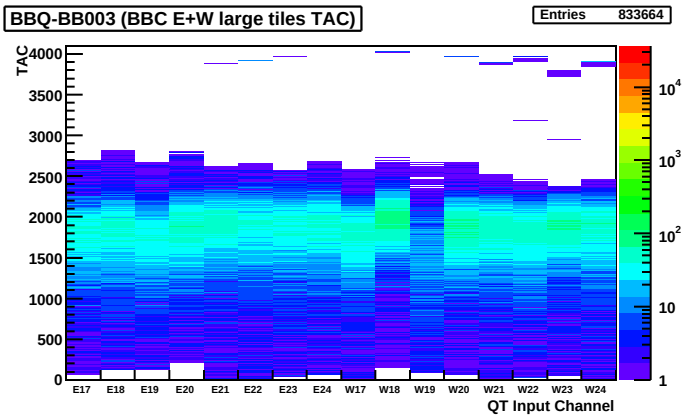

BBQ-BB001 (BBC east small tiles ADC)
Entries 833664
ADC
QT Input Channel

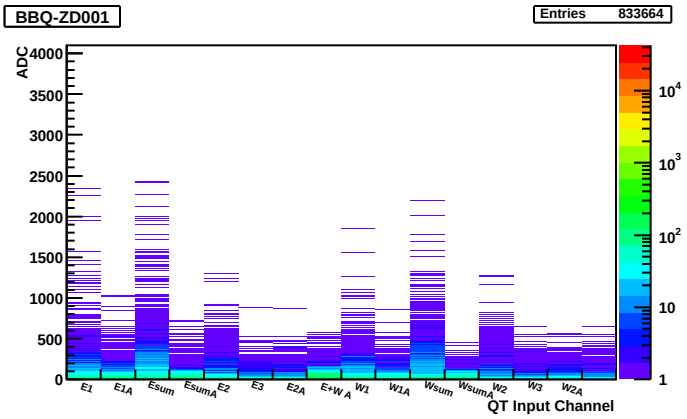

BBQ-BB001 (BBC east small tiles TAC)
Entries 833664
TAC
QT Input Channel

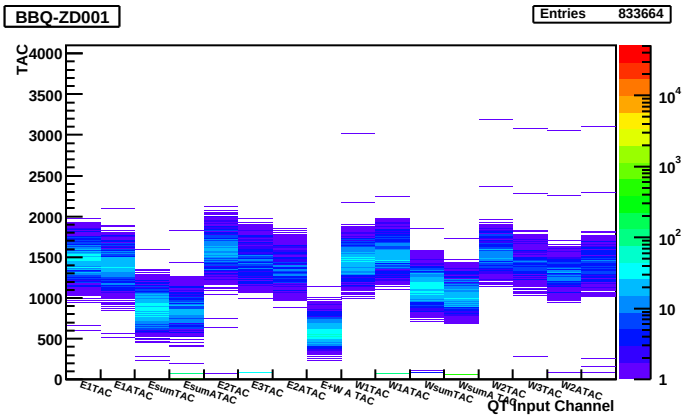

BBQ-BB002 (BBC west small tiles ADC)
Entries 833664
ADC
QT Input Channel

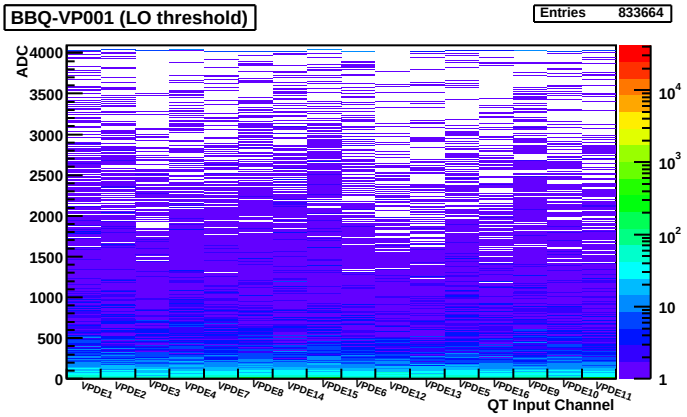

BBQ-BB002 (BBC west small tiles TAC)
Entries 833664
TAC
QT Input Channel

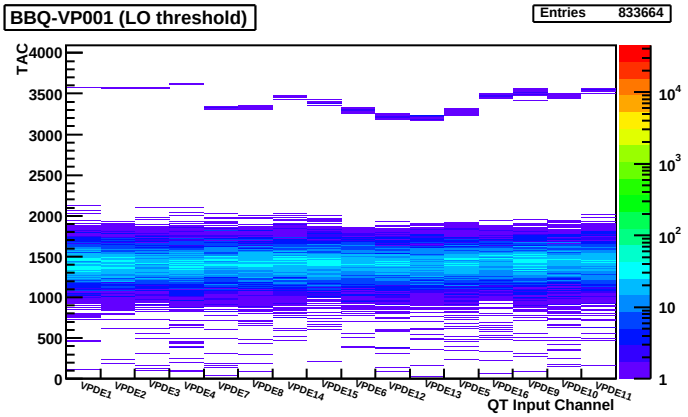

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 833664
ADC
QT Input Channel

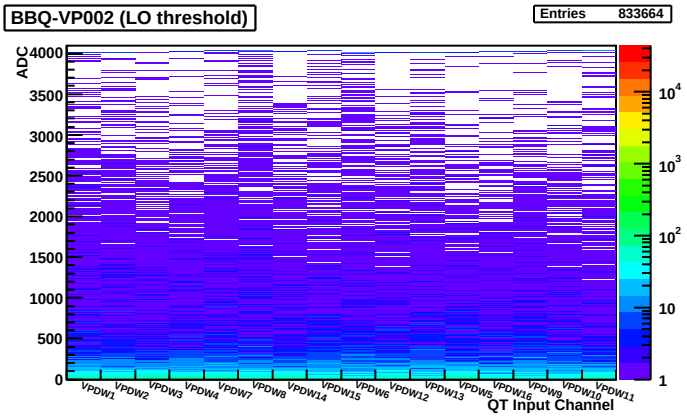

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 833664
TAC
QT Input Channel

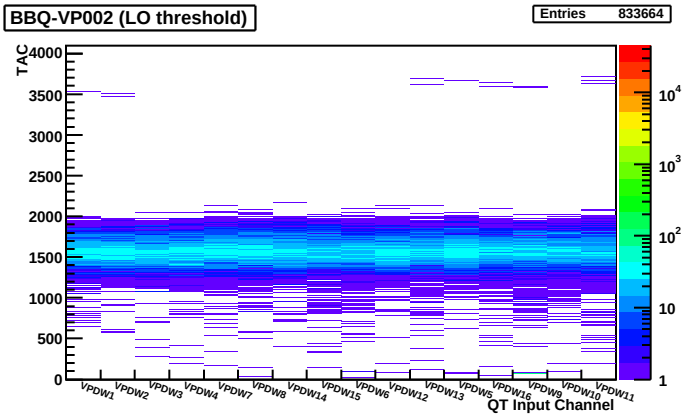

BBQ-ZD001
Entries 833664
ADC
QT Input Channel

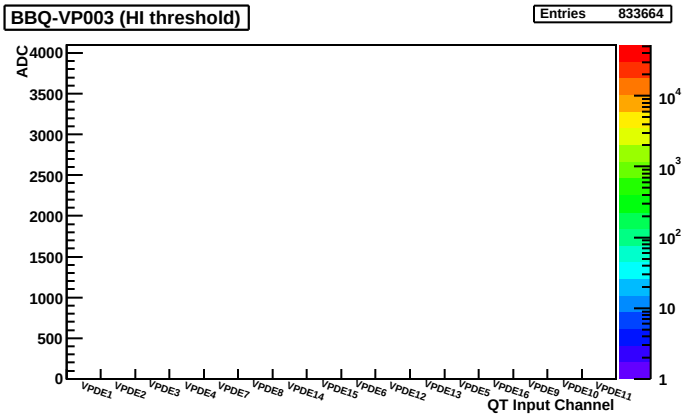

BBQ-ZD001
Entries 833664
TAC
QT Input Channel

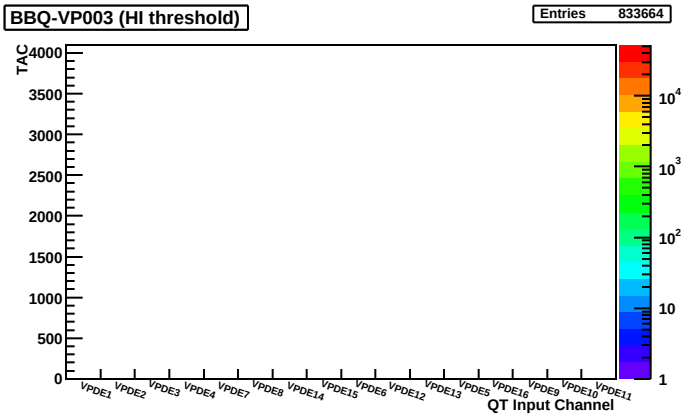

BBQ-VP001 (LO threshold)
Entries 833664
ADC
QT Input Channel

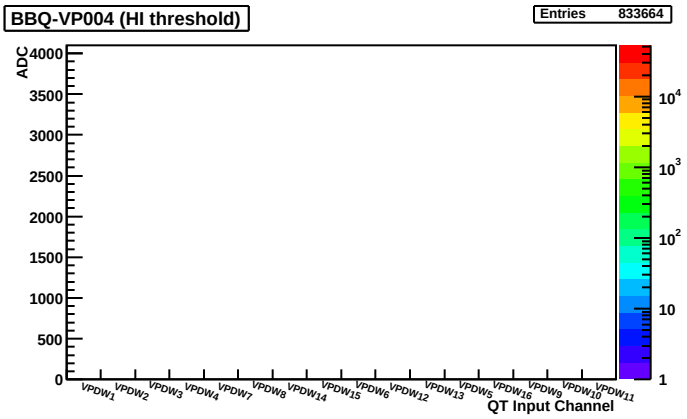

BBQ-VP001 (LO threshold)
Entries 833664
TAC
QT Input Channel

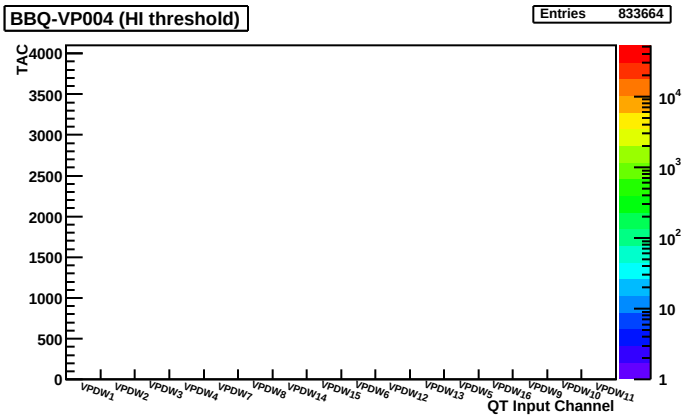

BBQ-VP002 (LO threshold)
Entries 833664
ADC
QT Input Channel

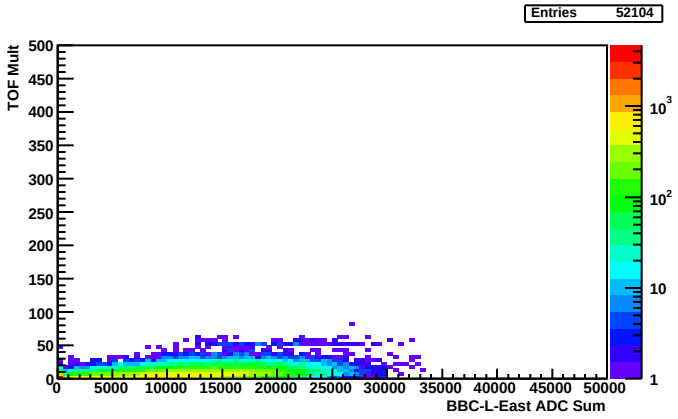

BBQ-VP002 (LO threshold)
Entries 833664
TAC
QT Input Channel

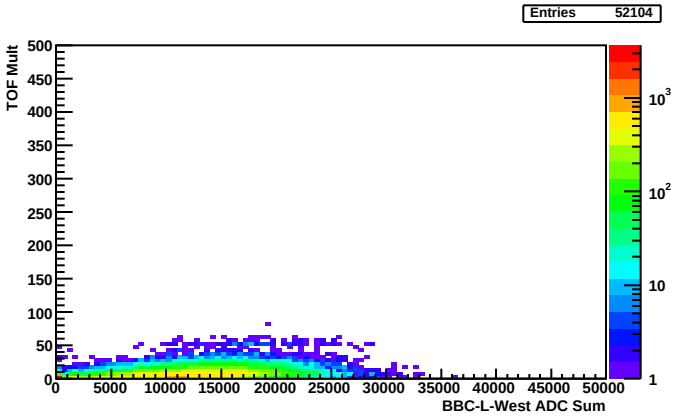

BBQ-VP003 (HI threshold)
Entries 833664
ADC
QT Input Channel

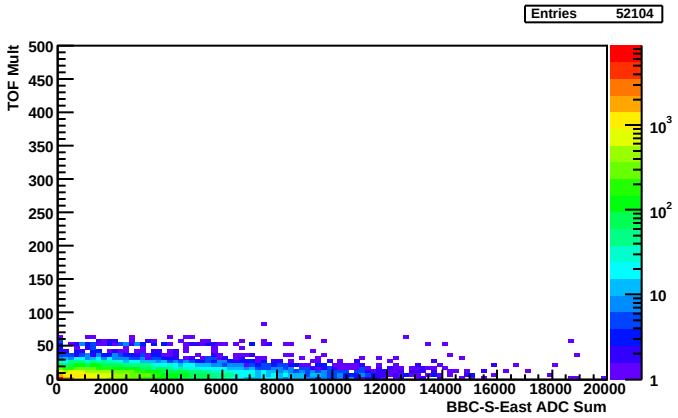

BBQ-VP003 (HI threshold)
Entries 833664
TAC
QT Input Channel

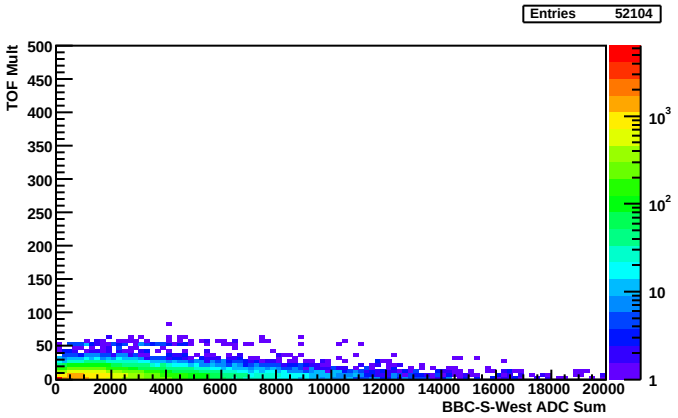

BBQ-VP004 (HI threshold)
Entries 833664
ADC
QT Input Channel

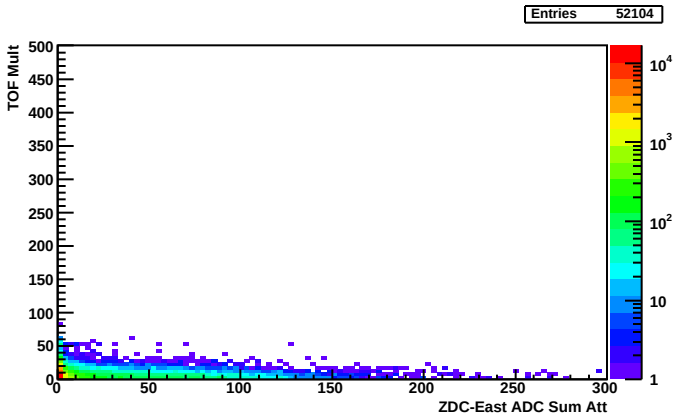

BBQ-VP004 (HI threshold)
Entries 833664
TAC
QT Input Channel

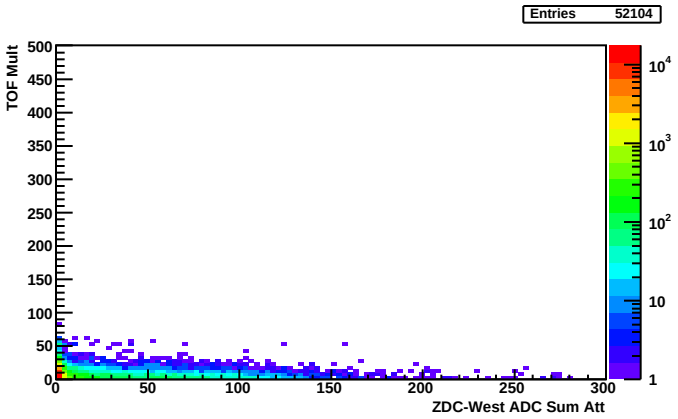

Entries 52104
TOF Mult
BBC-L-East ADC Sum

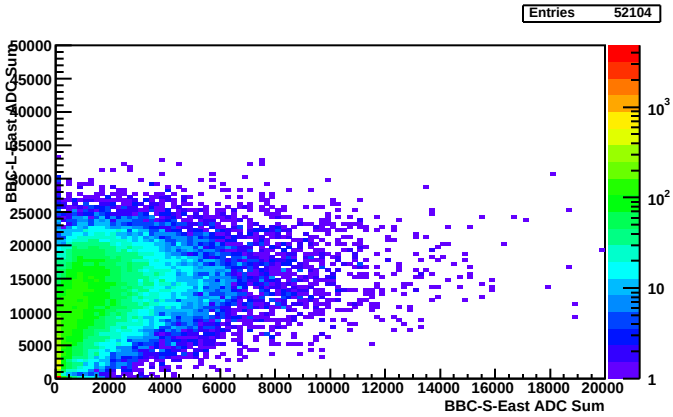

Entries 52104
TOF Mult
BBC-L-West ADC Sum

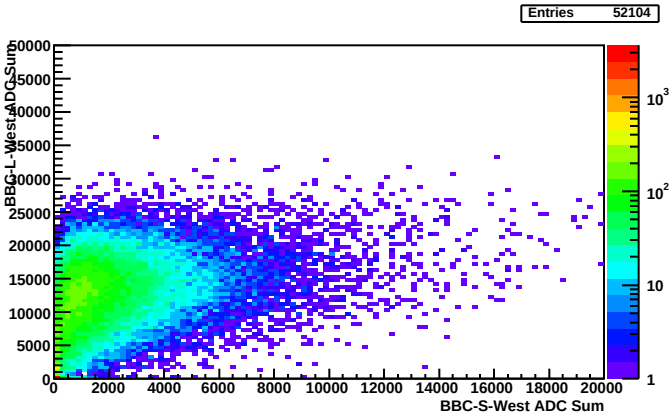

Entries 52104
TOF Mult
BBC-S-East ADC Sum

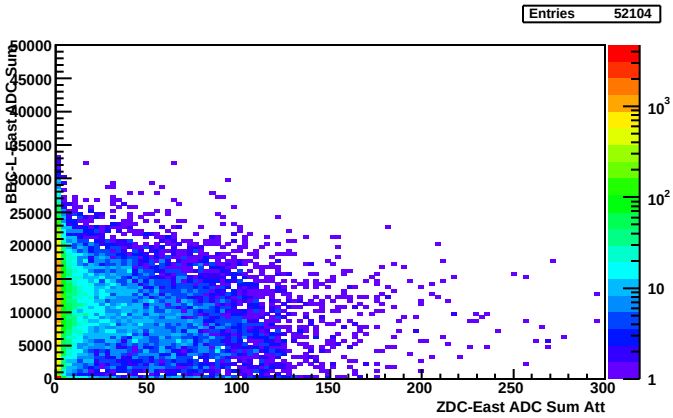

Entries 52104
TOF Mult
BBC-S-West ADC Sum

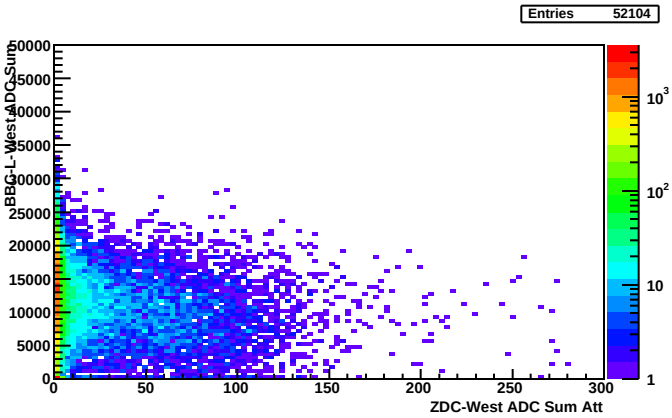

Entries 52104
TOF Mult
ZDC-East ADC Sum Att

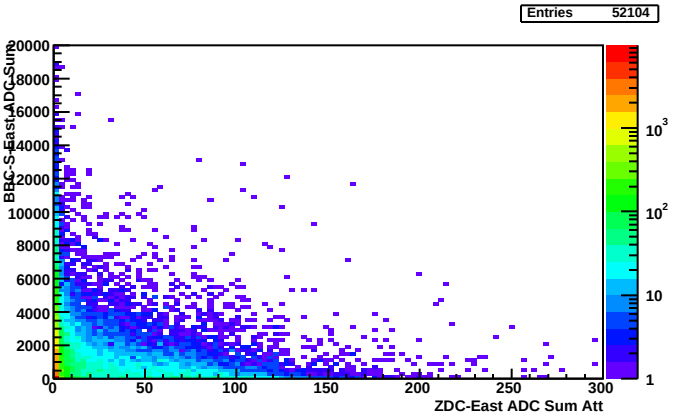

Entries 52104
TOF Mult
ZDC-West ADC Sum Att

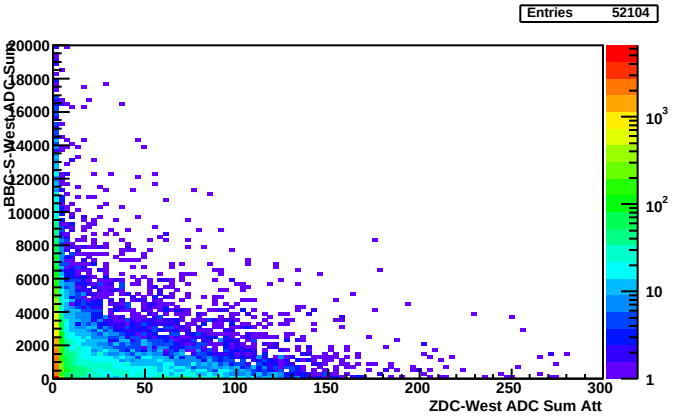

Entries 52104
BBC-L-East ADC Sum
BBC-S-East ADC Sum

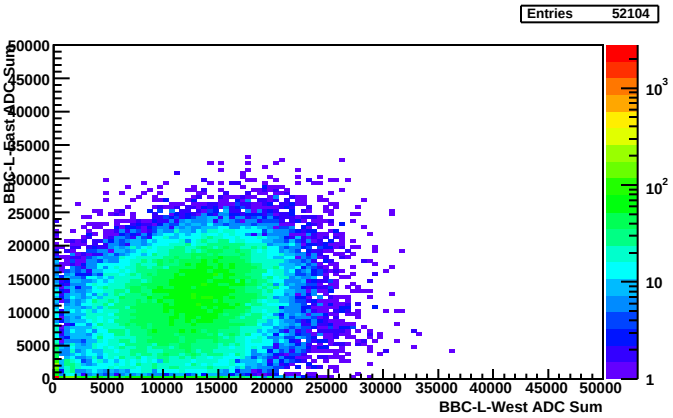

Entries 52104
BBC-L-West ADC Sum
BBC-S-West ADC Sum

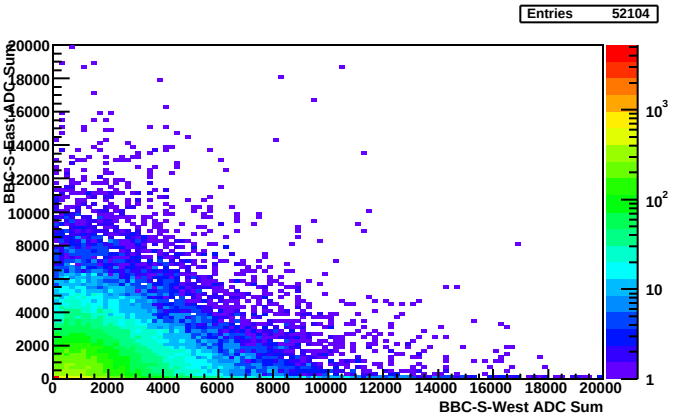

Entries 52104
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

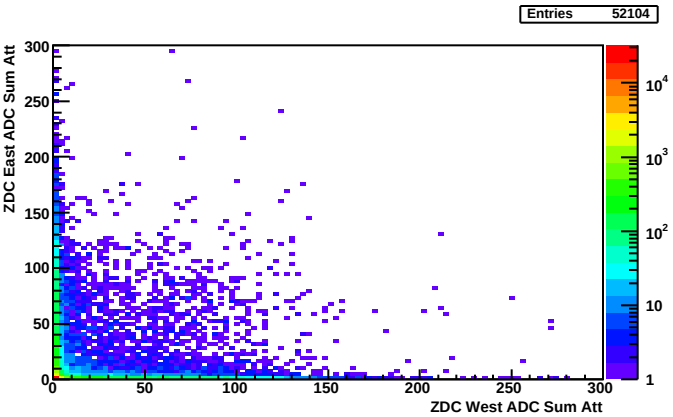

Entries 52104
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

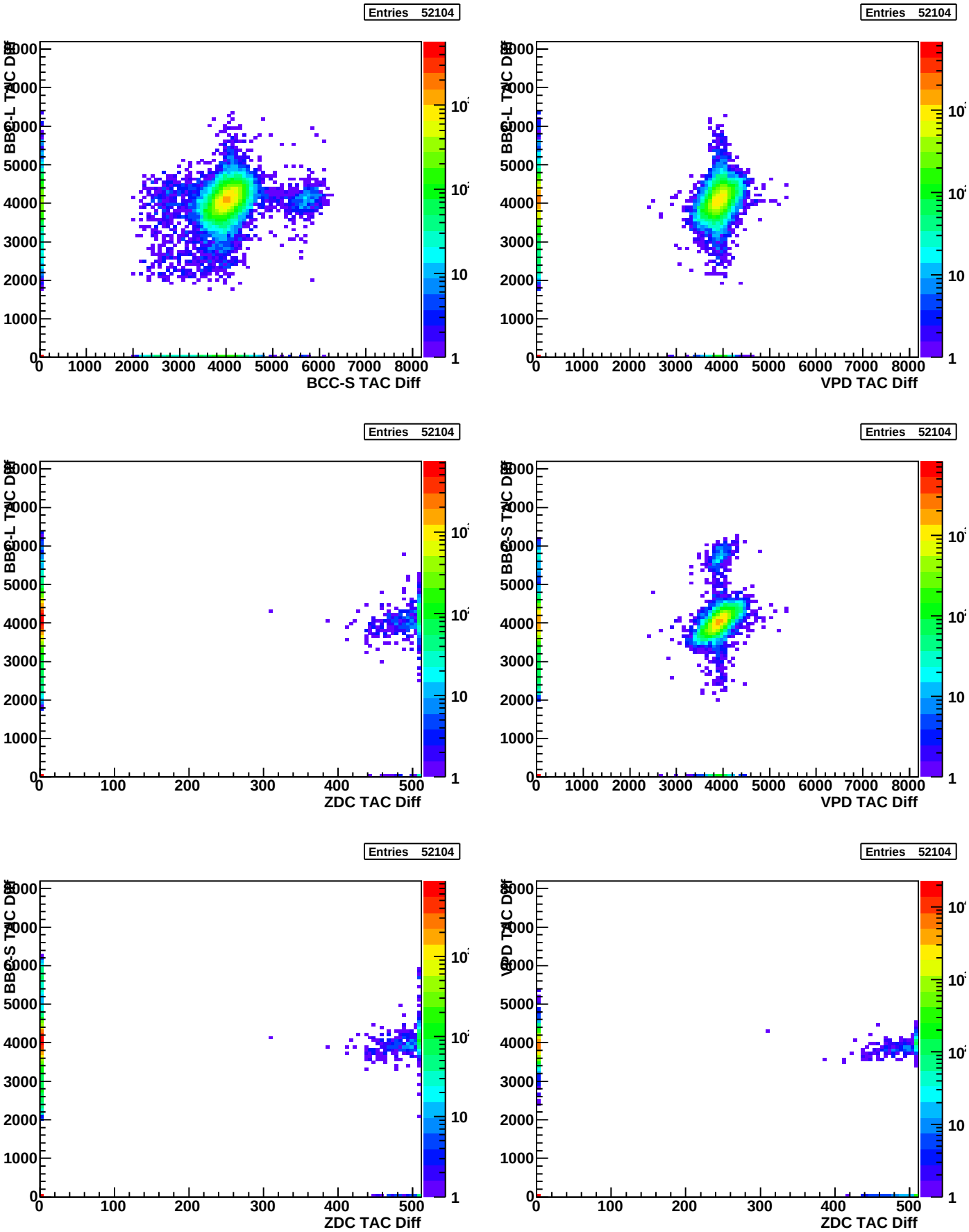

Entries 52104
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

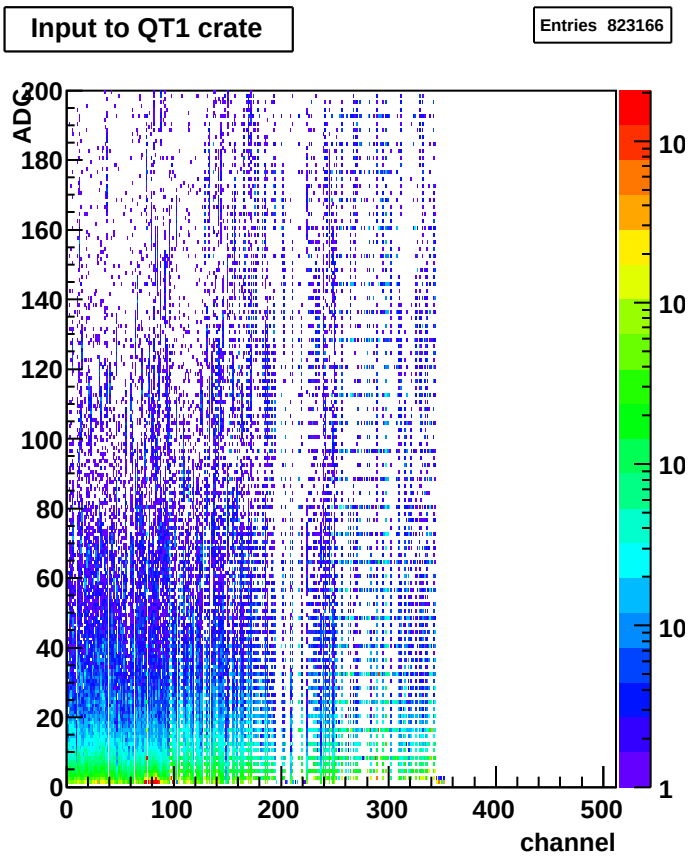

Entries 52104
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

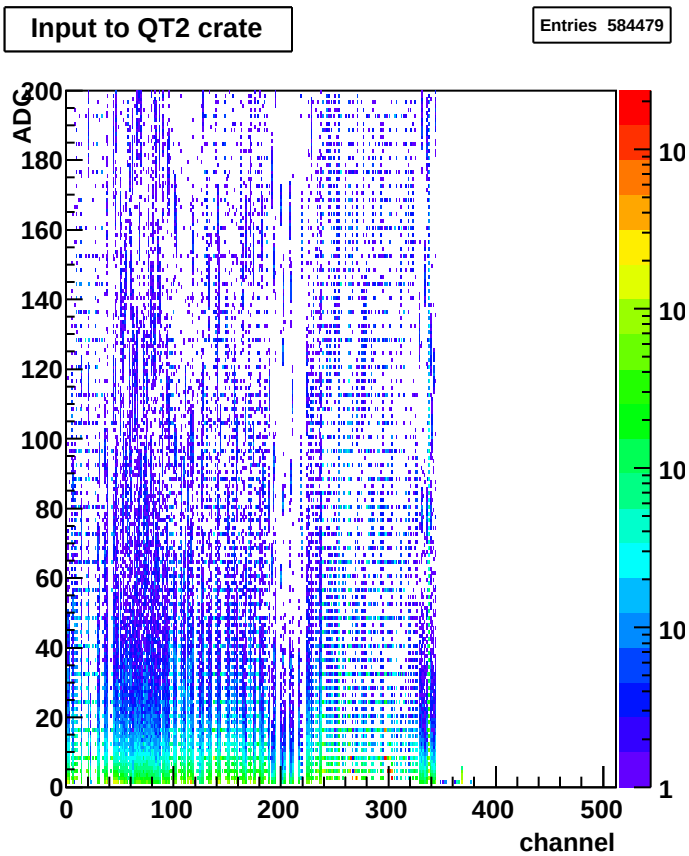

Entries 52104
BBC-L-East ADC Sum
BBC-L-West ADC Sum

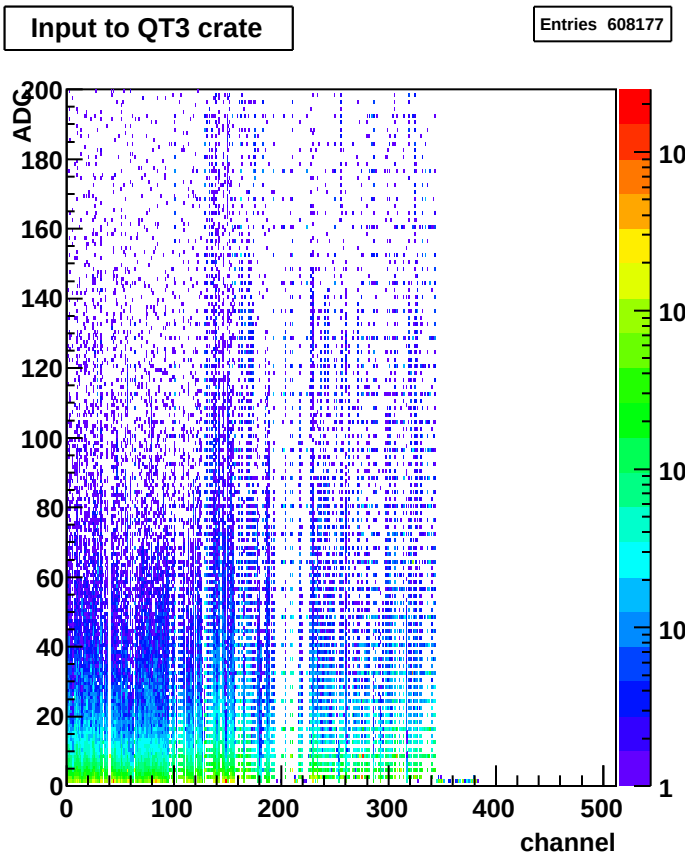

Entries 52104
BBC-S-East ADC Sum
BBC-S-West ADC Sum

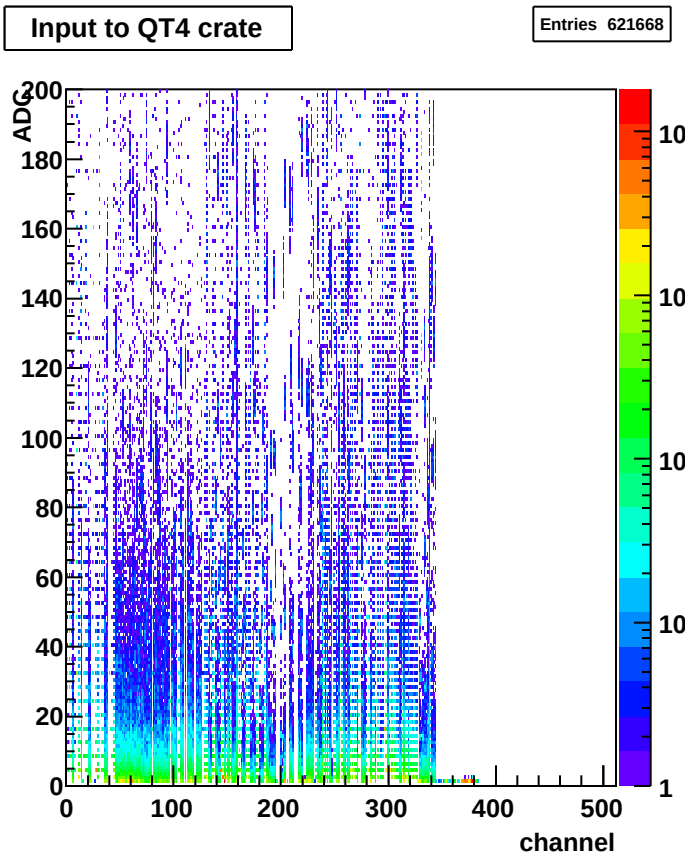

Entries 52104
ZDC East ADC Sum Att
ZDC West ADC Sum Att

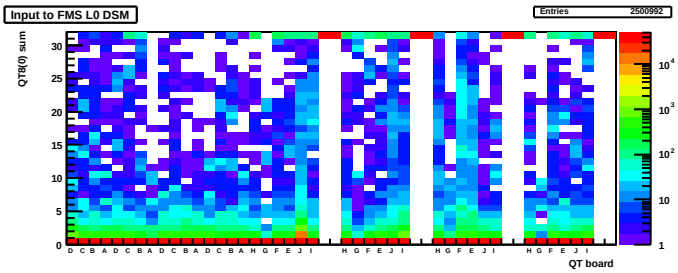

Entries 52104
BBC-L TAC Diff
BCC-S TAC Diff
Entries 52104
BBC-L TAC Diff
VPD TAC Diff
Entries 52104
BBC-L TAC Diff
ZDC TAC Diff
Entries 52104
BBC-S TAC Diff
VPD TAC Diff
Entries 52104
BBC-S TAC Diff
ZDC TAC Diff
Entries 52104
VPD TAC Diff
ZDC TAC Diff

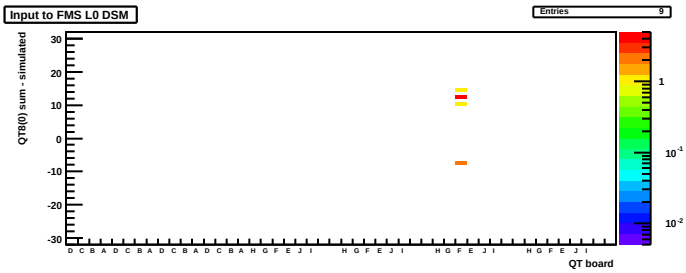

Input to QT1 crate
Entries 823166
ADC
channel

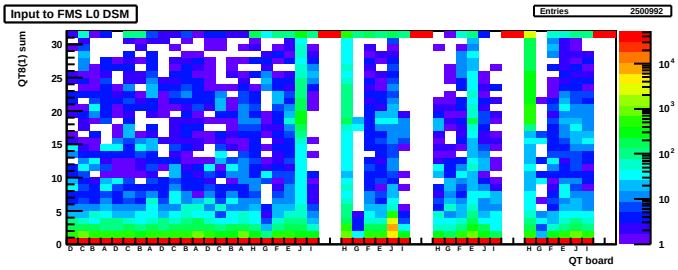

Input to QT2 crate
Entries 584479
ADC
channel

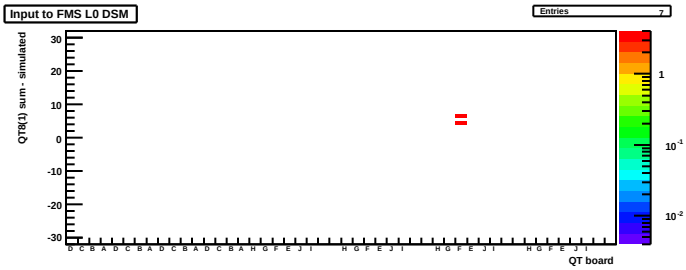

Input to QT3 crate
Entries 608177
ADC
channel

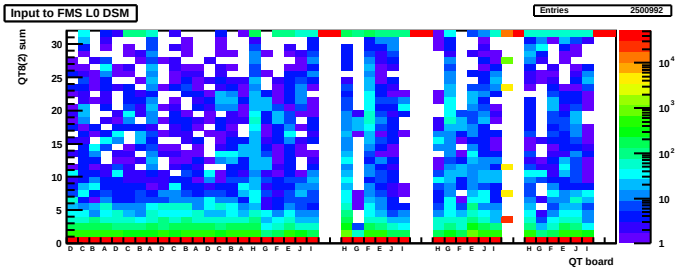

Input to QT4 crate
Entries 621668
ADC
channel

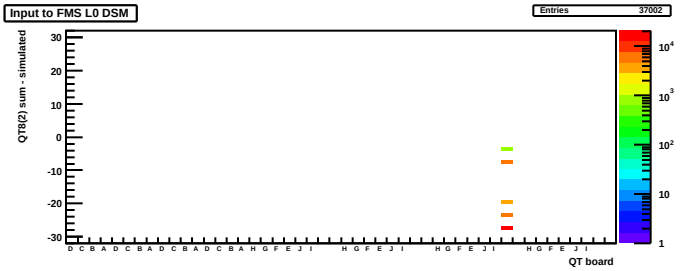

Input to FMS L0 DSM
Entries 2500992
QT8(0) sum
QT board

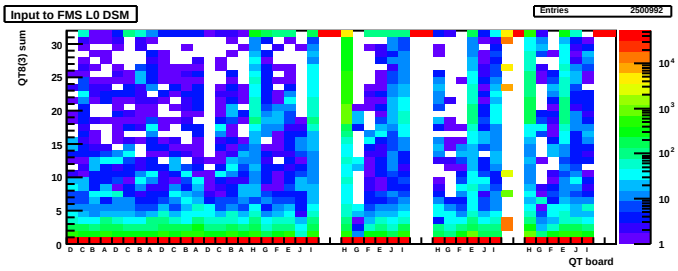

Input to FMS L0 DSM
Entries 9
QT8(0) sum - simulated
QT board

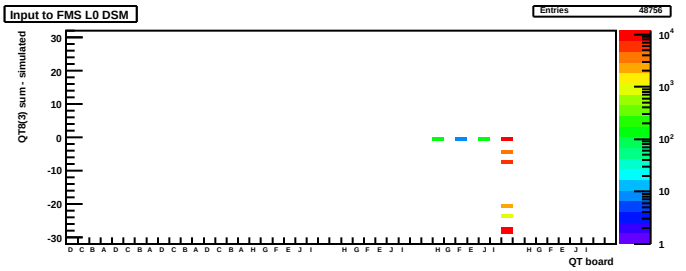

Input to FMS L0 DSM
Entries 2500992
QT8(1) sum
QT board

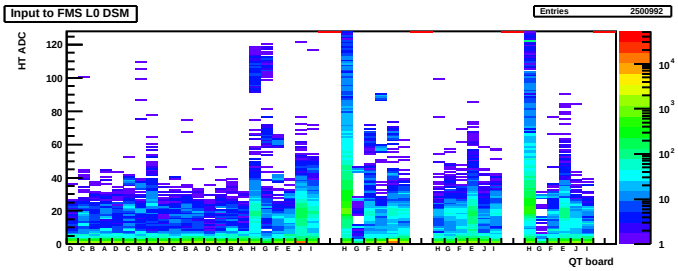

Input to FMS L0 DSM
Entries 7
QT8(1) sum - simulated
QT board

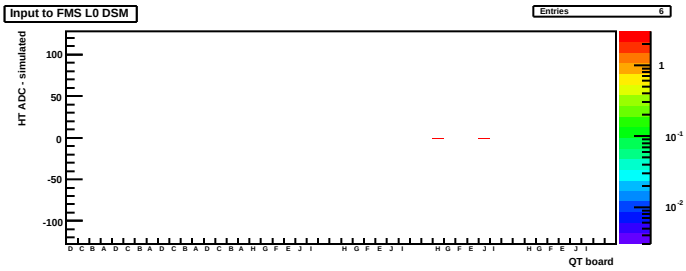

Input to FMS L0 DSM
Entries 2500992
QT8(2) sum
QT board

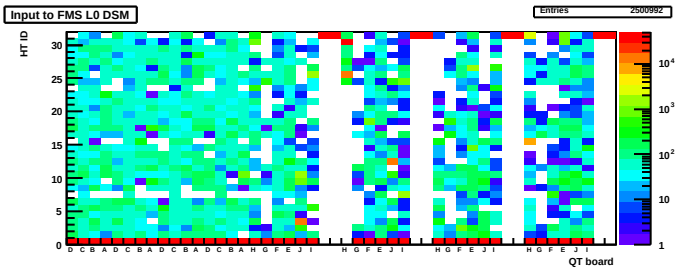

Input to FMS L0 DSM
Entries 37002
QT8(2) sum - simulated
QT board

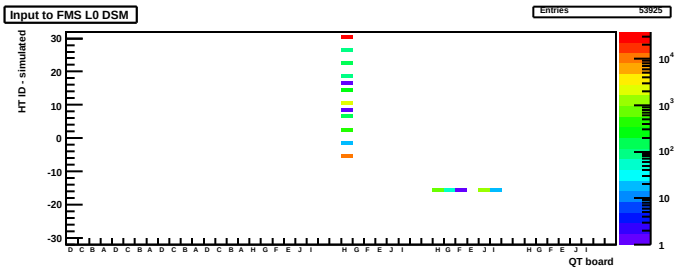

Input to FMS L0 DSM
Entries 2500992
QT8(3) sum
QT board

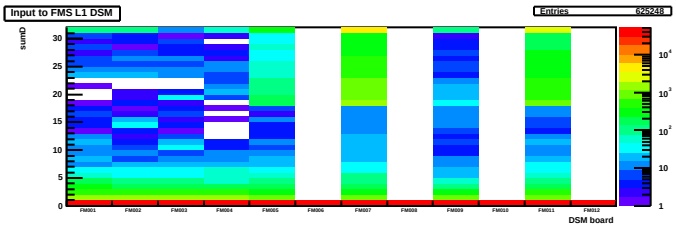

Input to FMS L0 DSM
Entries 48756
QT8(3) sum - simulated
QT board

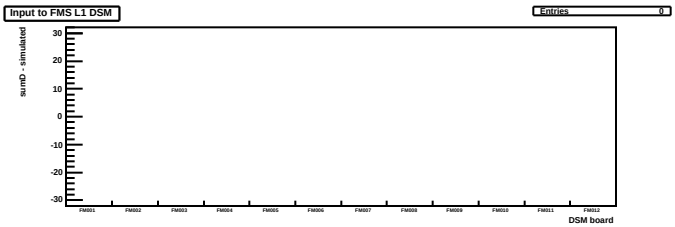

Input to FMS L0 DSM
Entries 2500992
HT ADC
QT board

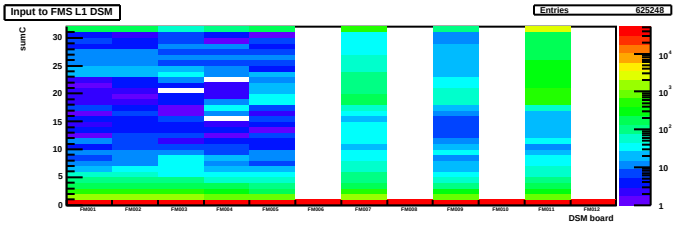

Input to FMS L0 DSM
Entries 6
HT ADC - simulated
QT board

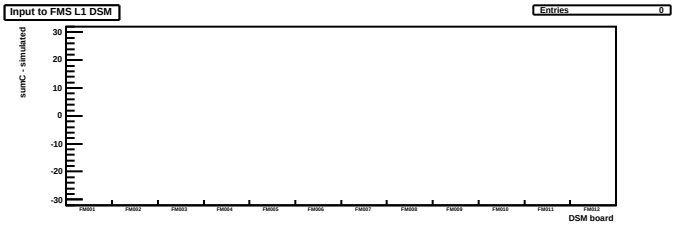

Input to FMS L0 DSM
Entries 2500992
HT ID
QT board

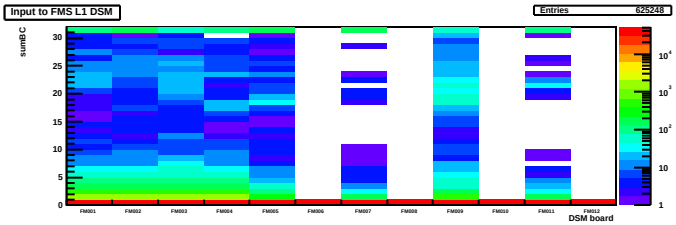

Input to FMS L0 DSM
Entries 53925
HT ID - simulated
QT board

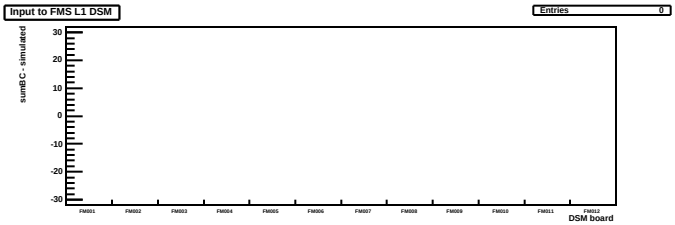

Input to FMS L1 DSM
Entries 625248
sumD
DSM board

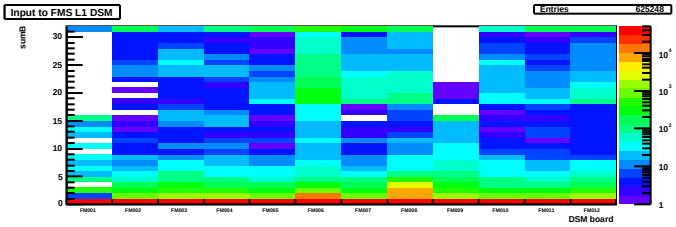

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

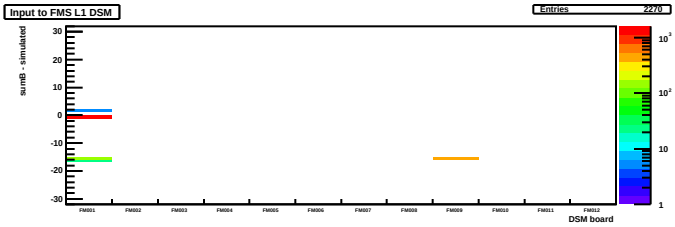

Input to FMS L1 DSM
Entries 625248
sumC
DSM board

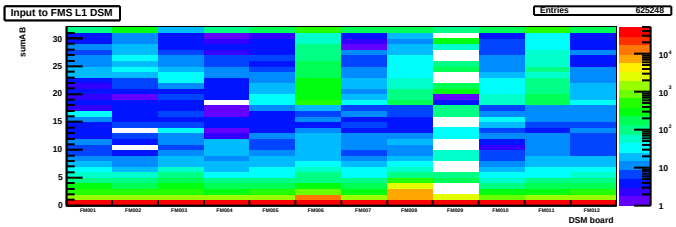

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

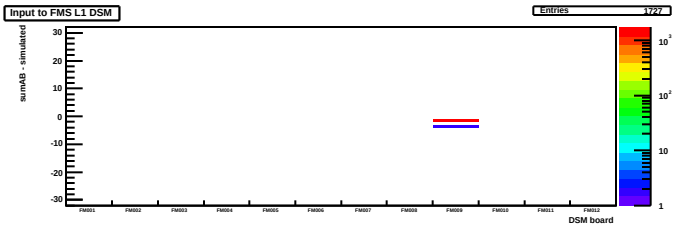

Input to FMS L1 DSM
Entries 625248
sumBC
DSM board

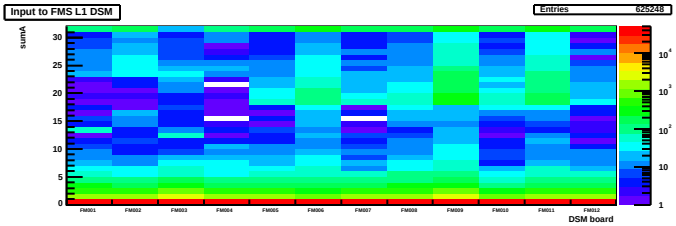

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

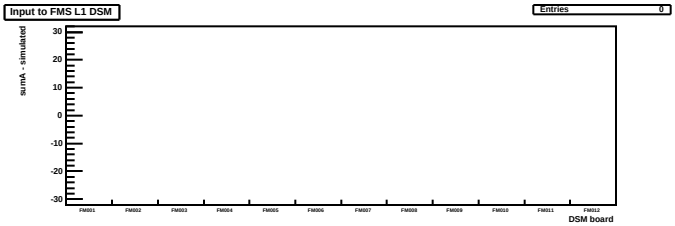

Input to FMS L1 DSM
Entries 625248
sumB
DSM board

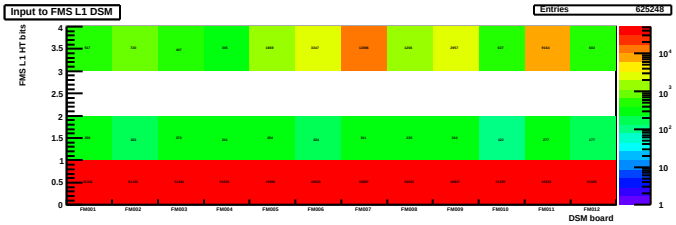

Input to FMS L1 DSM
Entries 2270
sumB - simulated
DSM board

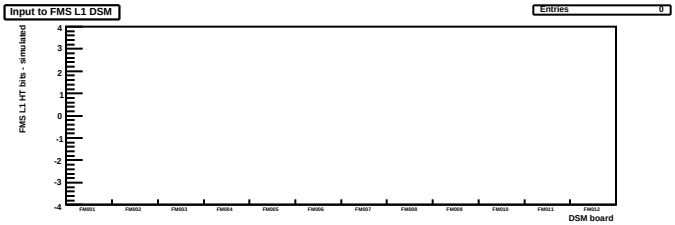

Input to FMS L1 DSM
Entries 625248
sumAB
DSM board

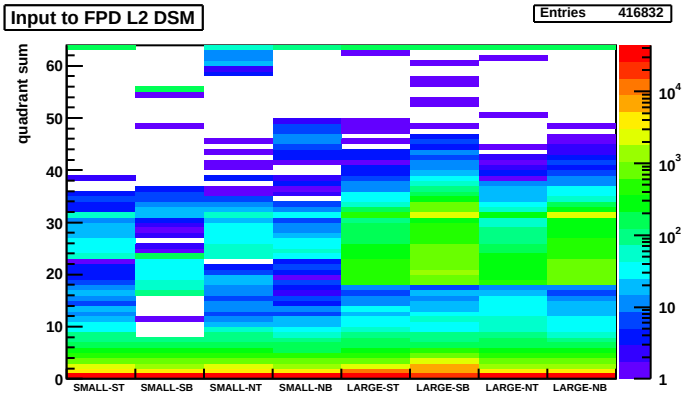

Input to FMS L1 DSM
Entries 1727
sumAB - simulated
DSM board

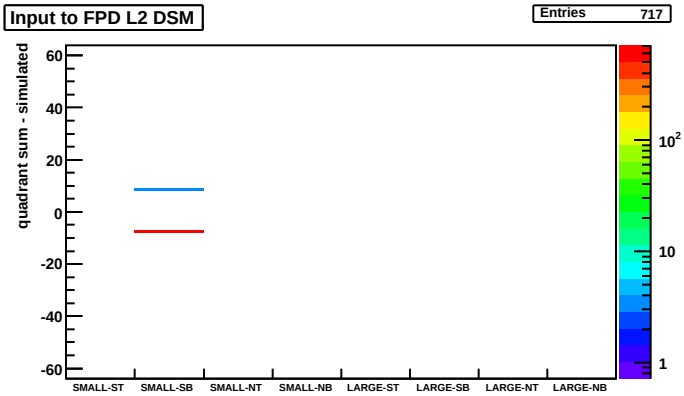

Input to FMS L1 DSM
Entries 625248
sumA
DSM board

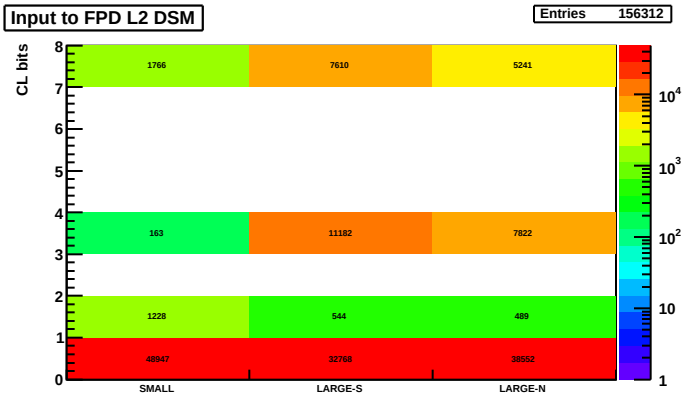

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

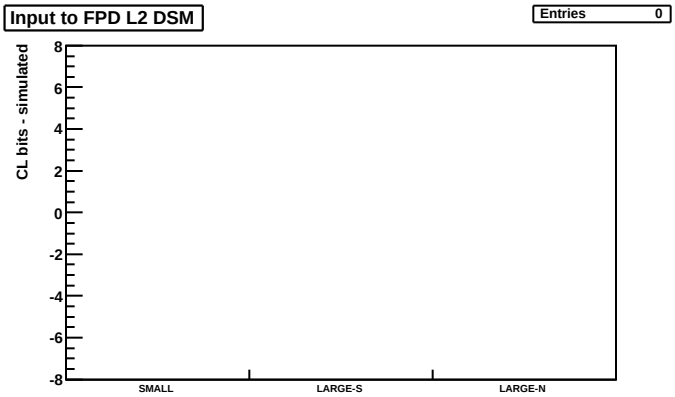

Input to FMS L1 DSM
Entries 625248
FMS L1 HT bits
DSM board

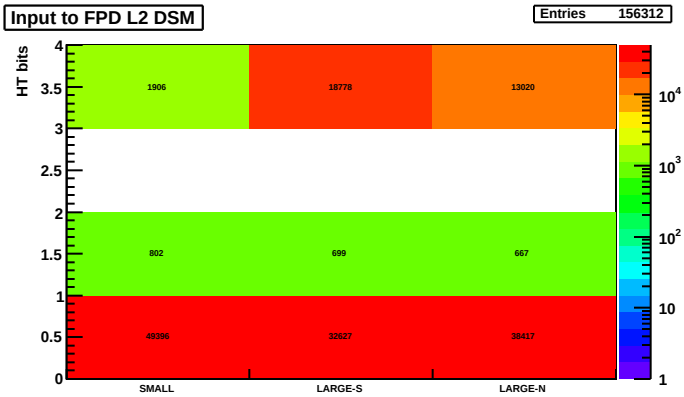

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

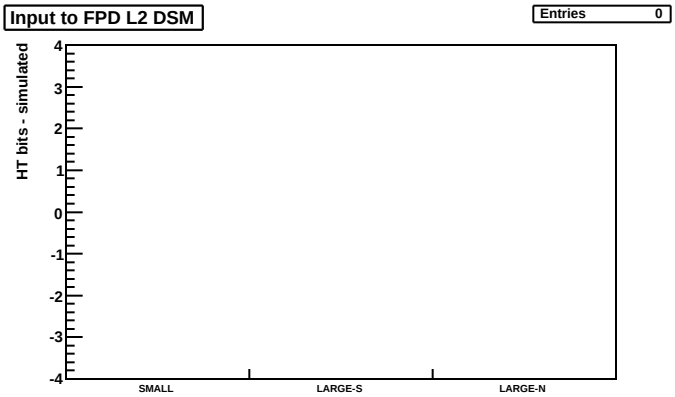

Input to FPD L2 DSM
Entries 416832
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

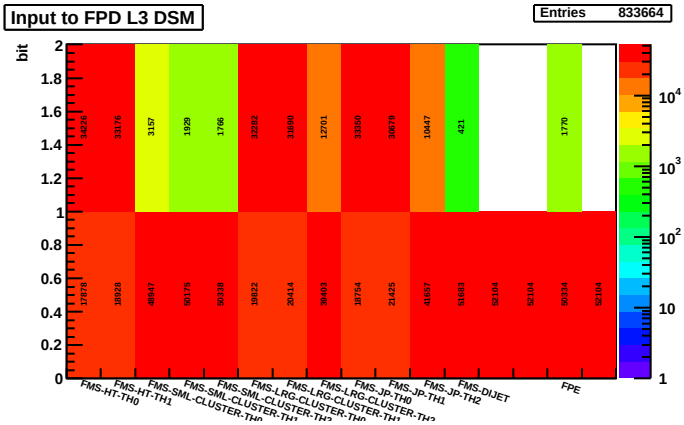

Input to FPD L2 DSM
Entries 717
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

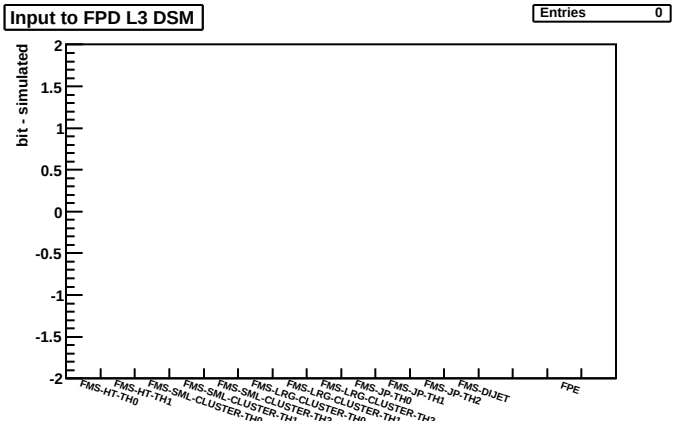

Input to FPD L2 DSM
Entries 156312
CL bits
1766
7610
5241
163
11182
7822
1228
544
489
48947
32768
38552
SMALL
LARGE-S
LARGE-N

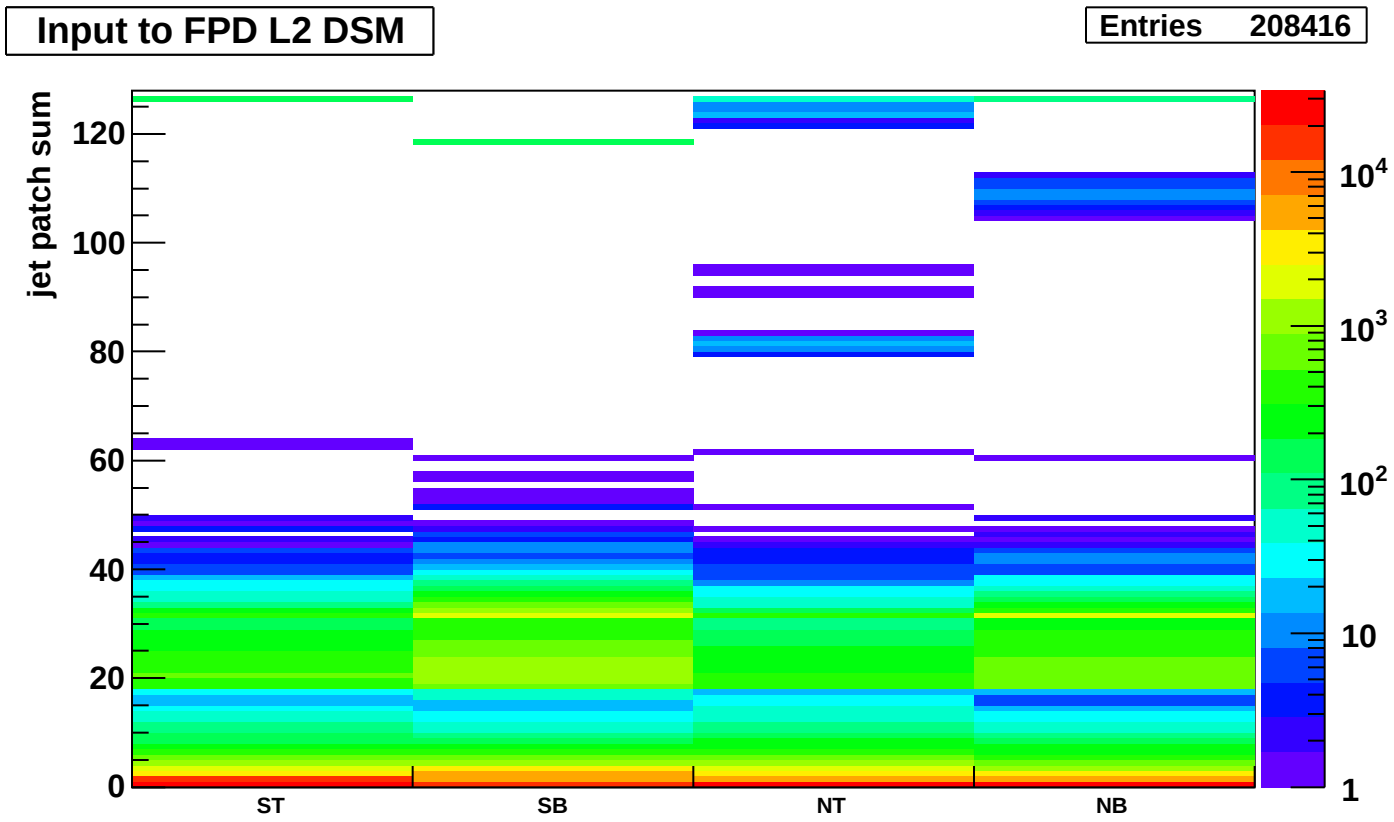

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

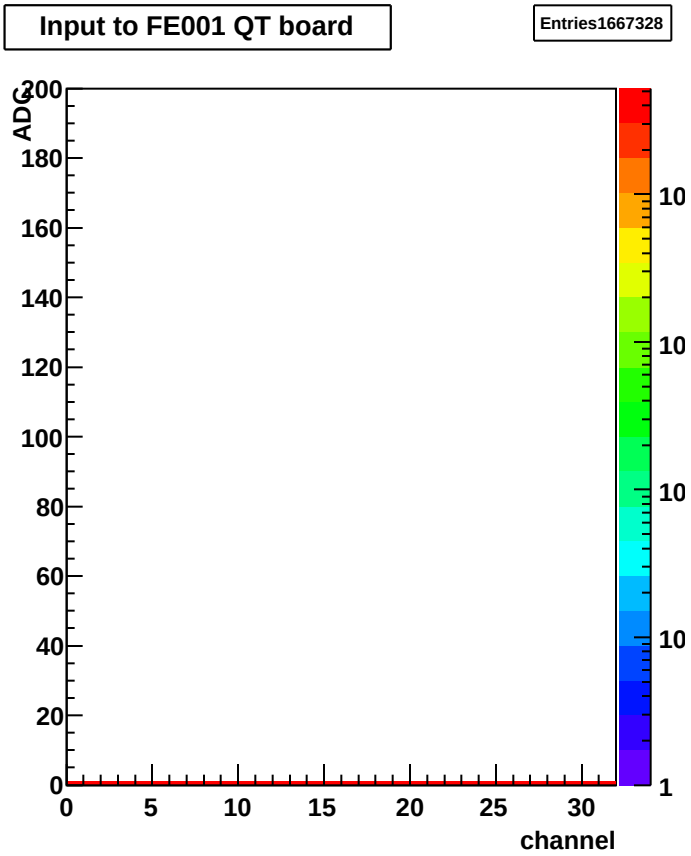

Input to FPD L2 DSM
Entries 156312
HT bits
1906
18778
13020
802
699
667
49396
32627
38417
SMALL
LARGE-S
LARGE-N

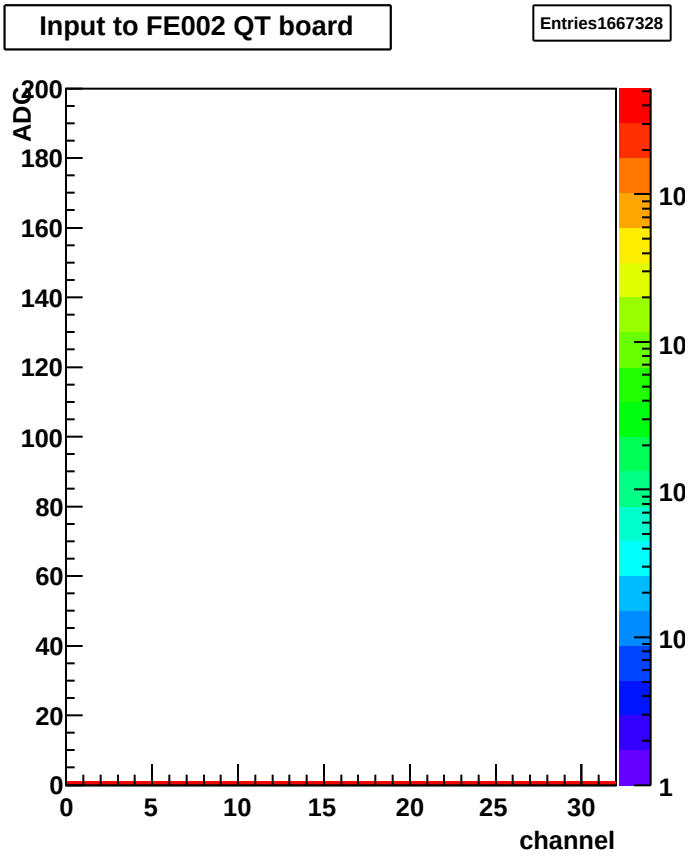

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

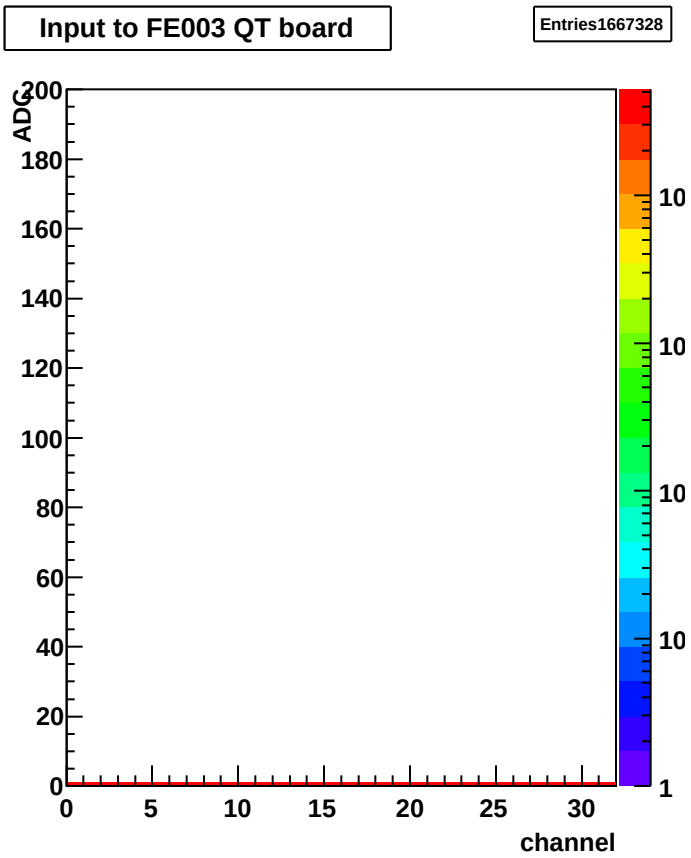

Input to FPD L3 DSM
Entries 833664
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

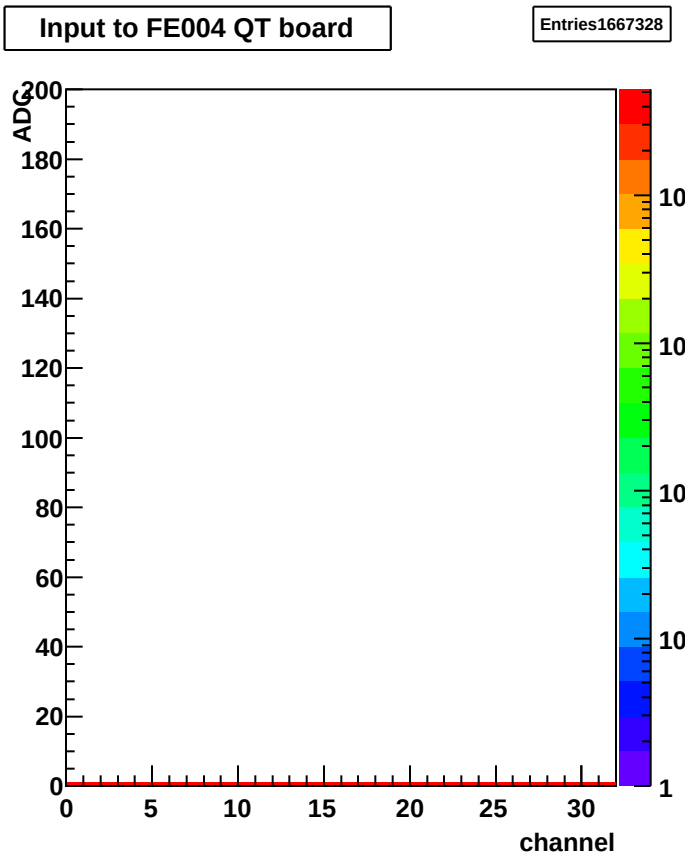

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

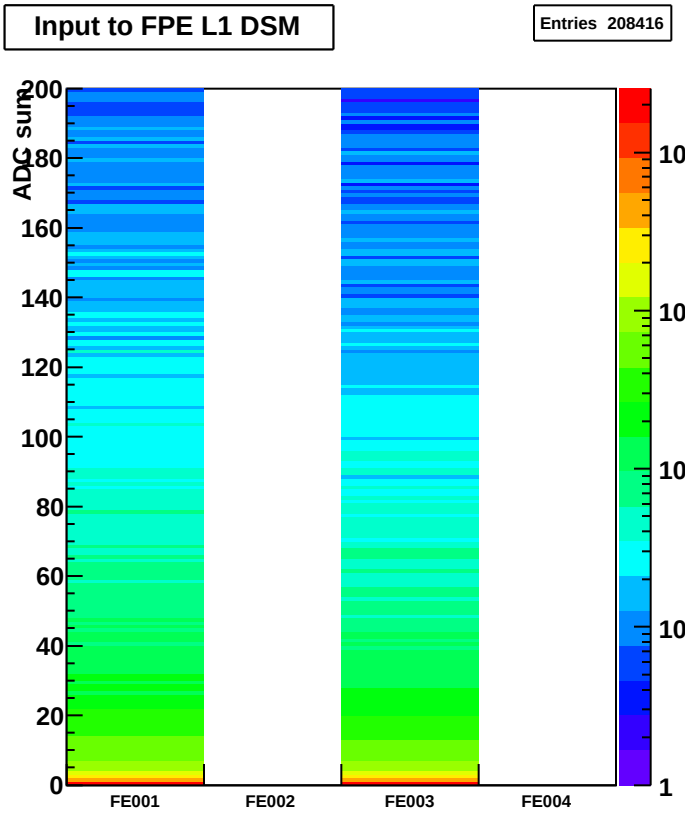

Input to FPD L2 DSM
Entries 208416
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

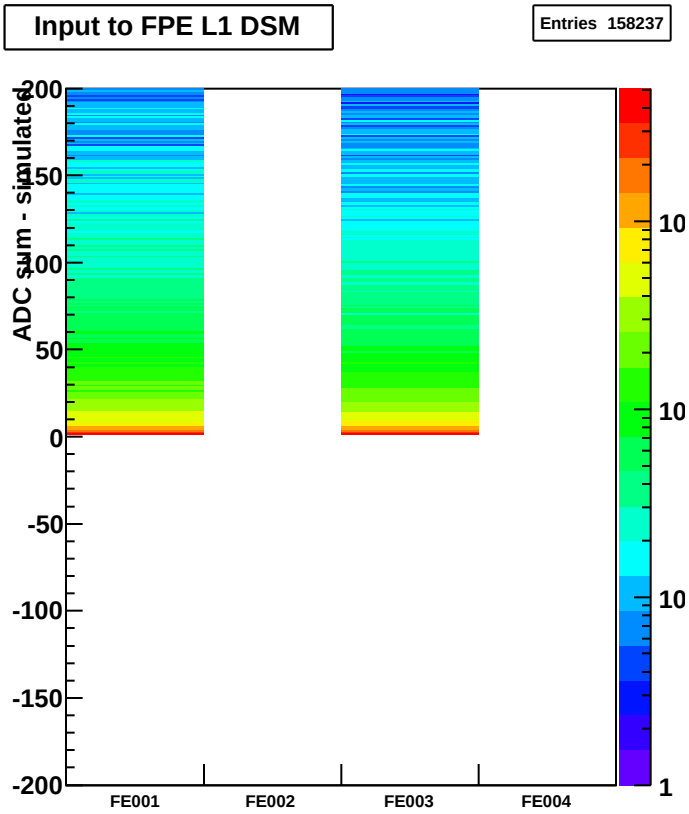

Input to FE001 QT board
Entries1667328
ADC
channel

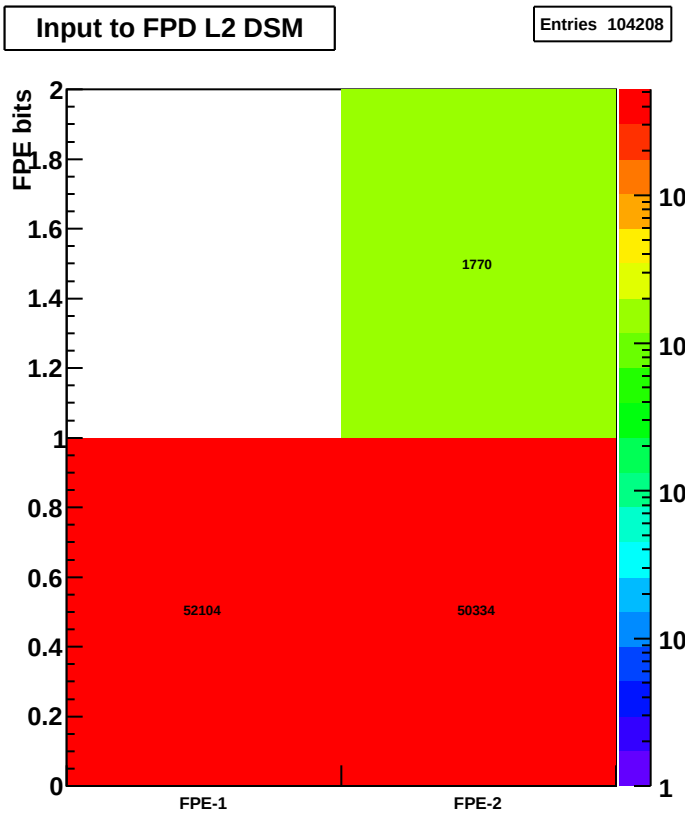

Input to FE002 QT board
Entries1667328
ADC
channel

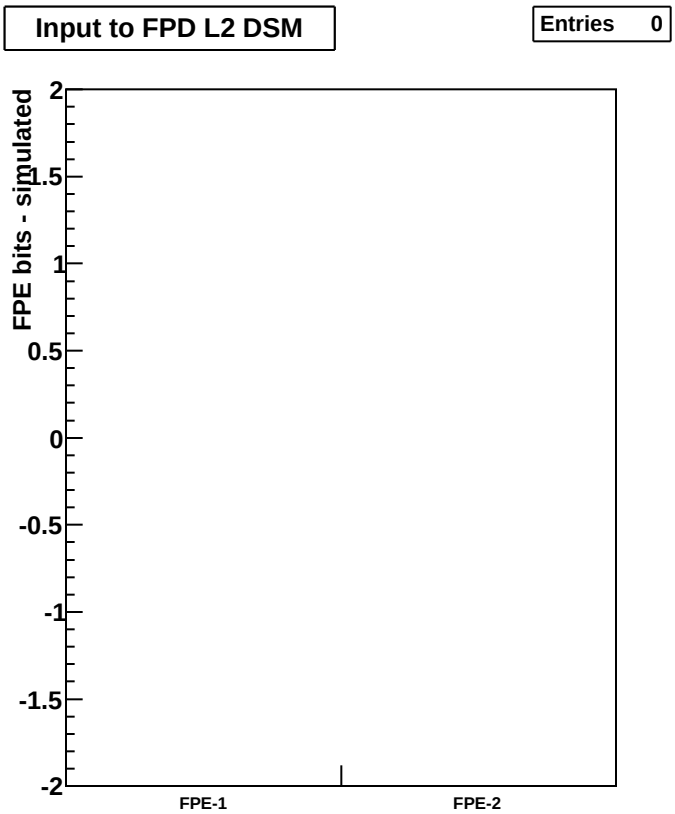

Input to FE003 QT board
Entries1667328
ADC
channel


Input to FE004 QT board
Entries1667328
ADC
channel


Input to FPE L1 DSM
Entries 208416
ADC sum
200
180
160
140
120
100
80
60
40
20
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 158237
ADC sum - simulated
200
150
100
50
0
-50
-100
-150
-200
FE001
FE002
FE003
FE004
10
10
10
1


Input to FPD L2 DSM
Entries 104208
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1770
52104
50334
FPE-1
FPE-2
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2