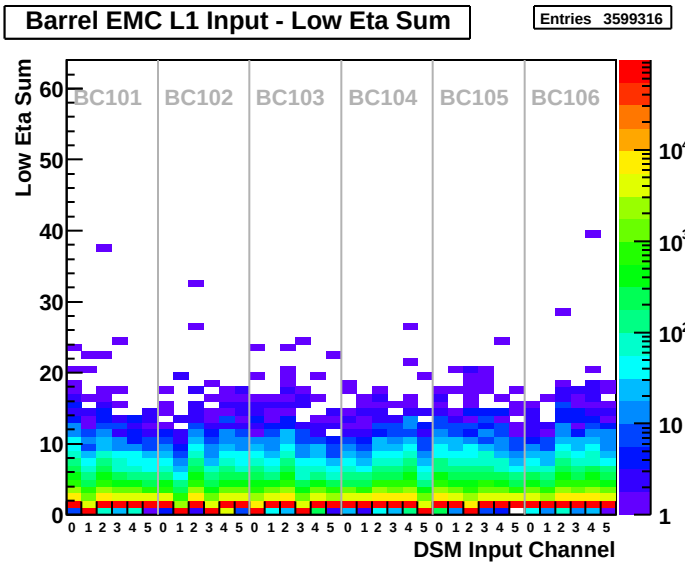
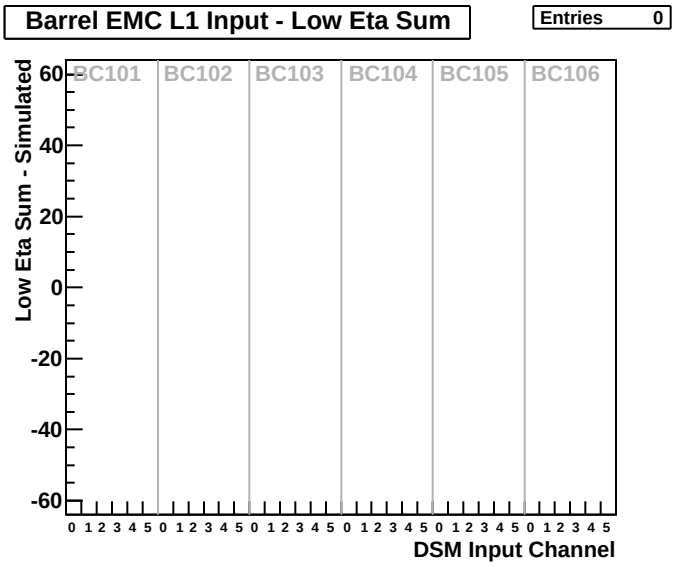
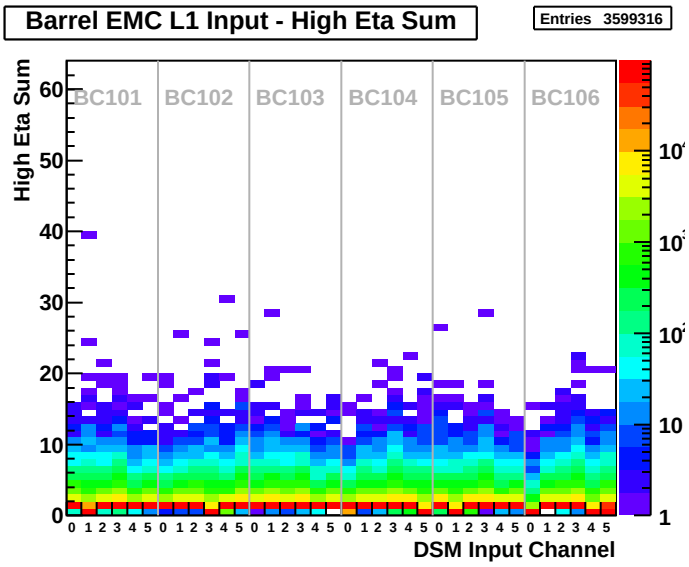
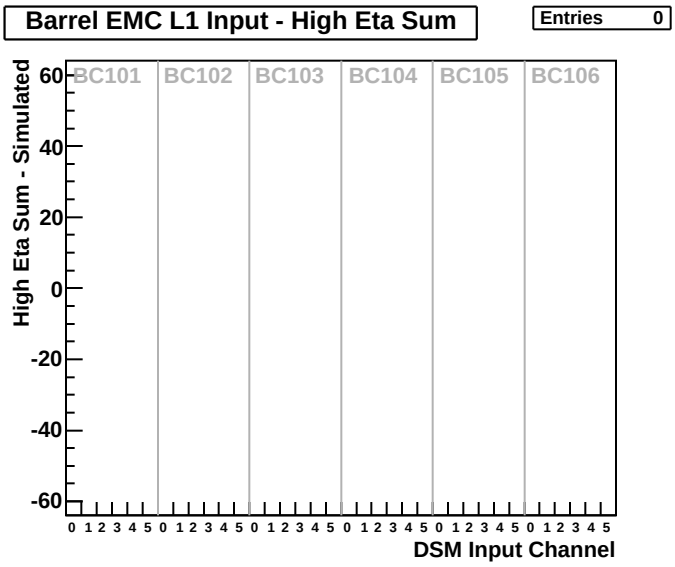
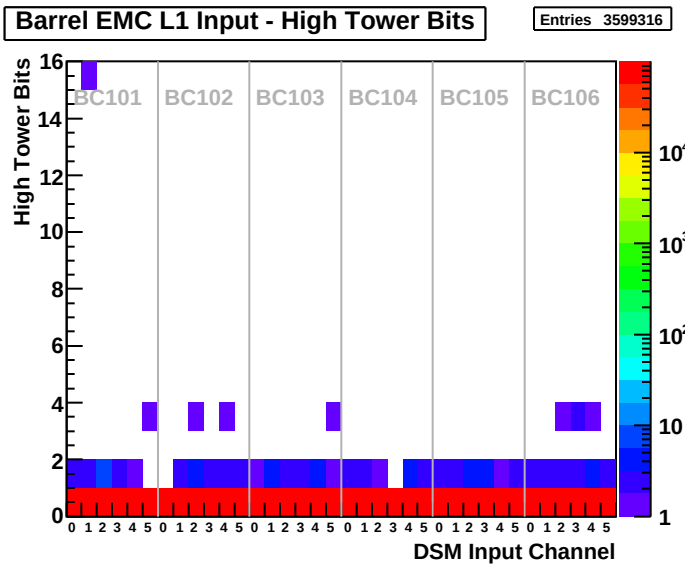
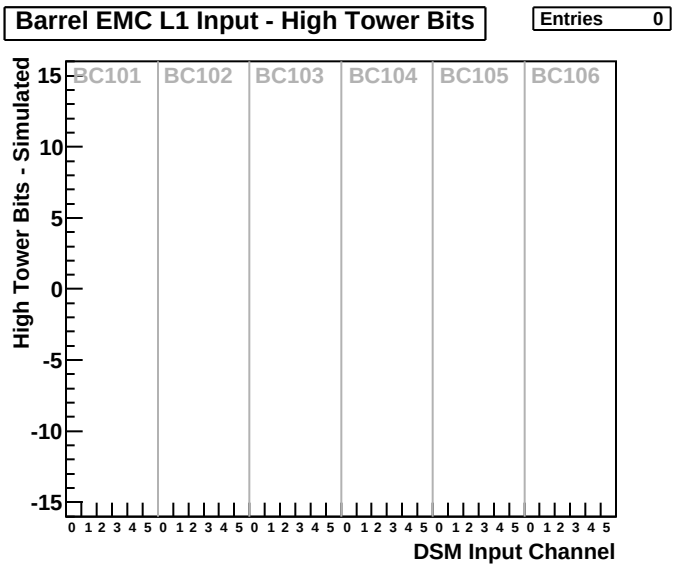

Barrel EMC L1 Input - Low Eta Sum
Entries 3599316
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

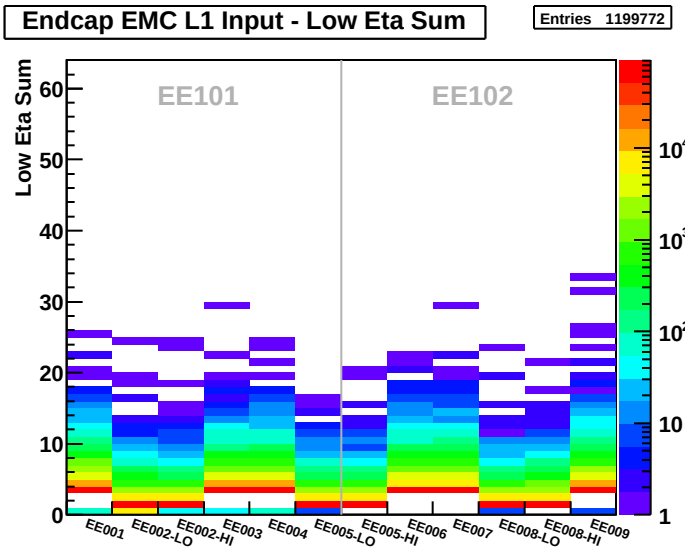

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

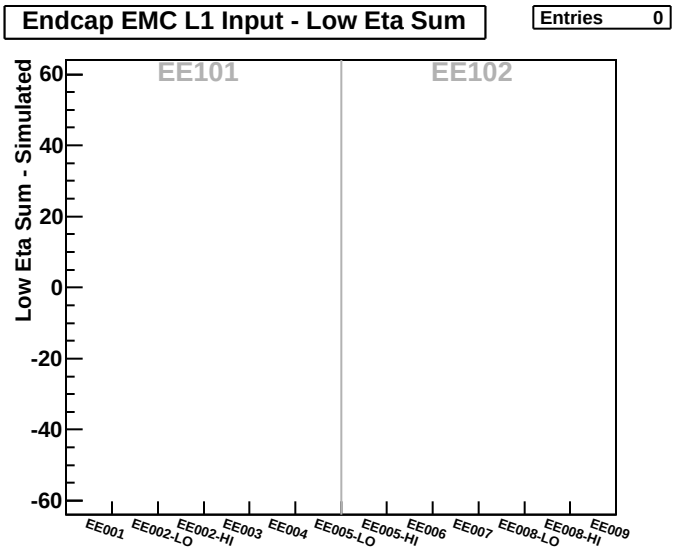

Barrel EMC L1 Input - High Eta Sum
Entries 3599316
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

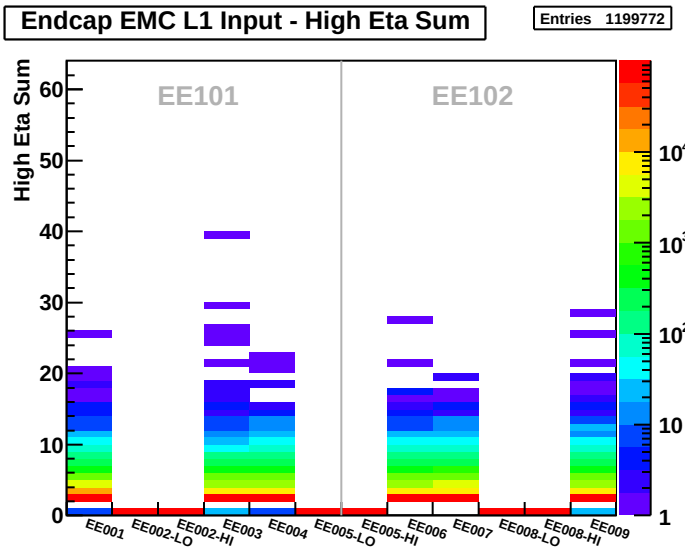

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

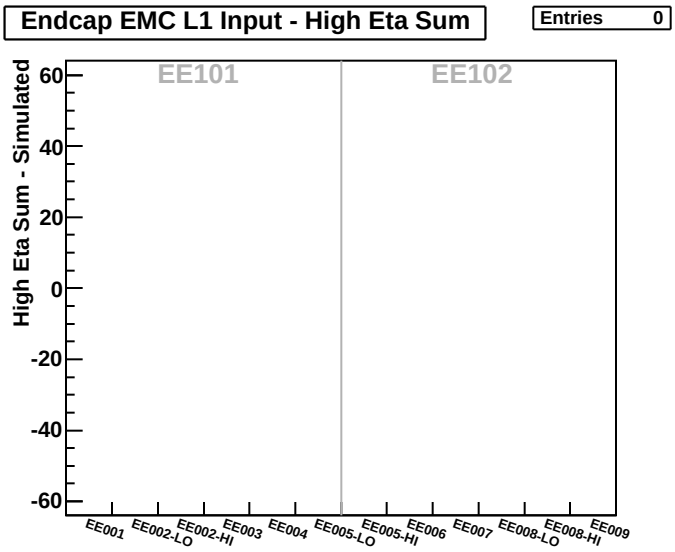

Barrel EMC L1 Input - High Tower Bits
Entries 3599316
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

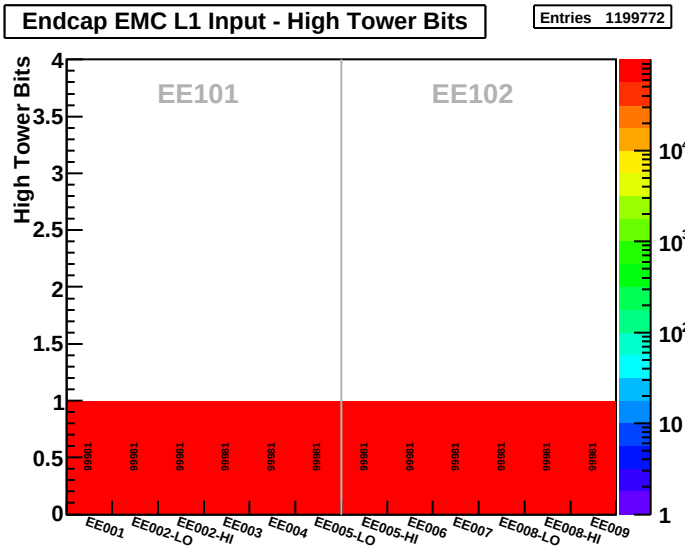

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

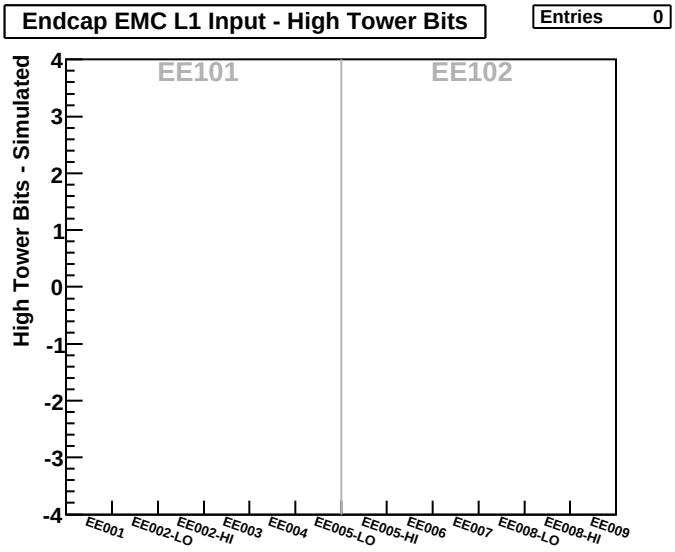

Endcap EMC L1 Input - Low Eta Sum
Entries 1199772
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

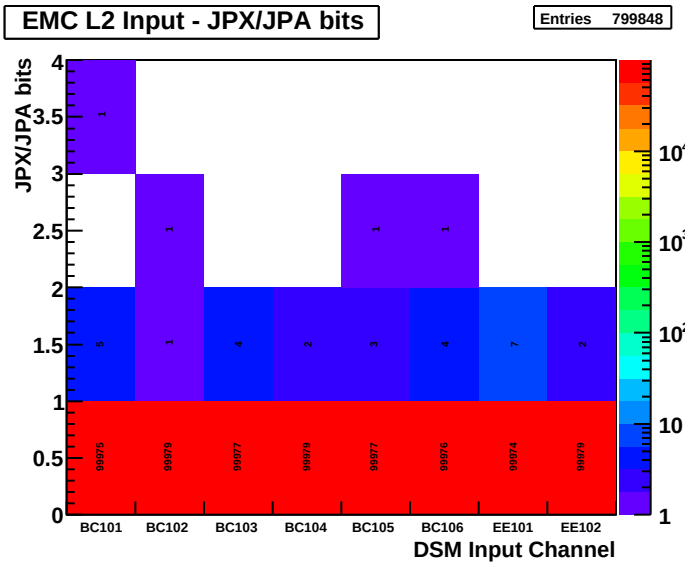

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

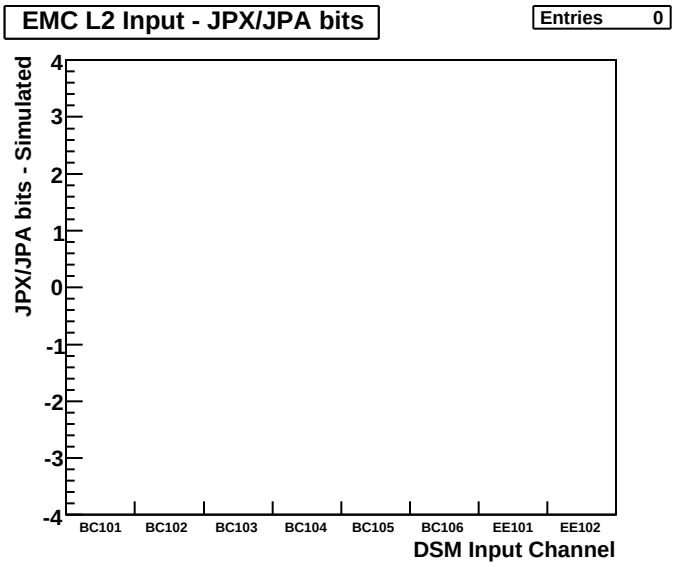

Endcap EMC L1 Input - High Eta Sum
Entries 1199772
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

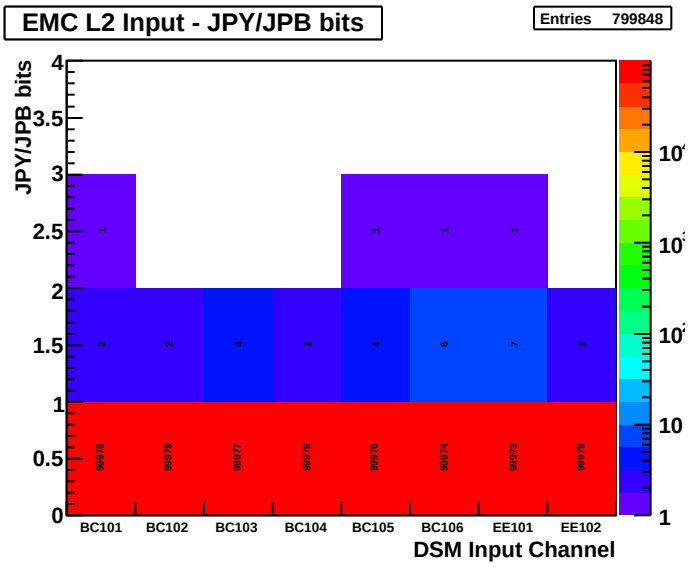

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

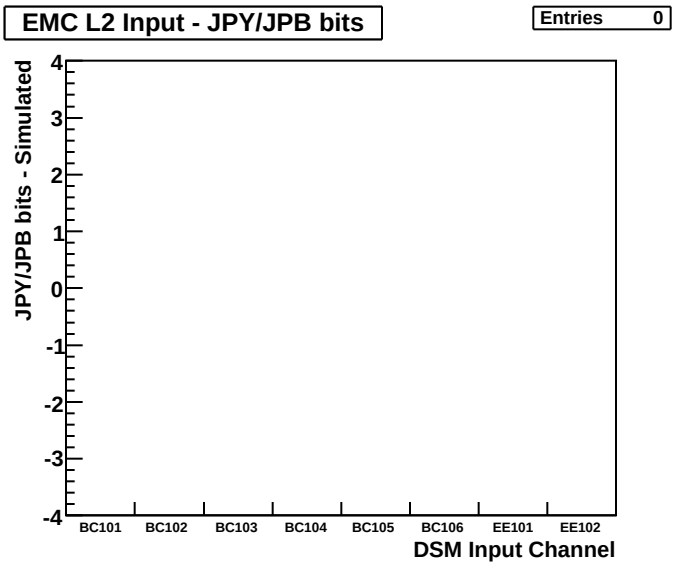

Endcap EMC L1 Input - High Tower Bits
Entries 1199772
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

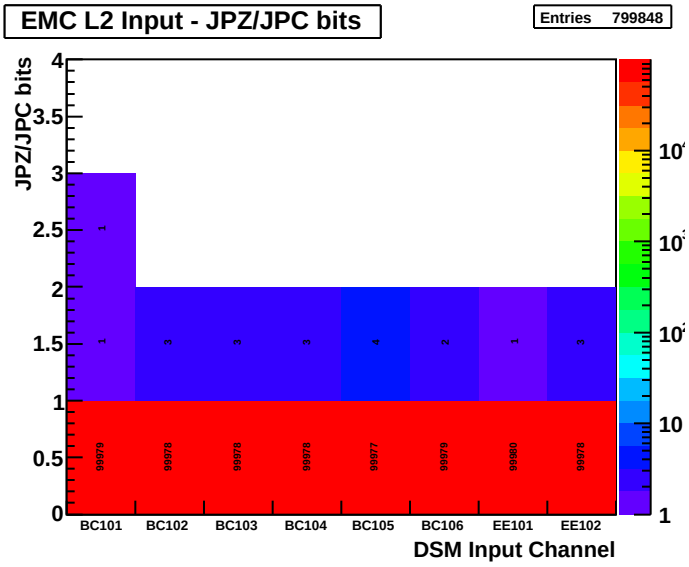

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

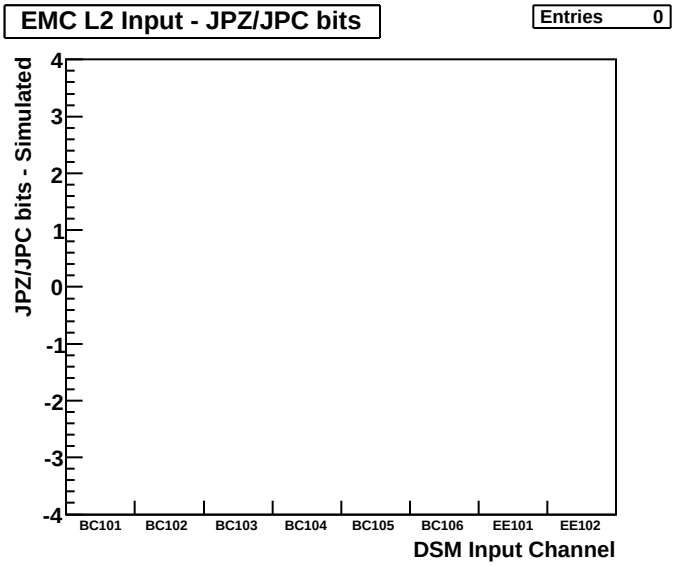

EMC L2 Input - JPX/JPA bits
Entries 799848
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

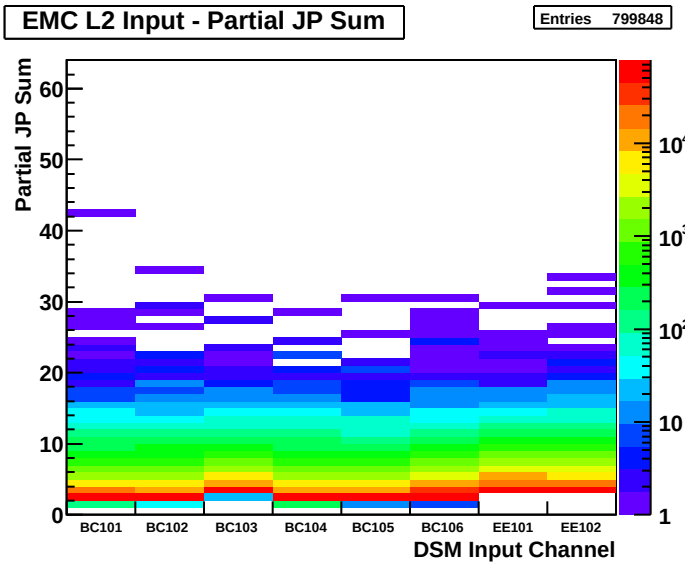

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

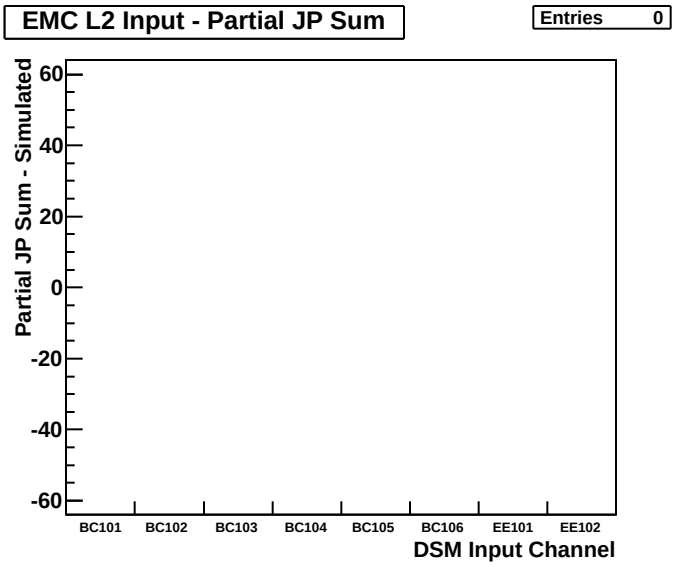

EMC L2 Input - JPY/JPB bits
Entries 799848
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

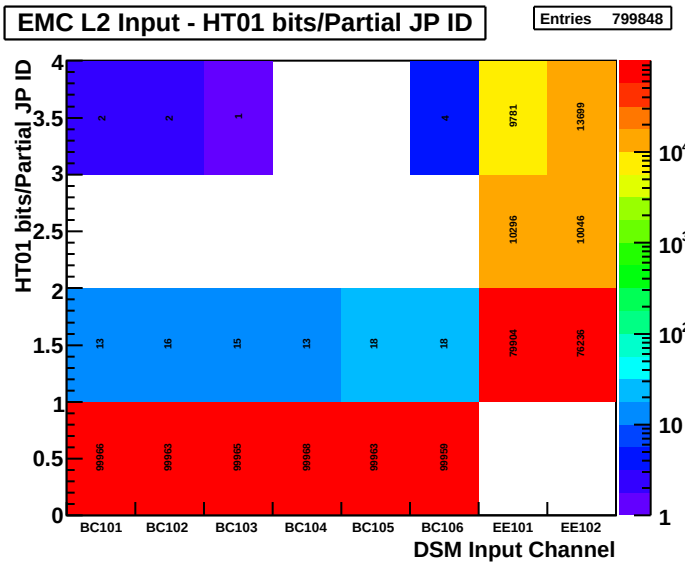

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

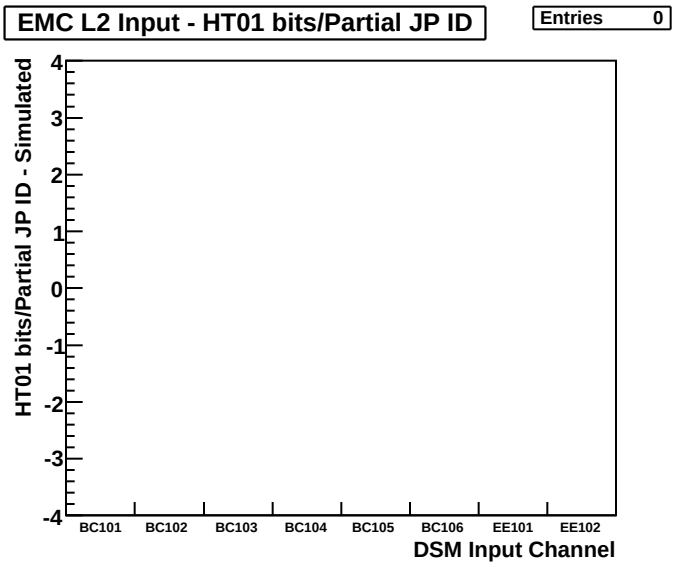

EMC L2 Input - JPZ/JPC bits
Entries 799848
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

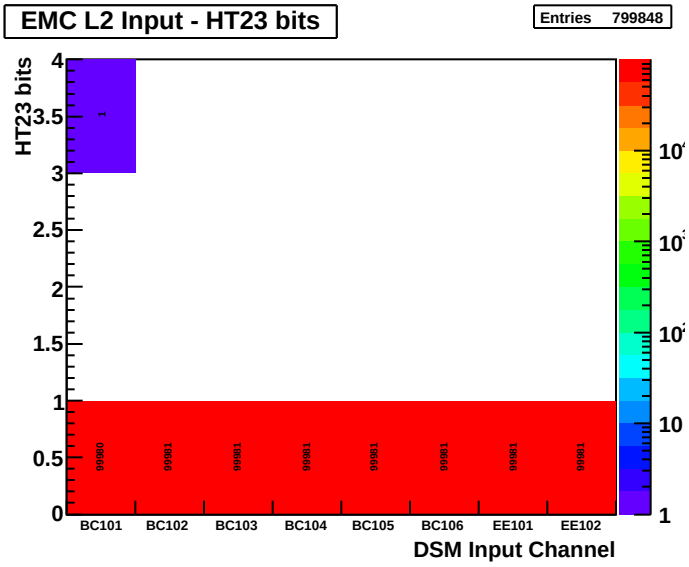

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

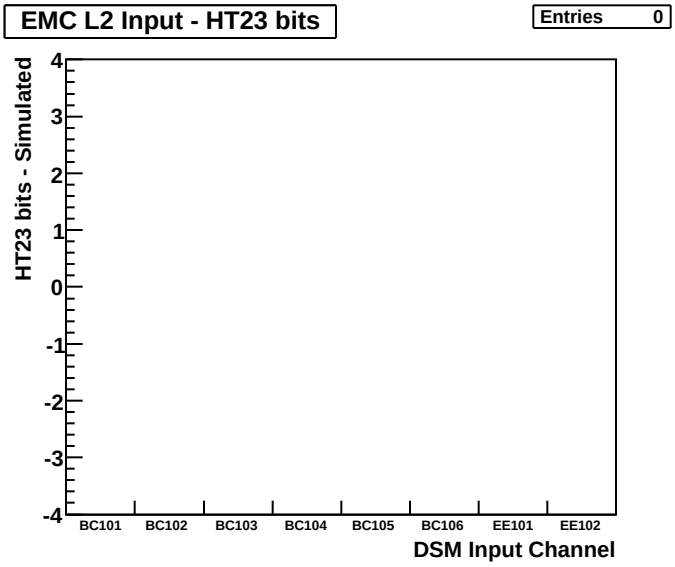

EMC L2 Input - Partial JP Sum
Entries 799848
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

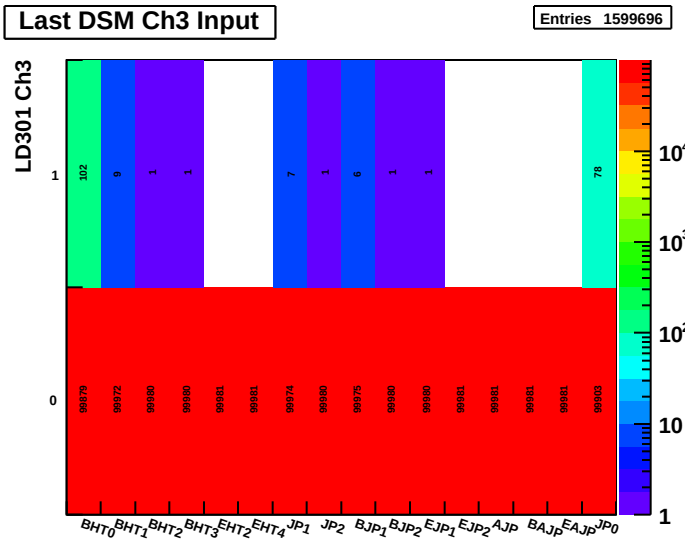

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

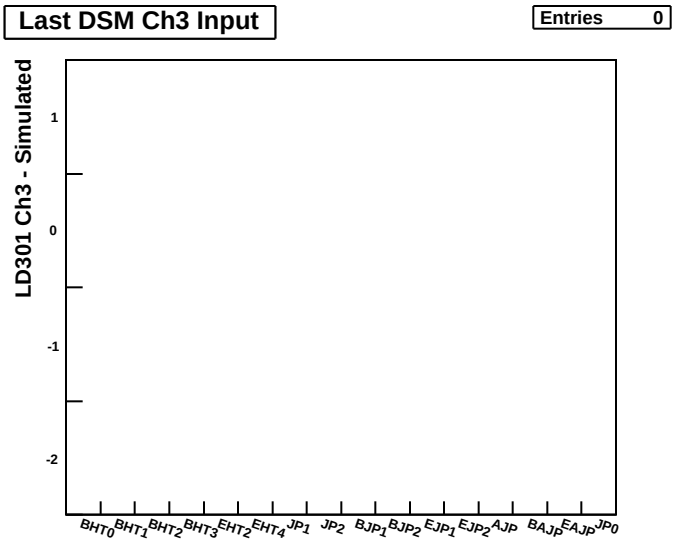

EMC L2 Input - HT01 bits/Partial JP ID
Entries 799848
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

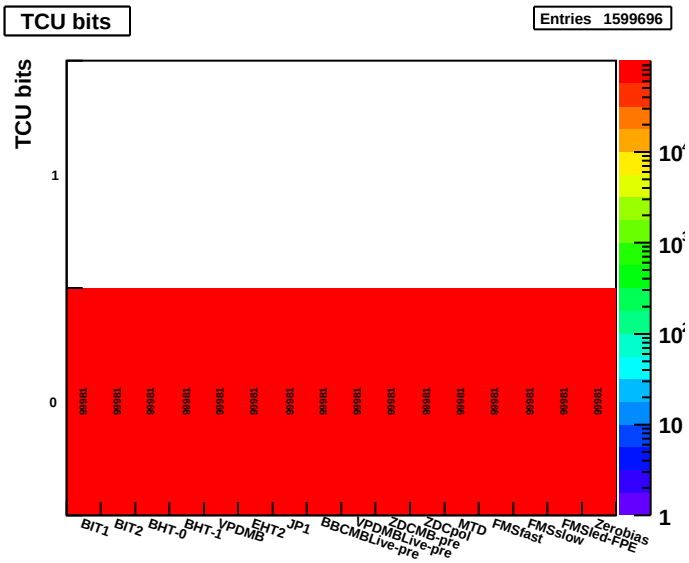

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

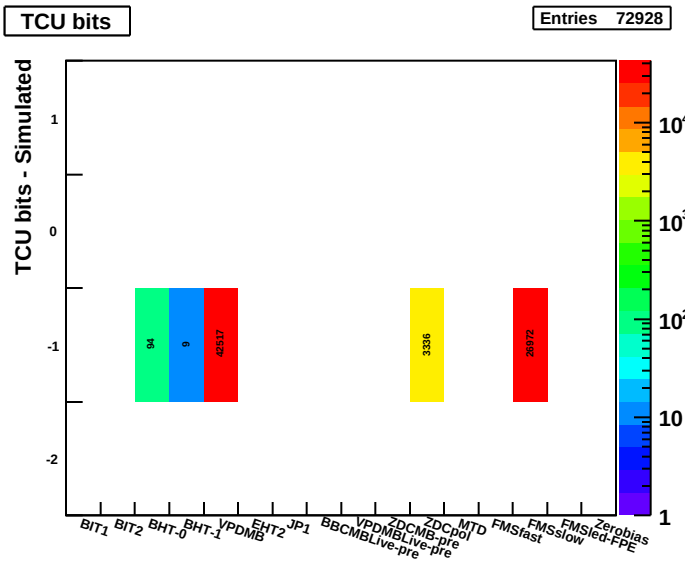

EMC L2 Input - HT23 bits
Entries 799848
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

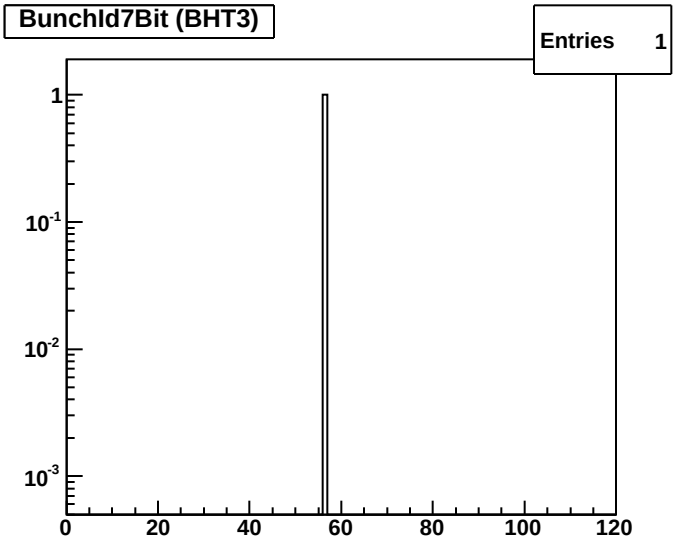

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

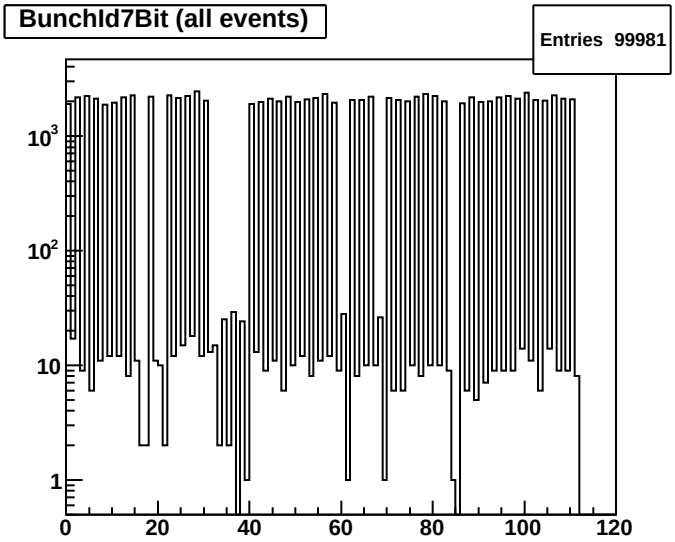

Last DSM Ch3 Input
Entries 1599696
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

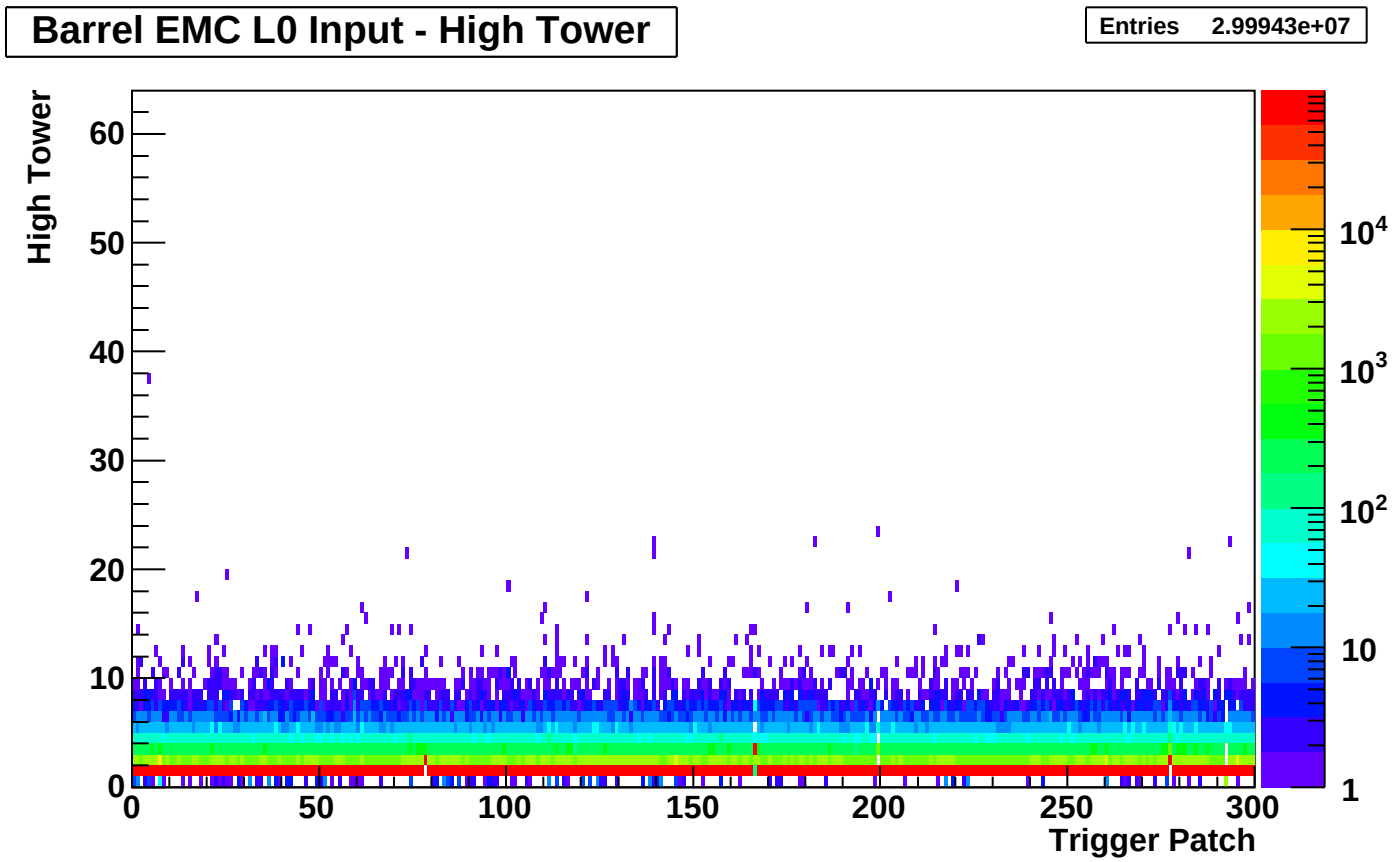

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

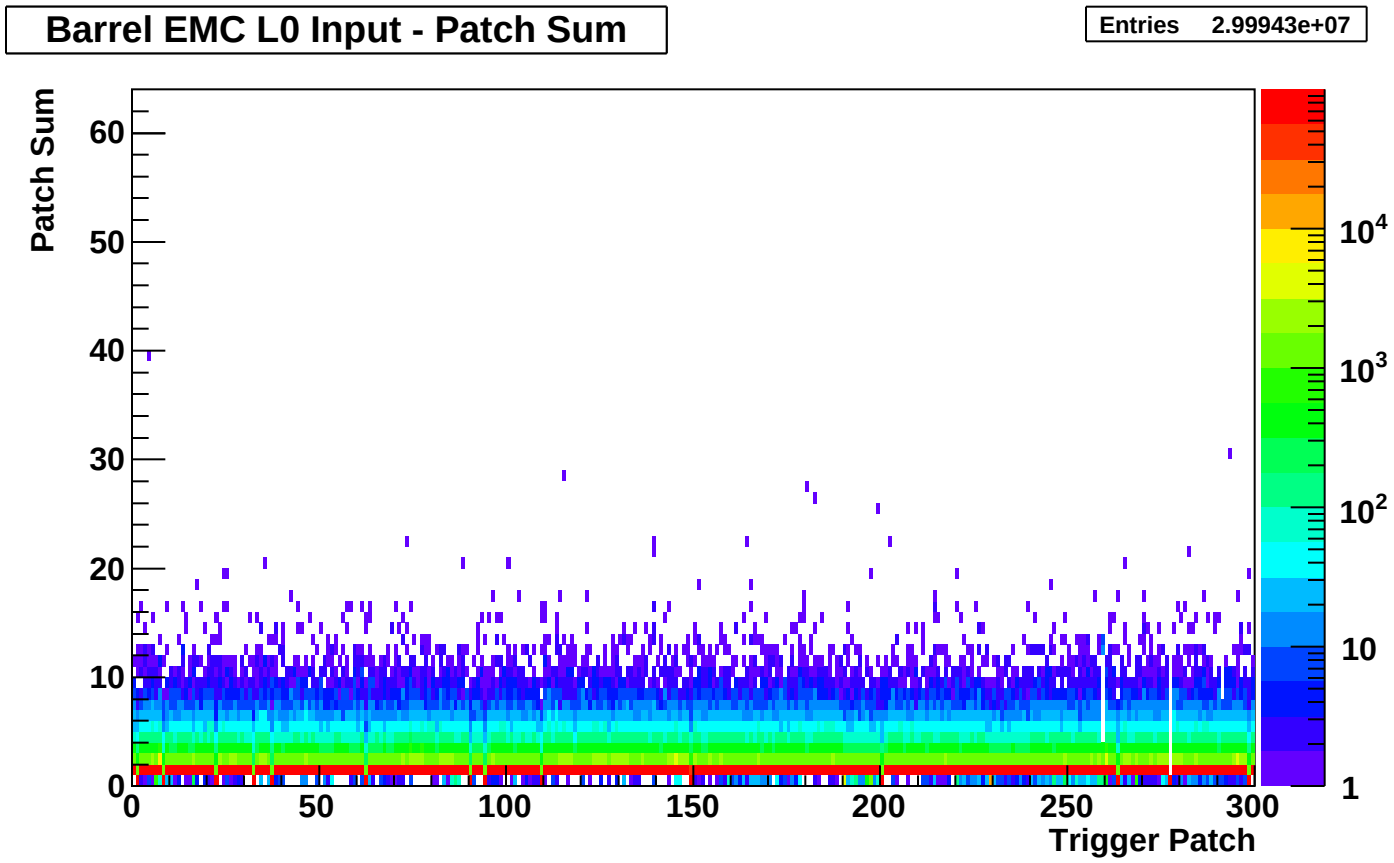

TCU bits
Entries 1599696
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

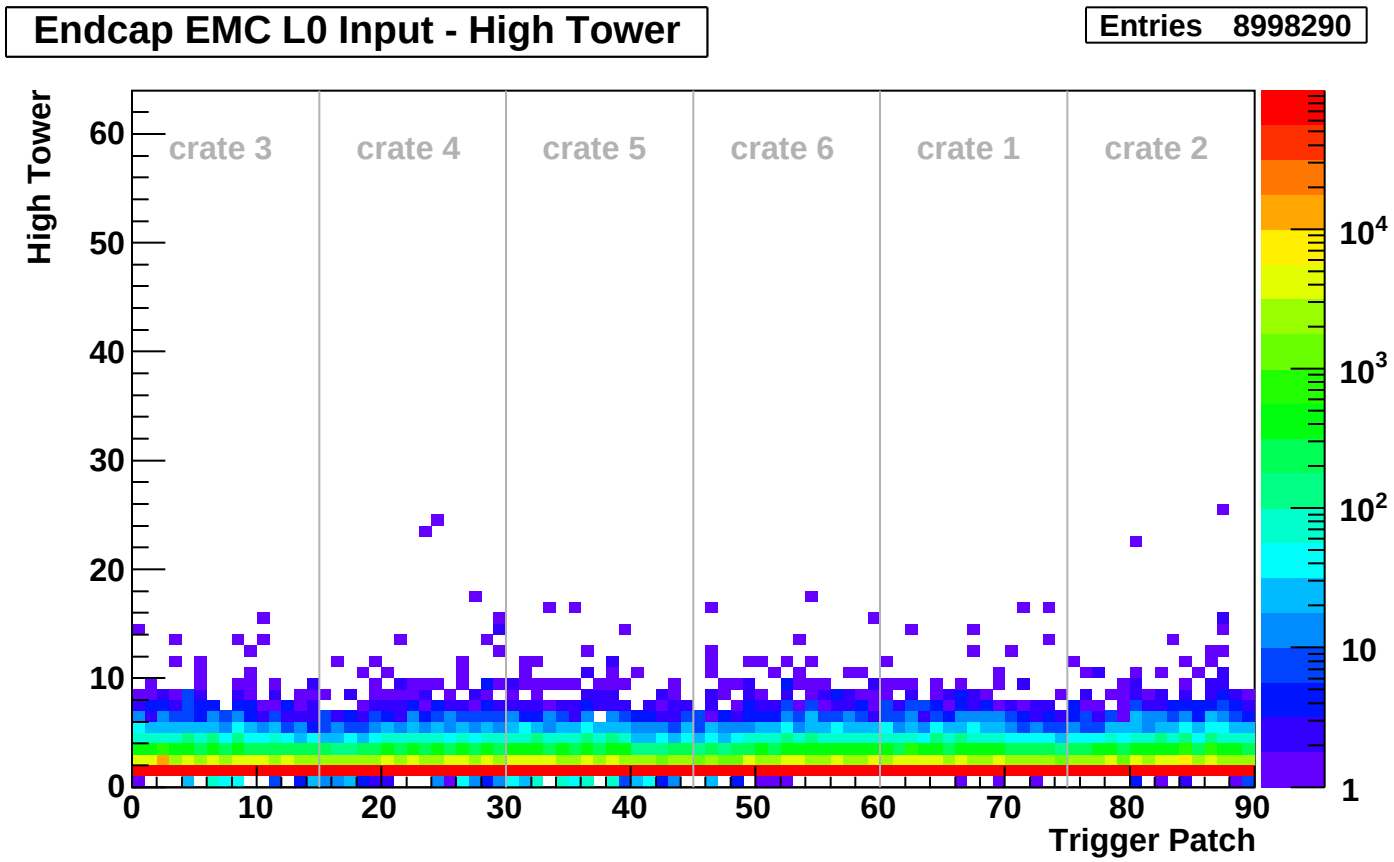

TCU bits
Entries 72928
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

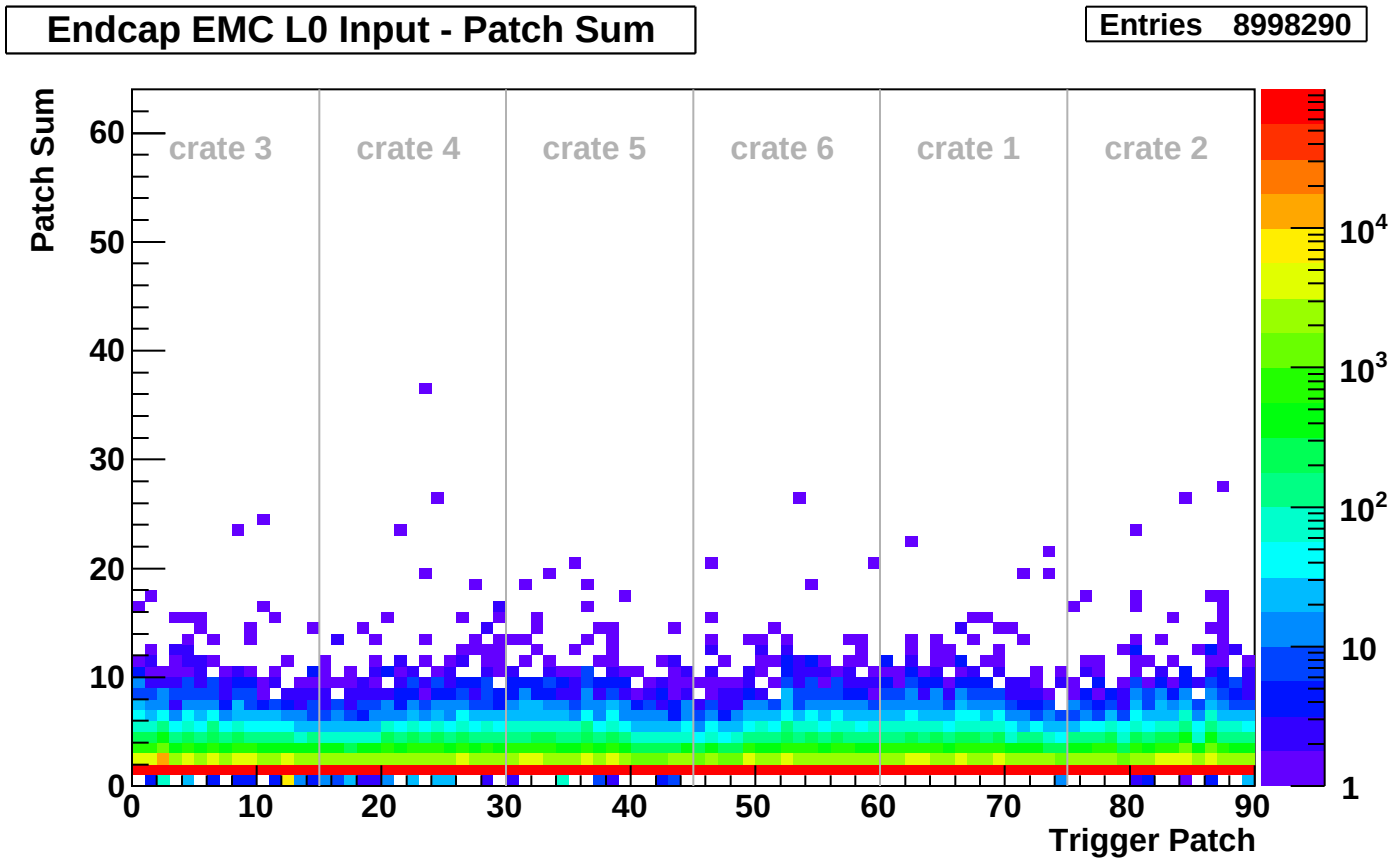

BunchId7Bit (BHT3)
Entries 1

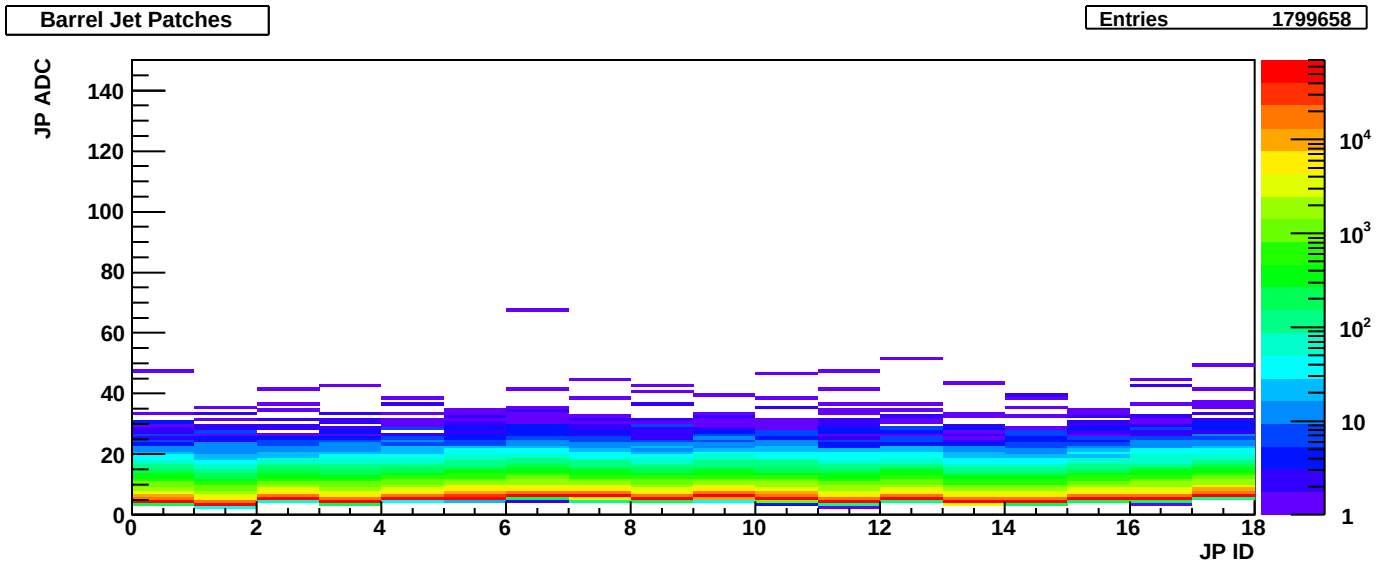

BunchId7Bit (all events)
Entries 99981

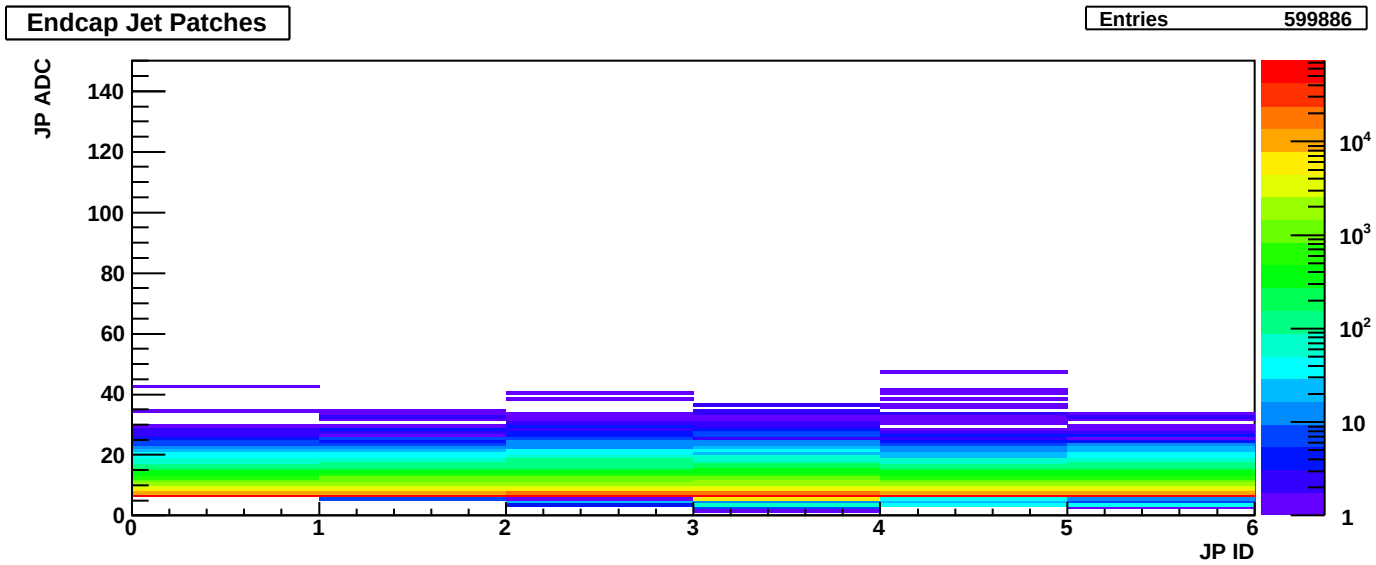

Barrel EMC L0 Input - High Tower
Entries 2.99943e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

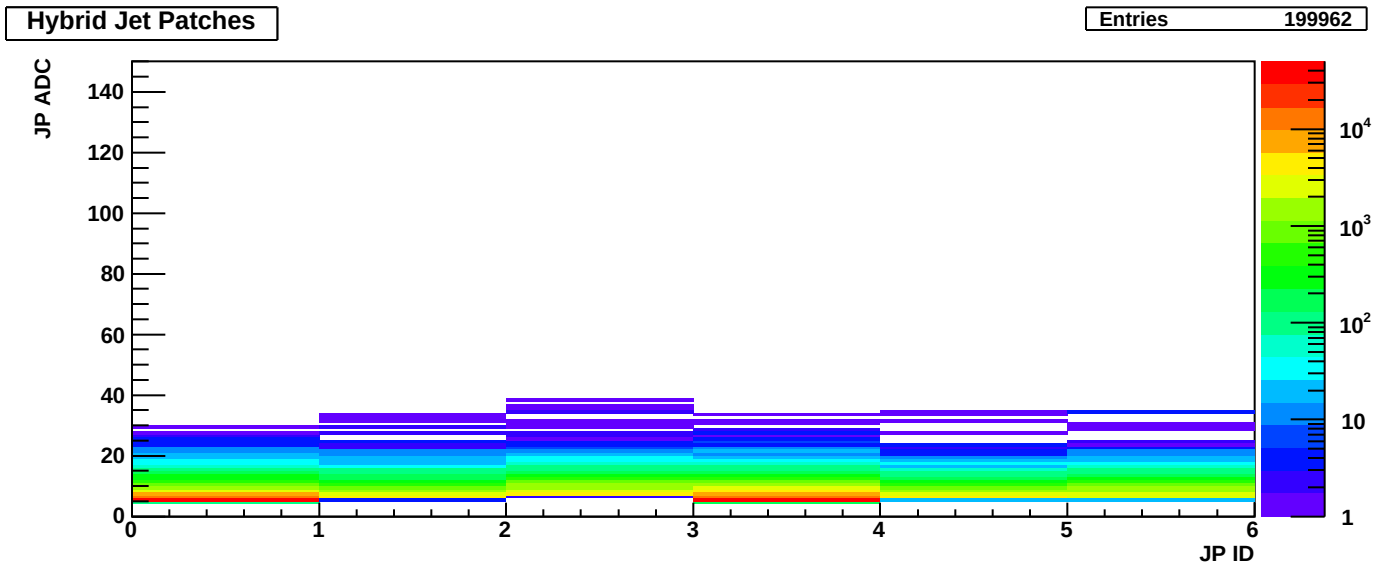

Barrel EMC L0 Input - Patch Sum
Entries 2.99943e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

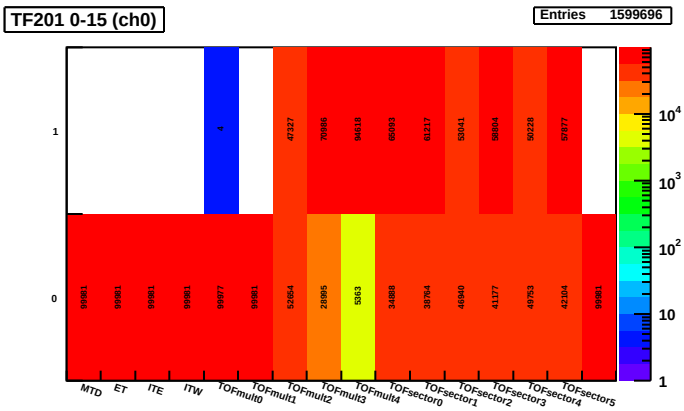

Endcap EMC L0 Input - High Tower
Entries 8998290
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

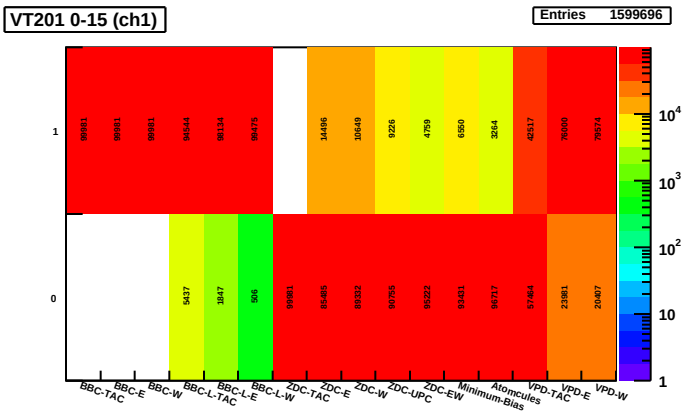

Endcap EMC L0 Input - Patch Sum
Entries 8998290
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

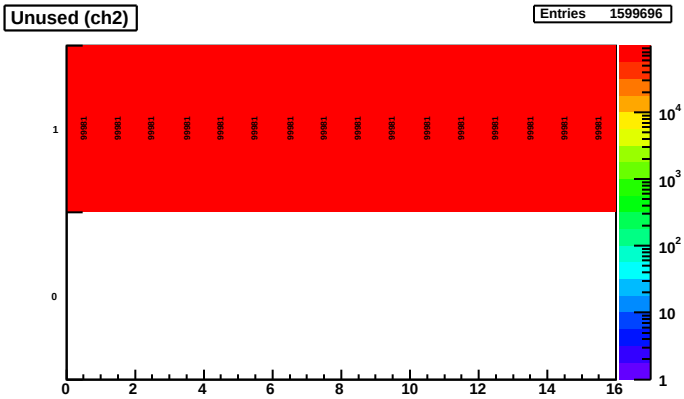

Barrel Jet Patches
Entries 1799658
JP ADC
JP ID

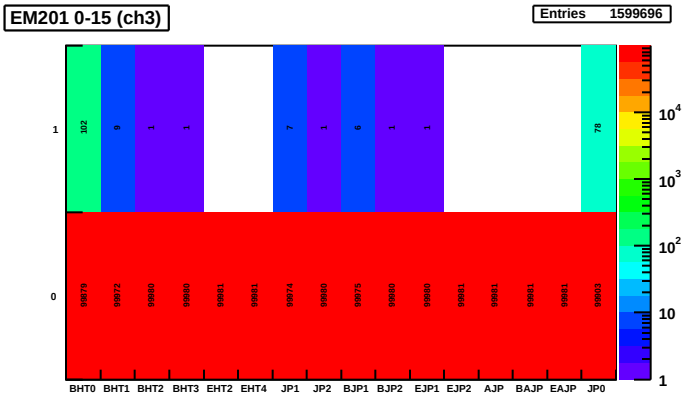

Endcap Jet Patches
Entries 599886
JP ADC
JP ID

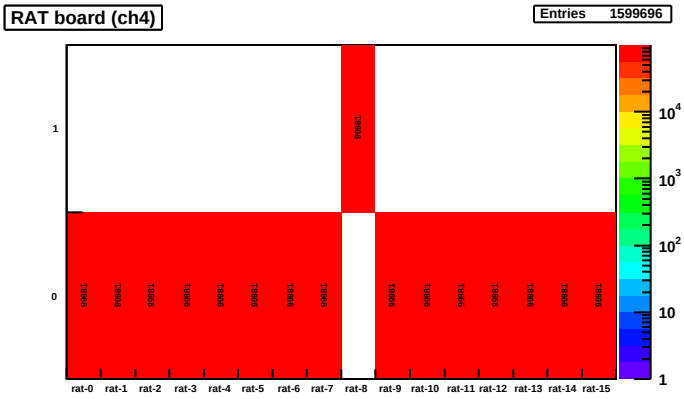

Hybrid Jet Patches
Entries 199962
JP ADC
JP ID

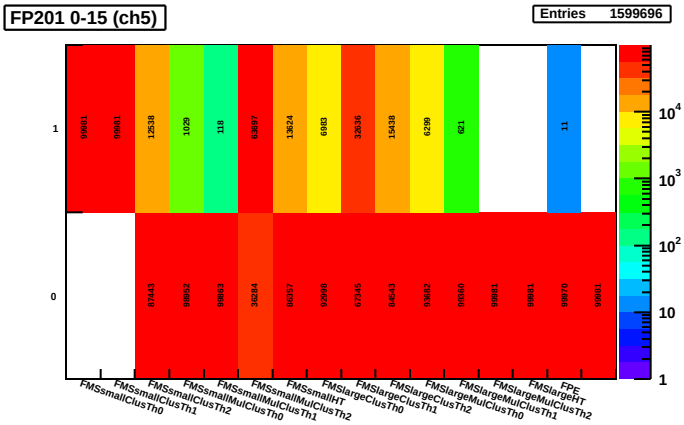

TF201 0-15 (ch0)
Entries 1599696
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

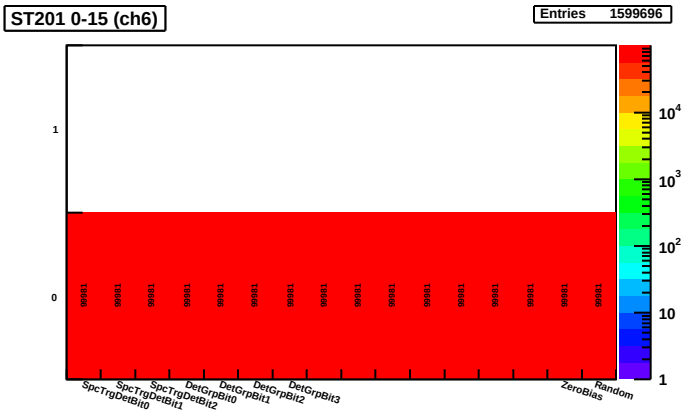

VT201 0-15 (ch1)
Entries 1599696
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

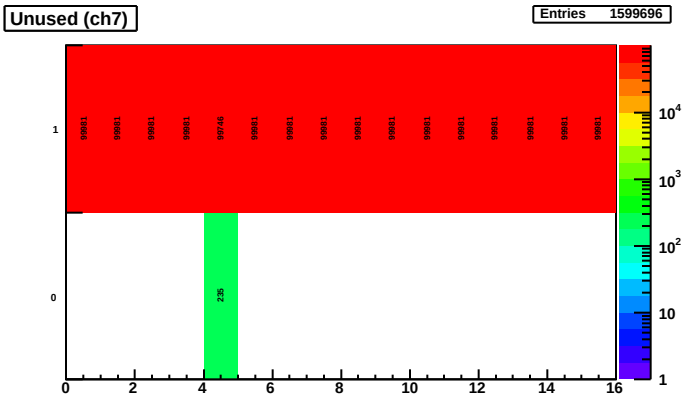

Unused (ch2)
Entries 1599696

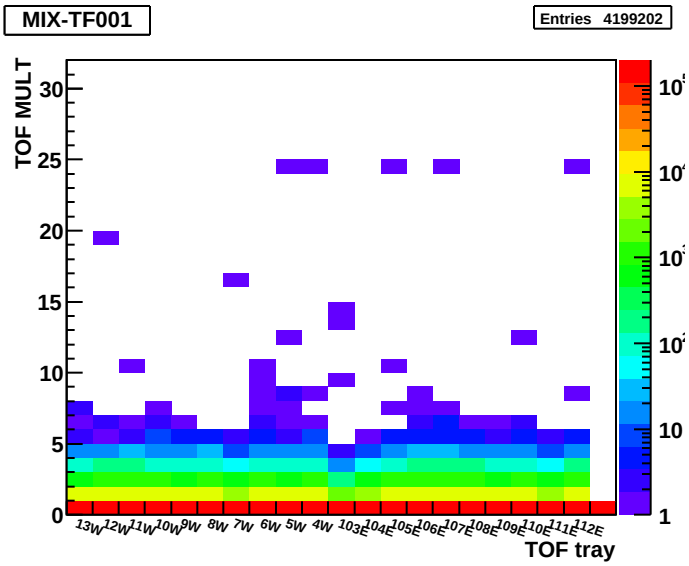

EM201 0-15 (ch3)
Entries 1599696
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

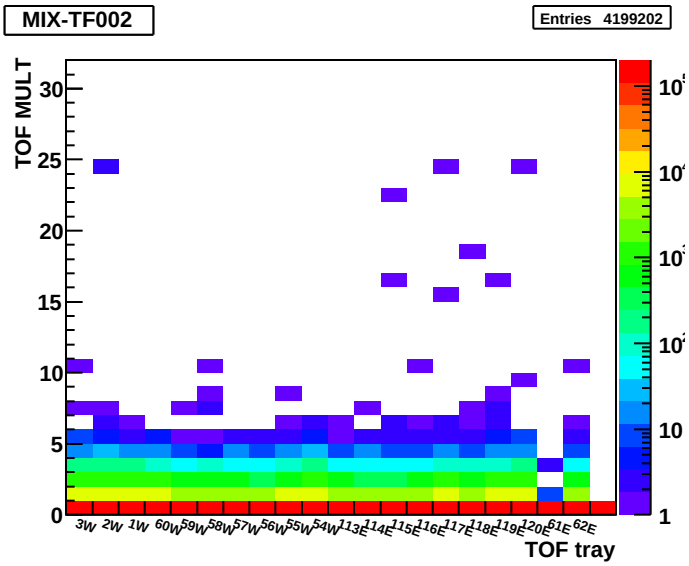

RAT board (ch4)
Entries 1599696
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

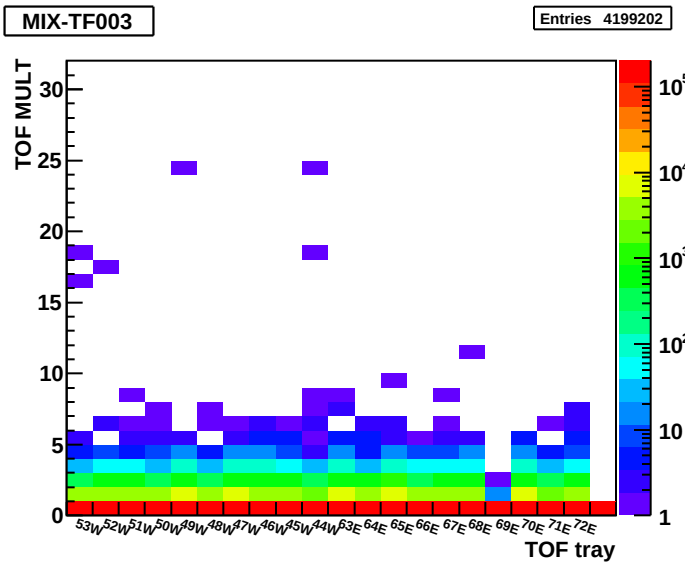

FP201 0-15 (ch5)
Entries 1599696
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

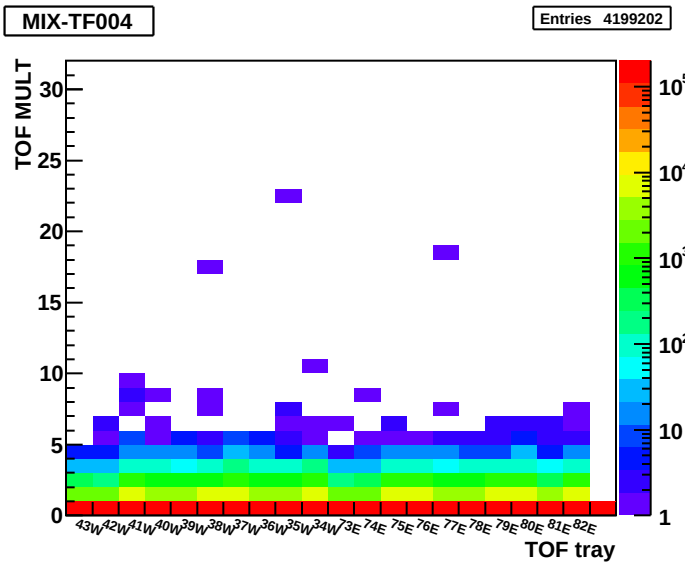

ST201 0-15 (ch6)
Entries 1599696
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

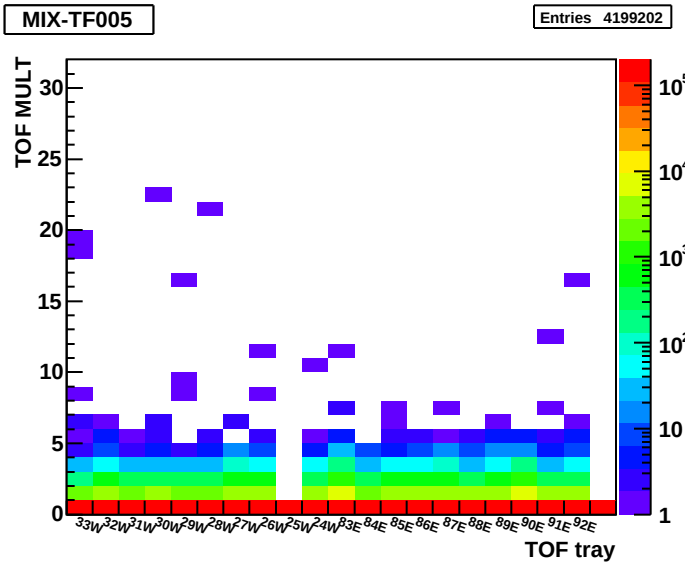

Unused (ch7)
Entries 1599696

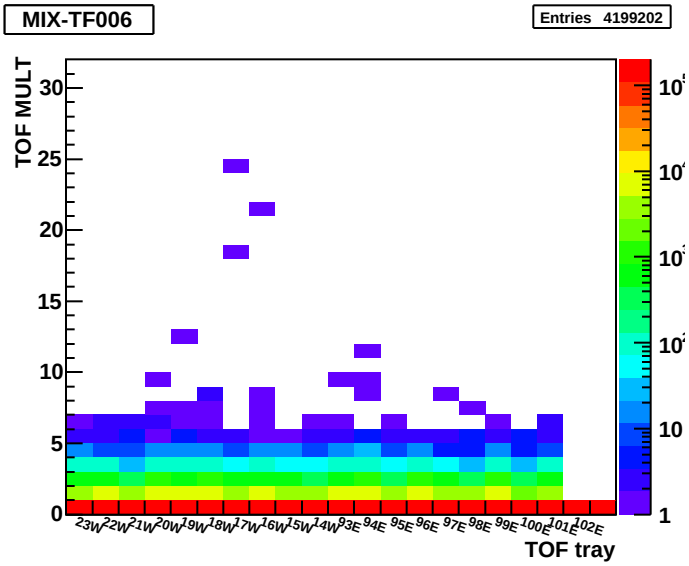

MIX-TF001
Entries 4199202
TOF MULT
TOF tray

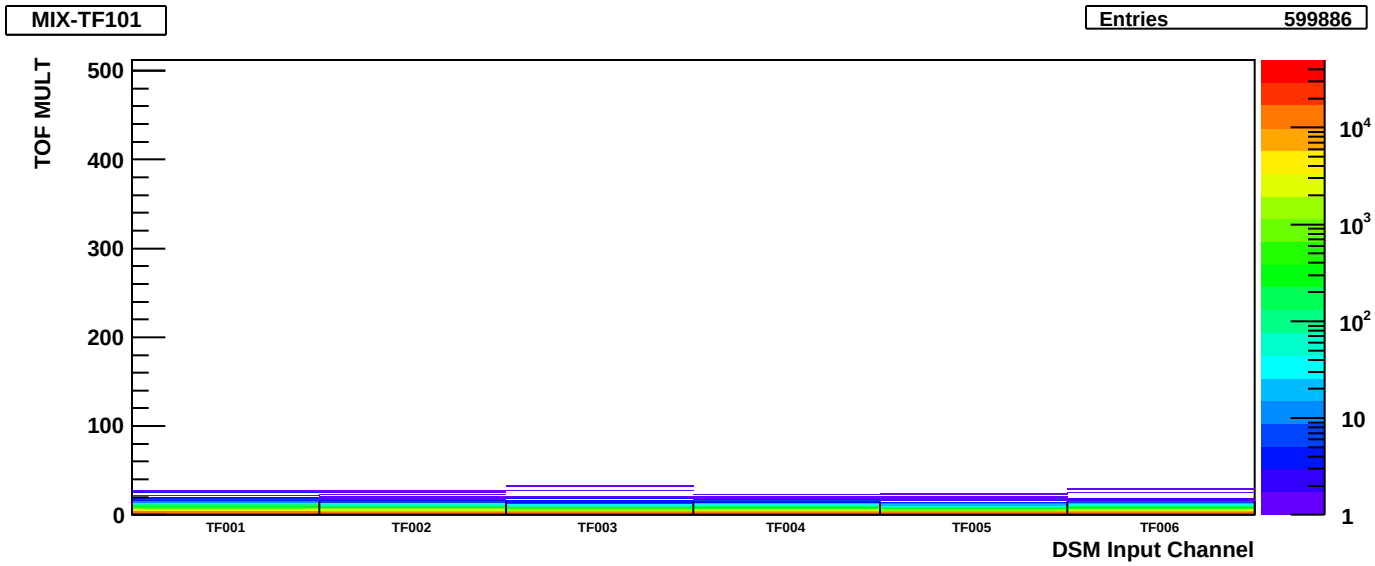

MIX-TF002
Entries 4199202
TOF MULT
TOF tray

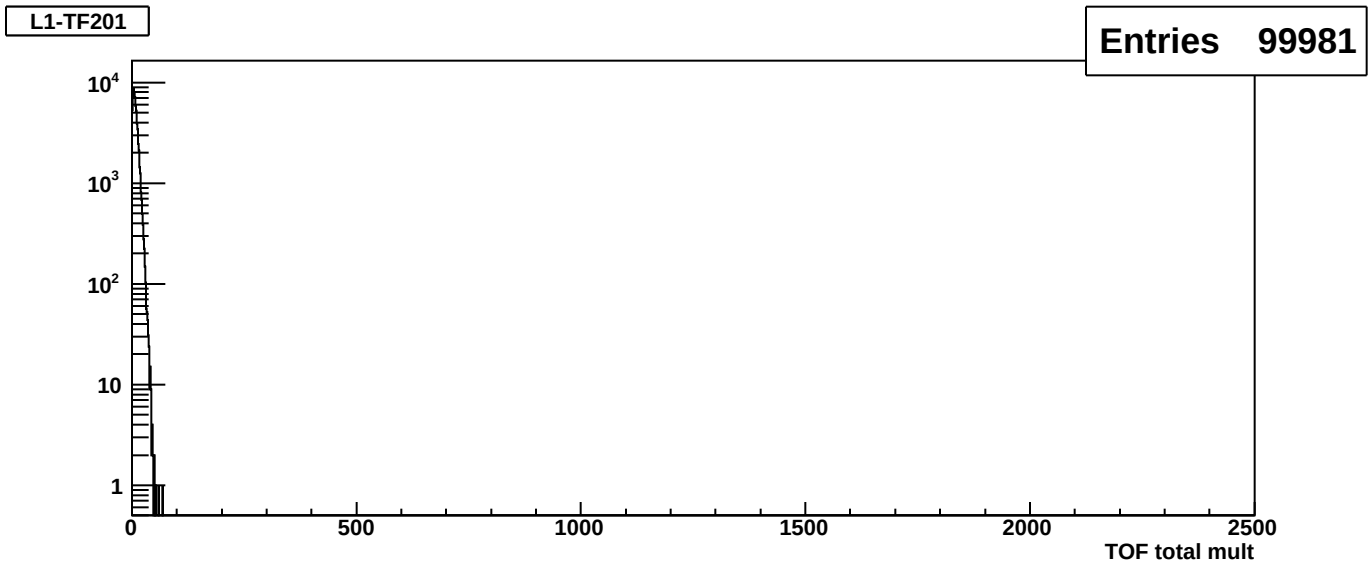

MIX-TF003
Entries 4199202
TOF MULT
TOF tray

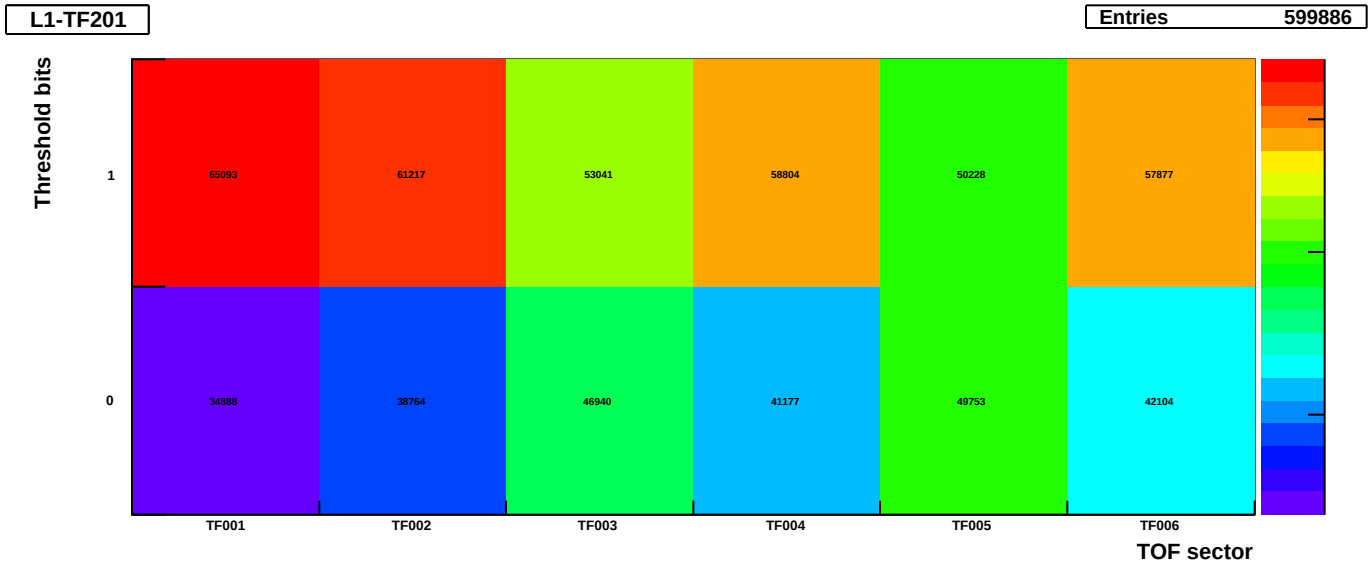

MIX-TF004
Entries 4199202
TOF MULT
TOF tray

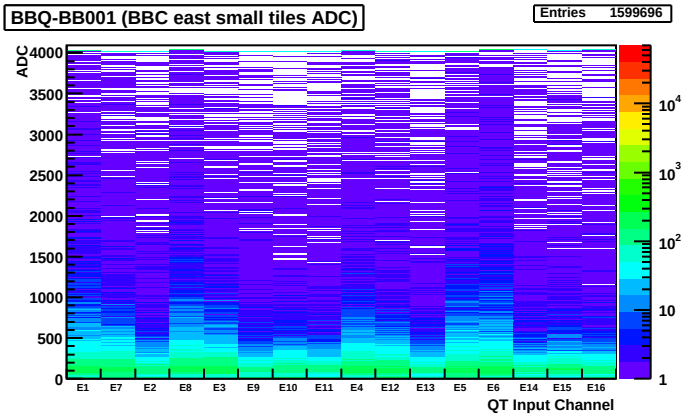

MIX-TF005
Entries 4199202
TOF MULT
TOF tray

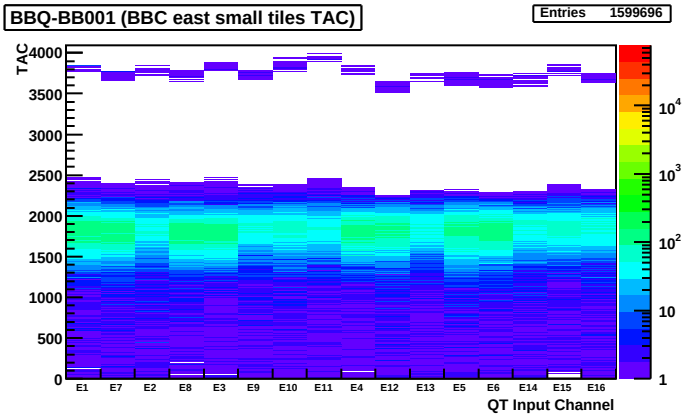

MIX-TF006
Entries 4199202
TOF MULT
TOF tray

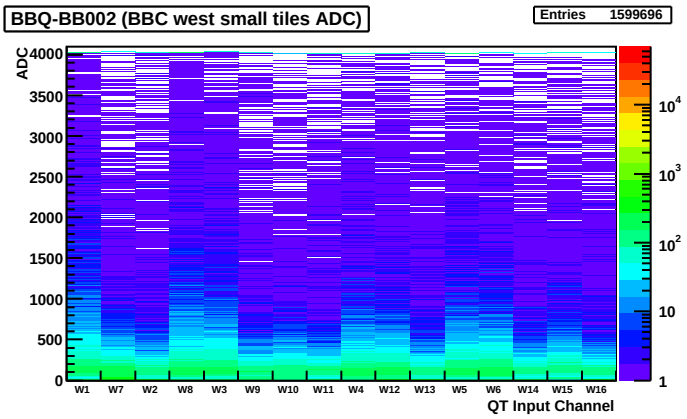

MIX-TF101
Entries 599886
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

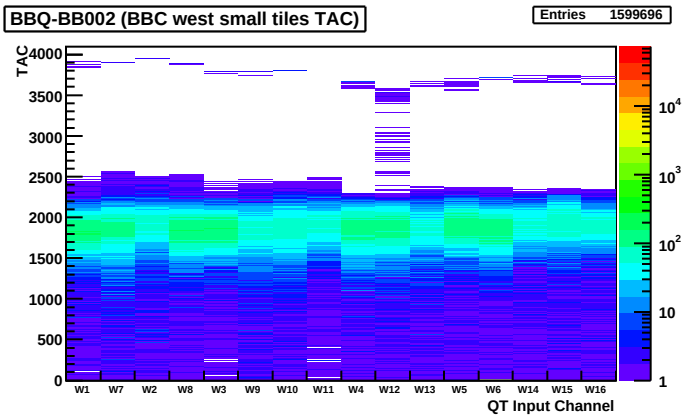

L1-TF201
Entries 99981
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

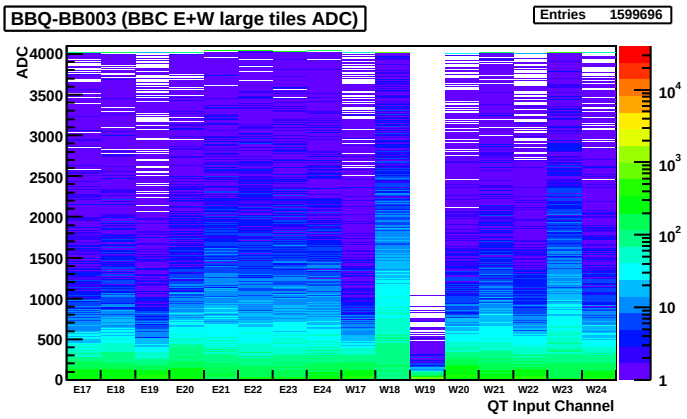

L1-TF201
Entries 599886
Threshold bits
1
0
65093
61217
53041
58804
50228
57877
34888
38764
46940
41177
49753
42104
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

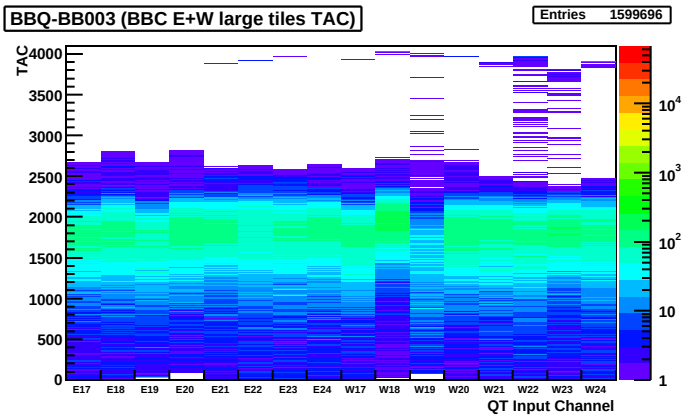

BBQ-BB001 (BBC east small tiles ADC)
Entries 1599696
ADC
QT Input Channel

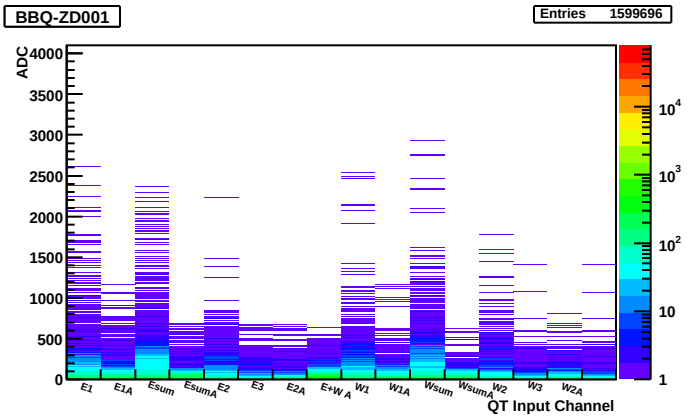

BBQ-BB001 (BBC east small tiles TAC)
Entries 1599696
TAC
QT Input Channel

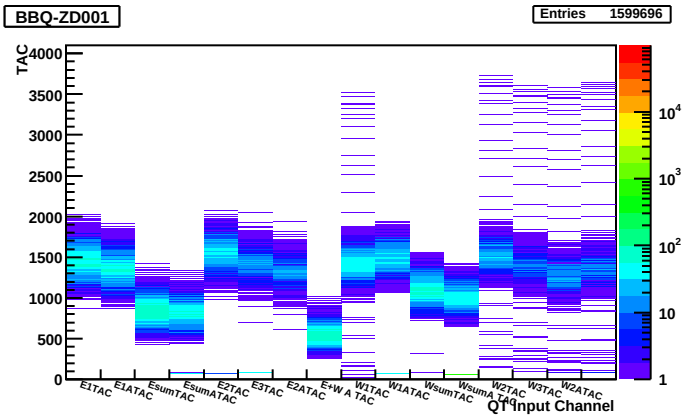

BBQ-BB002 (BBC west small tiles ADC)
Entries 1599696
ADC
QT Input Channel

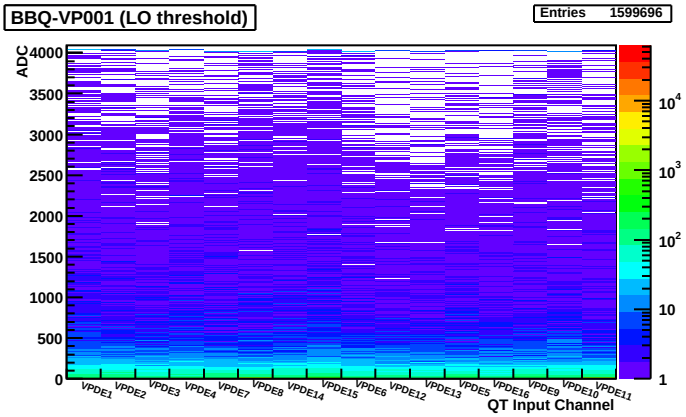

BBQ-BB002 (BBC west small tiles TAC)
Entries 1599696
TAC
QT Input Channel

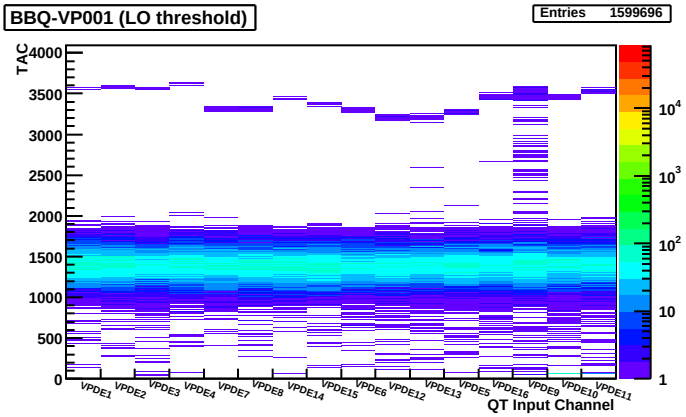

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1599696
ADC
QT Input Channel

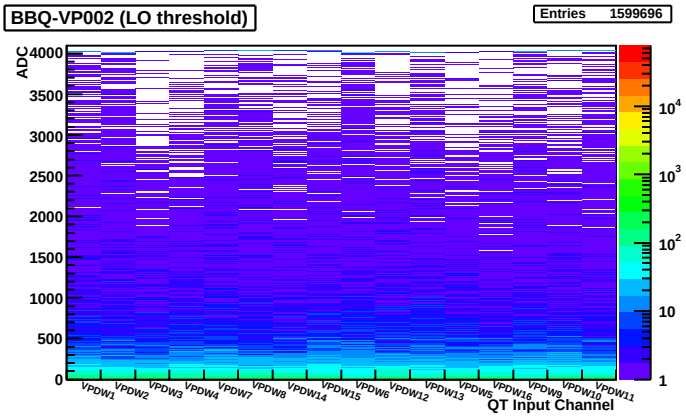

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1599696
TAC
QT Input Channel

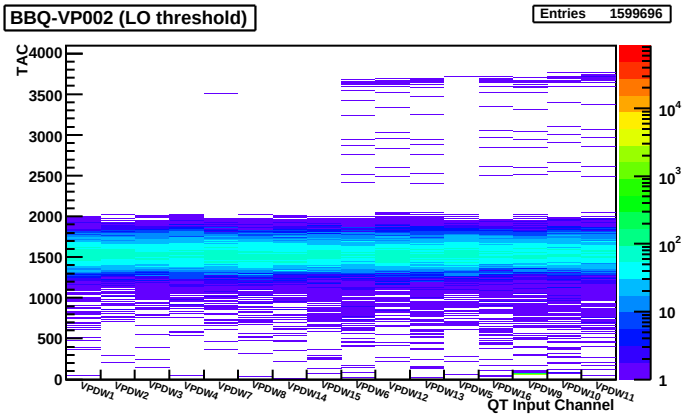

BBQ-ZD001
Entries 1599696
ADC
QT Input Channel

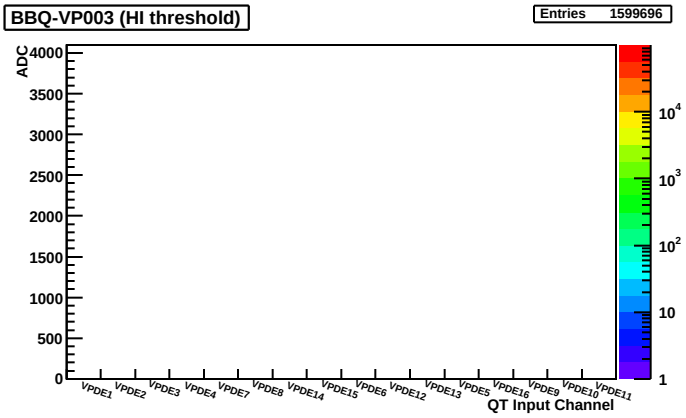

BBQ-ZD001
Entries 1599696
TAC
QT Input Channel

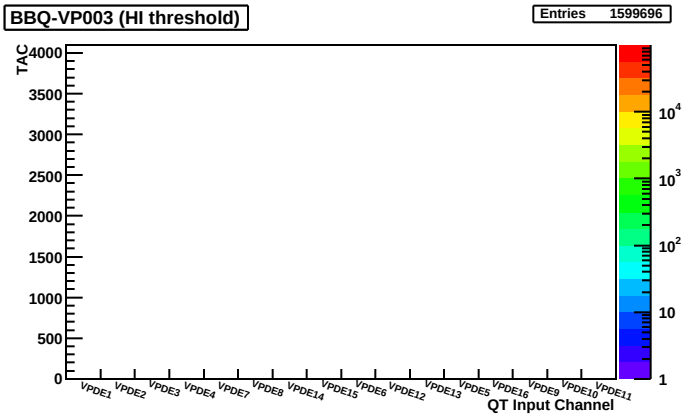

BBQ-VP001 (LO threshold)
Entries 1599696
ADC
QT Input Channel

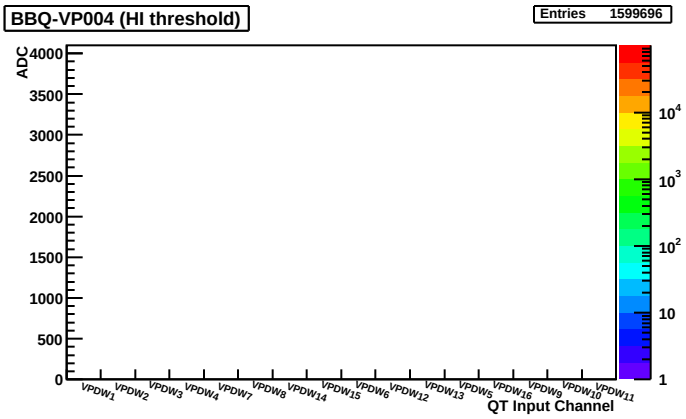

BBQ-VP001 (LO threshold)
Entries 1599696
TAC
QT Input Channel

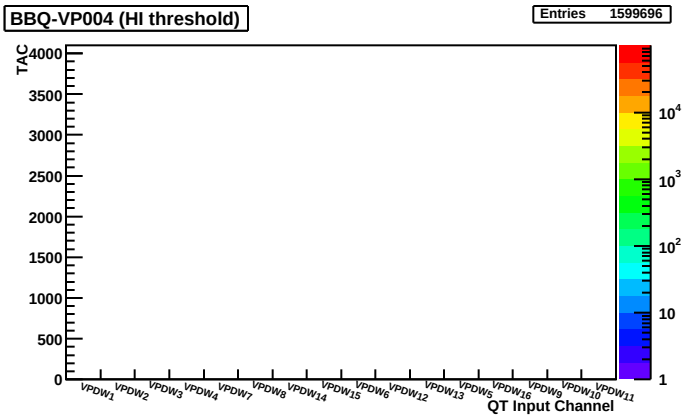

BBQ-VP002 (LO threshold)
Entries 1599696
ADC
QT Input Channel

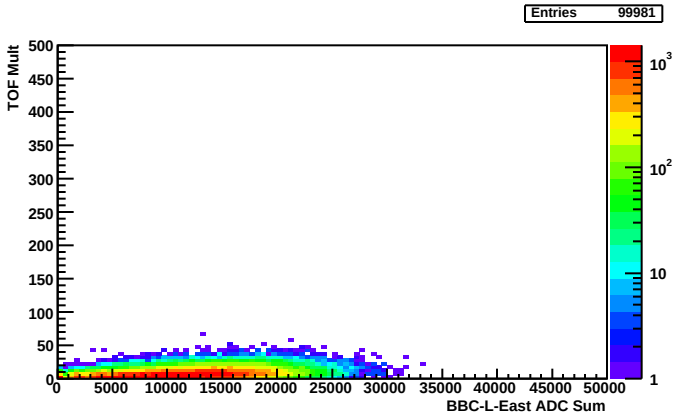

BBQ-VP002 (LO threshold)
Entries 1599696
TAC
QT Input Channel

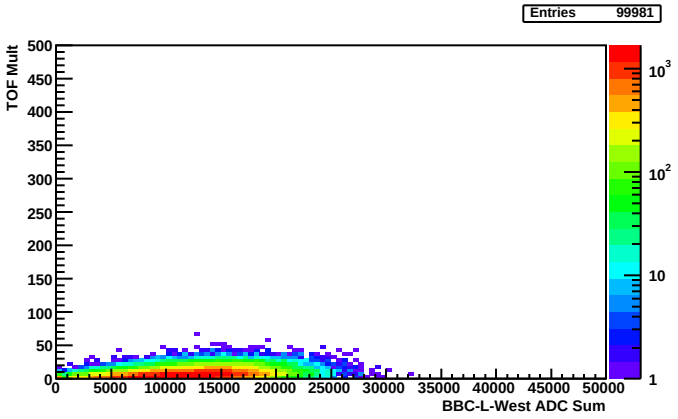

BBQ-VP003 (HI threshold)
Entries 1599696
ADC
QT Input Channel

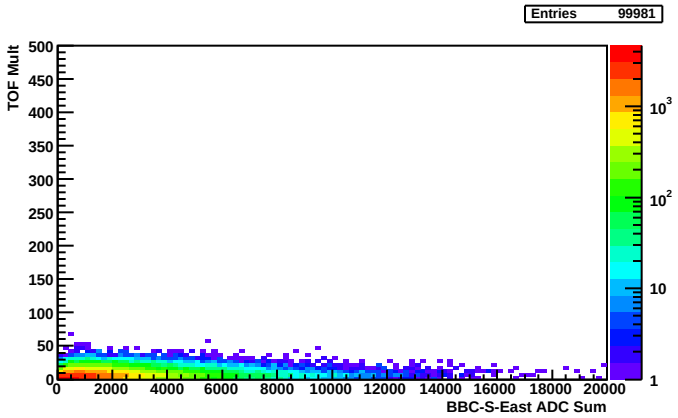

BBQ-VP003 (HI threshold)
Entries 1599696
TAC
QT Input Channel

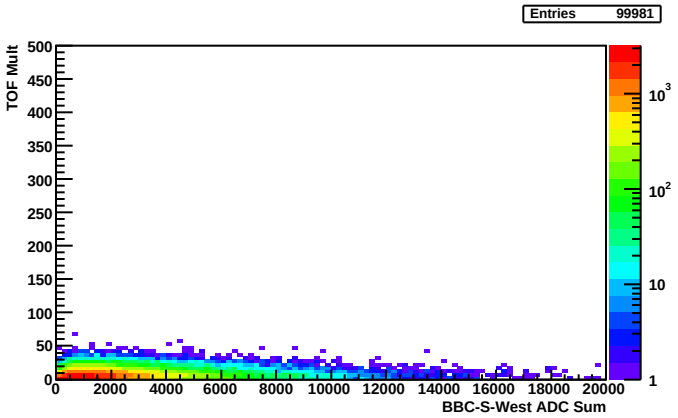

BBQ-VP004 (HI threshold)
Entries 1599696
ADC
QT Input Channel

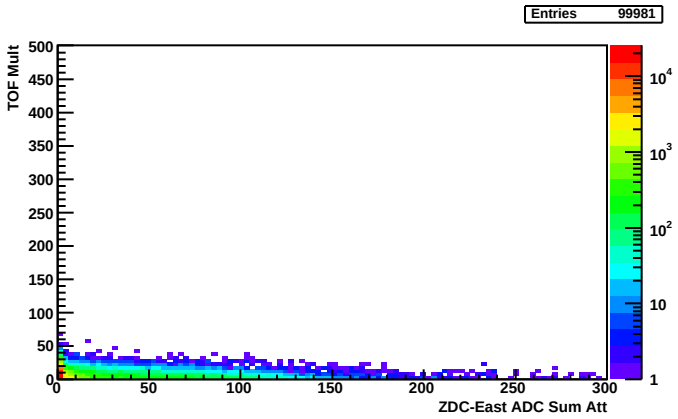

BBQ-VP004 (HI threshold)
Entries 1599696
TAC
QT Input Channel

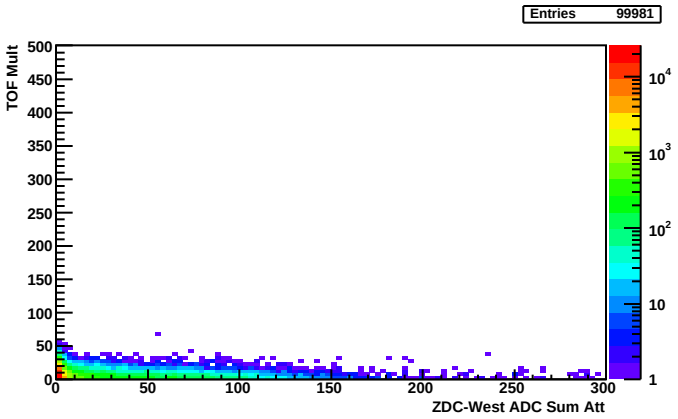

Entries 99981
TOF Mult
BBC-L-East ADC Sum

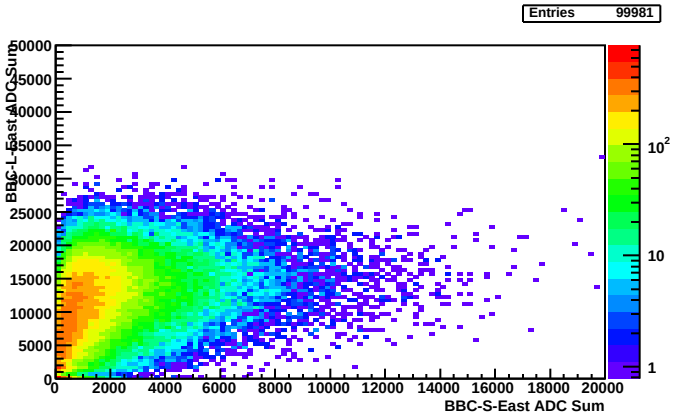

Entries 99981
TOF Mult
BBC-L-West ADC Sum

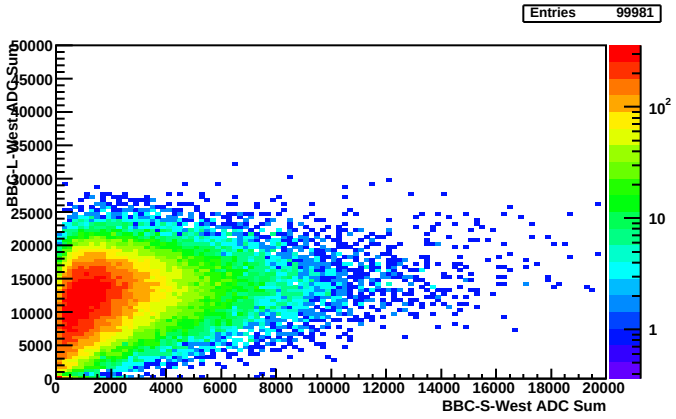

Entries 99981
TOF Mult
BBC-S-East ADC Sum

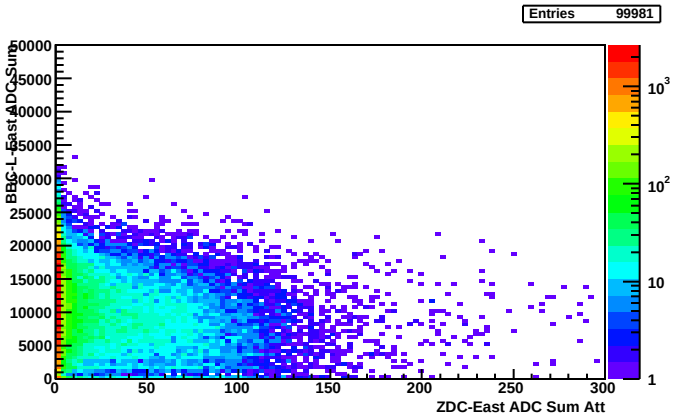

Entries 99981
TOF Mult
BBC-S-West ADC Sum

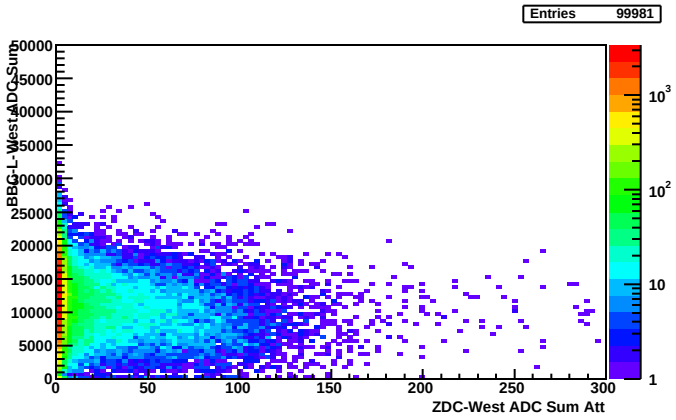

Entries 99981
TOF Mult
ZDC-East ADC Sum Att

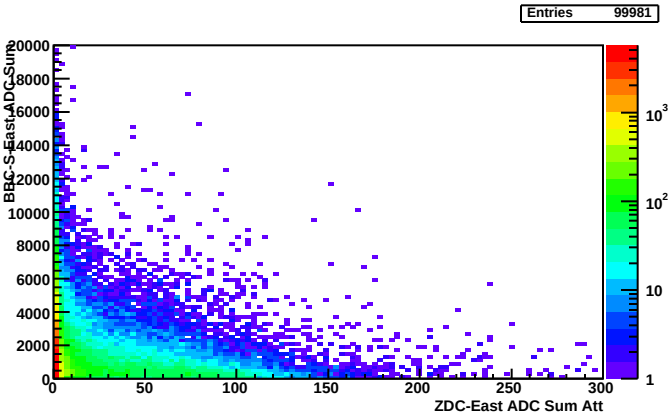

Entries 99981
TOF Mult
ZDC-West ADC Sum Att

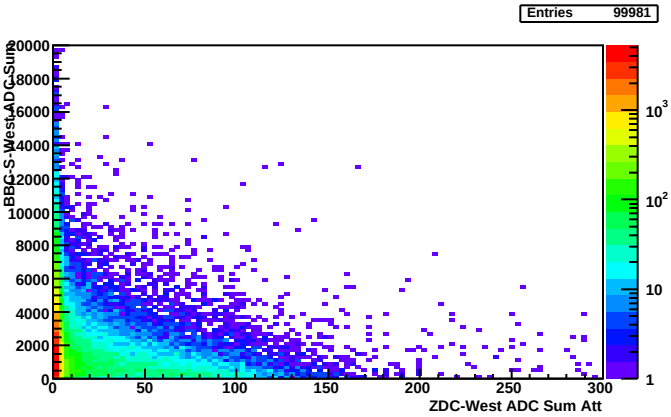

Entries 99981
BBC-L-East ADC Sum
BBC-S-East ADC Sum

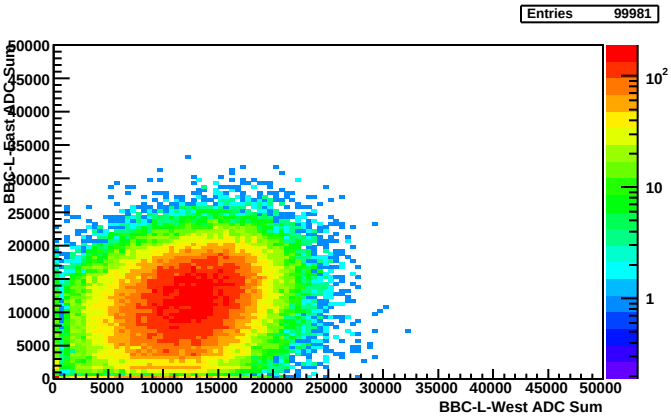

Entries 99981
BBC-L-West ADC Sum
BBC-S-West ADC Sum

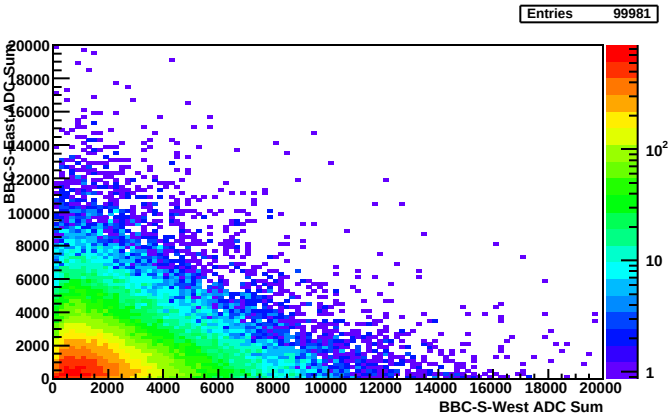

Entries 99981
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

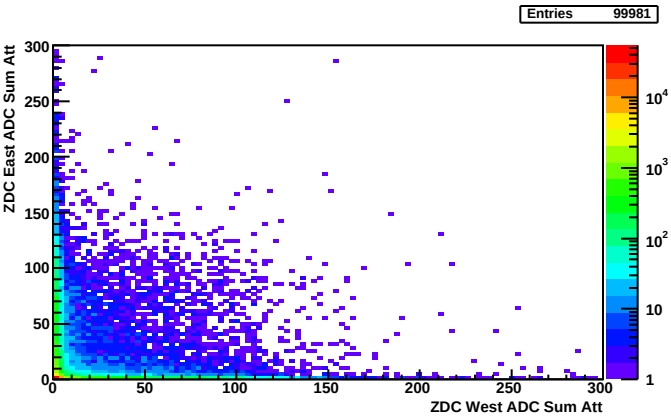

Entries 99981
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

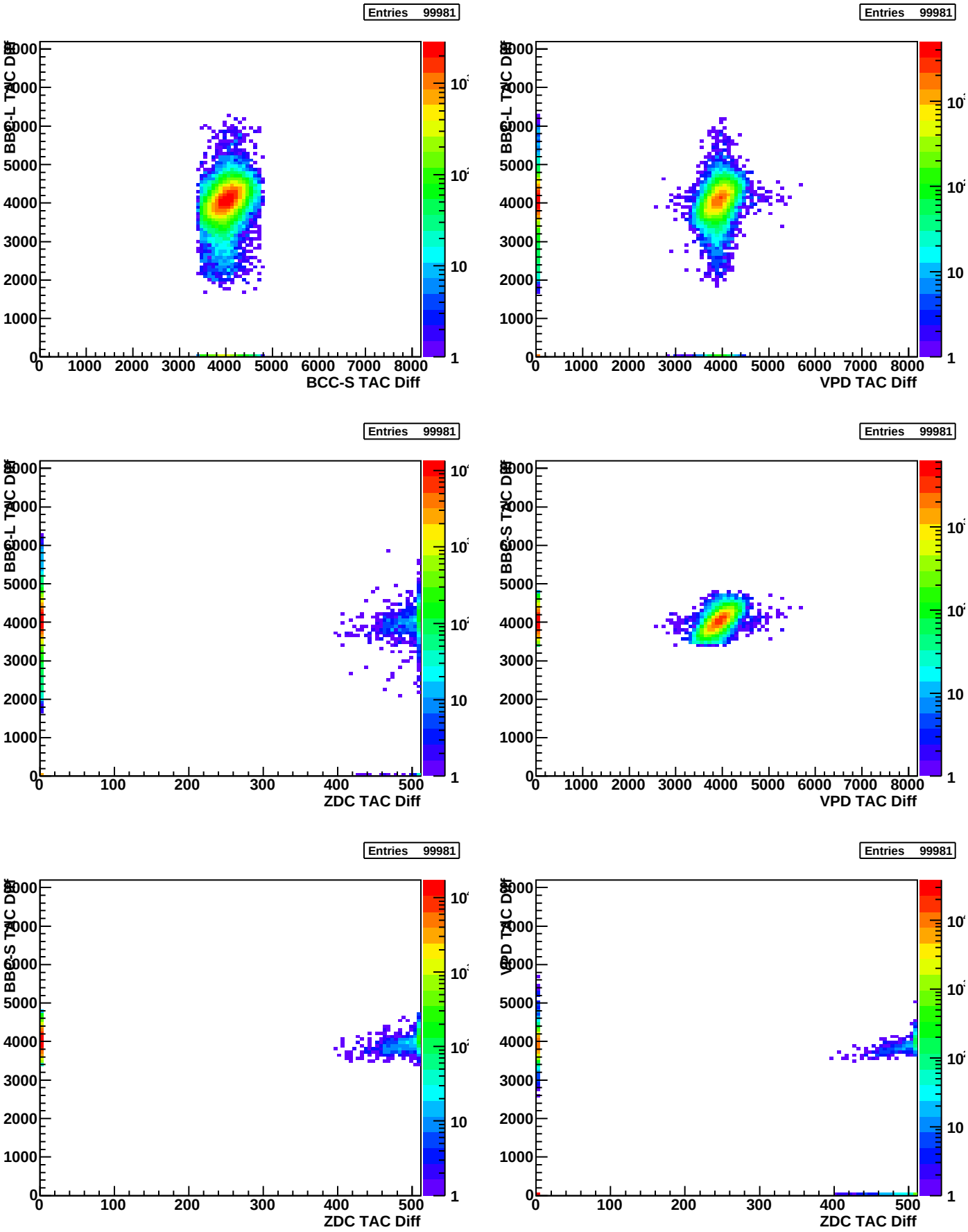

Entries 99981
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

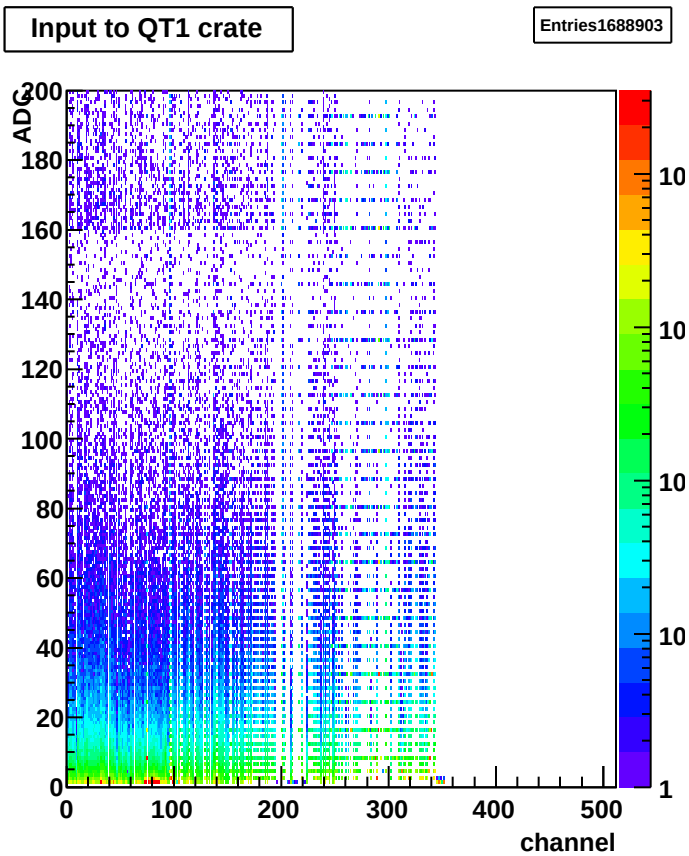

Entries 99981
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

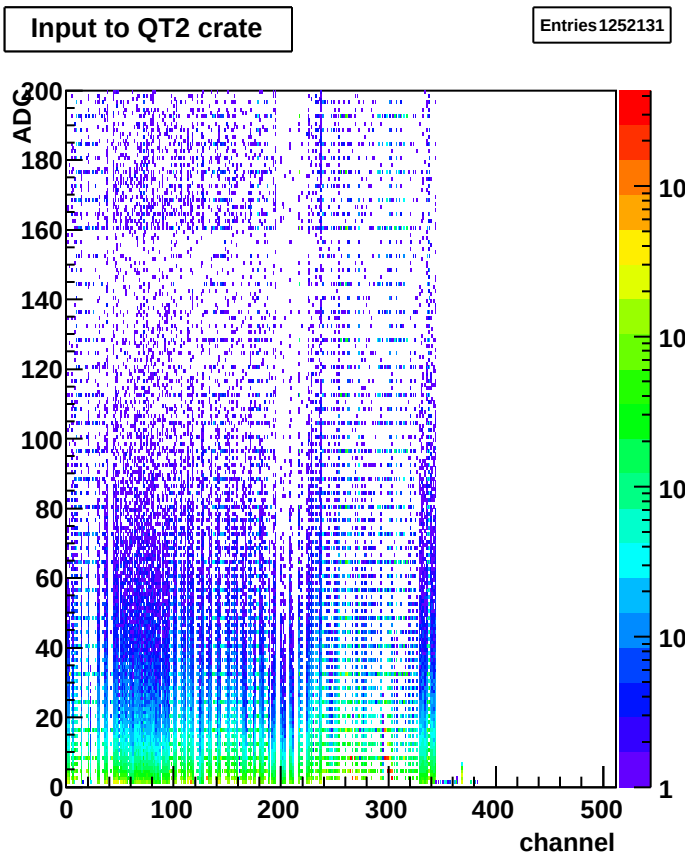

Entries 99981
BBC-L-East ADC Sum
BBC-L-West ADC Sum

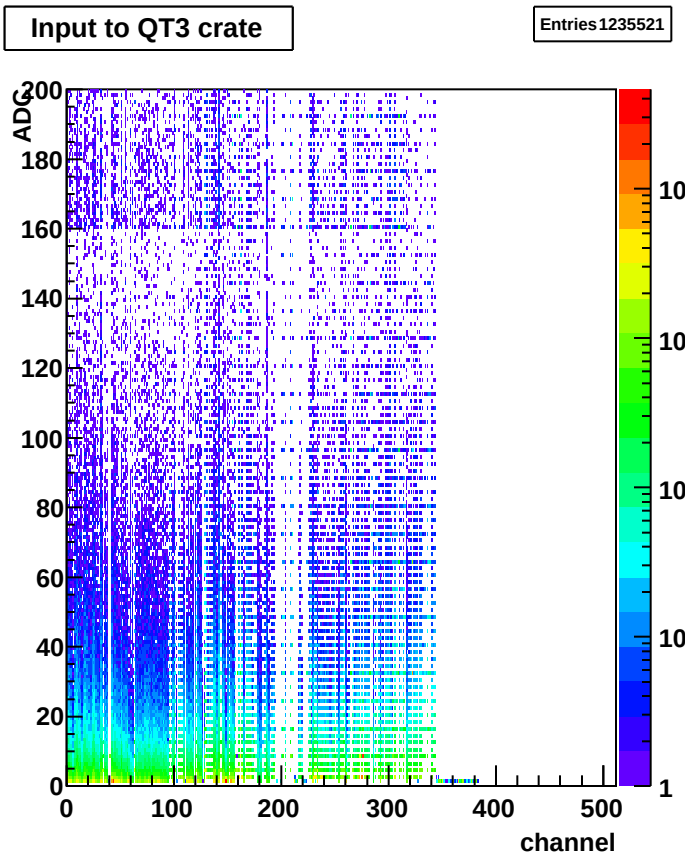

Entries 99981
BBC-S-East ADC Sum
BBC-S-West ADC Sum

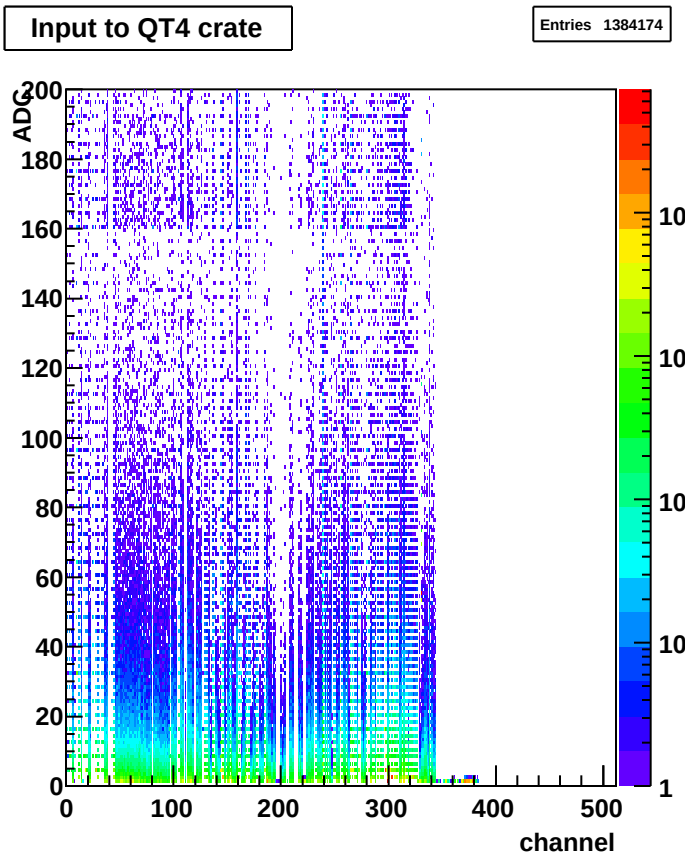

Entries 99981
ZDC East ADC Sum Att
ZDC West ADC Sum Att

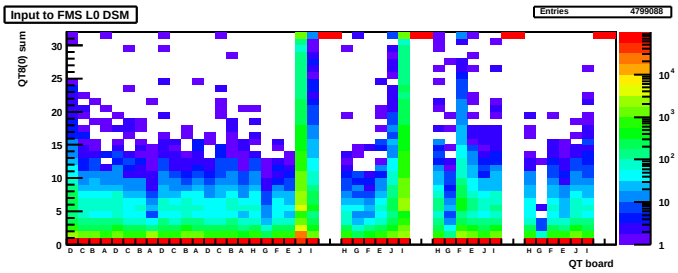

Entries 99981
BBC-L TAC Diff
BCC-S TAC Diff
Entries 99981
BBC-L TAC Diff
VPD TAC Diff
Entries 99981
BBC-L TAC Diff
ZDC TAC Diff
Entries 99981
BBC-S TAC Diff
VPD TAC Diff
Entries 99981
BBC-S TAC Diff
ZDC TAC Diff
Entries 99981
VPD TAC Diff
ZDC TAC Diff

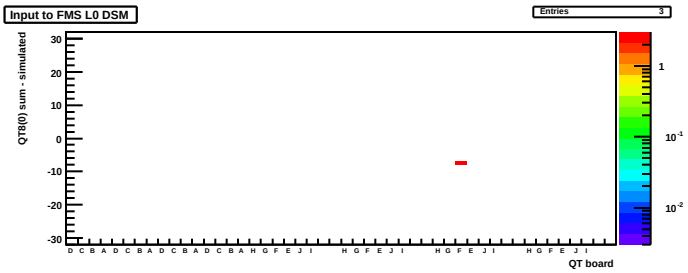

Input to QT1 crate
Entries 1688903
ADC
channel

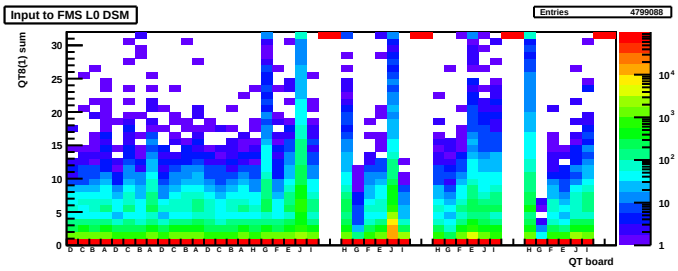

Input to QT2 crate
Entries 1252131
ADC
channel

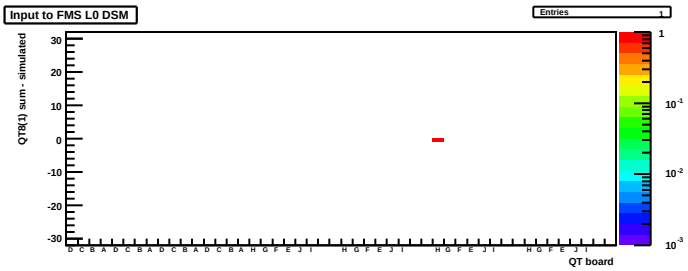

Input to QT3 crate
Entries 1235521
ADC
channel

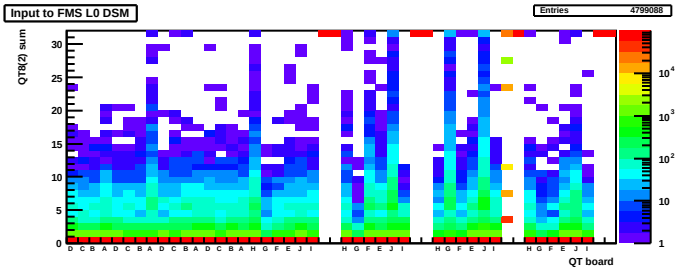

Input to QT4 crate
Entries 1384174
ADC
channel

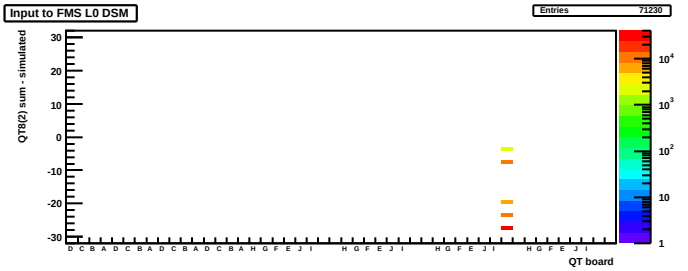

Input to FMS L0 DSM
Entries 4799088
QT8(0) sum
QT board

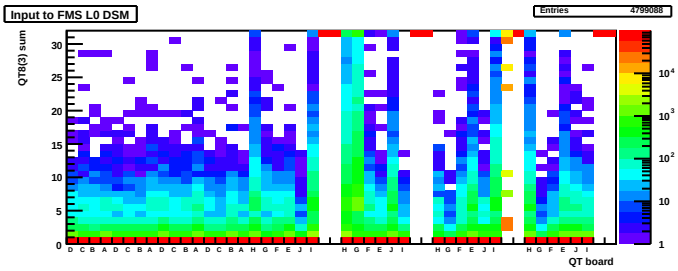

Input to FMS L0 DSM
Entries 3
QT8(0) sum - simulated
QT board

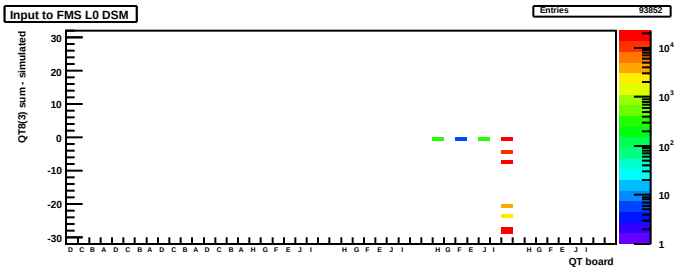

Input to FMS L0 DSM
Entries 4799088
QT8(1) sum
QT board

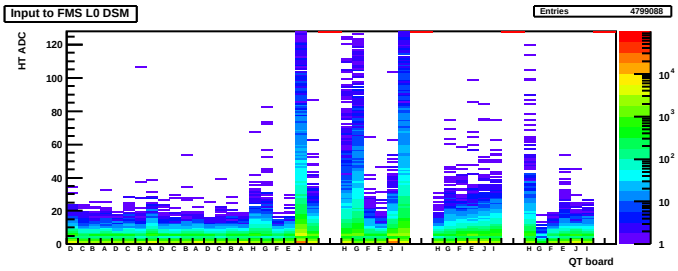

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

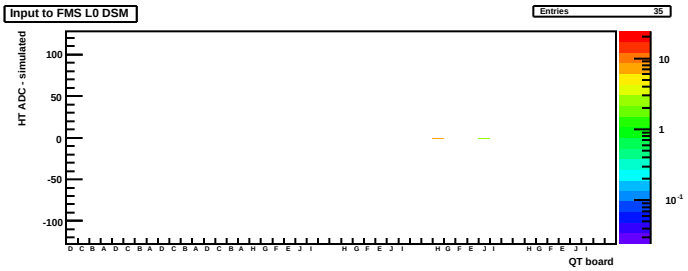

Input to FMS L0 DSM
Entries 4799088
QT8(2) sum
QT board

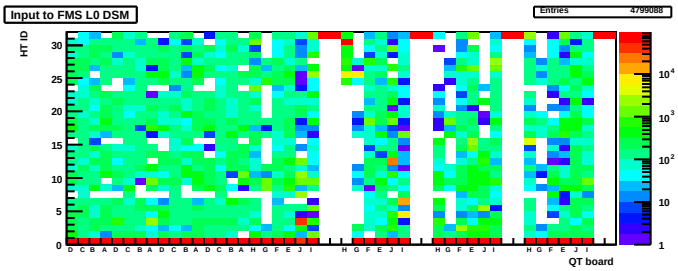

Input to FMS L0 DSM
Entries 71230
QT8(2) sum - simulated
QT board

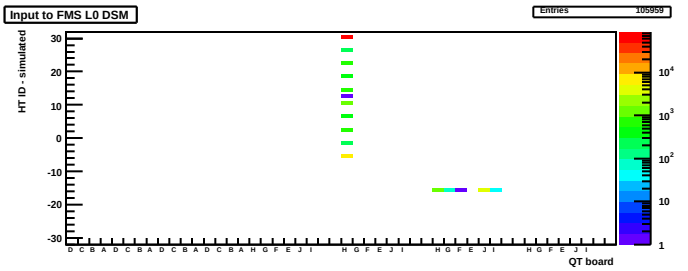

Input to FMS L0 DSM
Entries 4799088
QT8(3) sum
QT board

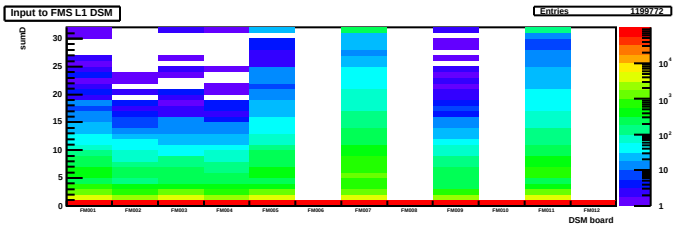

Input to FMS L0 DSM
Entries 93852
QT8(3) sum - simulated
QT board

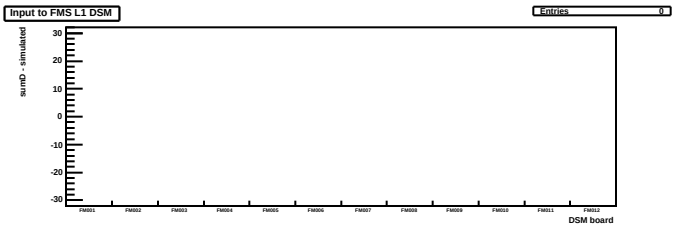

Input to FMS L0 DSM
Entries 4799088
HT ADC
QT board

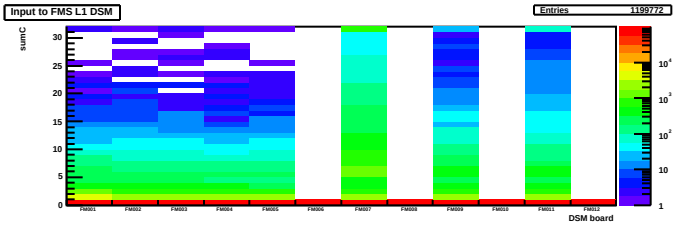

Input to FMS L0 DSM
Entries 35
HT ADC - simulated
QT board

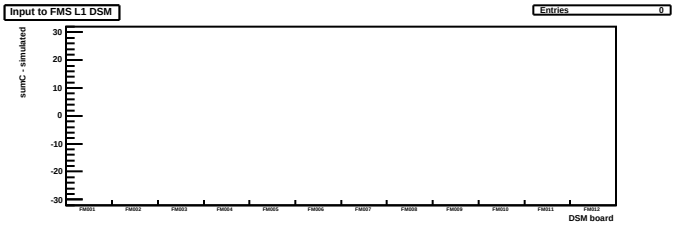

Input to FMS L0 DSM
Entries 4799088
HT ID
QT board

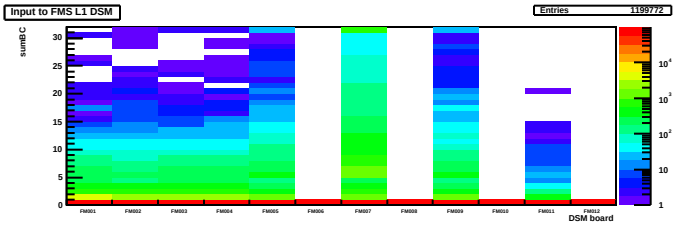

Input to FMS L0 DSM
Entries 105959
HT ID - simulated
QT board

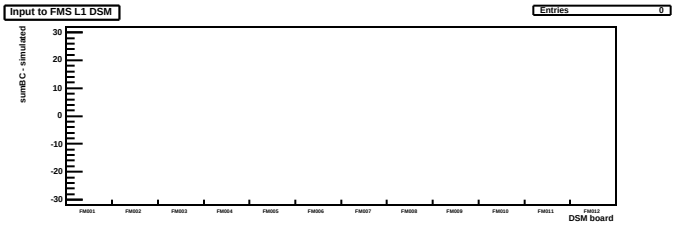

Input to FMS L1 DSM
Entries 1199772
sumD
DSM board

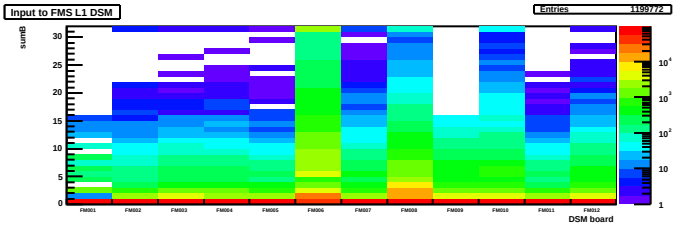

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

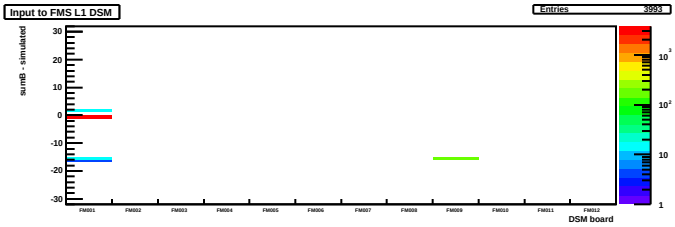

Input to FMS L1 DSM
Entries 1199772
sumC
DSM board

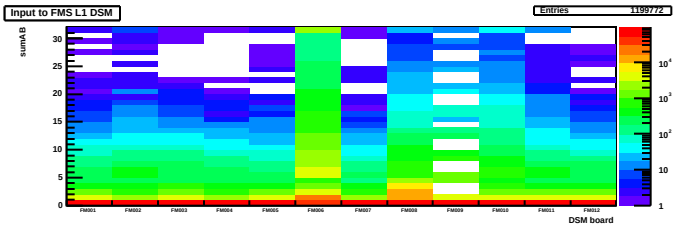

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

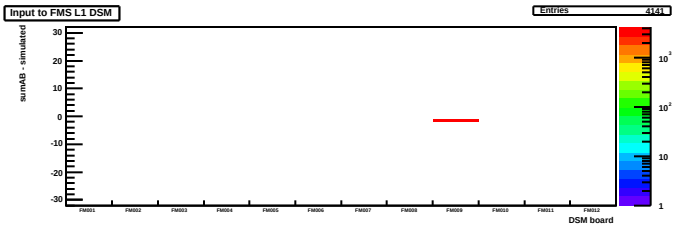

Input to FMS L1 DSM
Entries 1199772
sumBC
DSM board

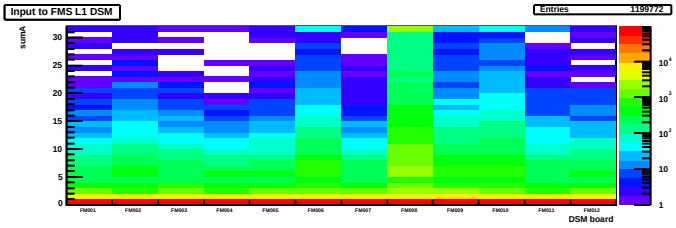

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

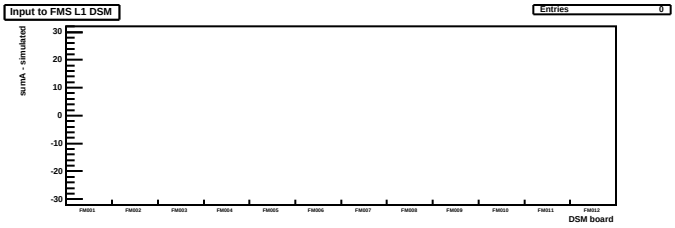

Input to FMS L1 DSM
Entries 1199772
sumB
DSM board

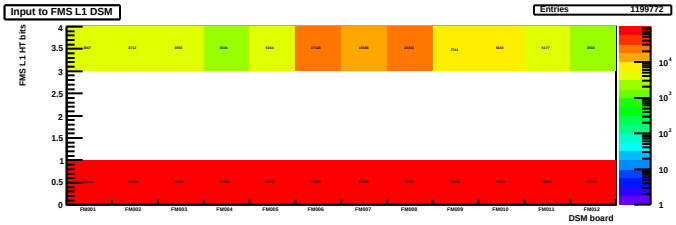

Input to FMS L1 DSM
Entries 3993
sumB - simulated
DSM board

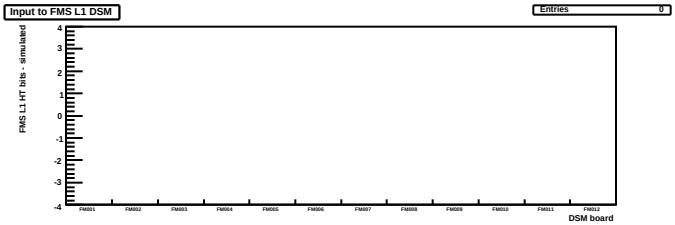

Input to FMS L1 DSM
Entries 1199772
sumAB
DSM board

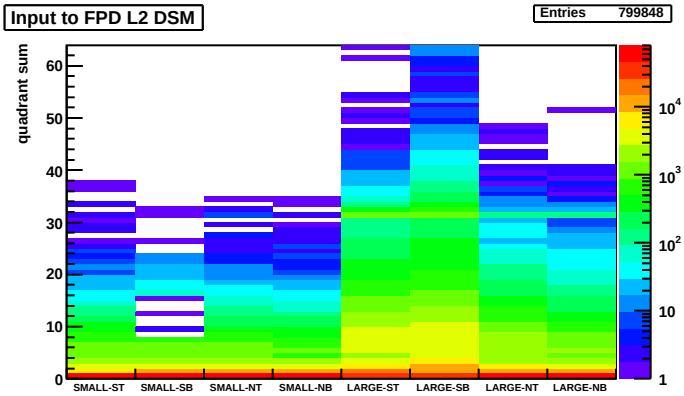

Input to FMS L1 DSM
Entries 4141
sumAB - simulated
DSM board

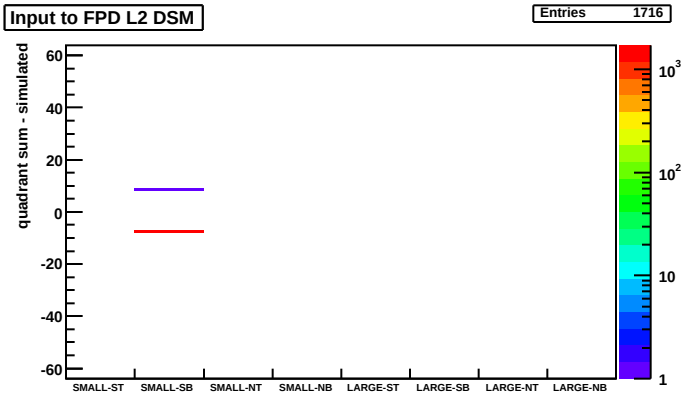

Input to FMS L1 DSM
Entries 1199772
sumA
DSM board

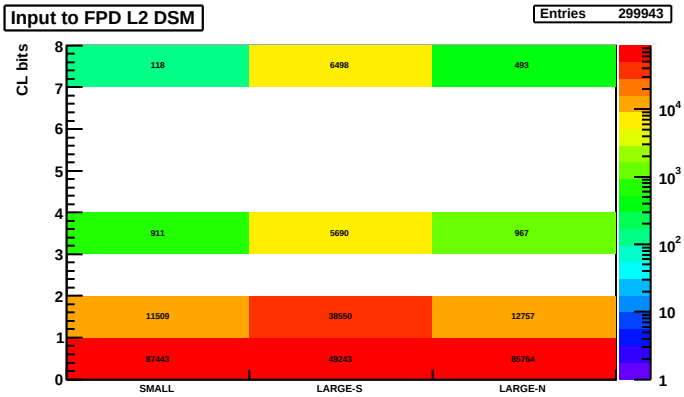

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

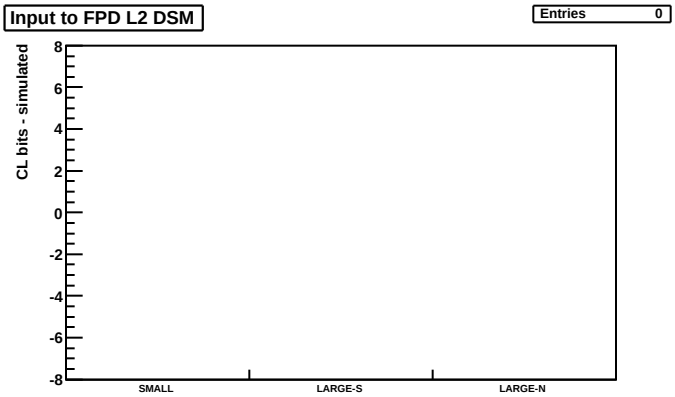

Input to FMS L1 DSM
Entries 1199772
FMS L1 HT bits
DSM board

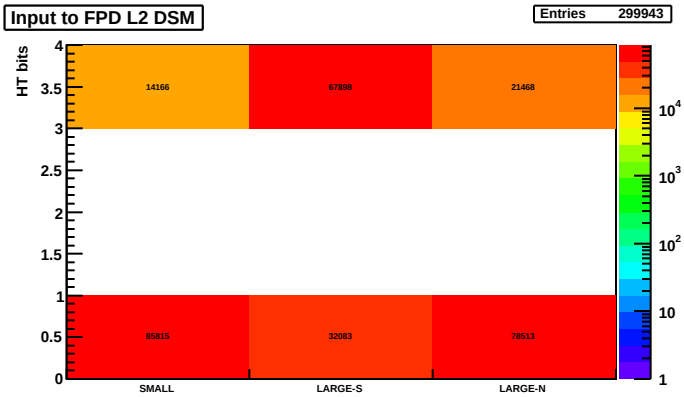

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

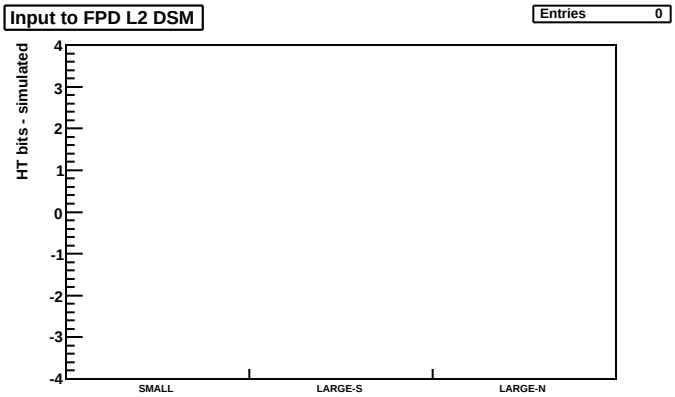

Input to FPD L2 DSM
Entries 799848
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

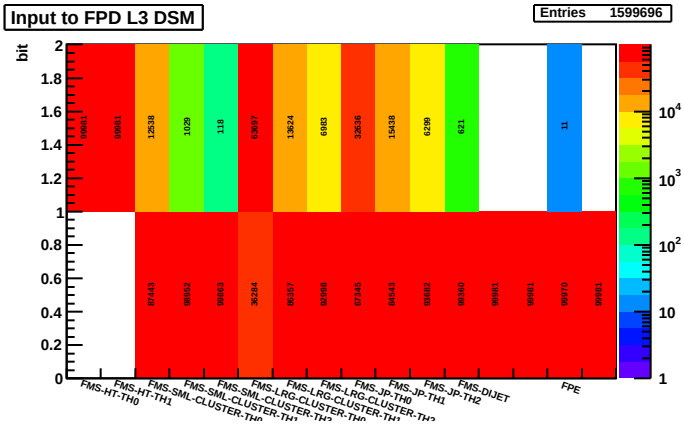

Input to FPD L2 DSM
Entries 1716
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

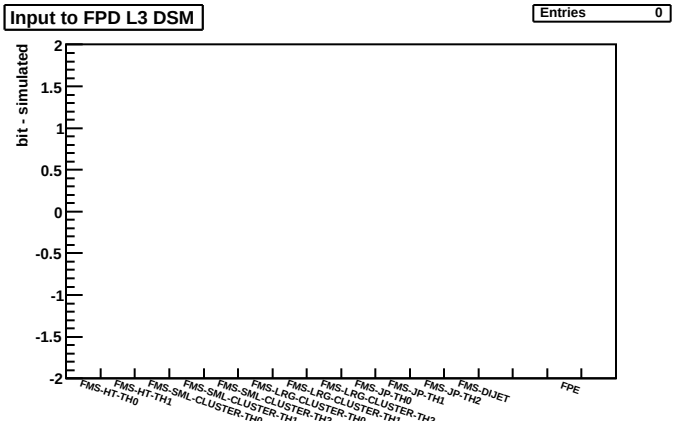

Input to FPD L2 DSM
Entries 299943
CL bits
118
6498
493
911
5690
967
11509
38550
12757
87443
49243
85764
SMALL
LARGE-S
LARGE-N

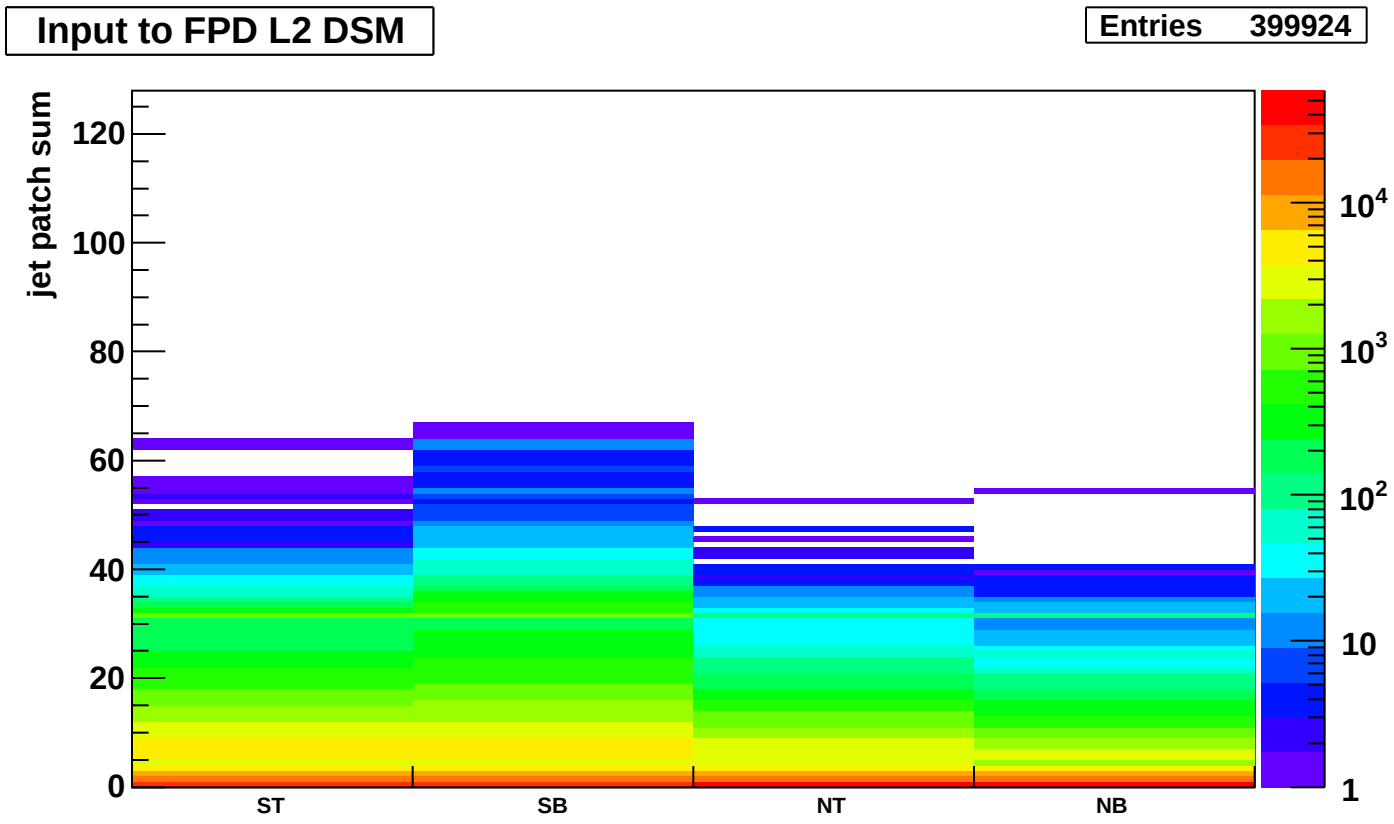

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

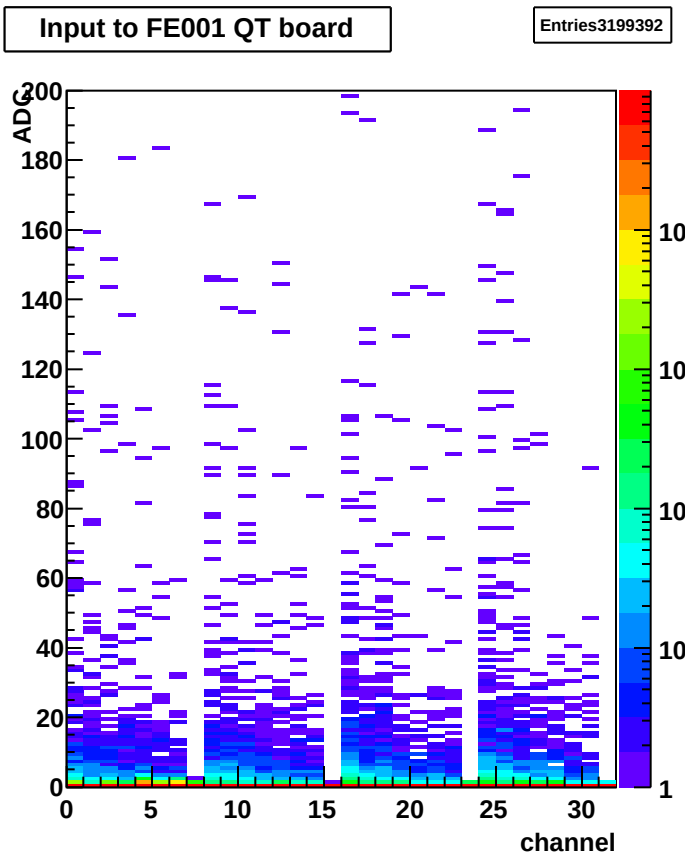

Input to FPD L2 DSM
Entries 299943
HT bits
14166
67898
21468
85815
32083
78513
SMALL
LARGE-S
LARGE-N

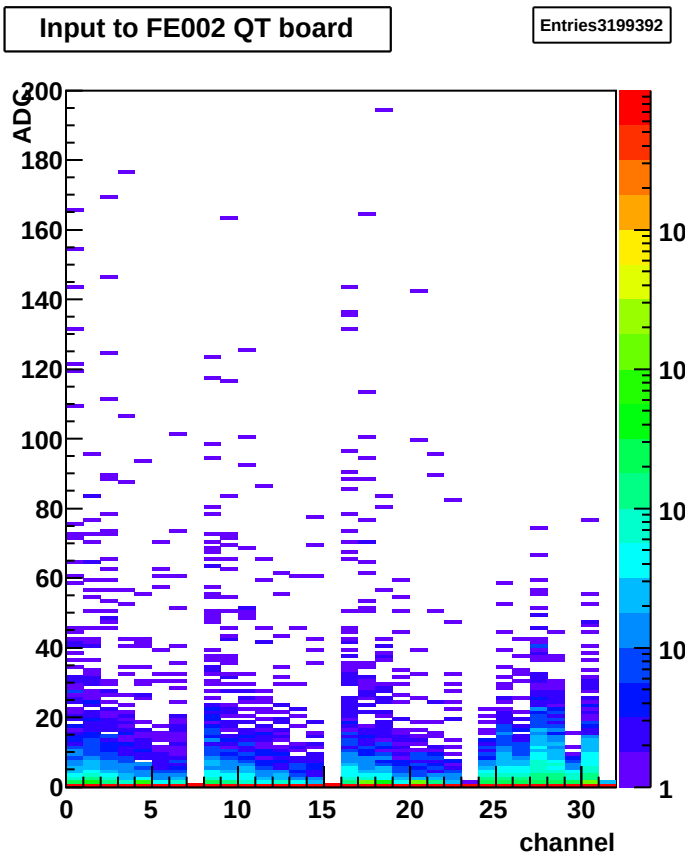

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

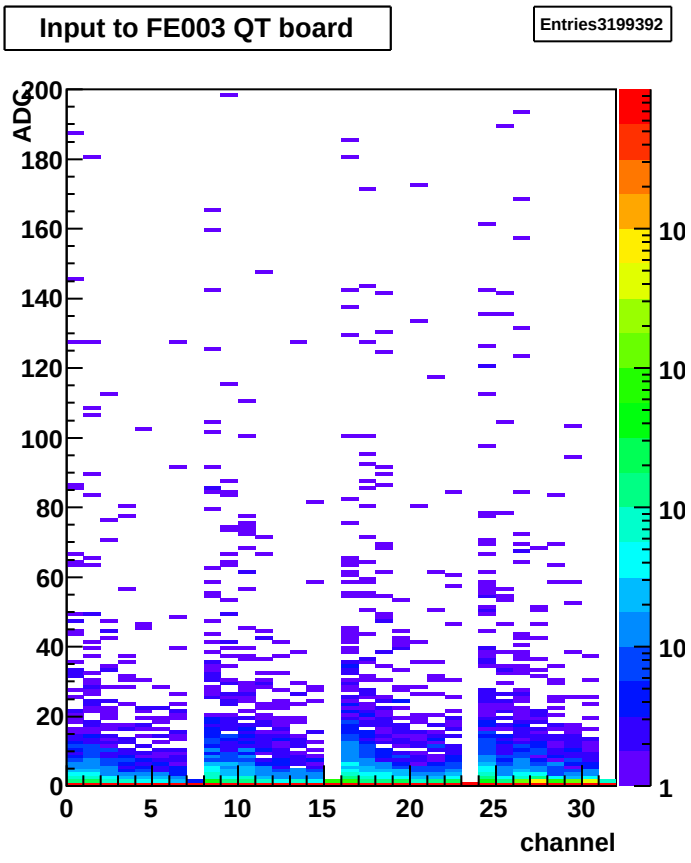

Input to FPD L3 DSM
Entries 1599696
bit
99931
99931
12538
1029
118
63697
13624
6983
32636
15438
6299
621
11
97443
98952
99863
36284
98357
92998
67345
85543
93682
99380
99931
99931
99970
99931
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

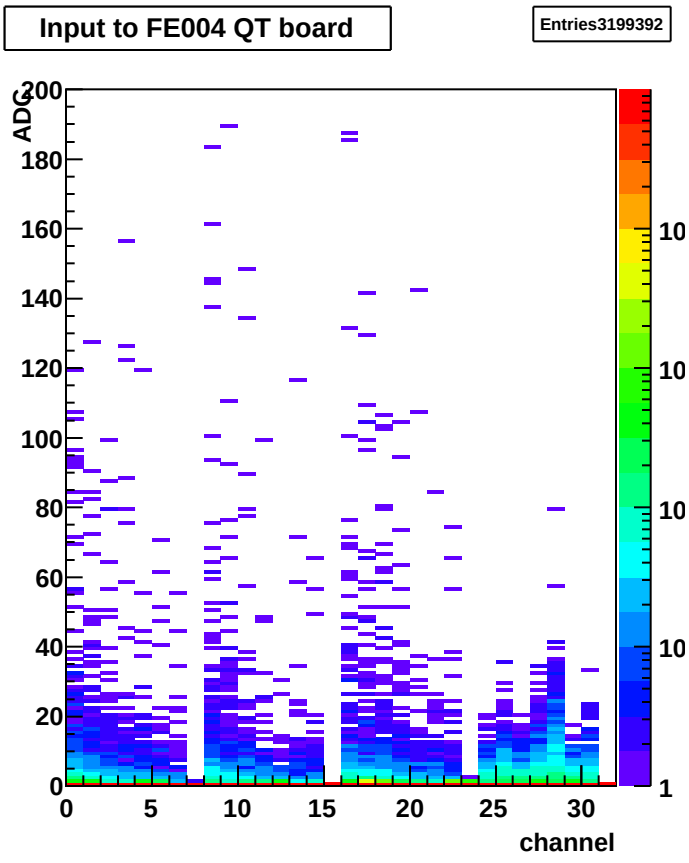

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

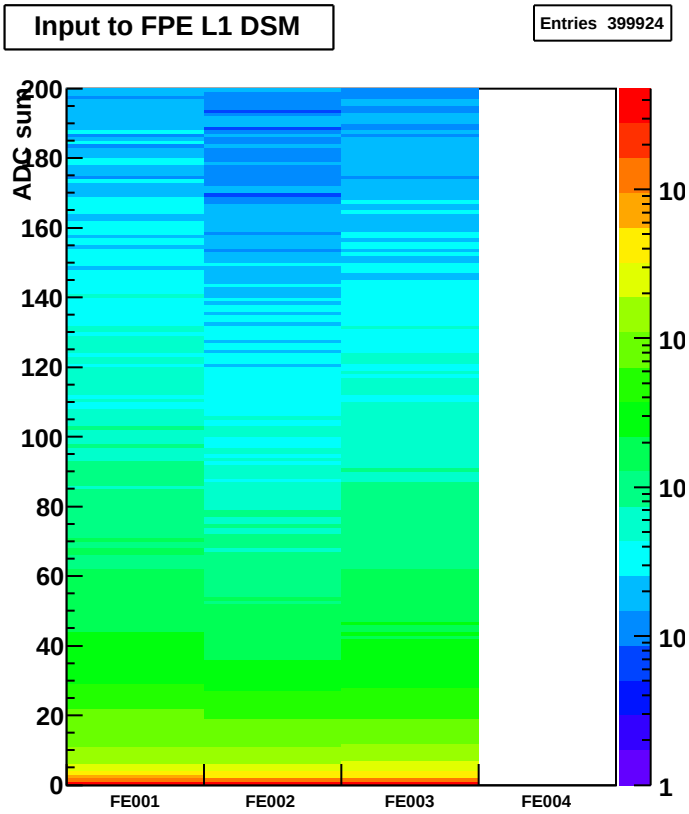

Input to FPD L2 DSM
Entries 399924
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

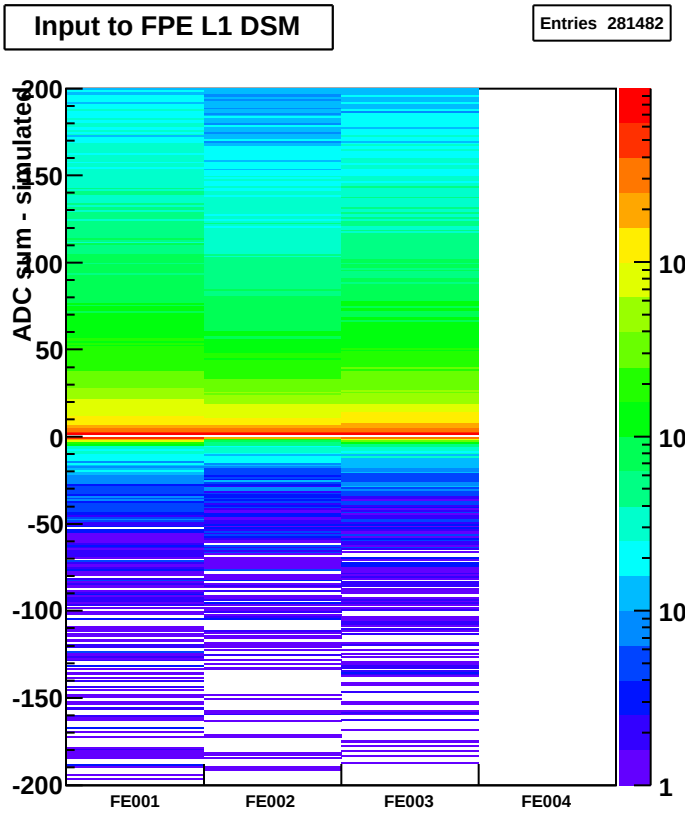

Input to FE001 QT board
Entries3199392
ADC
channel

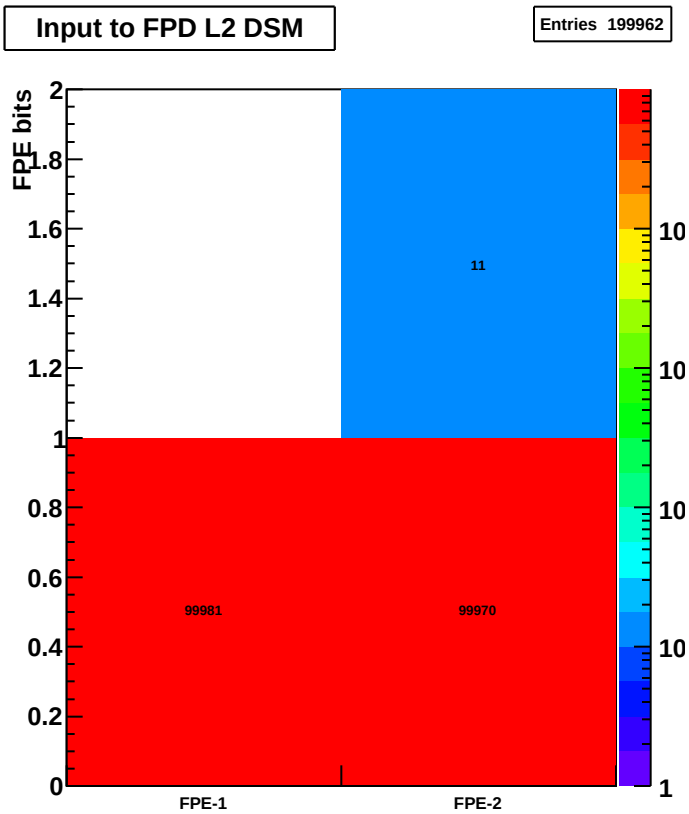

Input to FE002 QT board
Entries3199392
ADC
channel

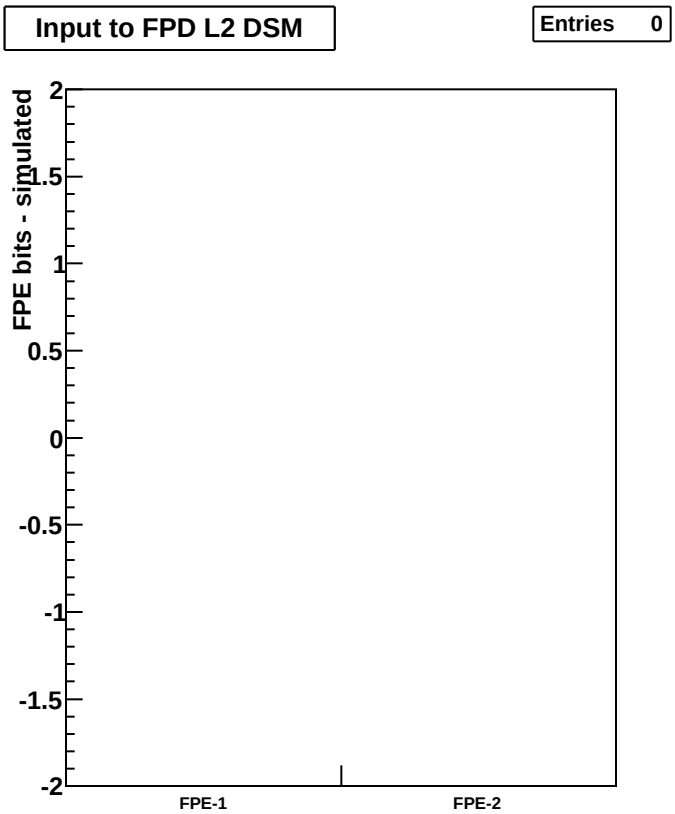

Input to FE003 QT board
Entries3199392
ADC
channel


Input to FE004 QT board
Entries3199392
ADC
channel


Input to FPE L1 DSM
Entries 399924
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 281482
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 199962
FPE bits
11
99981
99970
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2