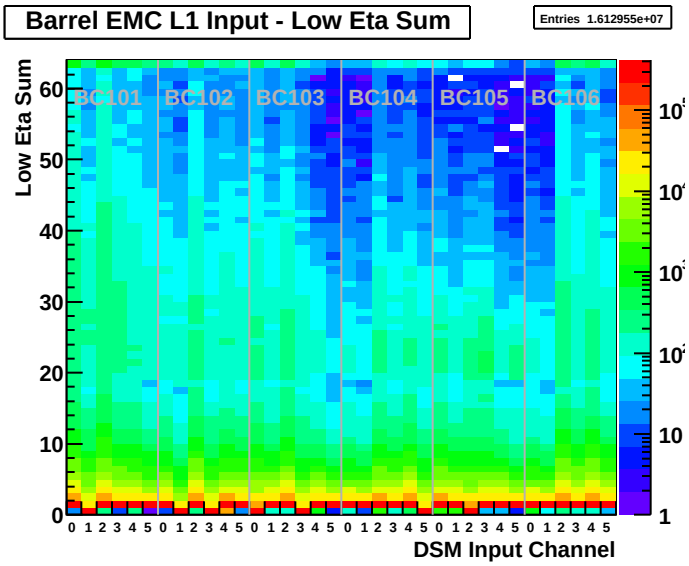
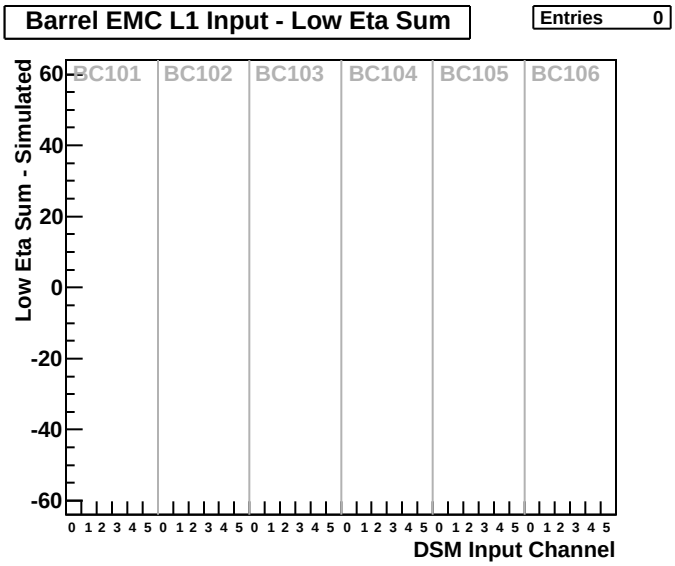
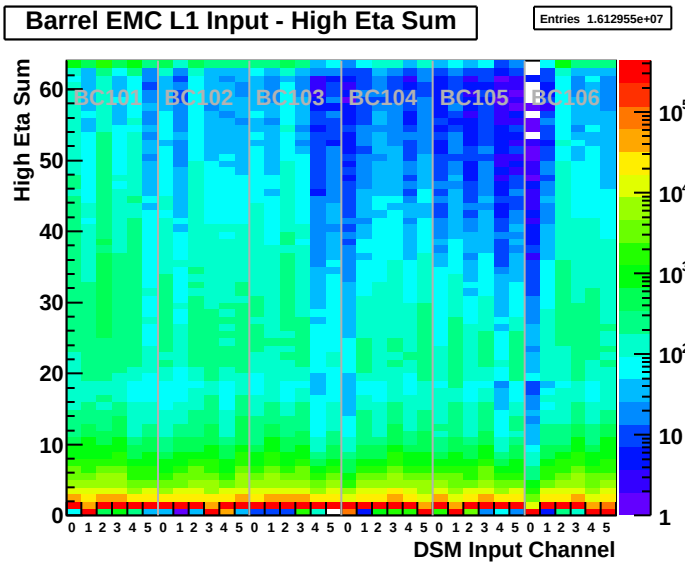
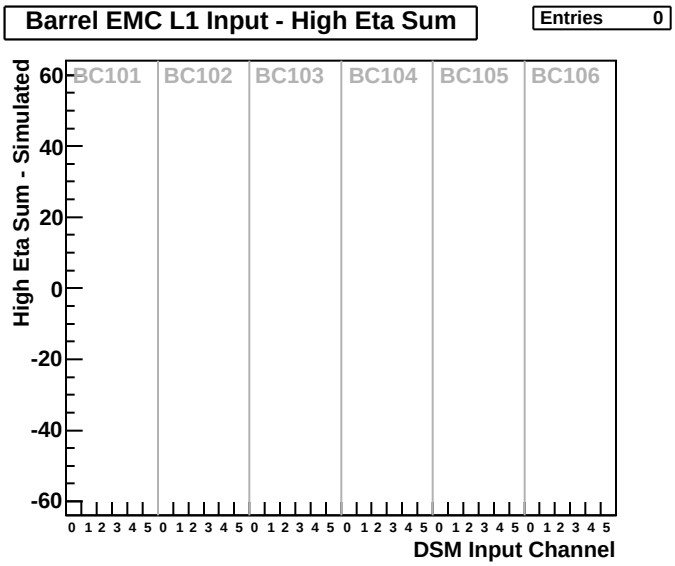
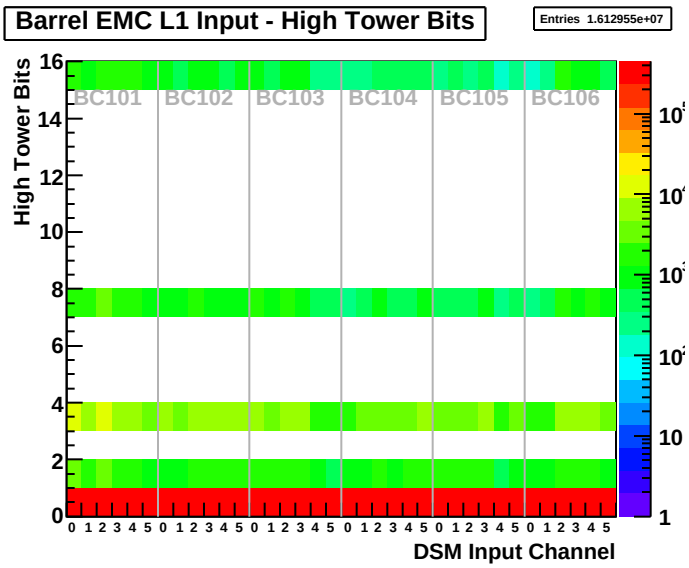
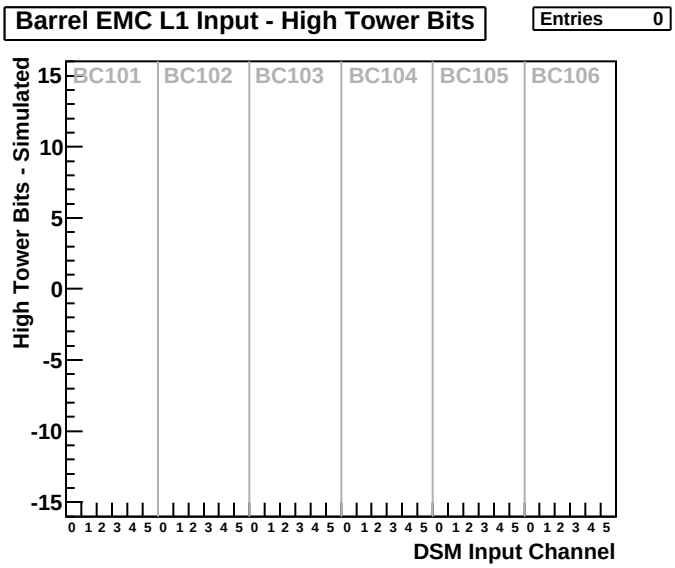

Barrel EMC L1 Input - Low Eta Sum
Entries 1.612955e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

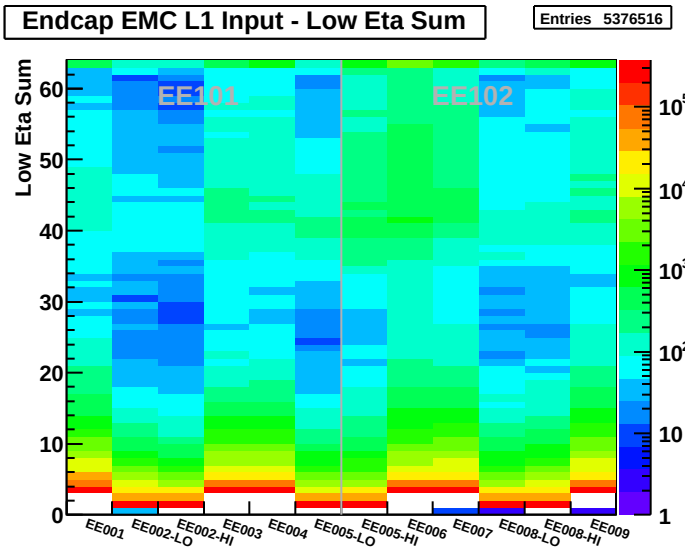

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

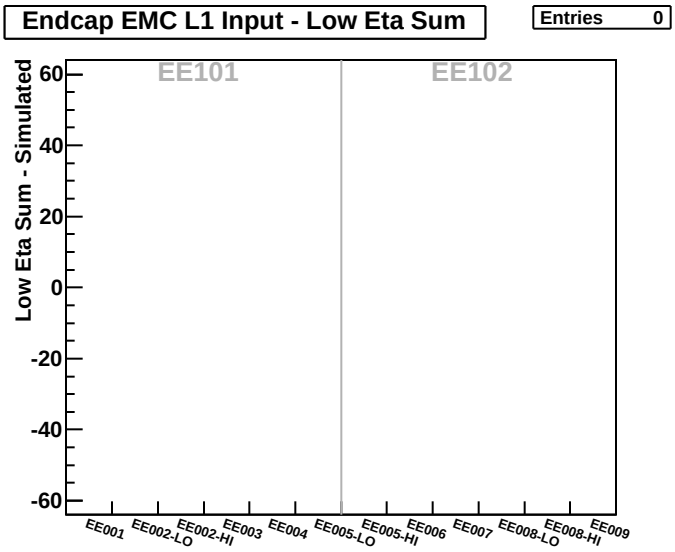

Barrel EMC L1 Input - High Eta Sum
Entries 1.612955e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

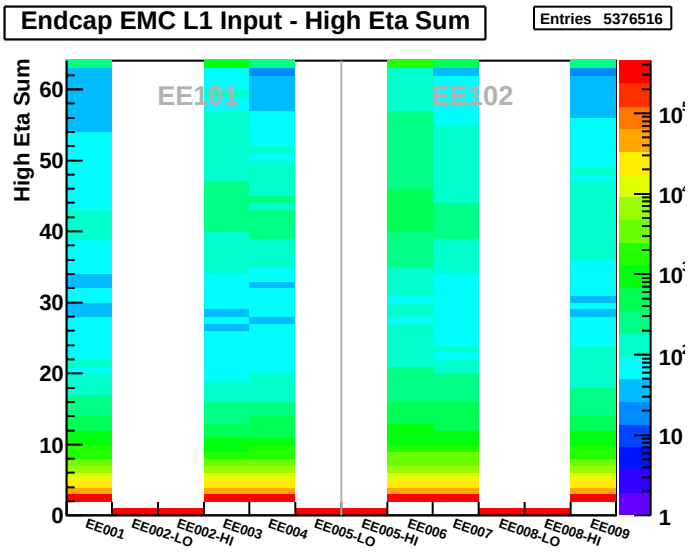

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

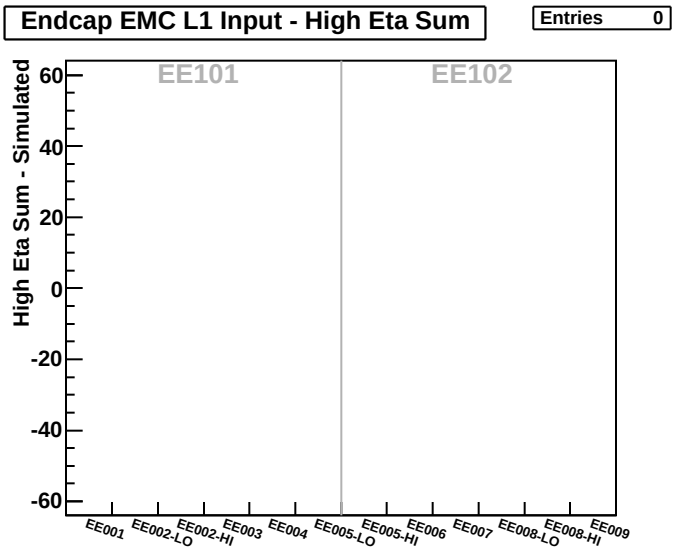

Barrel EMC L1 Input - High Tower Bits
Entries 1.612955e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

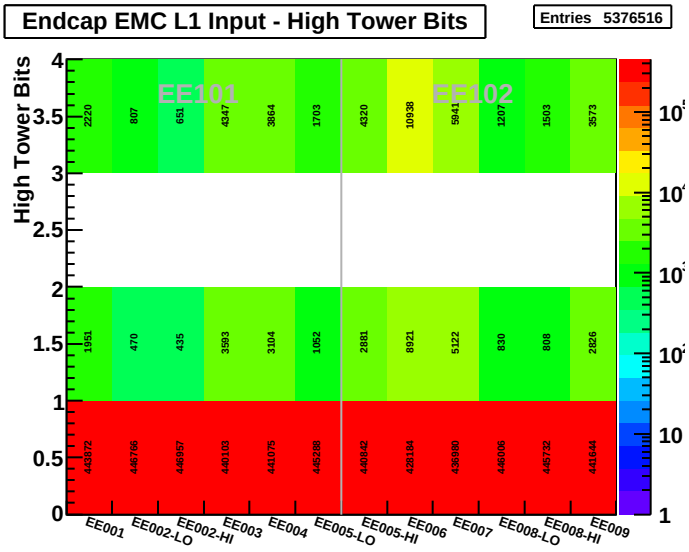

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

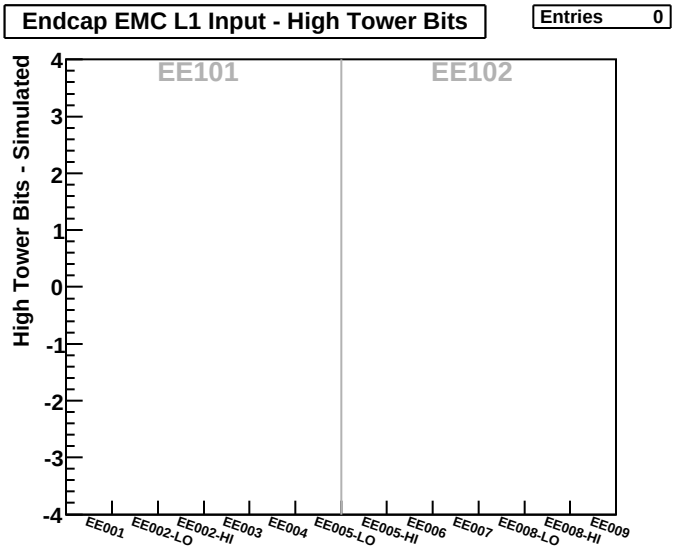

Endcap EMC L1 Input - Low Eta Sum
Entries 5376516
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

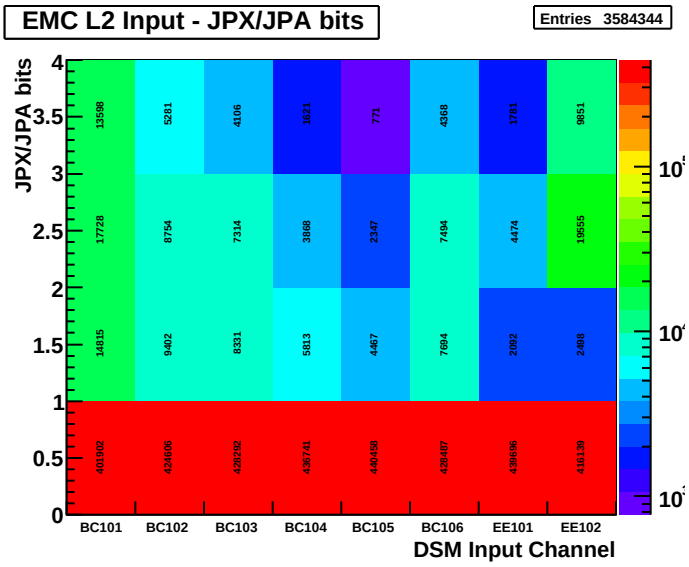

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

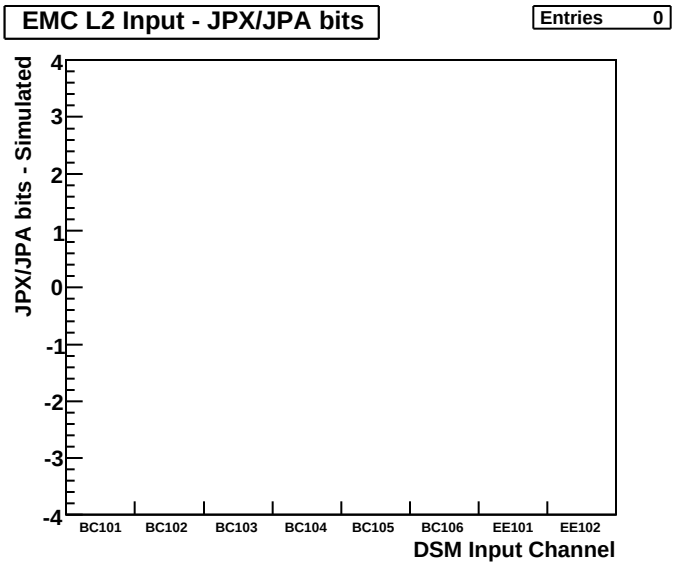

Endcap EMC L1 Input - High Eta Sum
Entries 5376516
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

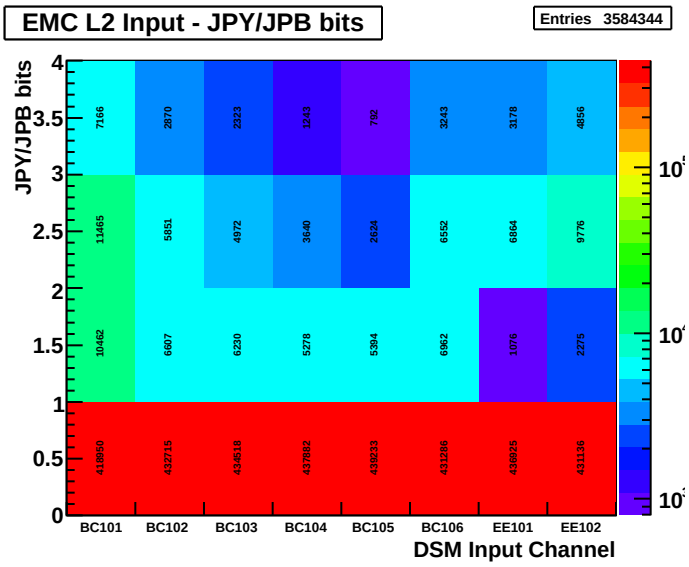

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

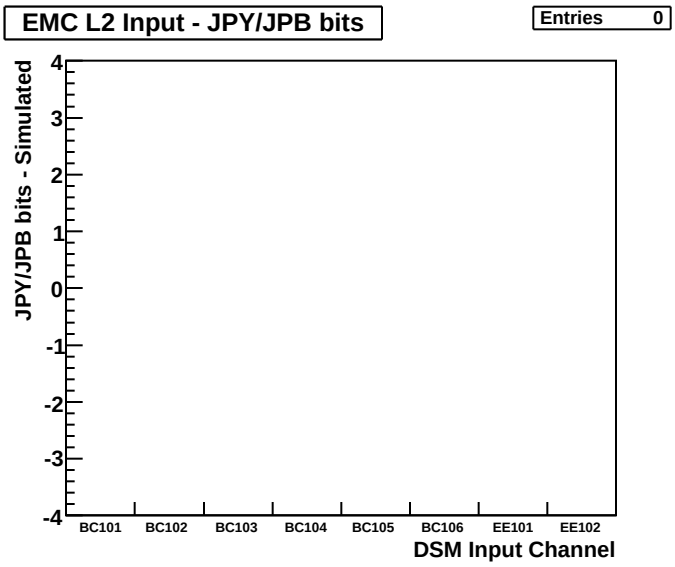

Endcap EMC L1 Input - High Tower Bits
Entries 5376516
High Tower Bits
EE101
EE102
2220 807 651 4347 3864 1703 4320 10938 5941 1207 1503 3573
1951 470 435 3593 3104 1052 2881 8921 5122 830 808 2826
443972 446766 446957 440103 441075 445288 440842 428184 436980 446006 445732 441644
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

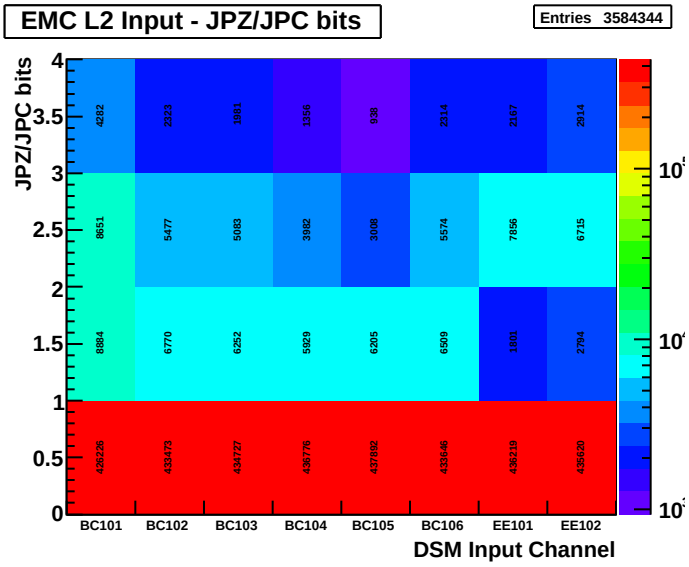

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

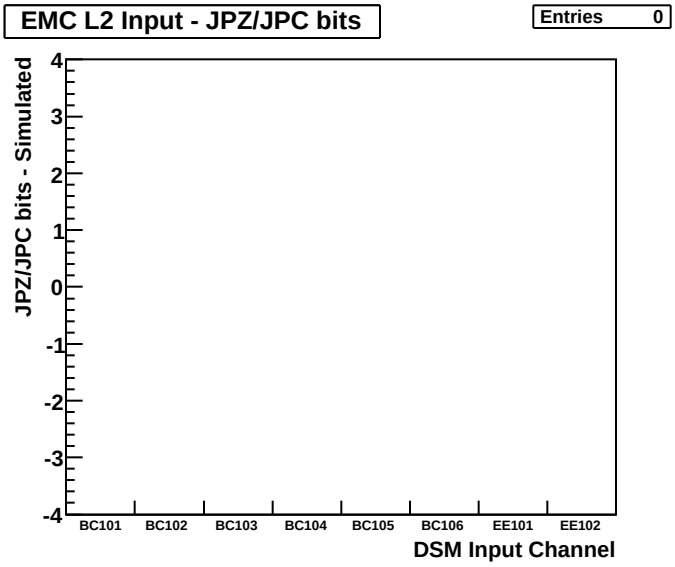

EMC L2 Input - JPX/JPA bits
Entries 3584344
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
13598 5281 4106 1621 771 4368 1781 9851
17728 8754 7314 3868 2347 7494 4474 19555
14815 9402 8331 5813 4467 7694 2092 2498
401902 424606 428282 438741 440458 429487 439696 416139

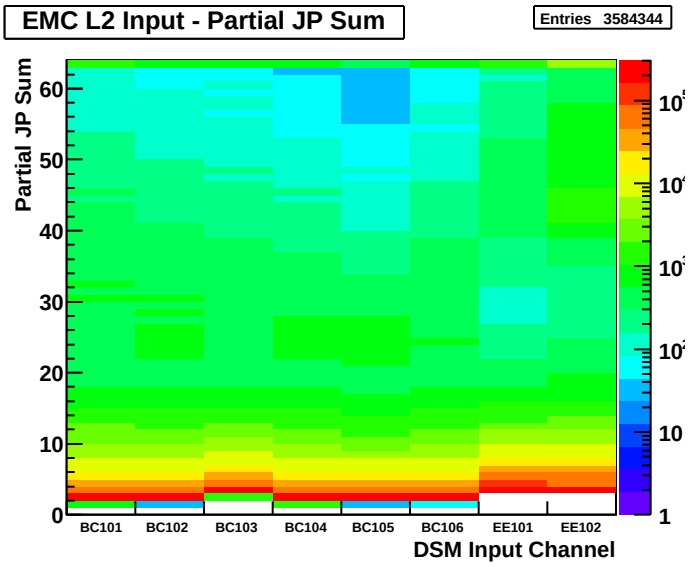

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

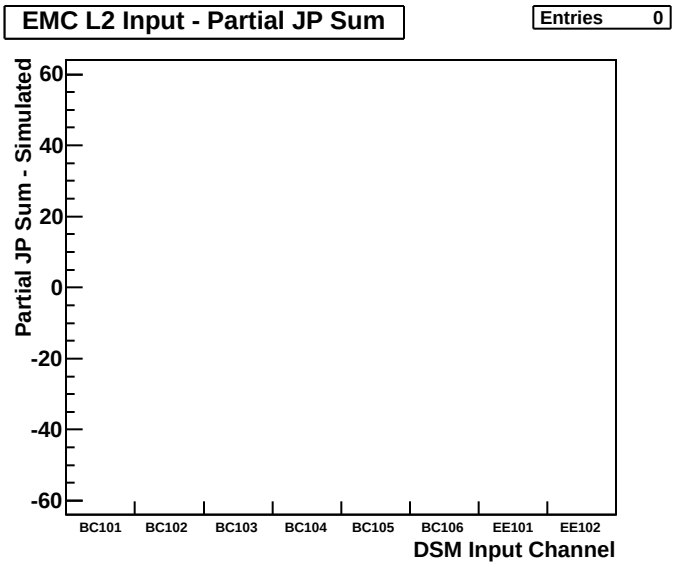

EMC L2 Input - JPY/JPB bits
Entries 3584344
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7166 2870 2323 1243 792 3243 3178 4856
11465 5851 4972 3640 2624 6552 6864 9776
10462 6607 6230 5278 5394 6962 1076 2275
418950 432715 434518 437882 439233 431286 438925 431136

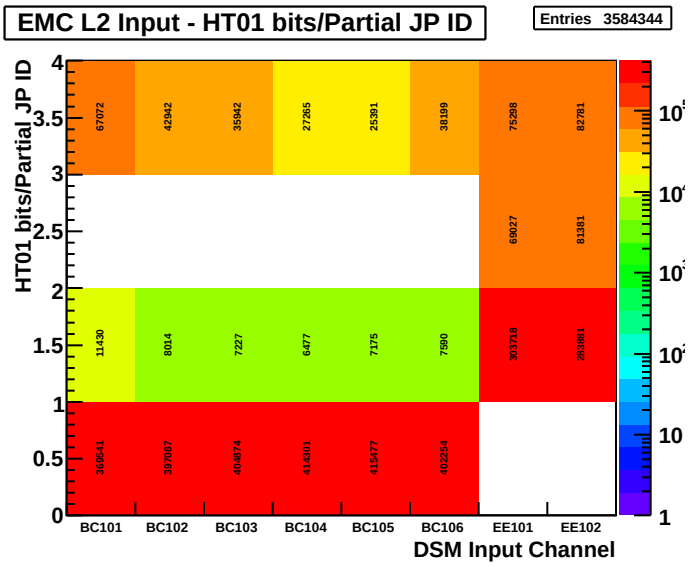

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

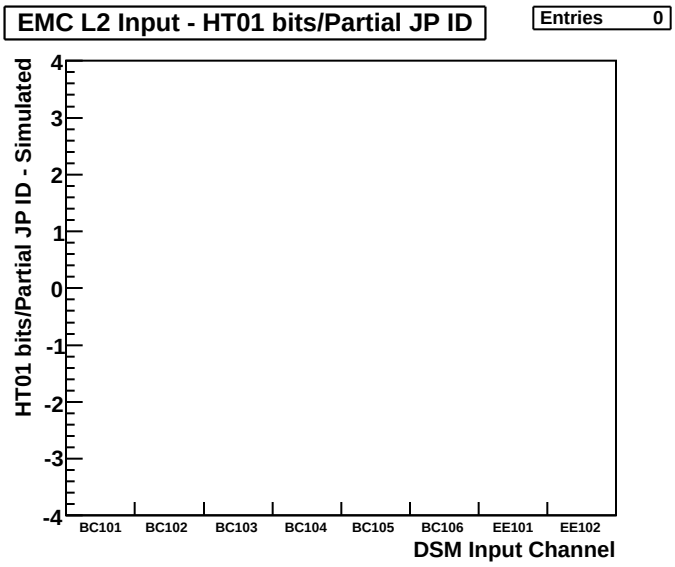

EMC L2 Input - JPZ/JPC bits
Entries 3584344
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4282 2323 1981 1356 938 2314 2167 2914
8651 5477 5083 3982 3008 5574 7856 6715
8884 6770 6252 5929 6205 6509 1801 2794
426226 433473 434727 436776 437892 433646 438219 435620

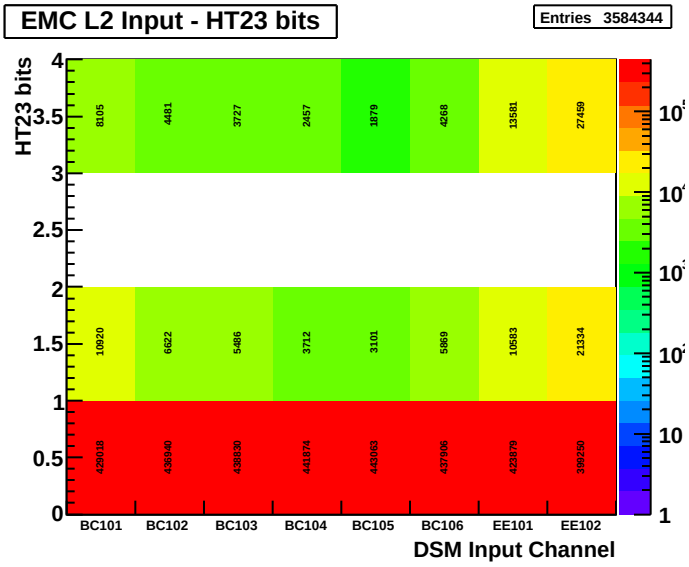

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

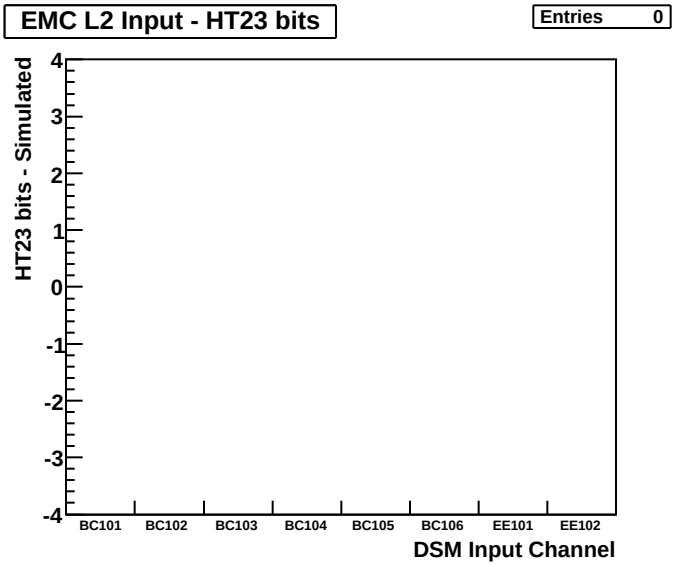

EMC L2 Input - Partial JP Sum
Entries 3584344
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

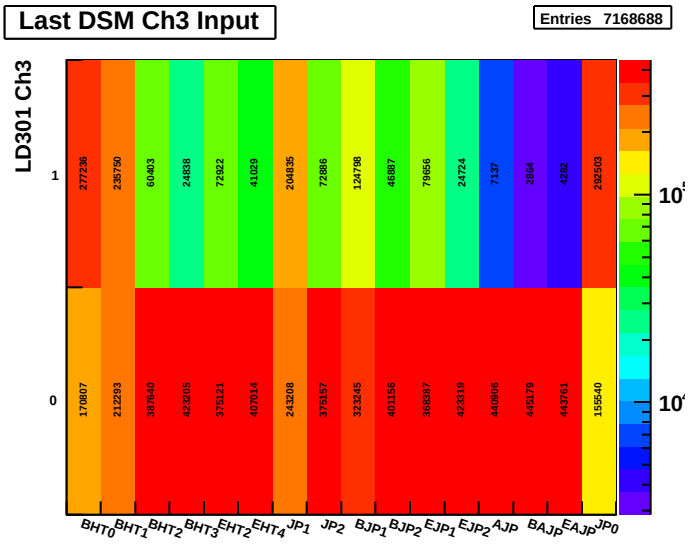

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

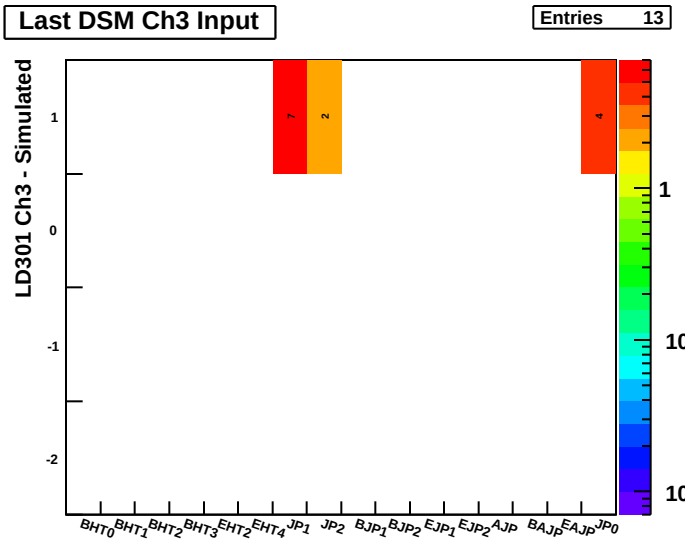

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3584344
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

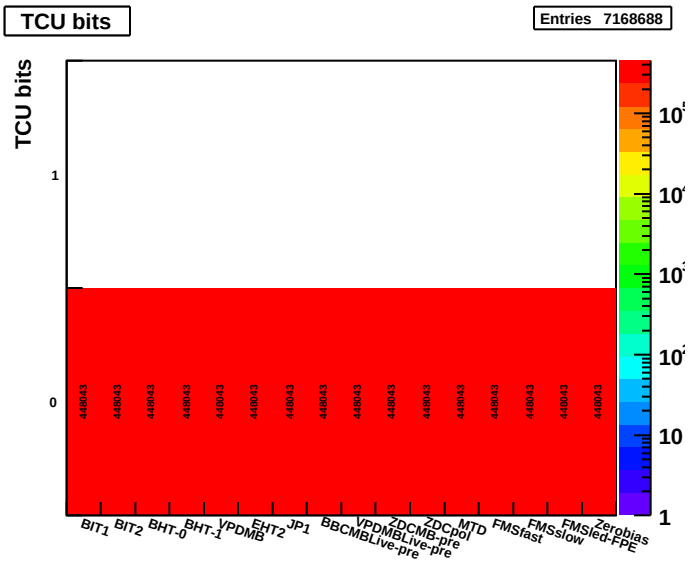

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

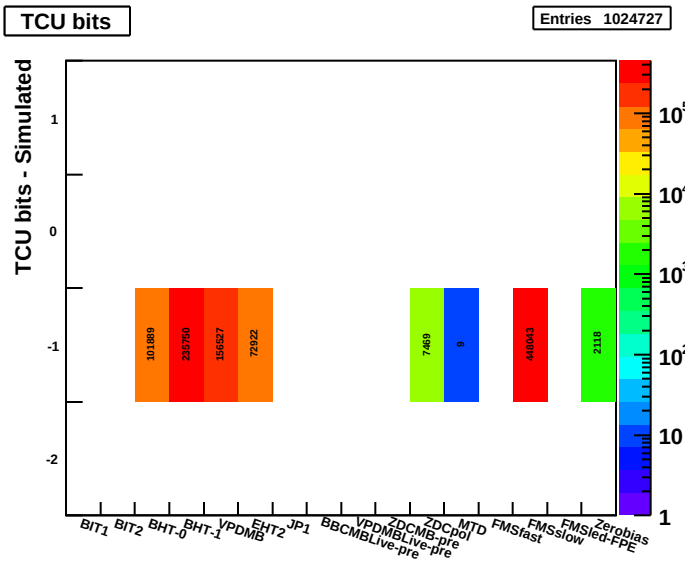

EMC L2 Input - HT23 bits
Entries 3584344
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

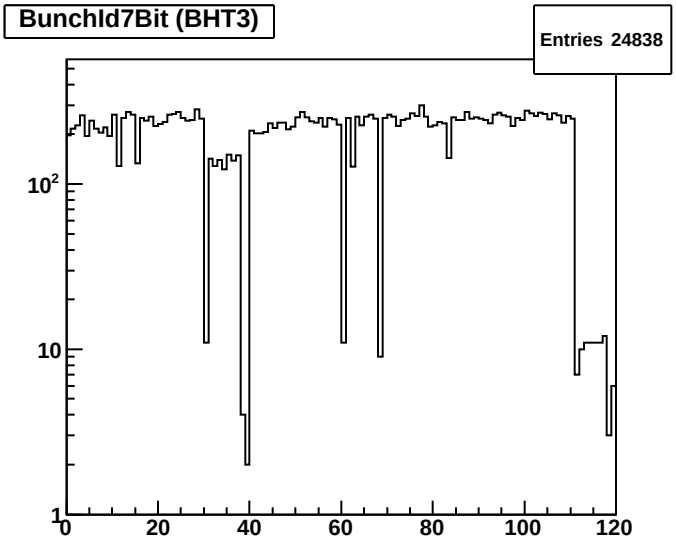

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

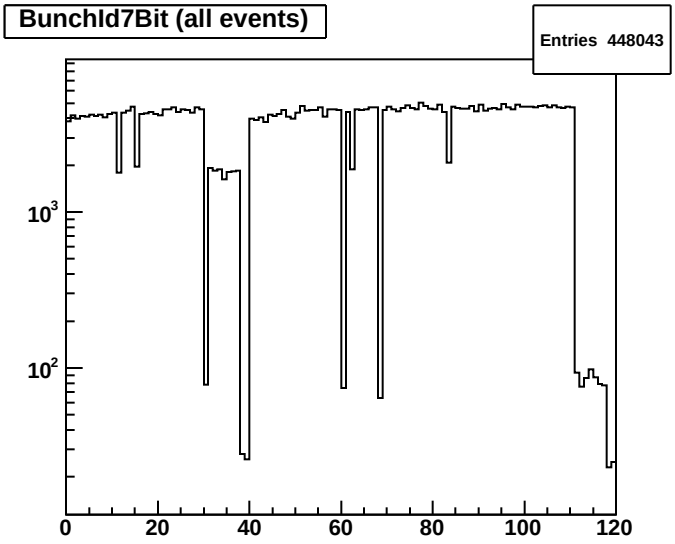

Last DSM Ch3 Input
Entries 7168688
LD301 Ch3
277236 235750 60403 24838 72922 41029 204835 72886 124798 46887 79656 24724 7137 2864 4282 292503
170807 212293 387640 422205 375121 407014 243208 375157 323245 401156 368387 423319 440906 445179 443761 155540
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

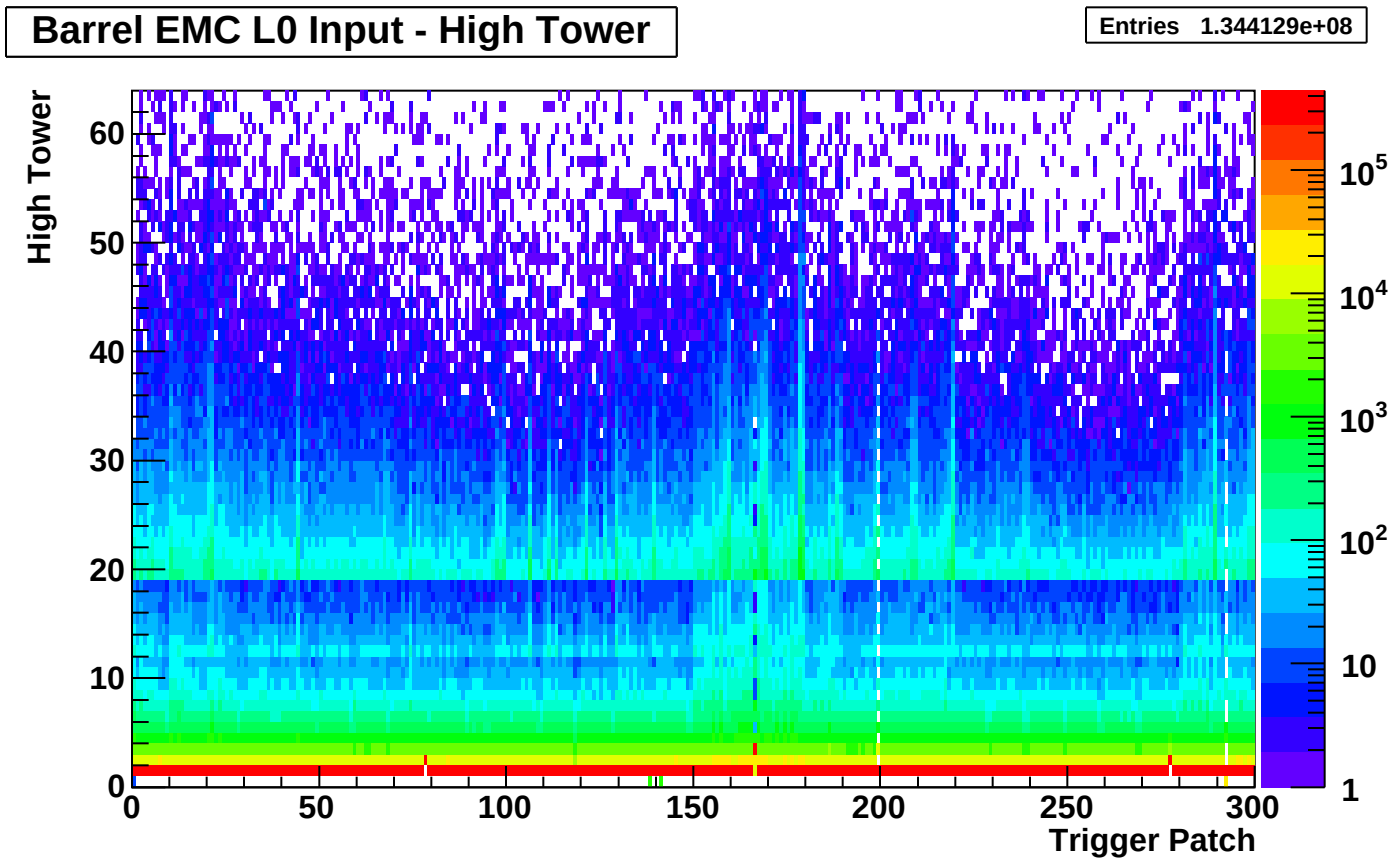

Last DSM Ch3 Input
Entries 13
LD301 Ch3 - Simulated
7 2 4
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

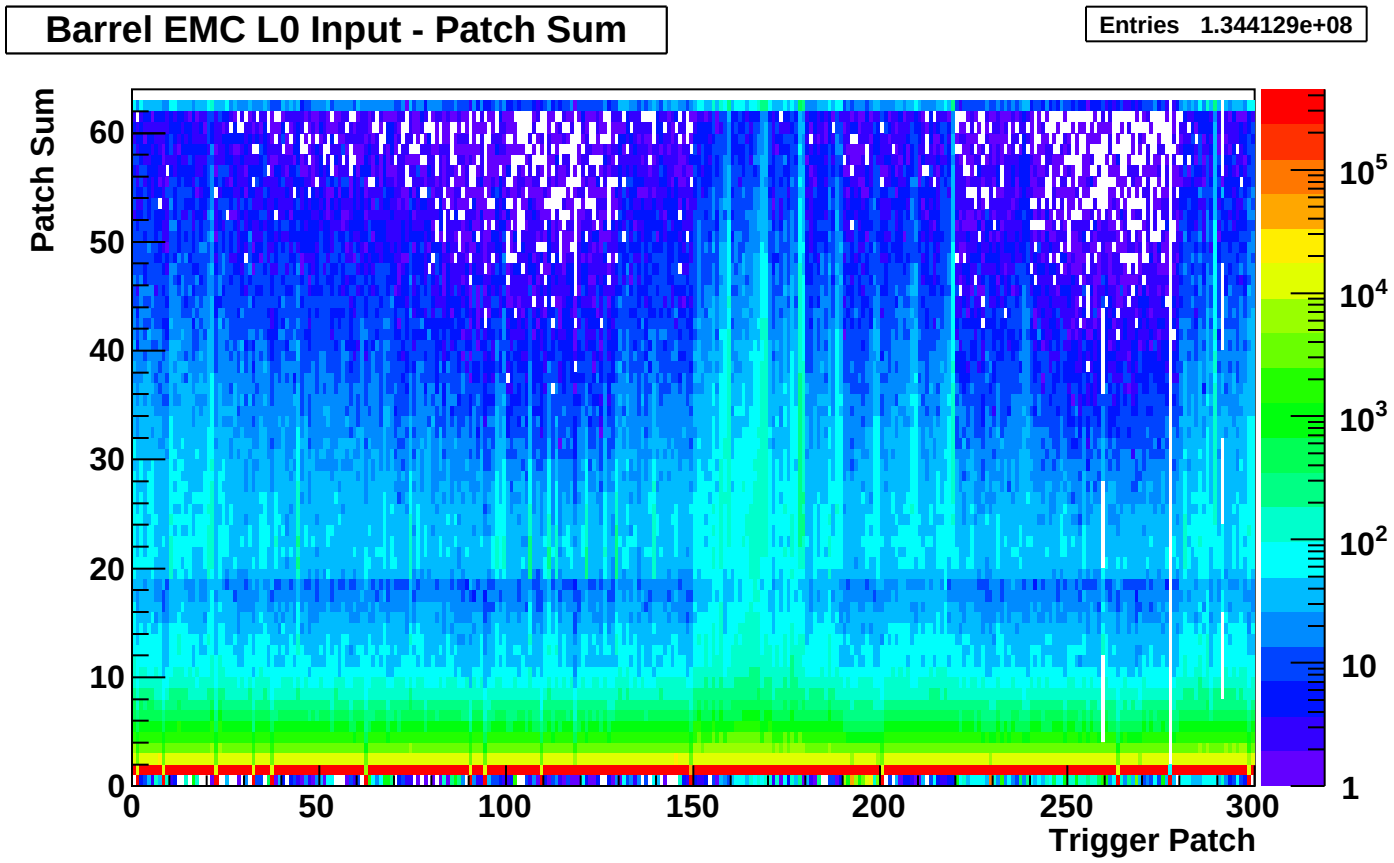

TCU bits
Entries 7168688
TCU bits
448043 448043 448043 448043 448043 448043 448043 448043 448043 448043 448043 448043 448043 448043 448043 448043
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

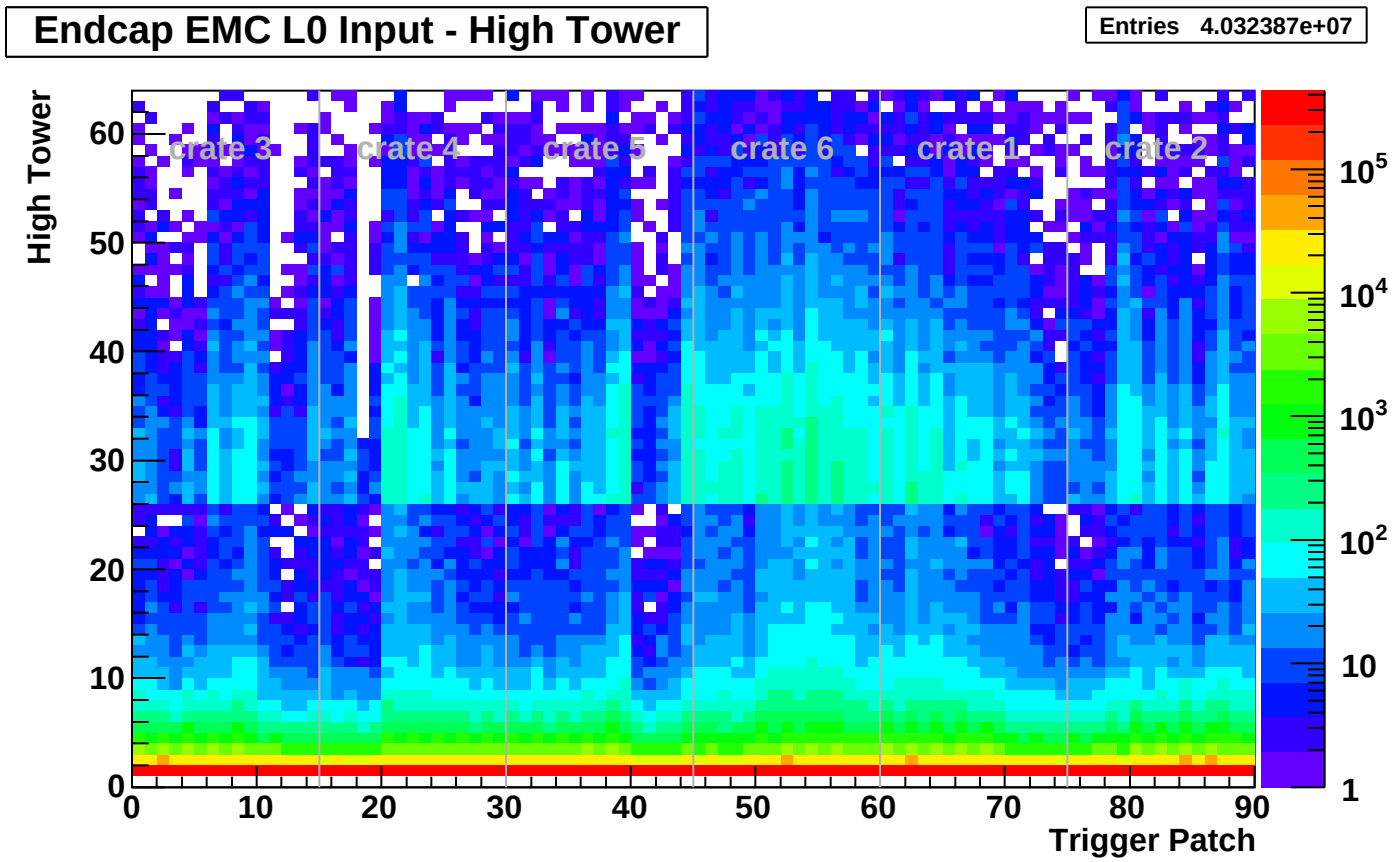

TCU bits
Entries 1024727
TCU bits - Simulated
101889 235750 156527 72922 7469 9 448043 2118
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

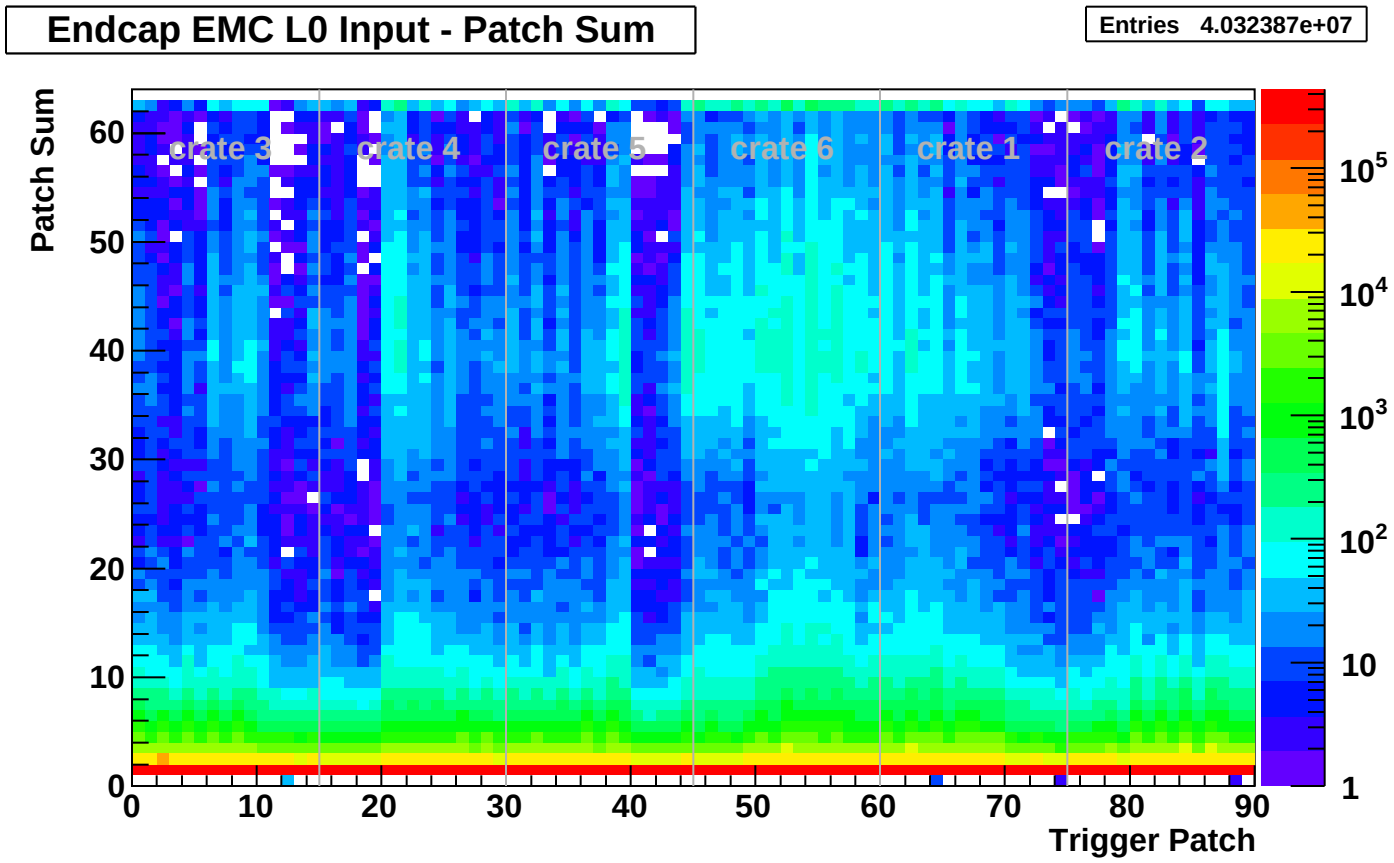

BunchId7Bit (BHT3)
Entries 24838

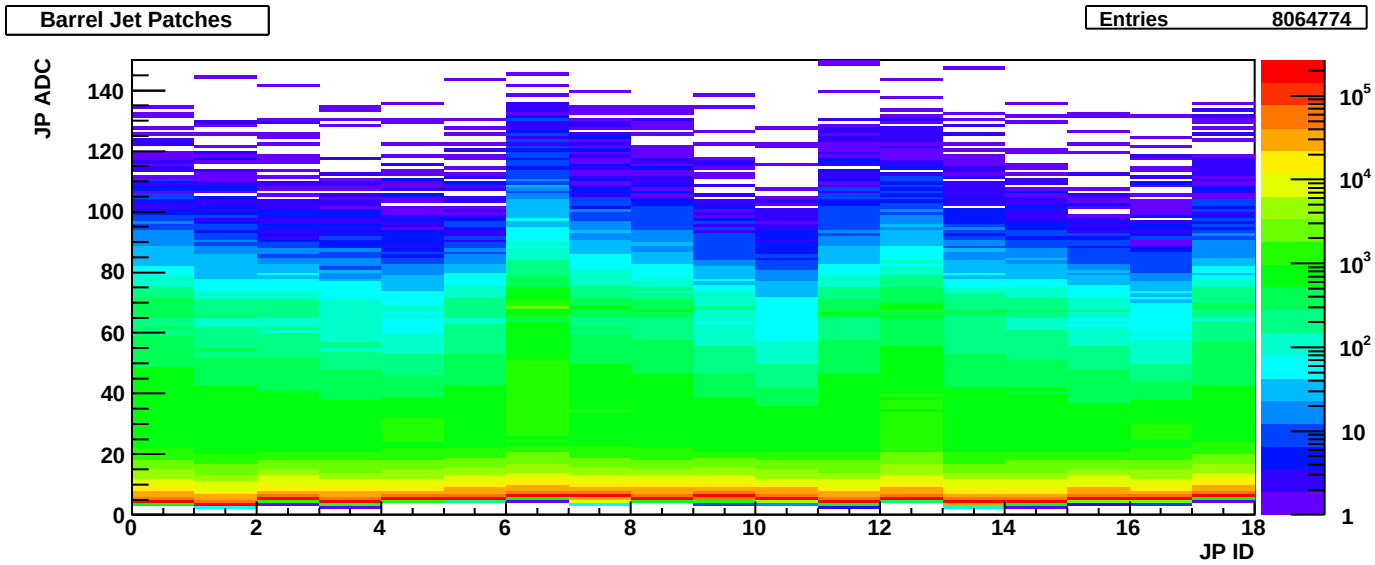

BunchId7Bit (all events)
Entries 448043

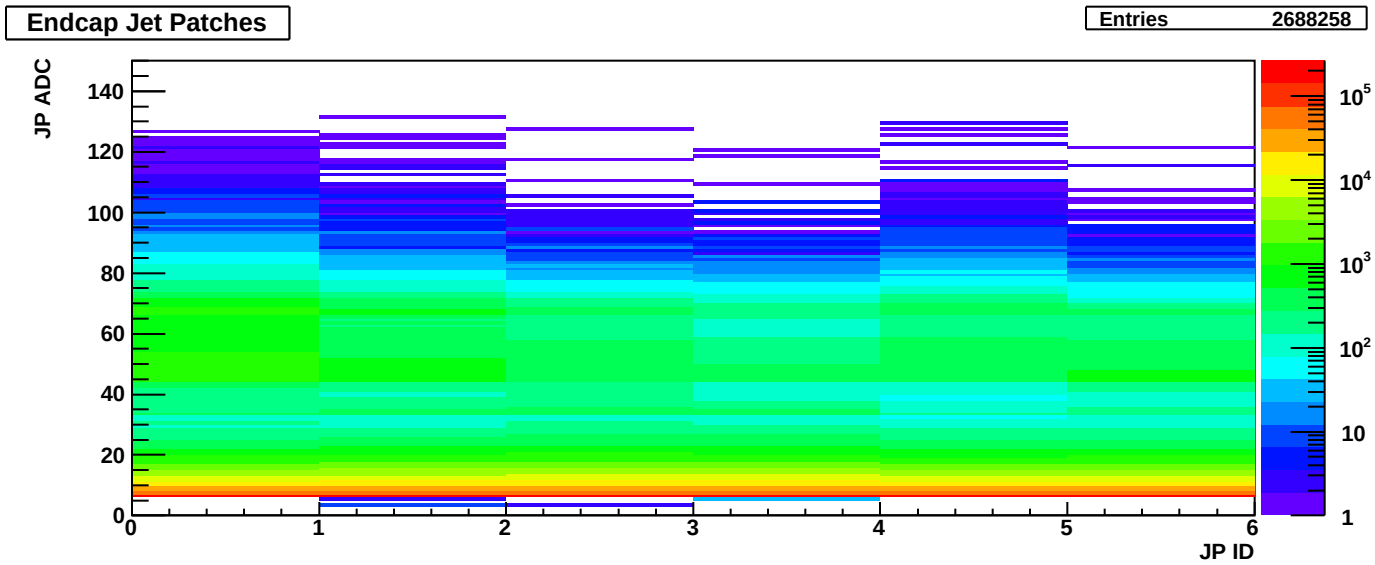

Barrel EMC L0 Input - High Tower
Entries 1.344129e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

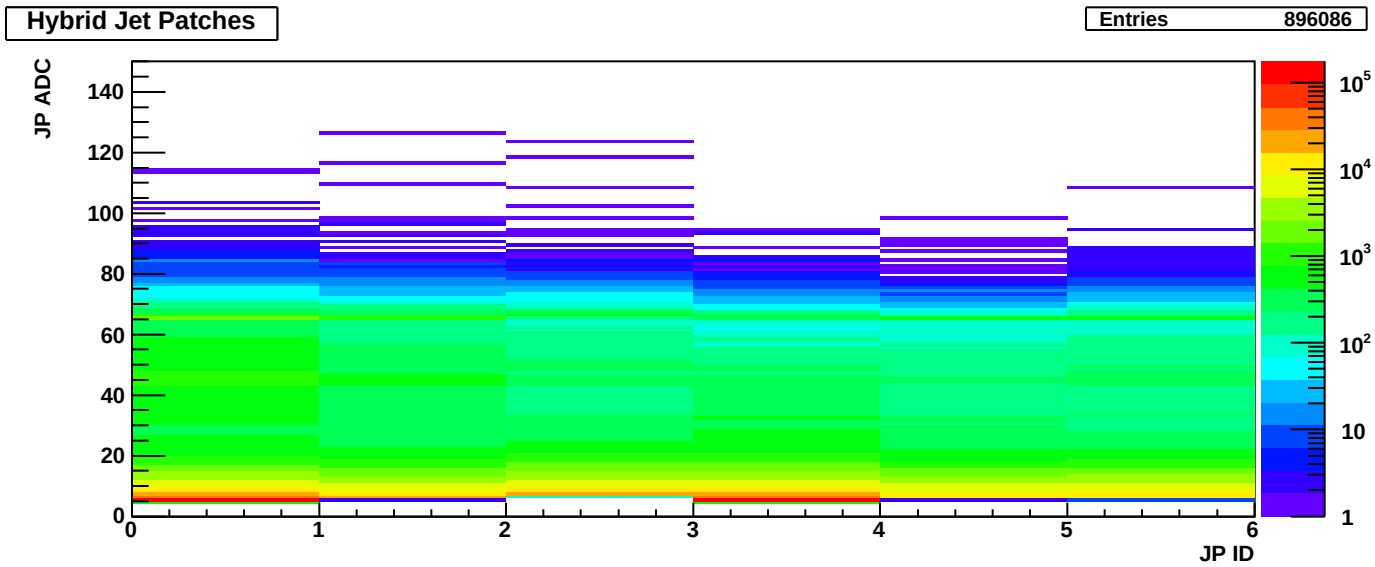

Barrel EMC L0 Input - Patch Sum
Entries 1.344129e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

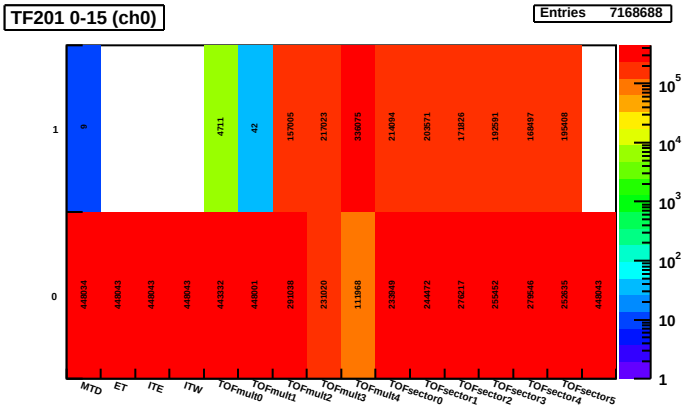

Endcap EMC L0 Input - High Tower
Entries 4.032387e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

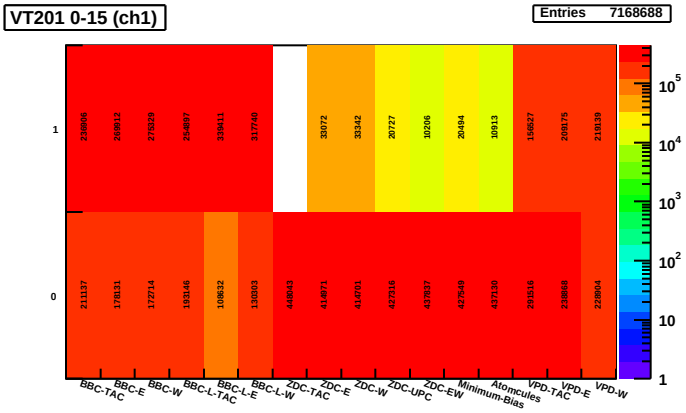

Endcap EMC L0 Input - Patch Sum
Entries 4.032387e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

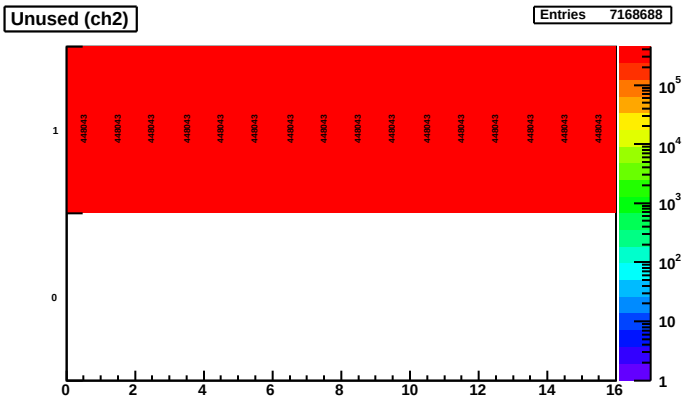

Barrel Jet Patches
Entries 8064774
JP ADC
JP ID

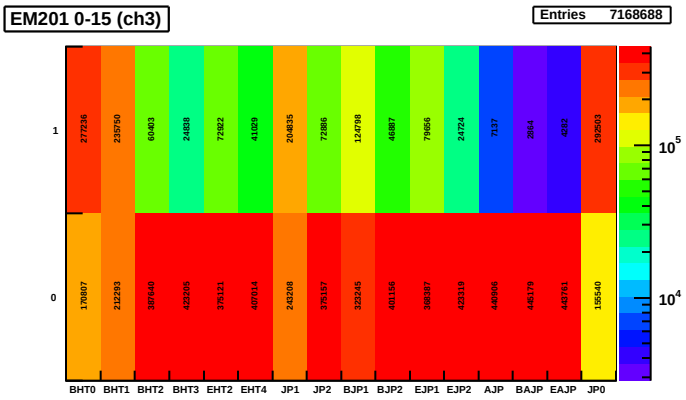

Endcap Jet Patches
Entries 2688258
JP ADC
JP ID

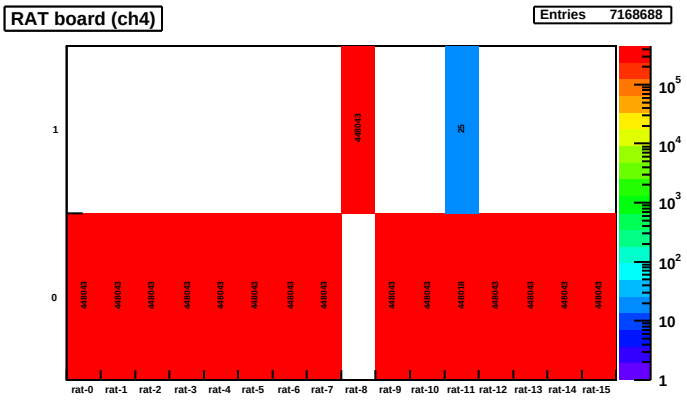

Hybrid Jet Patches
Entries 896086
JP ADC
JP ID

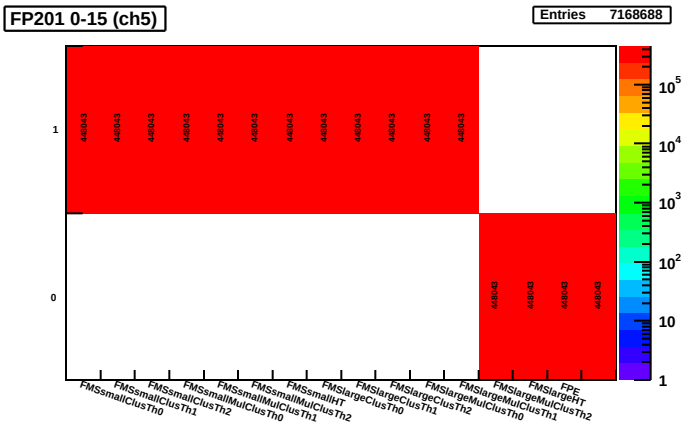

TF201 0-15 (ch0)
Entries 7168688
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

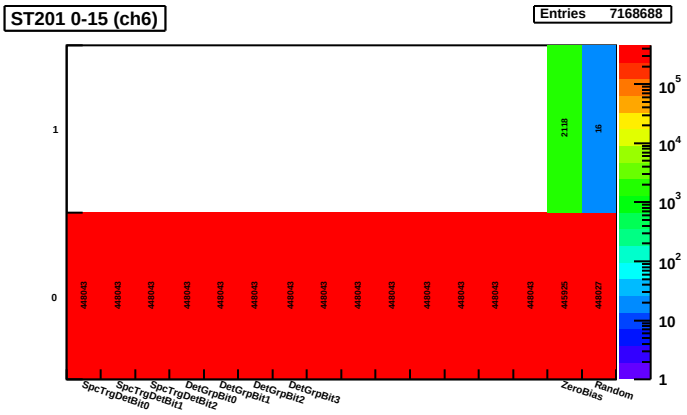

VT201 0-15 (ch1)
Entries 7168688
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

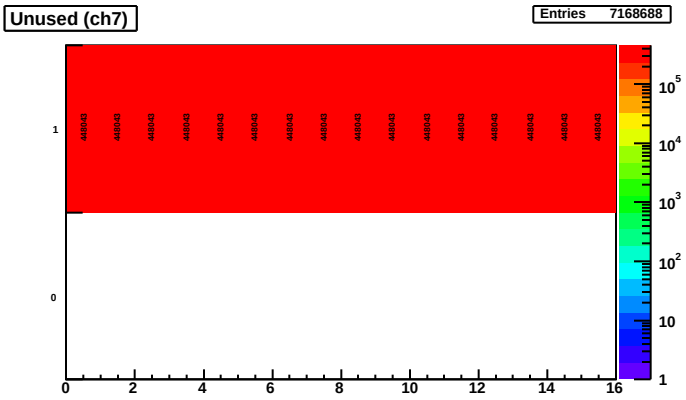

Unused (ch2)
Entries 7168688

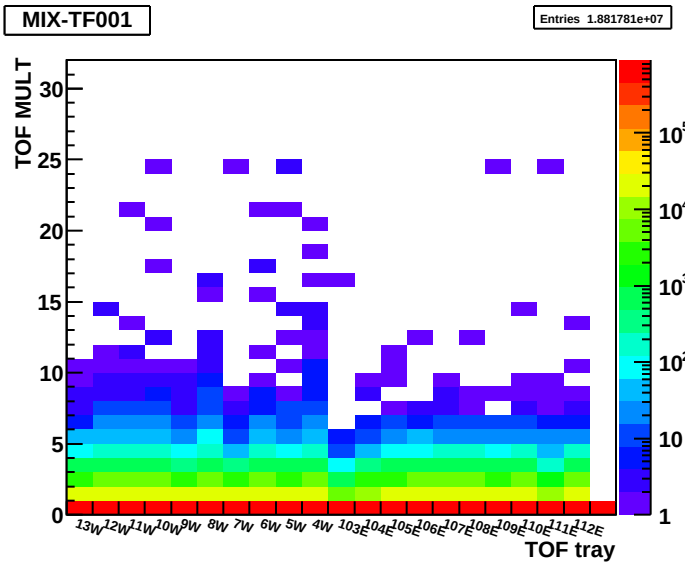

EM201 0-15 (ch3)
Entries 7168688
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

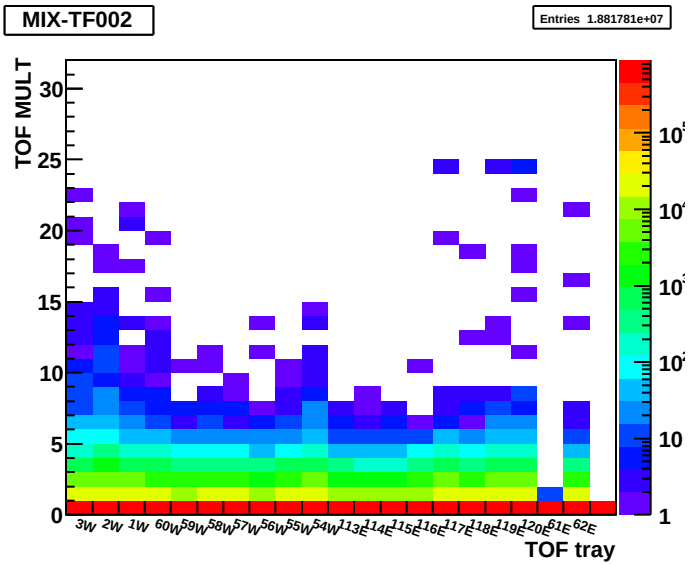

RAT board (ch4)
Entries 7168688
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

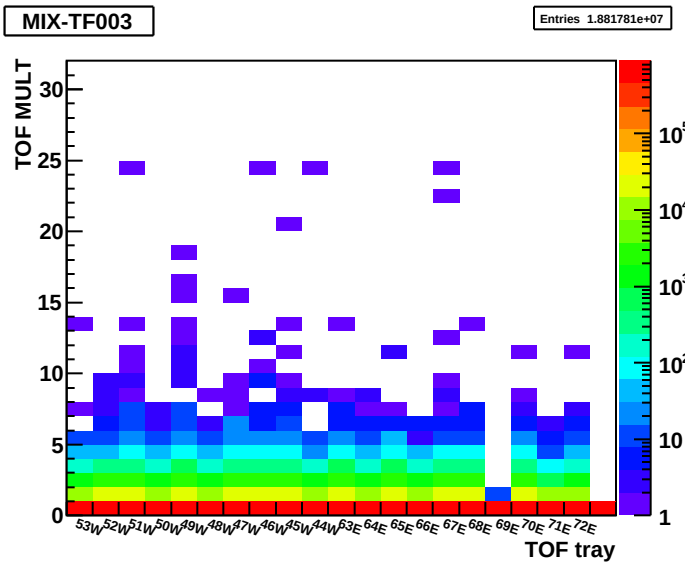

FP201 0-15 (ch5)
Entries 7168688
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

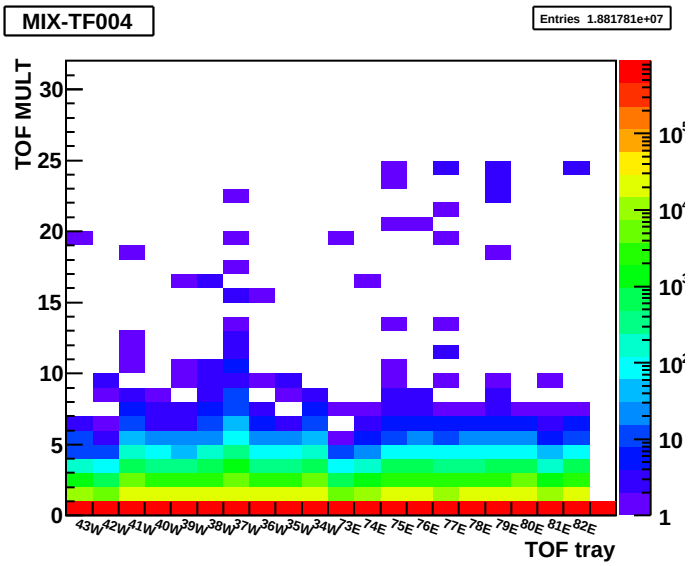

ST201 0-15 (ch6)
Entries 7168688
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

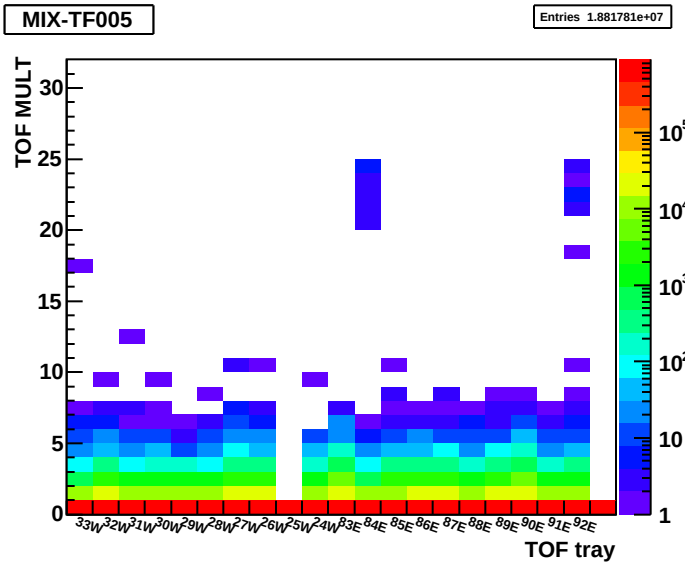

Unused (ch7)
Entries 7168688

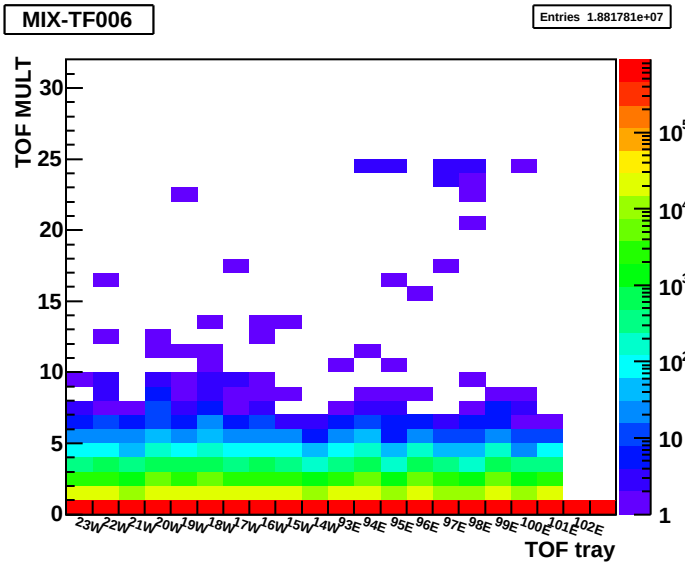

MIX-TF001
Entries 1.881781e+07
TOF MULT
TOF tray

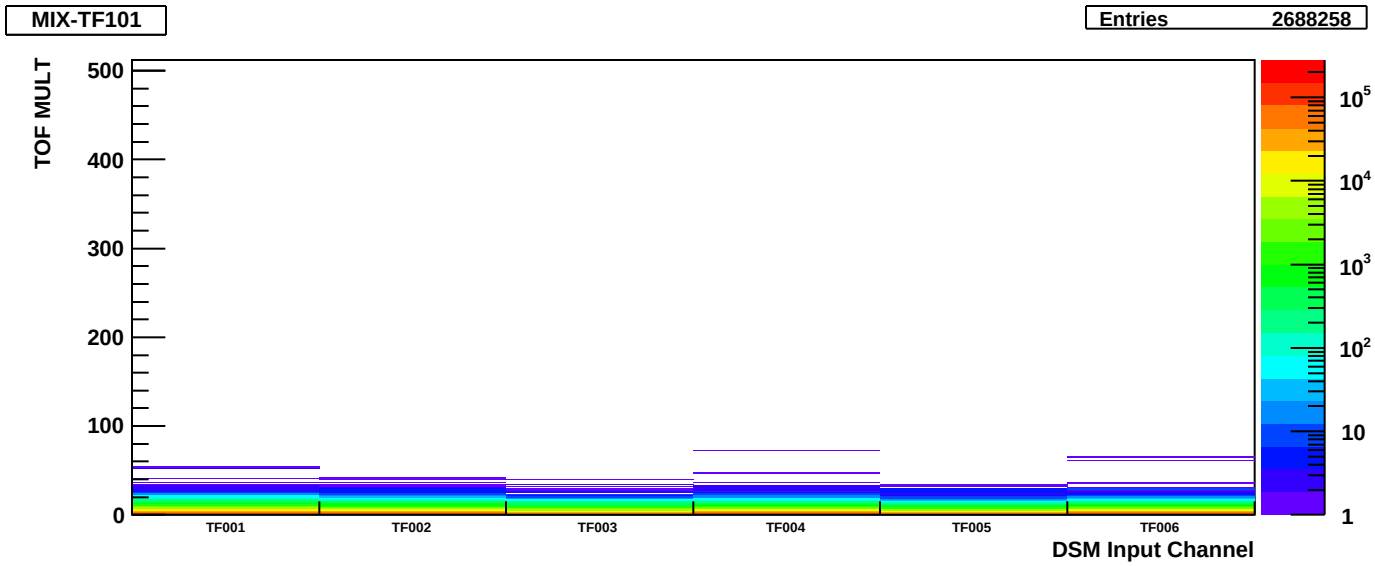

MIX-TF002
Entries 1.881781e+07
TOF MULT
TOF tray

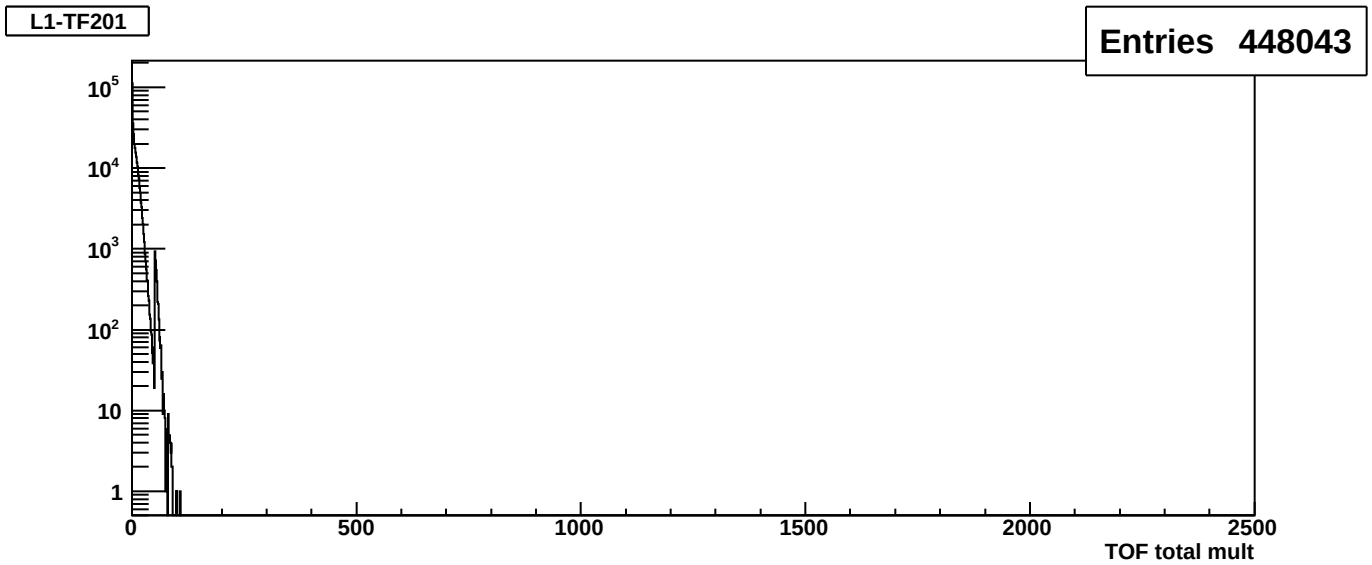

MIX-TF003
Entries 1.881781e+07
TOF MULT
TOF tray

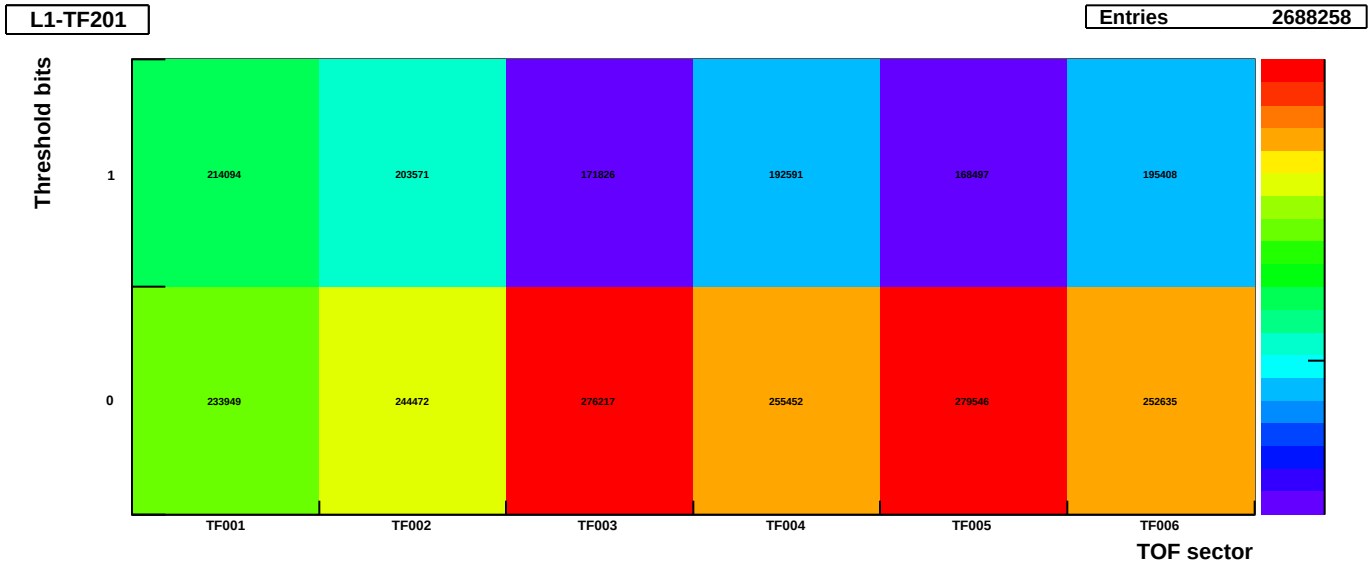

MIX-TF004
Entries 1.881781e+07
TOF MULT
TOF tray

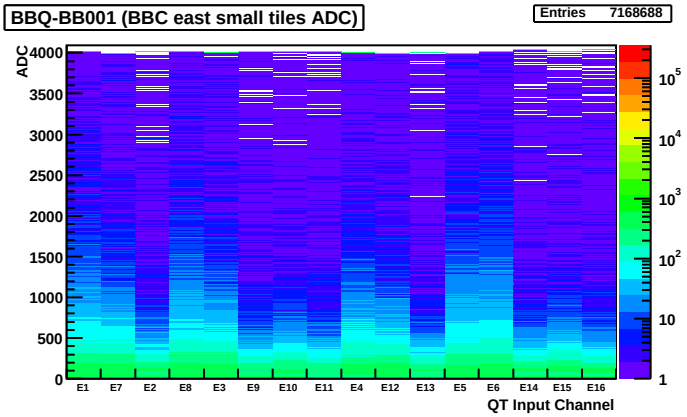

MIX-TF005
Entries 1.881781e+07
TOF MULT
TOF tray

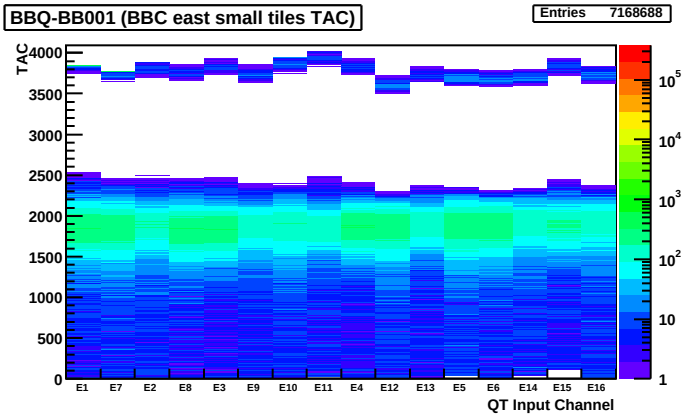

MIX-TF006
Entries 1.881781e+07
TOF MULT
TOF tray

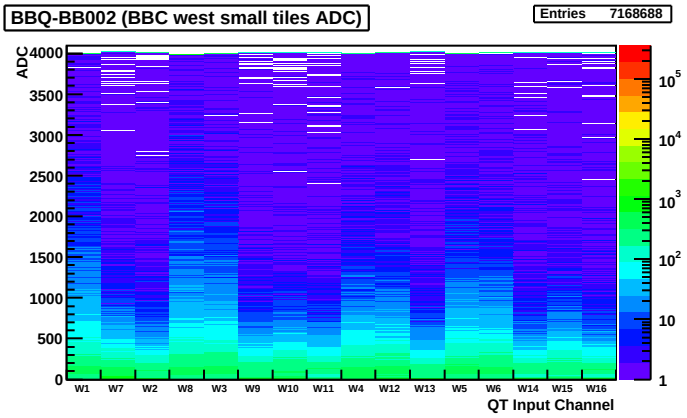

MIX-TF101
Entries 2688258
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

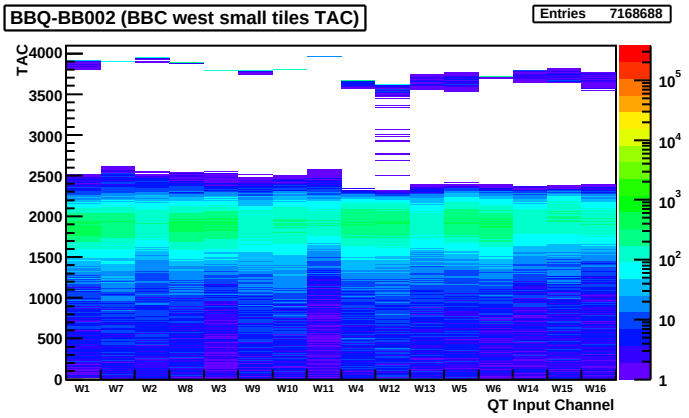

L1-TF201
Entries 448043
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

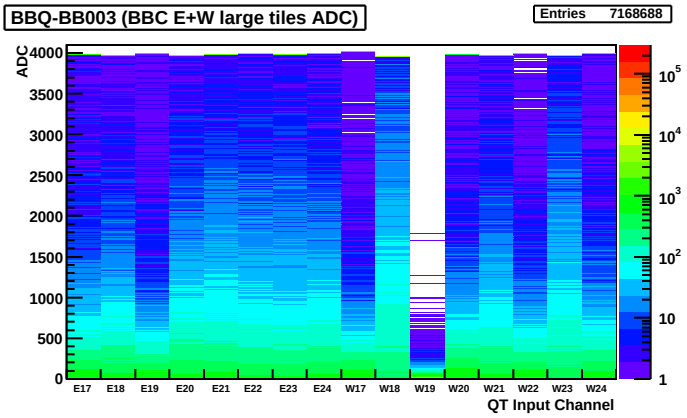

L1-TF201
Entries 2688258
Threshold bits
1
0
214094
203571
171826
192591
168497
195408
233949
244472
276217
255452
279546
252635
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

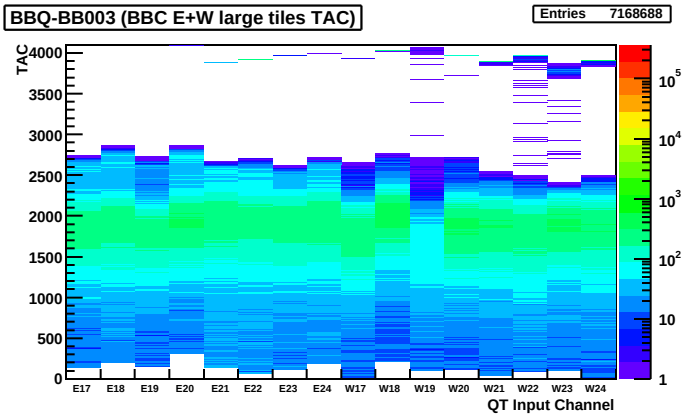

BBQ-BB001 (BBC east small tiles ADC)
Entries 7168688
ADC
QT Input Channel

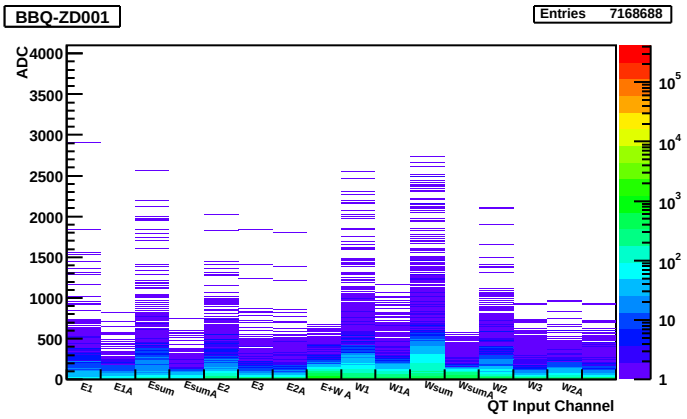

BBQ-BB001 (BBC east small tiles TAC)
Entries 7168688
TAC
QT Input Channel

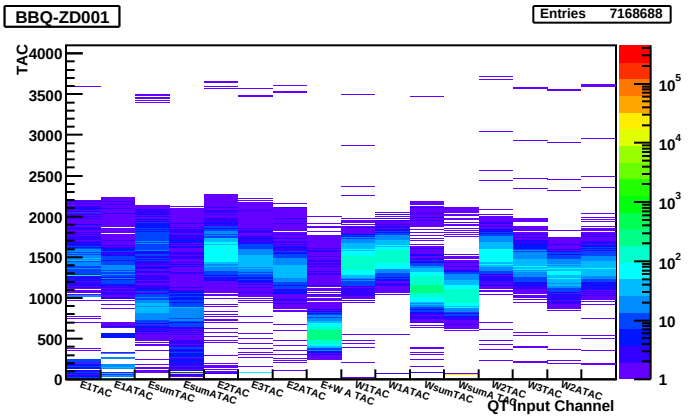

BBQ-BB002 (BBC west small tiles ADC)
Entries 7168688
ADC
QT Input Channel

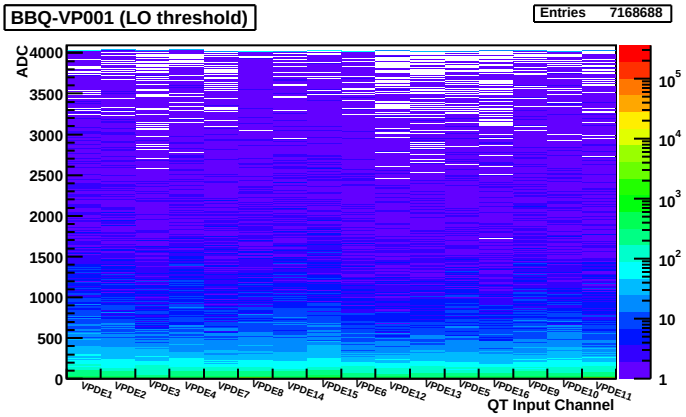

BBQ-BB002 (BBC west small tiles TAC)
Entries 7168688
TAC
QT Input Channel

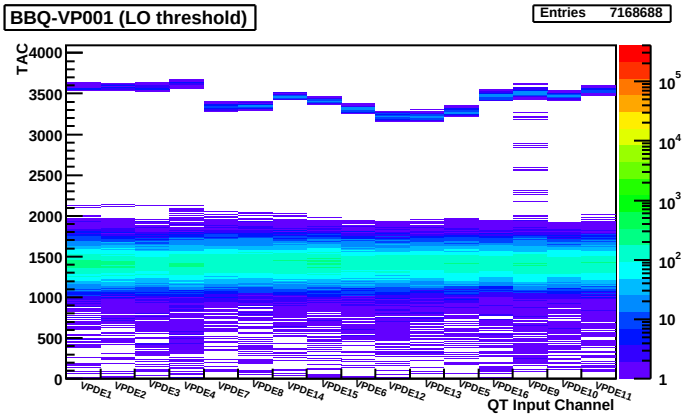

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7168688
ADC
QT Input Channel

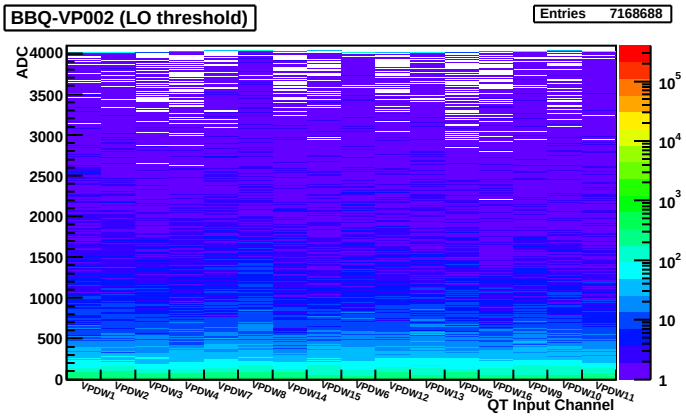

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7168688
TAC
QT Input Channel

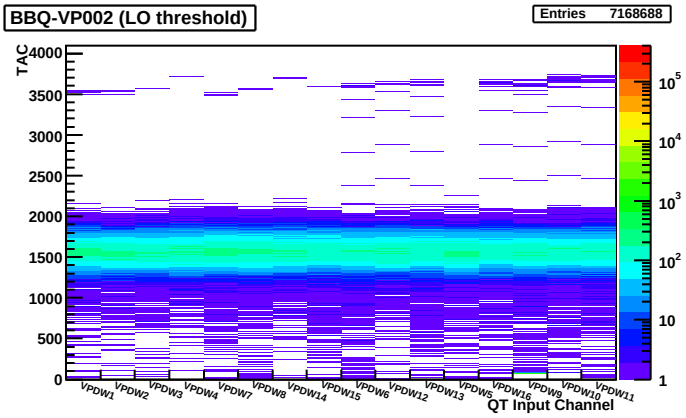

BBQ-ZD001
Entries 7168688
ADC
QT Input Channel

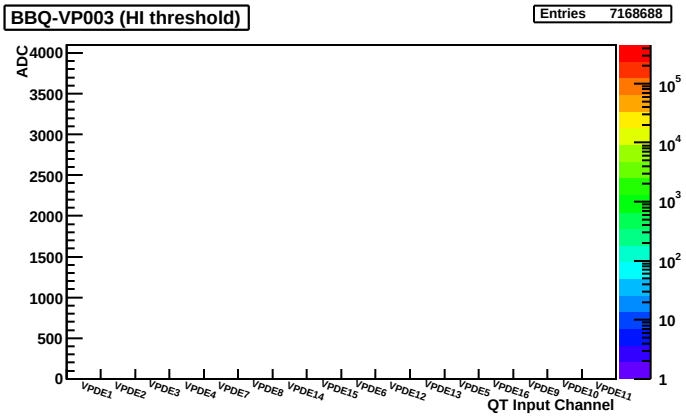

BBQ-ZD001
Entries 7168688
TAC
QT Input Channel

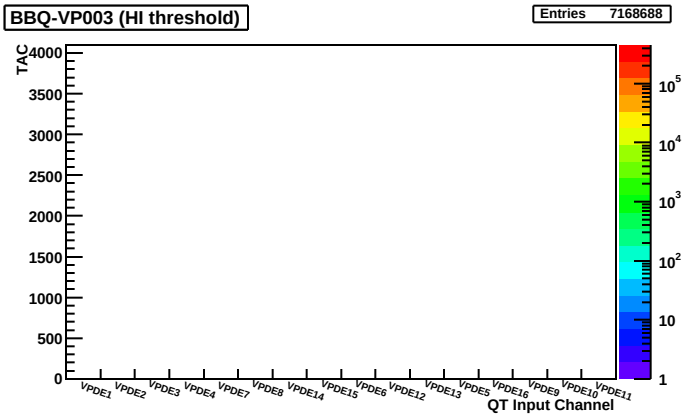

BBQ-VP001 (LO threshold)
Entries 7168688
ADC
QT Input Channel

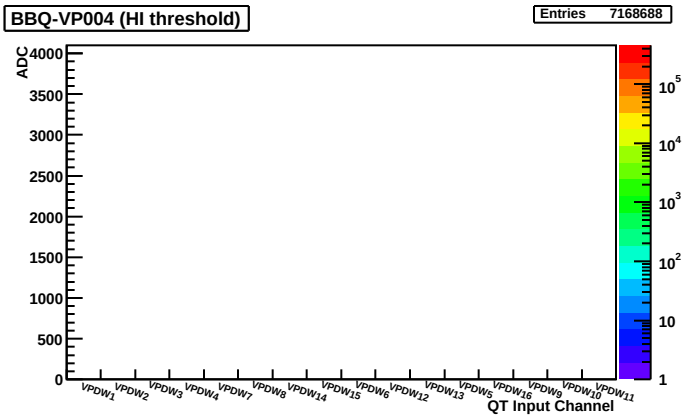

BBQ-VP001 (LO threshold)
Entries 7168688
TAC
QT Input Channel

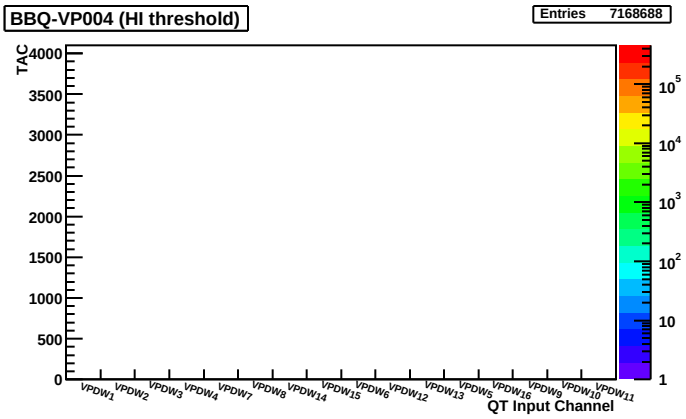

BBQ-VP002 (LO threshold)
Entries 7168688
ADC
QT Input Channel

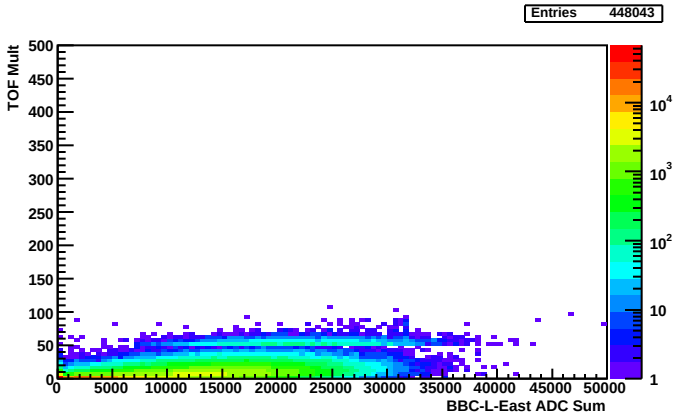

BBQ-VP002 (LO threshold)
Entries 7168688
TAC
QT Input Channel

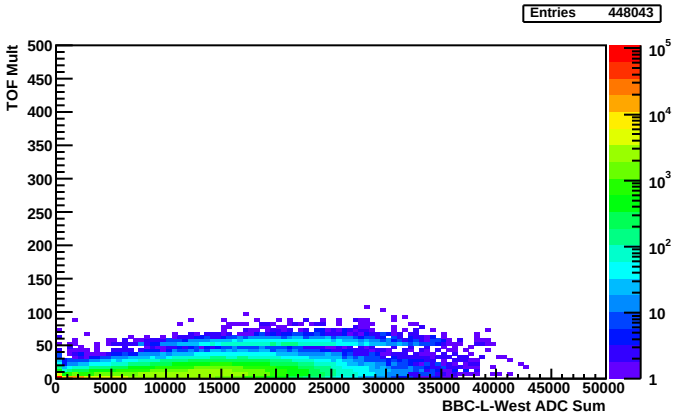

BBQ-VP003 (HI threshold)
Entries 7168688
ADC
QT Input Channel

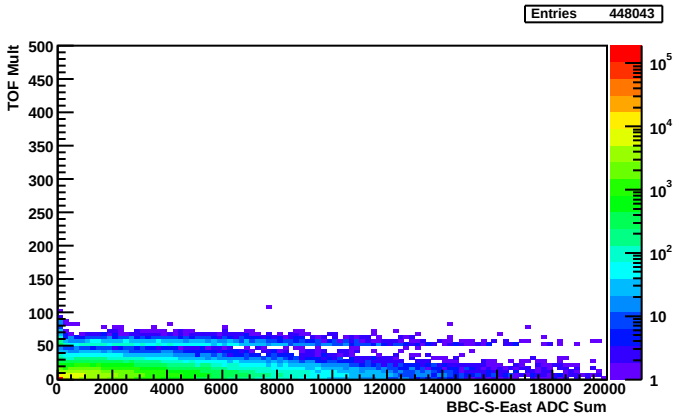

BBQ-VP003 (HI threshold)
Entries 7168688
TAC
QT Input Channel

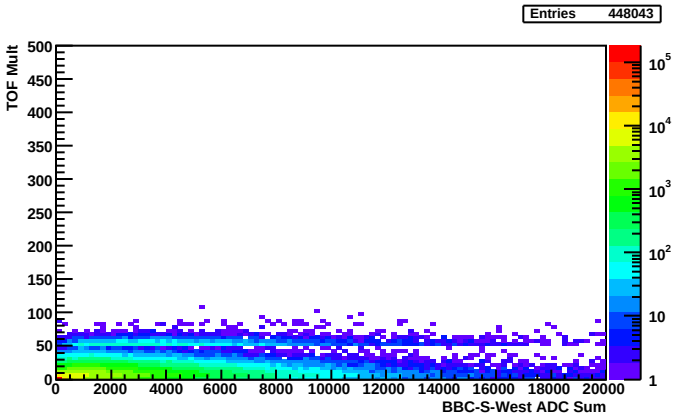

BBQ-VP004 (HI threshold)
Entries 7168688
ADC
QT Input Channel

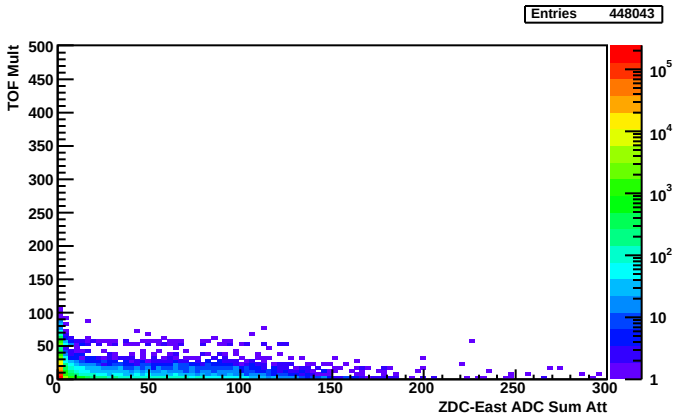

BBQ-VP004 (HI threshold)
Entries 7168688
TAC
QT Input Channel

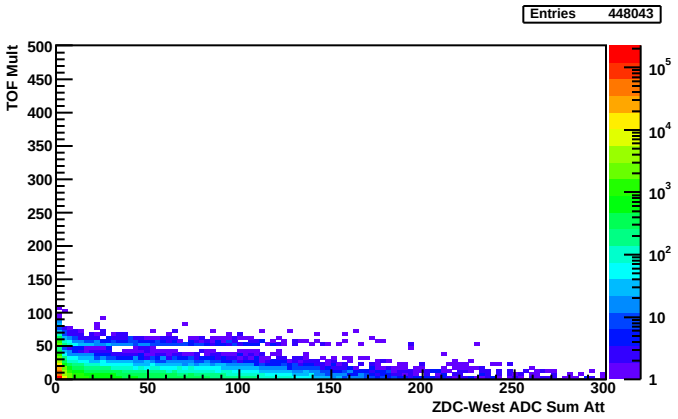

Entries 448043
TOF Mult
BBC-L-East ADC Sum

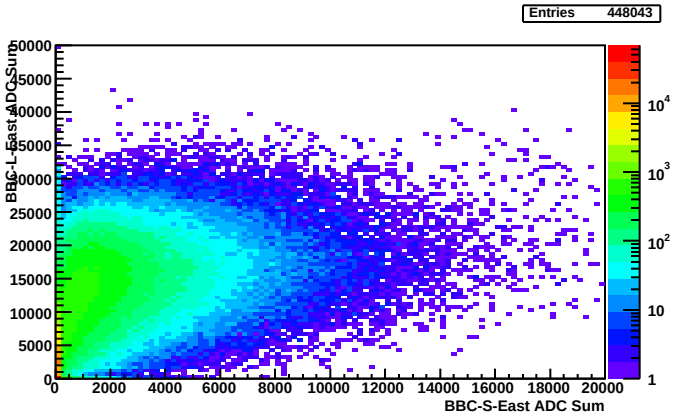

Entries 448043
TOF Mult
BBC-L-West ADC Sum

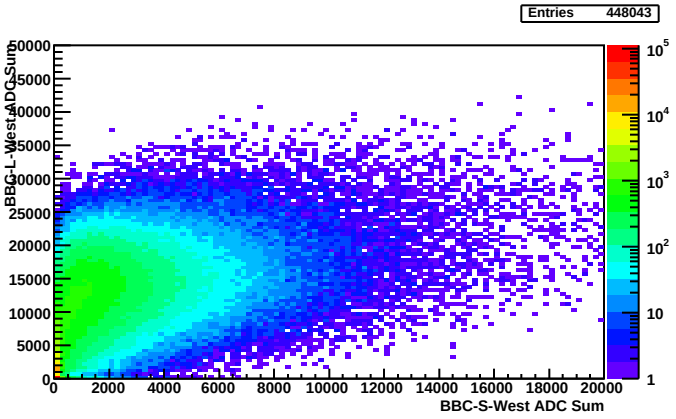

Entries 448043
TOF Mult
BBC-S-East ADC Sum

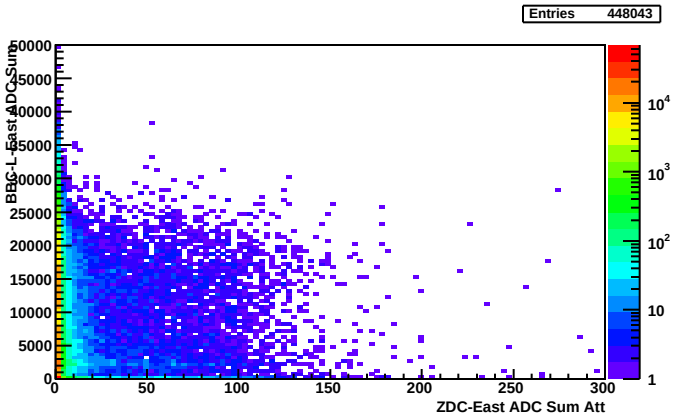

Entries 448043
TOF Mult
BBC-S-West ADC Sum

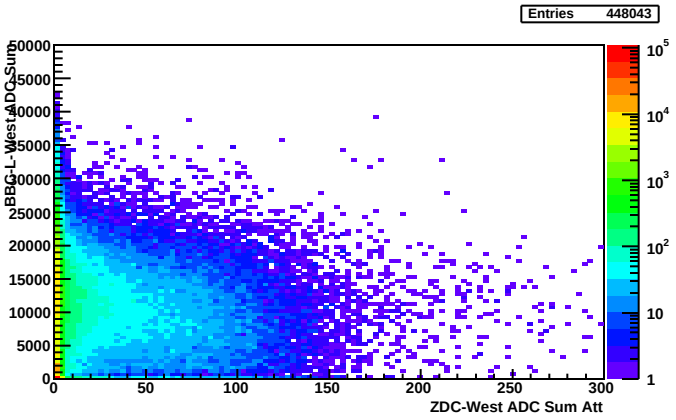

Entries 448043
TOF Mult
ZDC-East ADC Sum Att

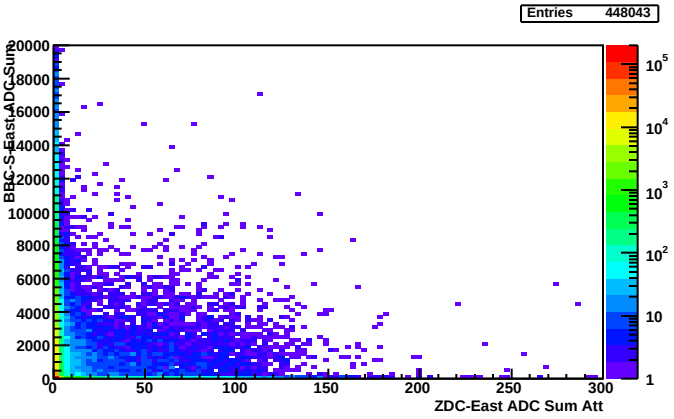

Entries 448043
TOF Mult
ZDC-West ADC Sum Att

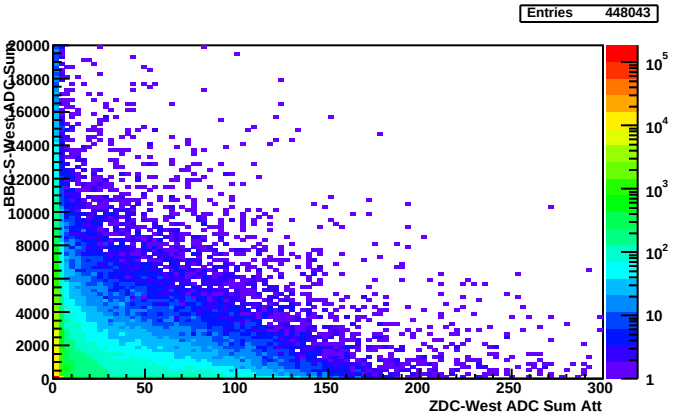

Entries 448043
BBC-L-East ADC Sum
BBC-S-East ADC Sum

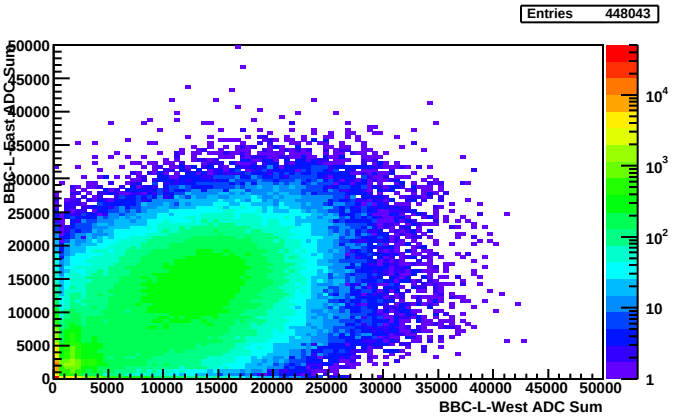

Entries 448043
BBC-L-West ADC Sum
BBC-S-West ADC Sum

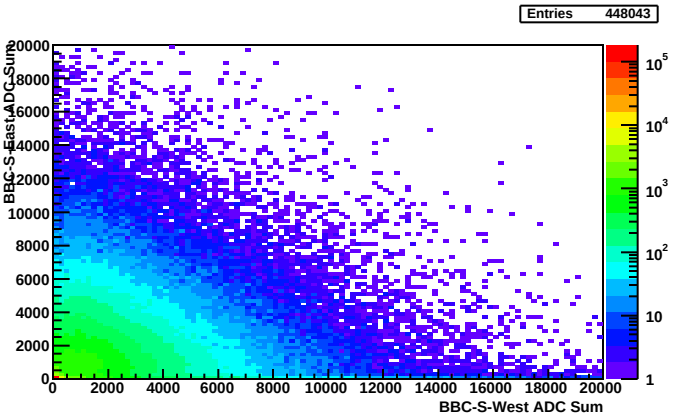

Entries 448043
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

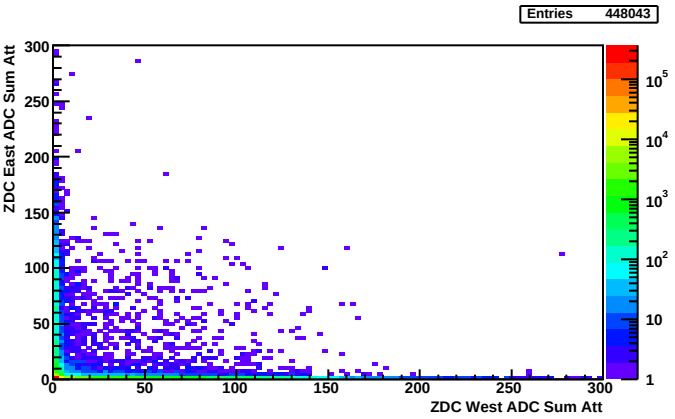

Entries 448043
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

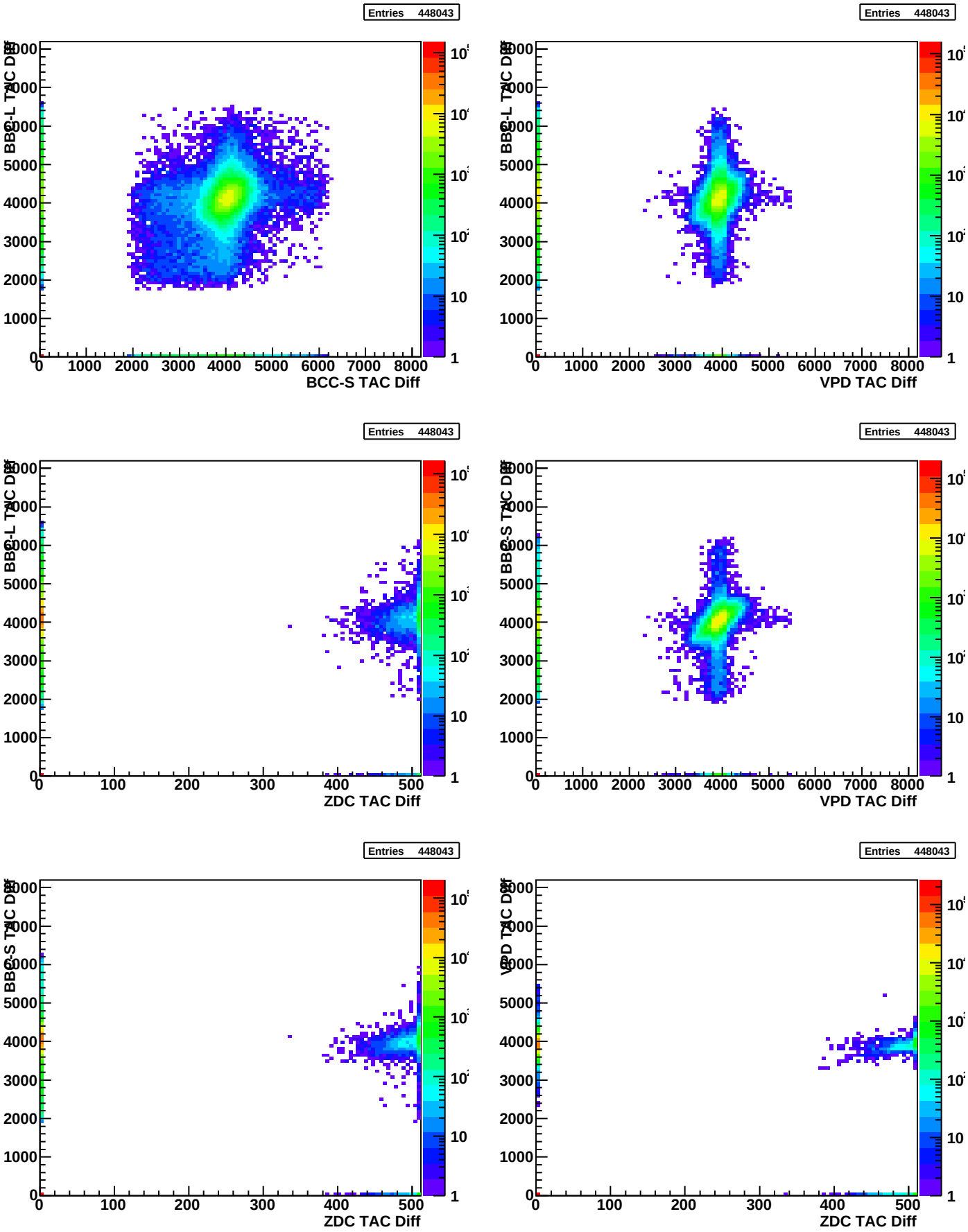

Entries 448043
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

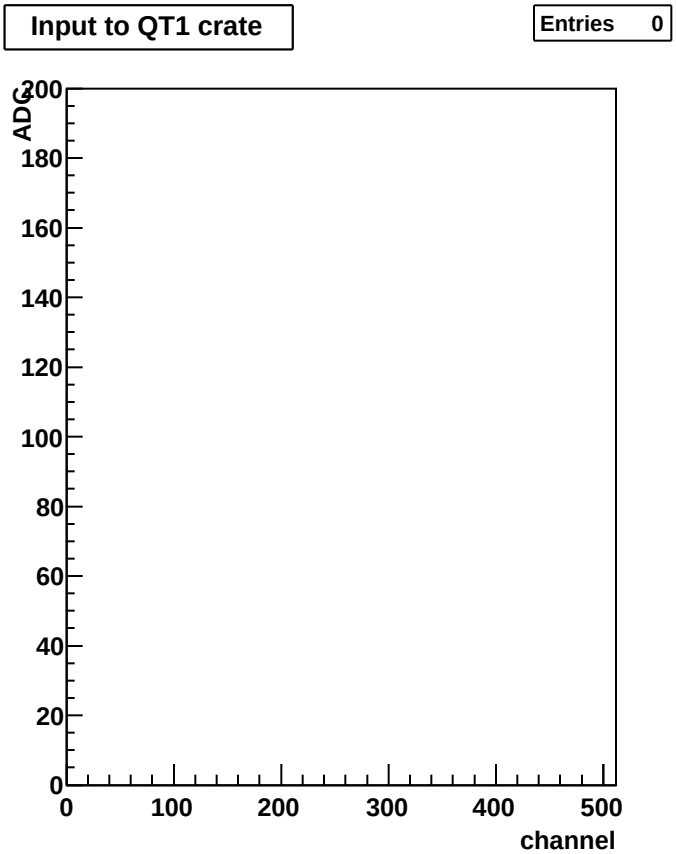

Entries 448043
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

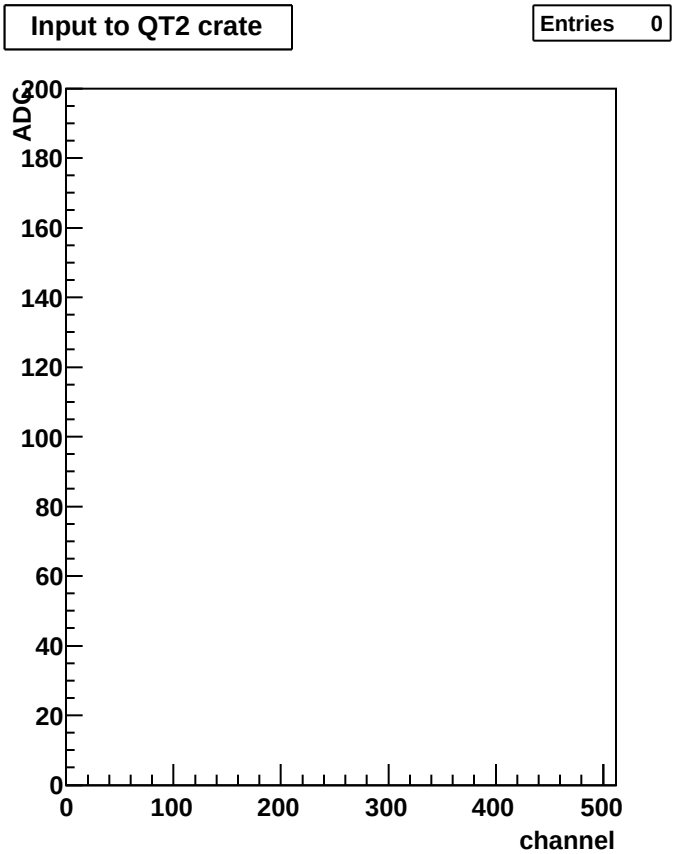

Entries 448043
BBC-L-East ADC Sum
BBC-L-West ADC Sum

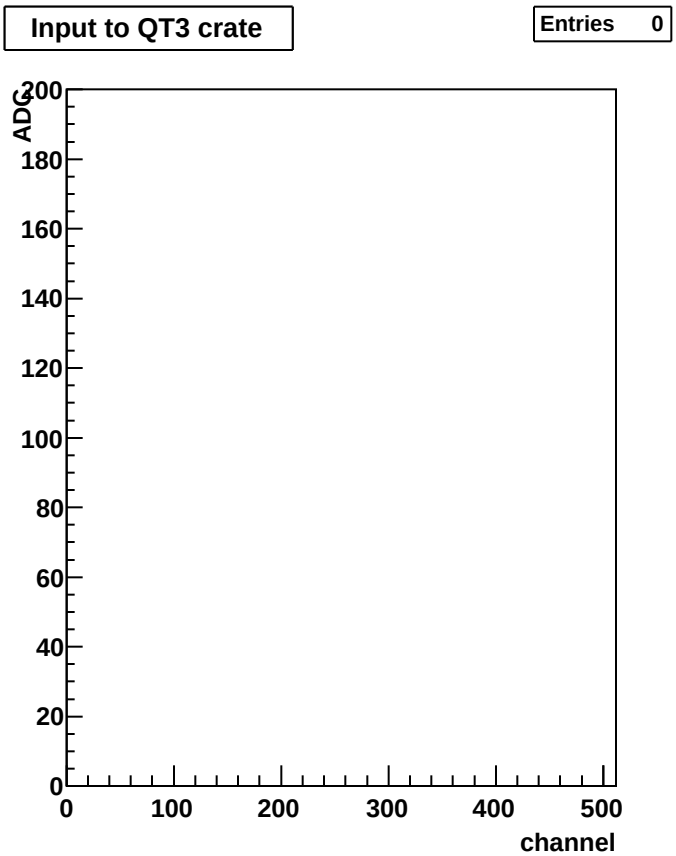

Entries 448043
BBC-S-East ADC Sum
BBC-S-West ADC Sum

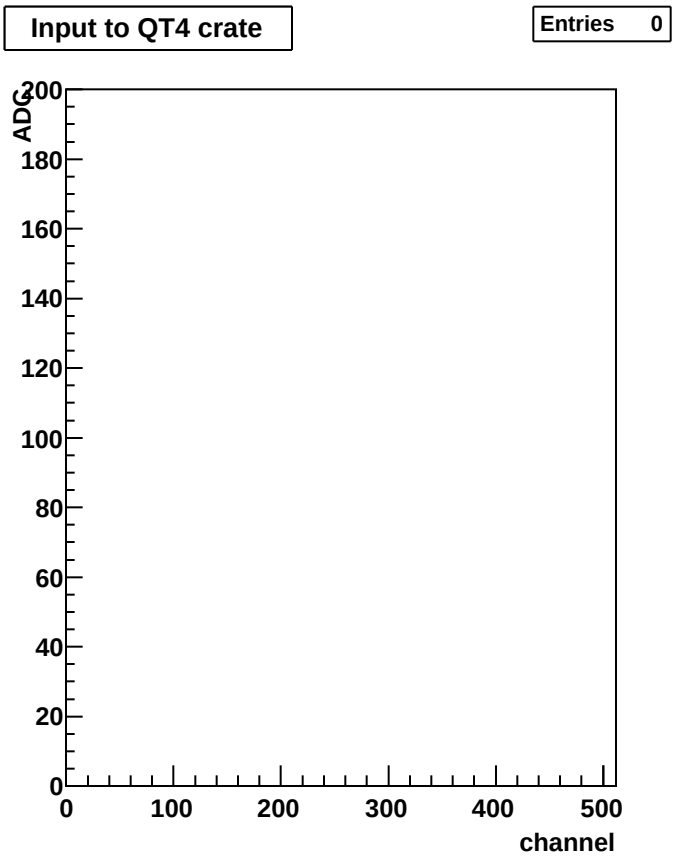

Entries 448043
ZDC East ADC Sum Att
ZDC West ADC Sum Att

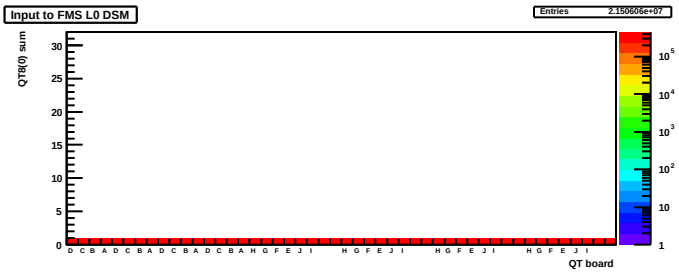

Entries 448043
BBC-L TAC Diff
BCC-S TAC Diff
Entries 448043
BBC-L TAC Diff
VPD TAC Diff
Entries 448043
BBC-L TAC Diff
ZDC TAC Diff
Entries 448043
BBC-S TAC Diff
VPD TAC Diff
Entries 448043
BBC-S TAC Diff
ZDC TAC Diff
Entries 448043
VPD TAC Diff
ZDC TAC Diff

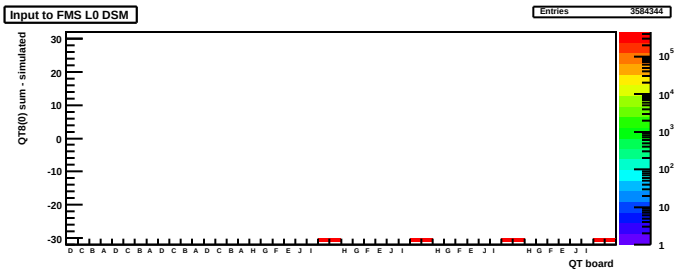

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

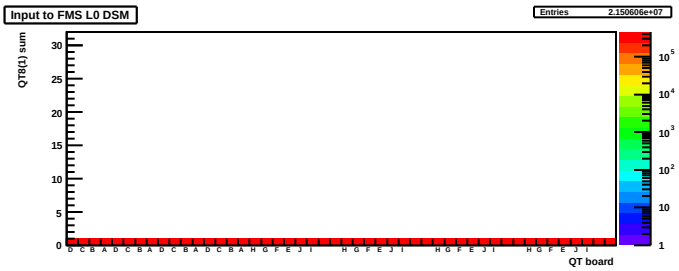

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

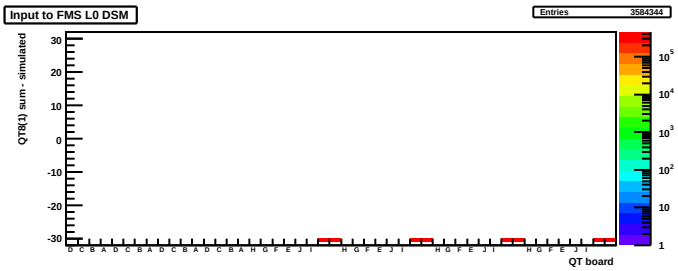

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

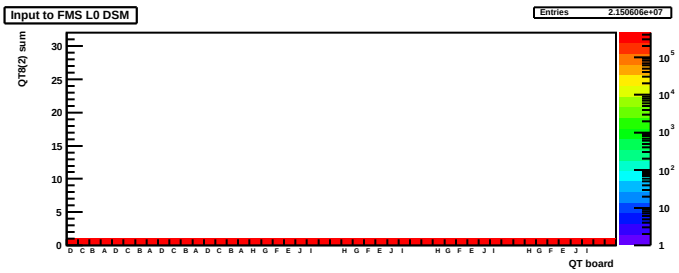

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

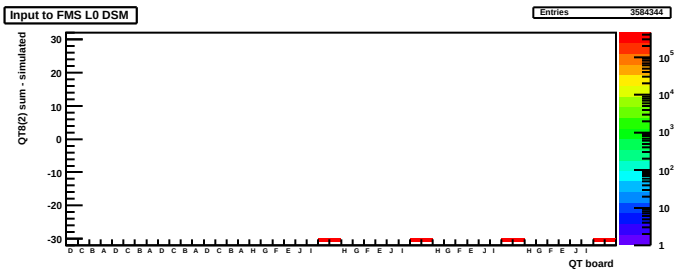

Input to FMS L0 DSM
Entries 2.150606e+07
QT8(0) sum
QT board

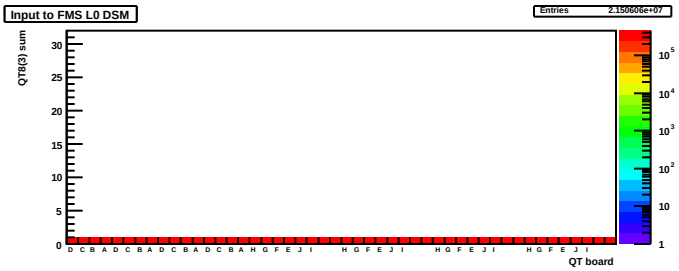

Input to FMS L0 DSM
Entries 3584344
QT8(0) sum - simulated
QT board

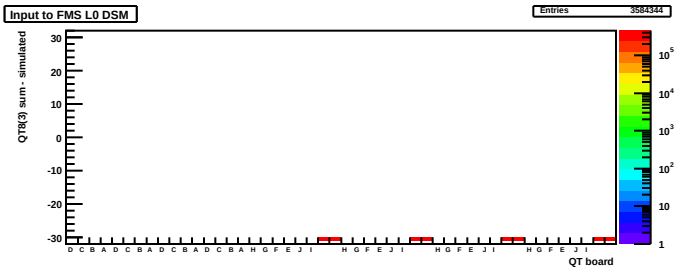

Input to FMS L0 DSM
Entries 2.150606e+07
QT8(1) sum
QT board

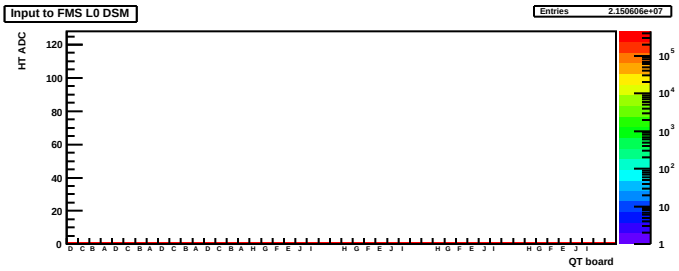

Input to FMS L0 DSM
Entries 3584344
QT8(1) sum - simulated
QT board

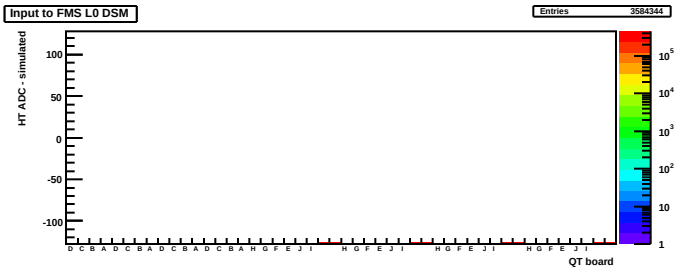

Input to FMS L0 DSM
Entries 2.150606e+07
QT8(2) sum
QT board

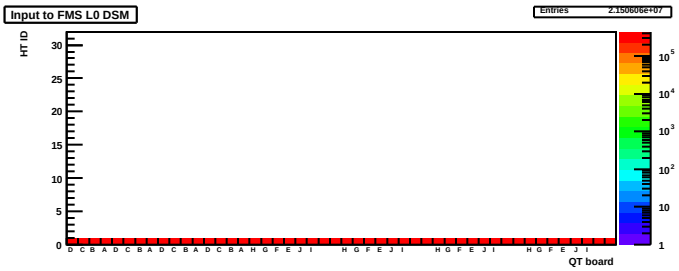

Input to FMS L0 DSM
Entries 3584344
QT8(2) sum - simulated
QT board

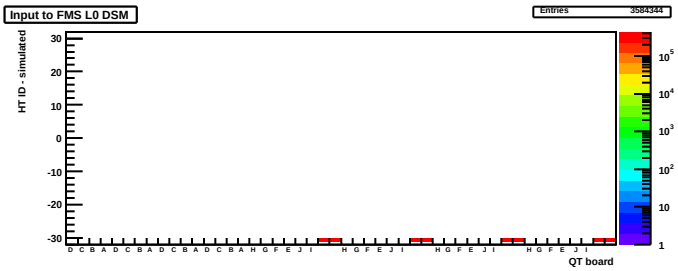

Input to FMS L0 DSM
Entries 2.150606e+07
QT8(3) sum
QT board

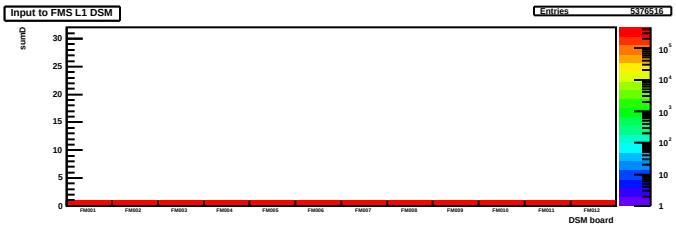

Input to FMS L0 DSM
Entries 3584344
QT8(3) sum - simulated
QT board

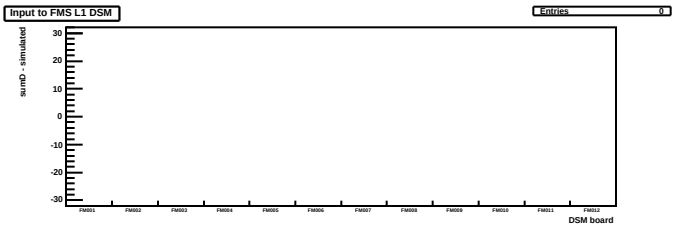

Input to FMS L0 DSM
Entries 2.150606e+07
HT ADC
QT board

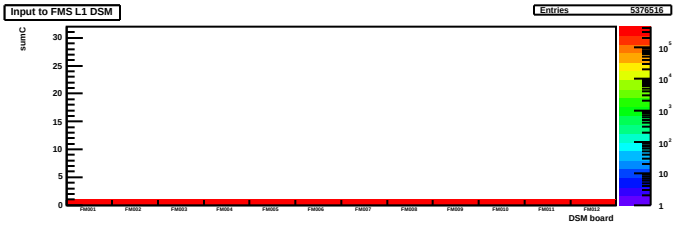

Input to FMS L0 DSM
Entries 3584344
HT ADC - simulated
QT board

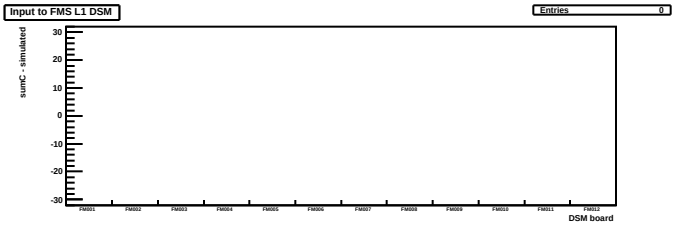

Input to FMS L0 DSM
Entries 2.150606e+07
HT ID
QT board

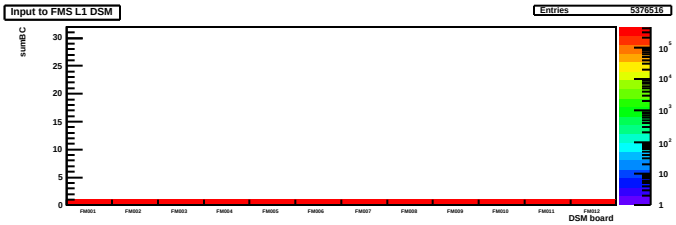

Input to FMS L0 DSM
Entries 3584344
HT ID - simulated
QT board

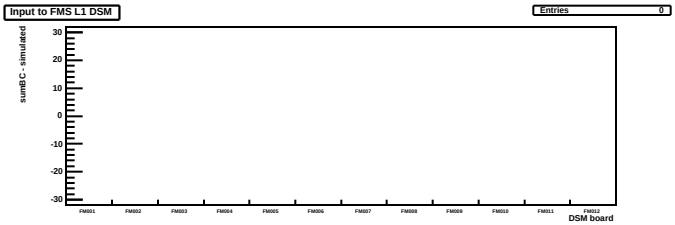

Input to FMS L1 DSM
Entries 5376516
sumD
DSM board

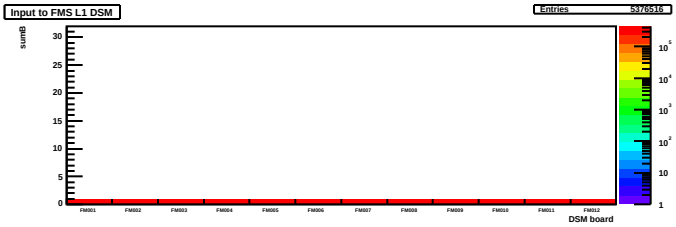

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

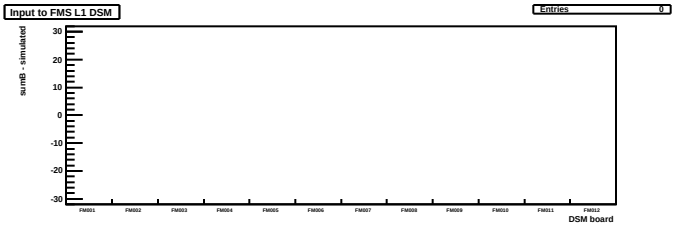

Input to FMS L1 DSM
Entries 5376516
sumC
DSM board

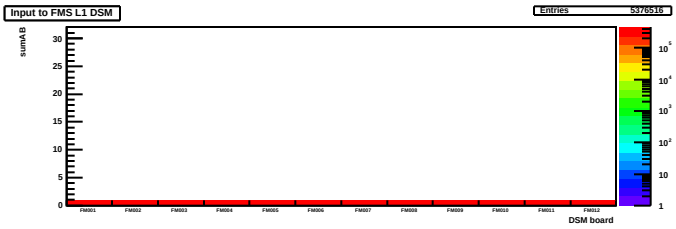

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

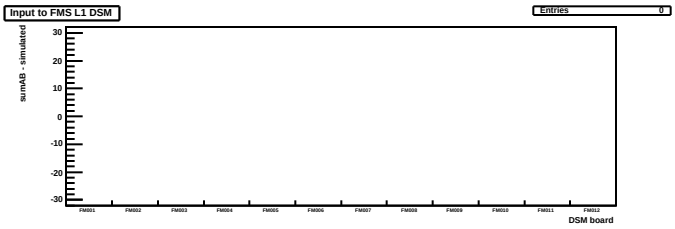

Input to FMS L1 DSM
Entries 5376516
sumBC
DSM board

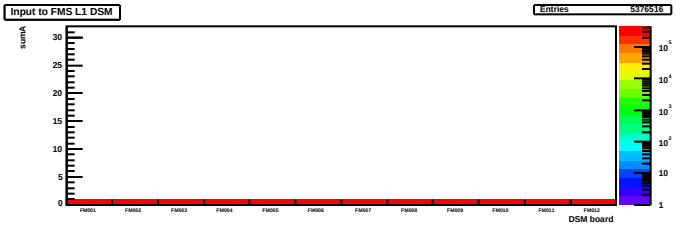

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

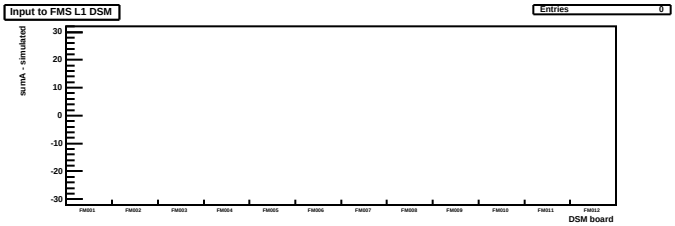

Input to FMS L1 DSM
Entries 5376516
sumB
DSM board

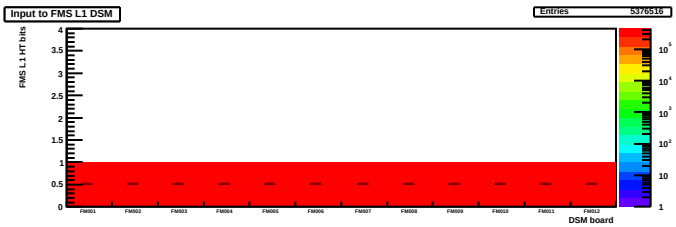

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

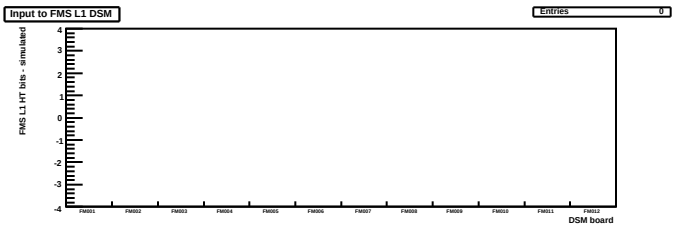

Input to FMS L1 DSM
Entries 5376516
sumAB
DSM board

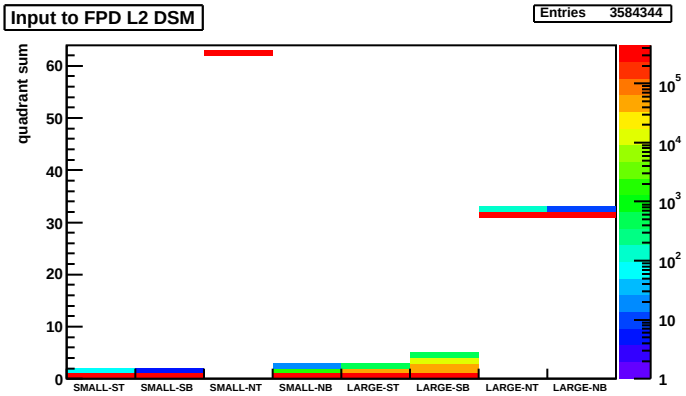

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

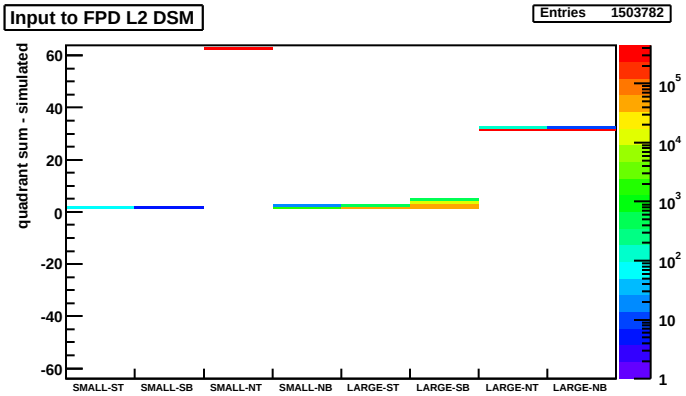

Input to FMS L1 DSM
Entries 5376516
sumA
DSM board

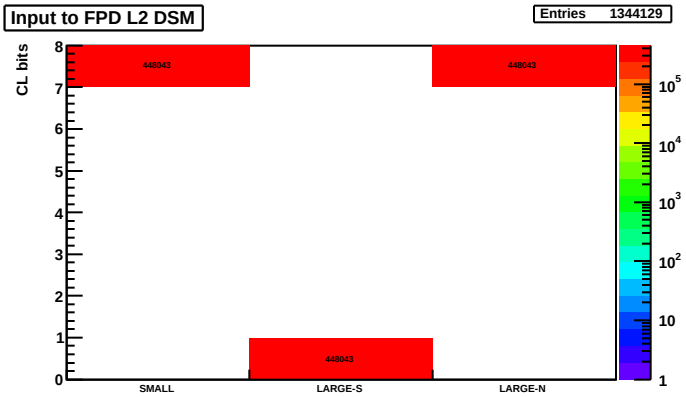

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

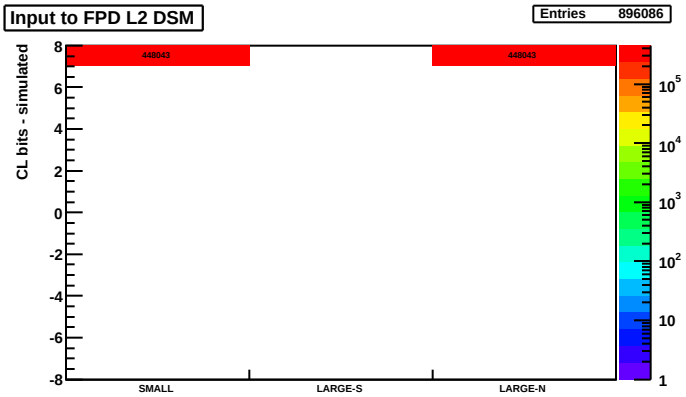

Input to FMS L1 DSM
Entries 5376516
FMS L1 HT bits
DSM board

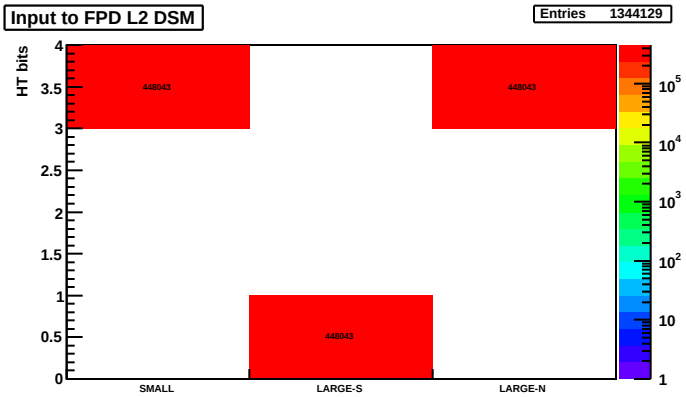

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

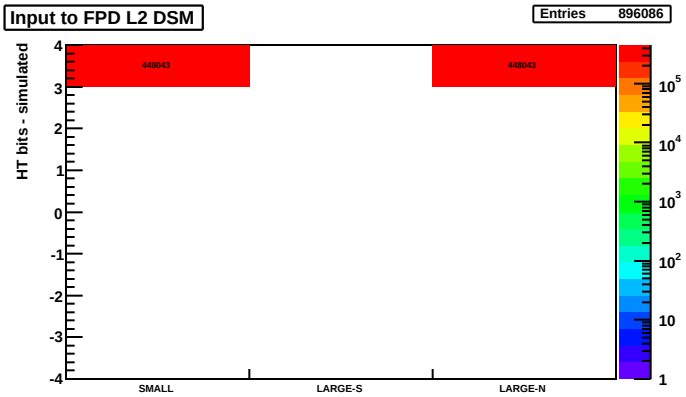

Input to FPD L2 DSM
Entries 3584344
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

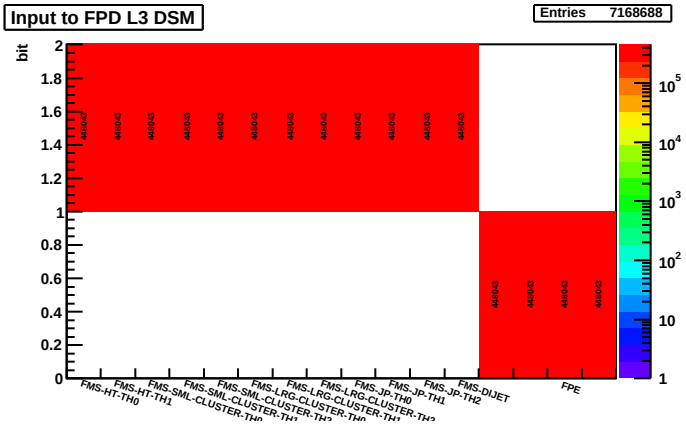

Input to FPD L2 DSM
Entries 1503782
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

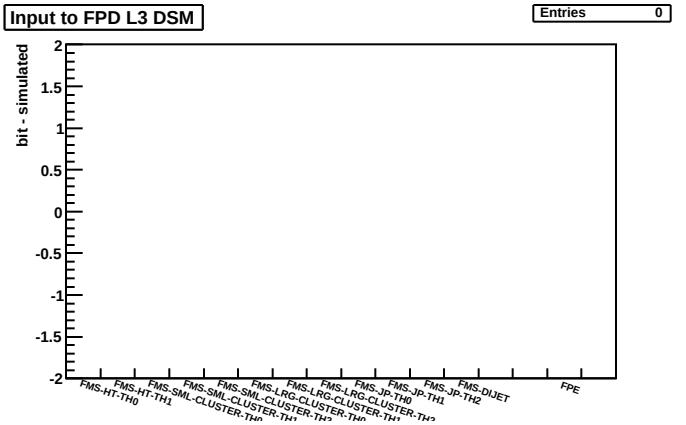

Input to FPD L2 DSM
Entries 1344129
CL bits
448043
SMALL
LARGE-S
LARGE-N

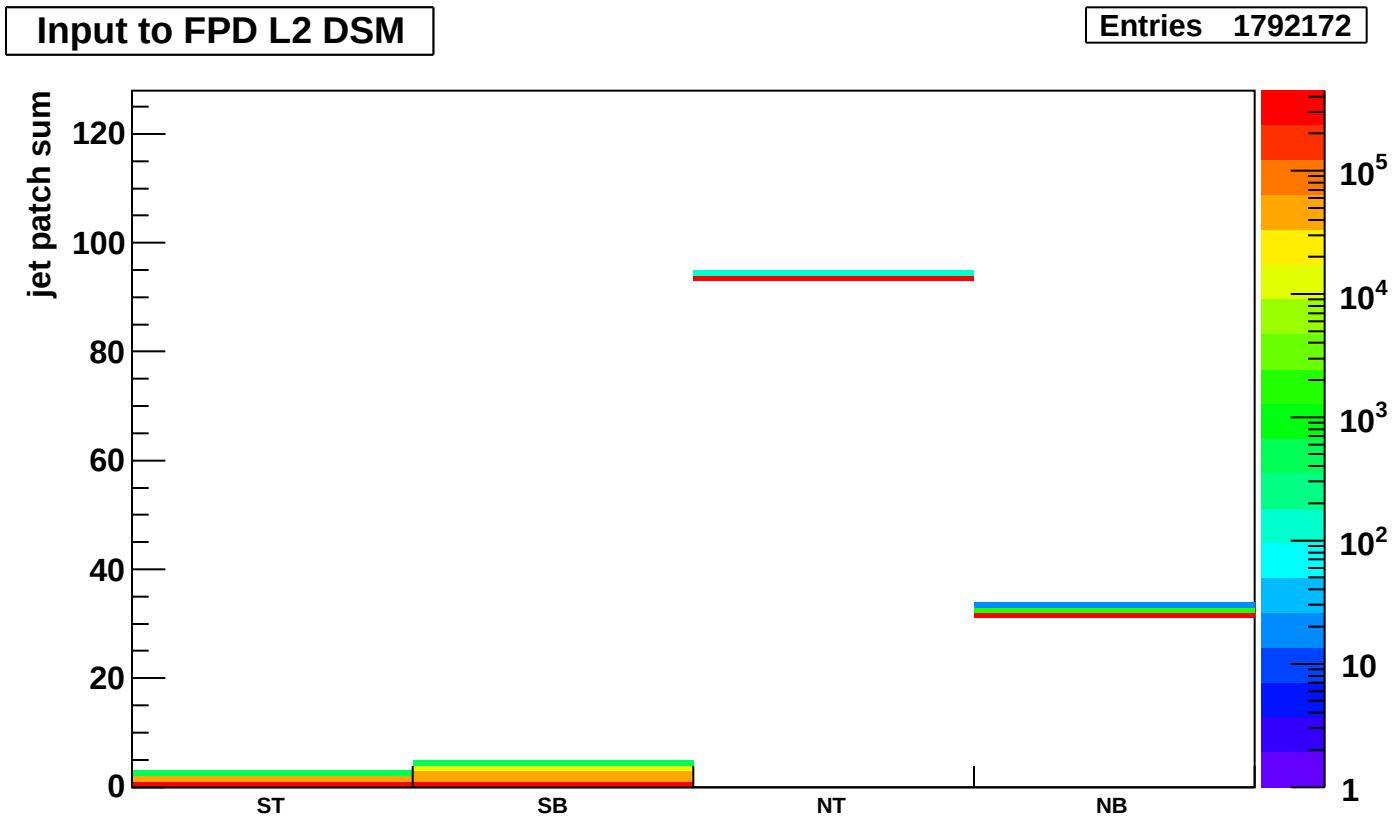

Input to FPD L2 DSM
Entries 896086
CL bits - simulated
448043
SMALL
LARGE-S
LARGE-N

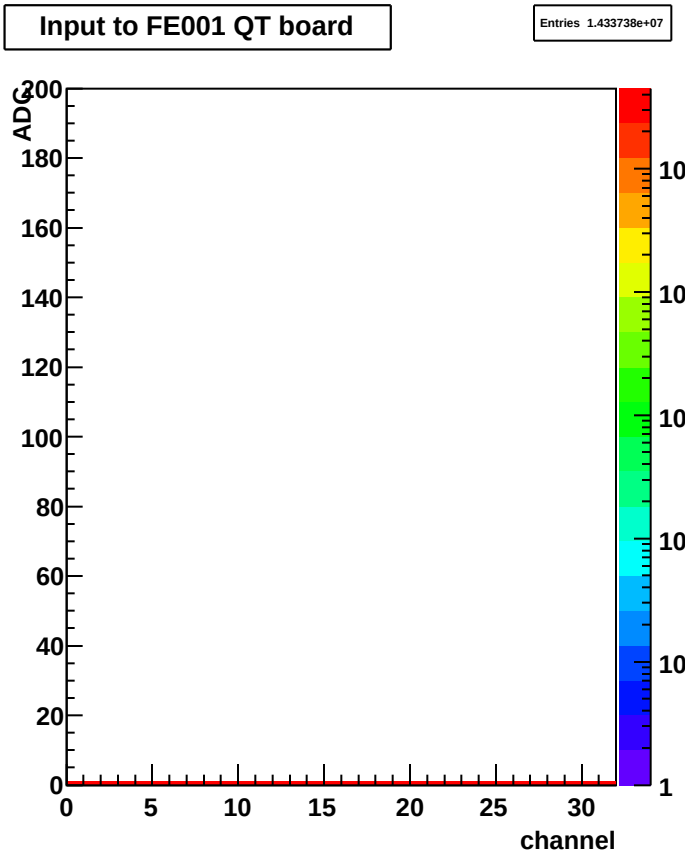

Input to FPD L2 DSM
Entries 1344129
HT bits
448043
SMALL
LARGE-S
LARGE-N

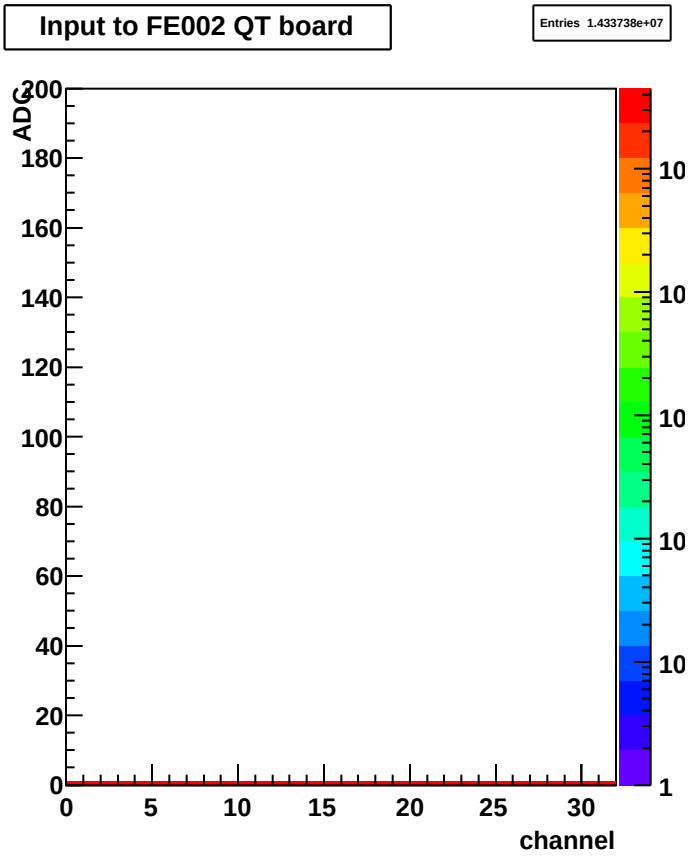

Input to FPD L2 DSM
Entries 896086
HT bits - simulated
448043
SMALL
LARGE-S
LARGE-N

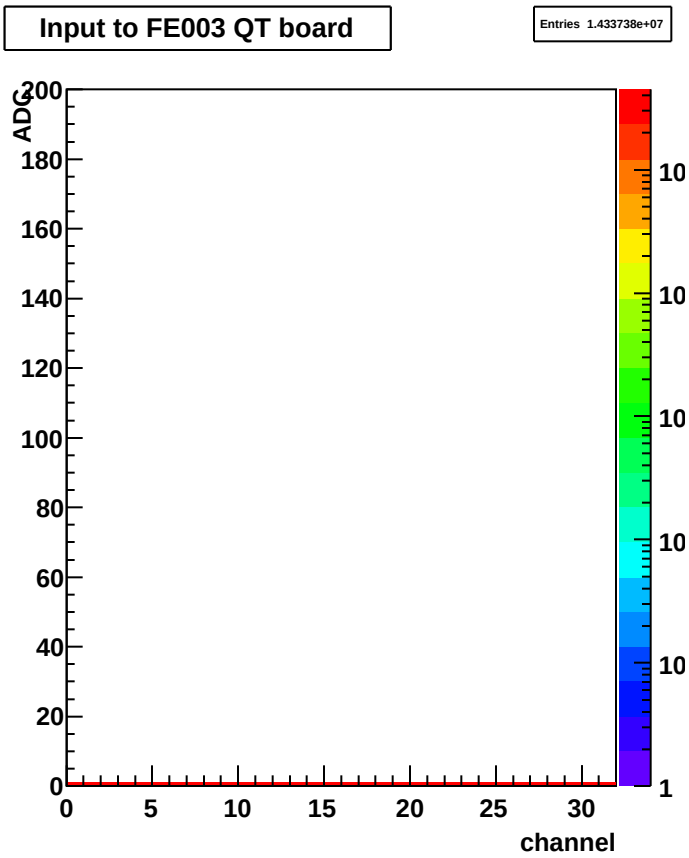

Input to FPD L3 DSM
Entries 7168688
bit
448043
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

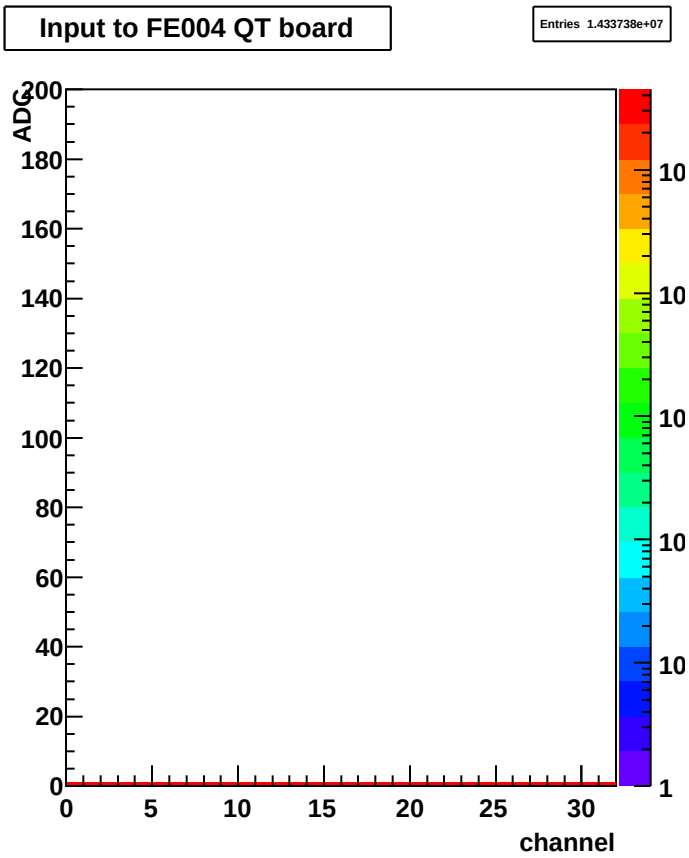

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

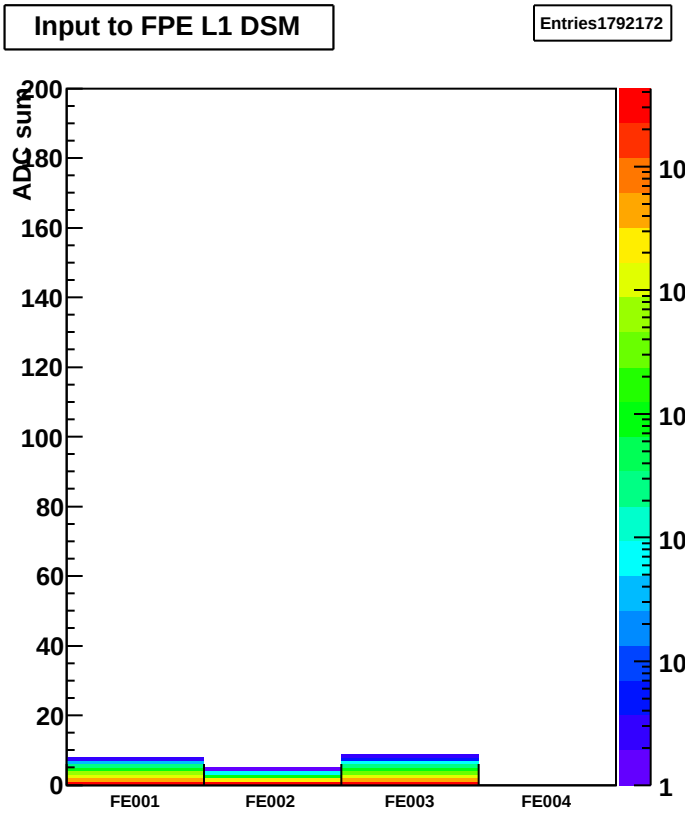

Input to FPD L2 DSM
Entries 1792172
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

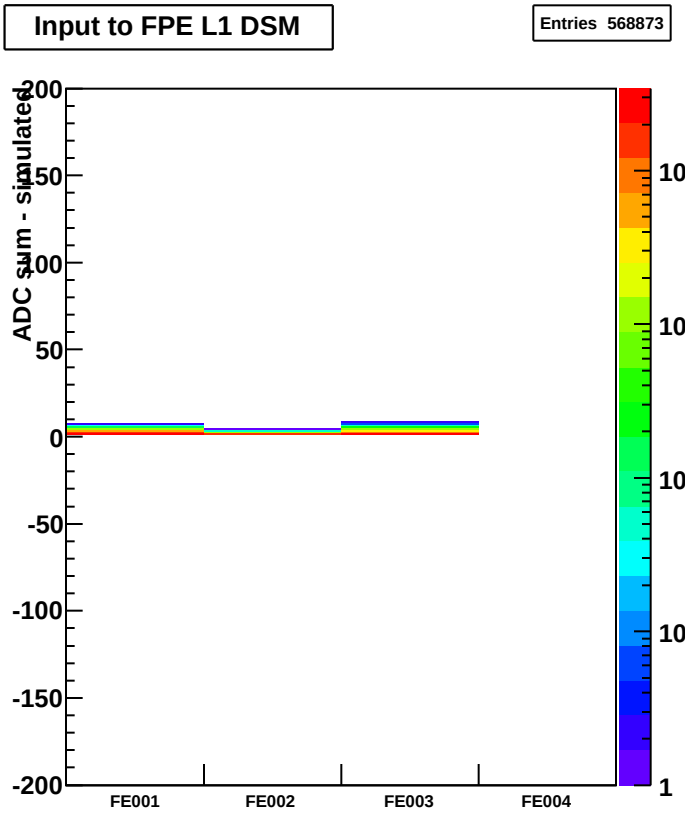

Input to FE001 QT board
Entries 1.433738e+07
ADC
channel

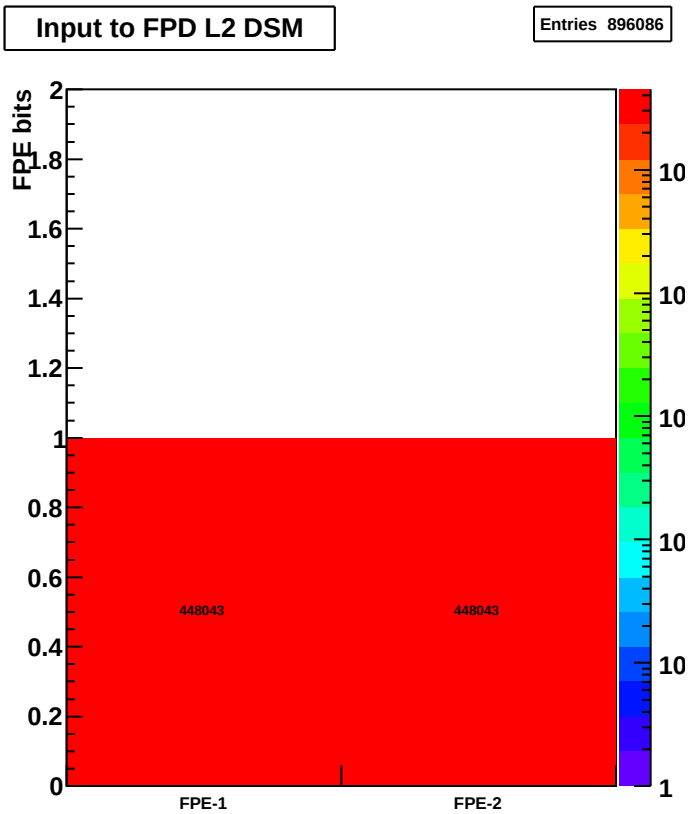

Input to FE002 QT board
Entries 1.433738e+07
ADC
channel

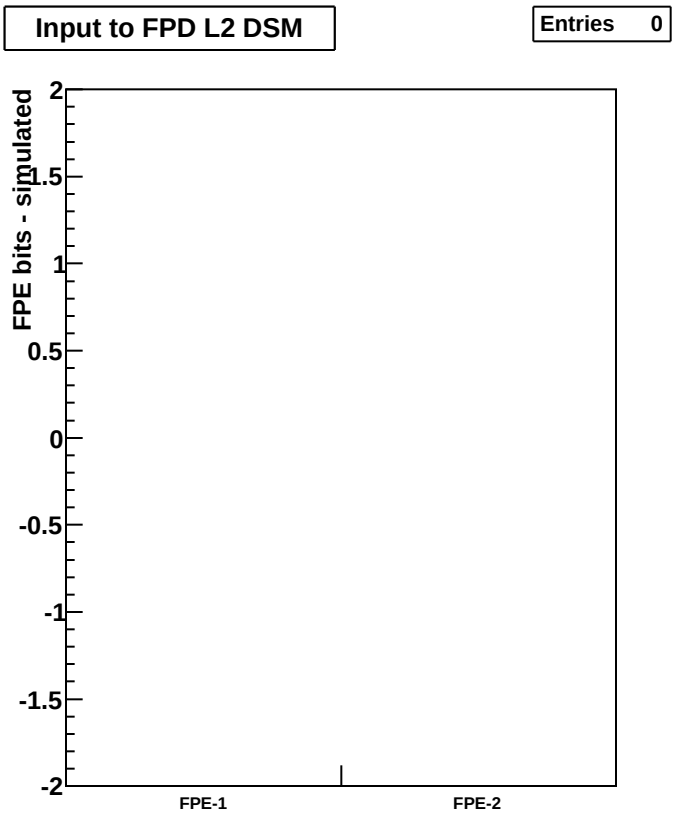

Input to FE003 QT board
Entries 1.433738e+07
ADC
channel


Input to FE004 QT board
Entries 1.433738e+07
ADC
channel


Input to FPE L1 DSM
Entries1792172
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 568873
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 896086
FPE bits
448043
448043
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2