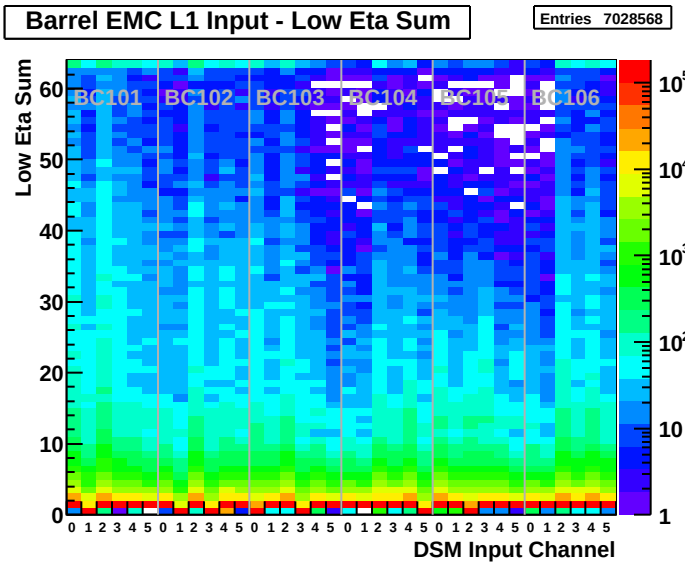
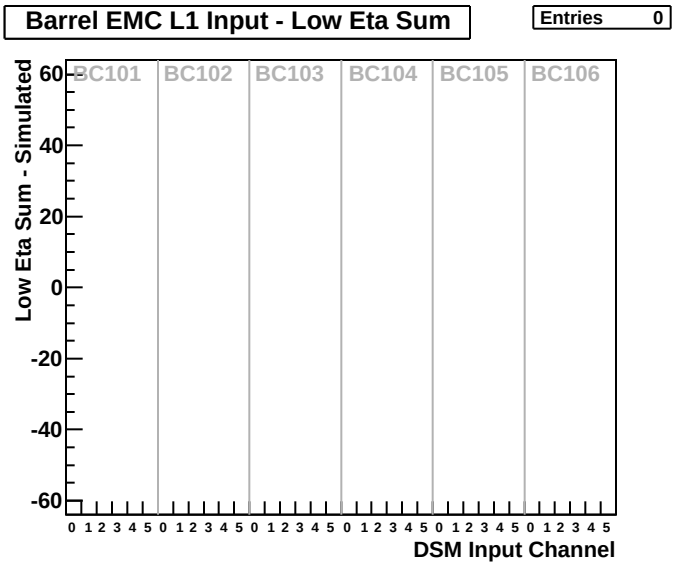
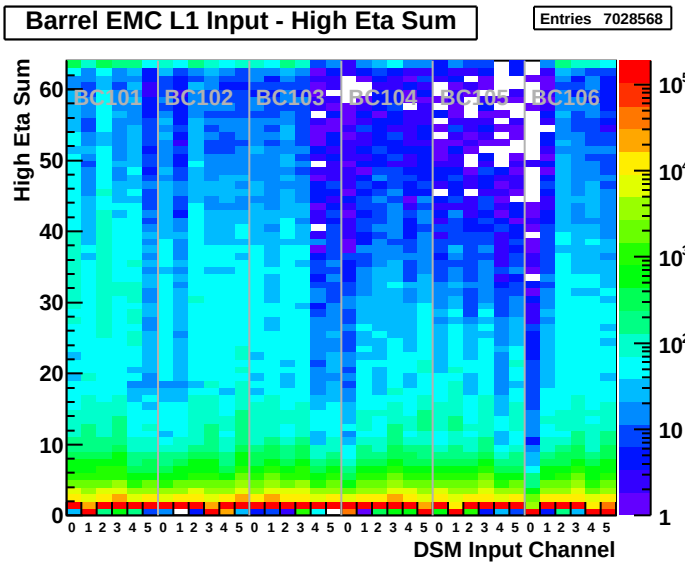
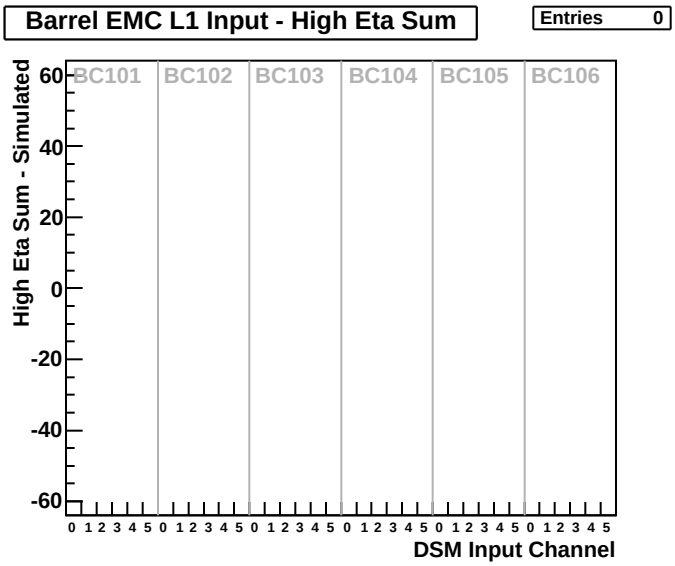
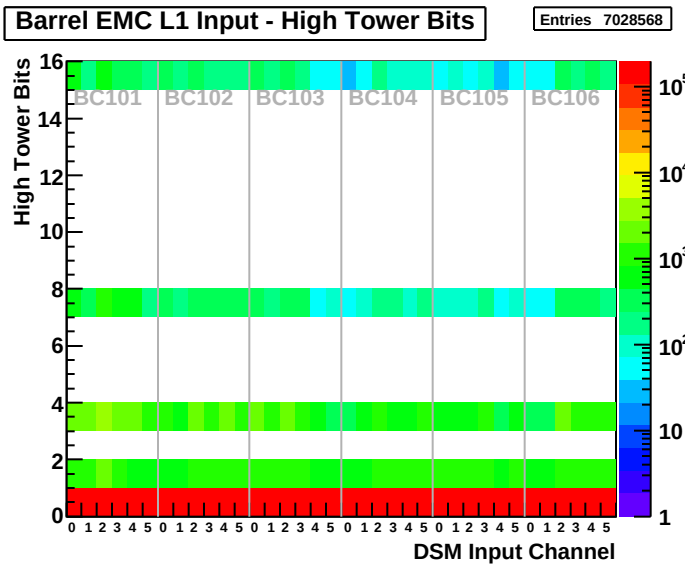
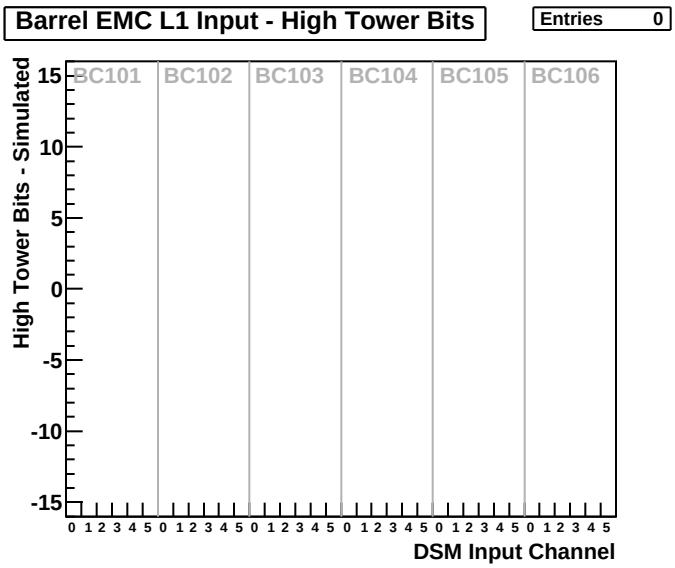

Barrel EMC L1 Input - Low Eta Sum
Entries 7028568
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

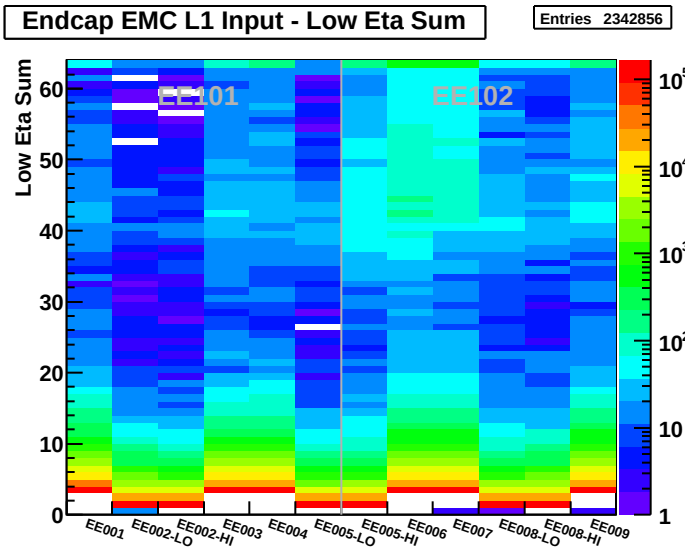

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

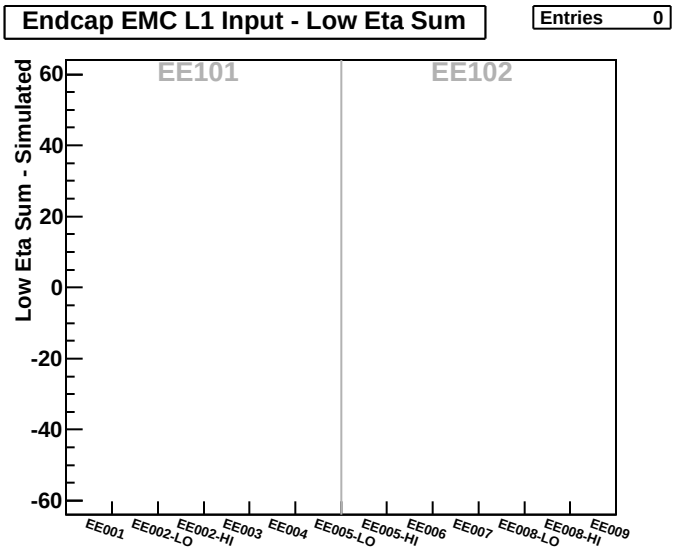

Barrel EMC L1 Input - High Eta Sum
Entries 7028568
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

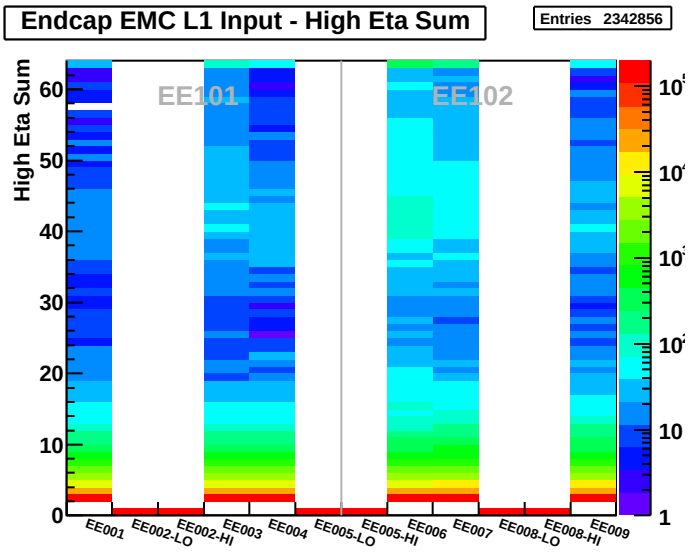

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

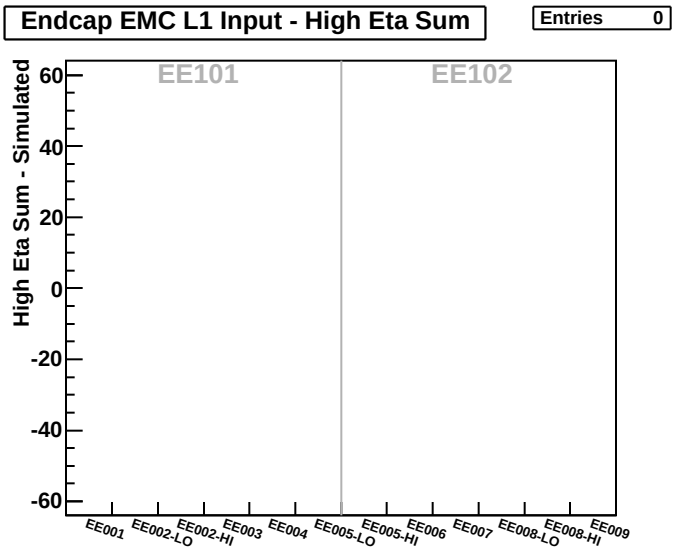

Barrel EMC L1 Input - High Tower Bits
Entries 7028568
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

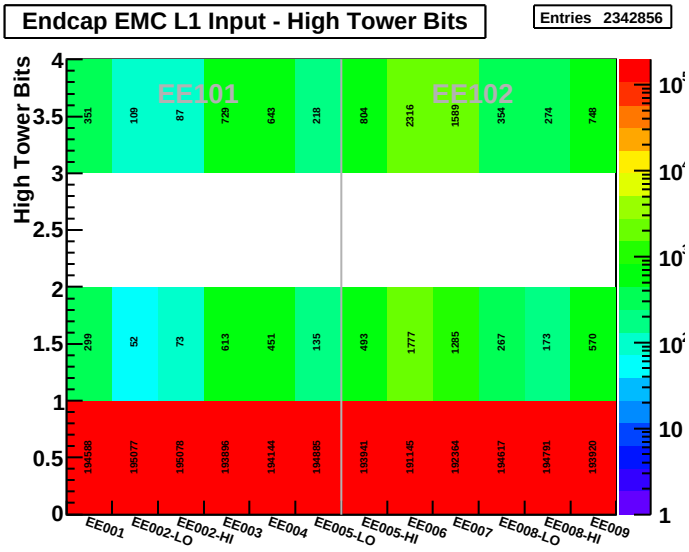

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

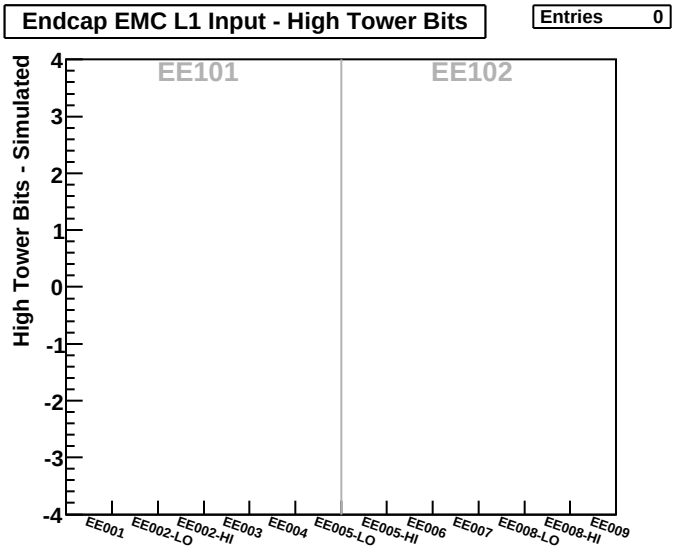

Endcap EMC L1 Input - Low Eta Sum
Entries 2342856
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

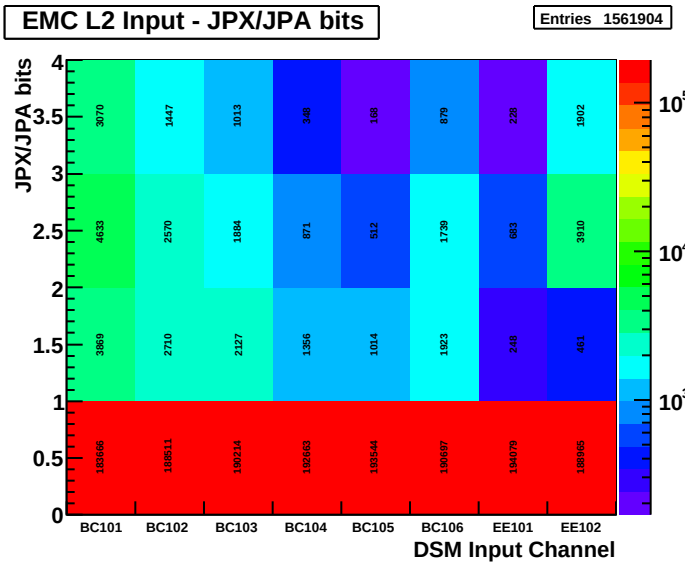

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

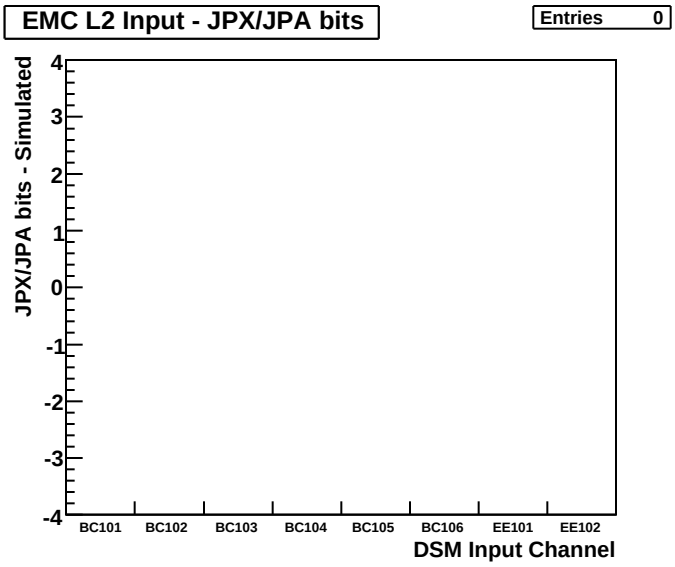

Endcap EMC L1 Input - High Eta Sum
Entries 2342856
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

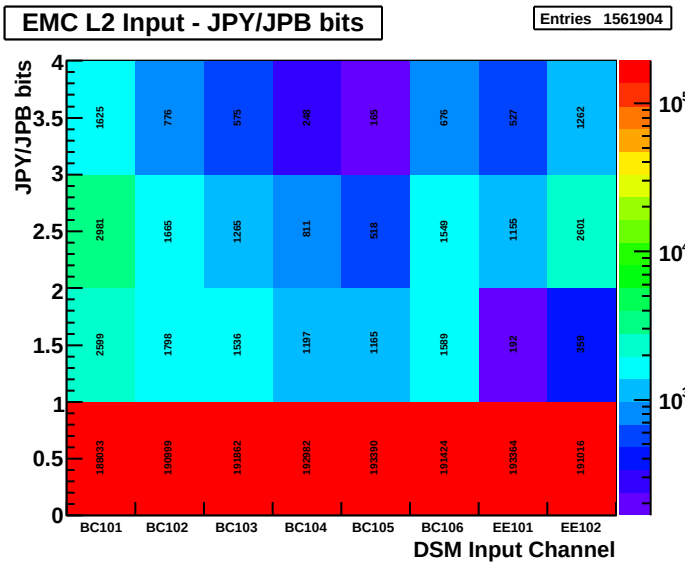

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

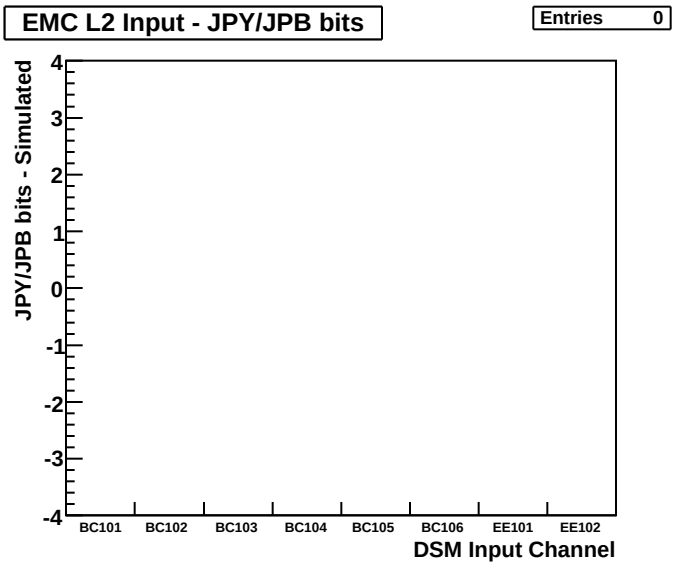

Endcap EMC L1 Input - High Tower Bits
Entries 2342856
High Tower Bits
EE101
EE102
351 109 87 729 643 218 804 2316 1589 354 274 748
299 52 73 613 451 135 493 1777 1285 267 173 570
194588 195077 195078 193396 194144 194885 193941 191145 192364 194617 194791 193920
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

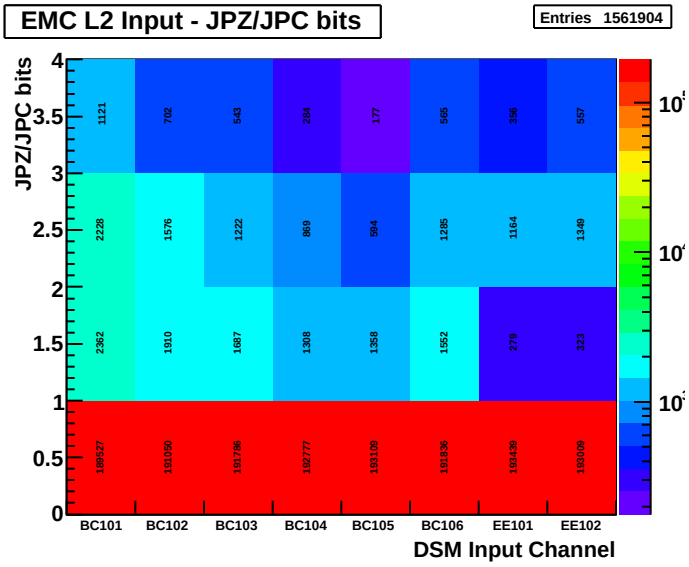

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

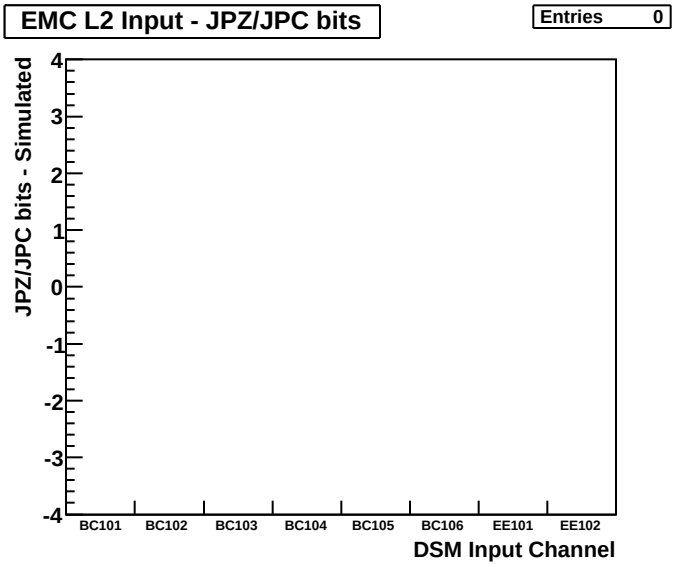

EMC L2 Input - JPX/JPA bits
Entries 1561904
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3070 1447 1013 348 168 879 228 1902
4633 2570 1884 871 512 1739 683 3910
3869 2710 2127 1356 1014 1923 248 461
183666 188511 190214 192663 193544 190697 194079 188965

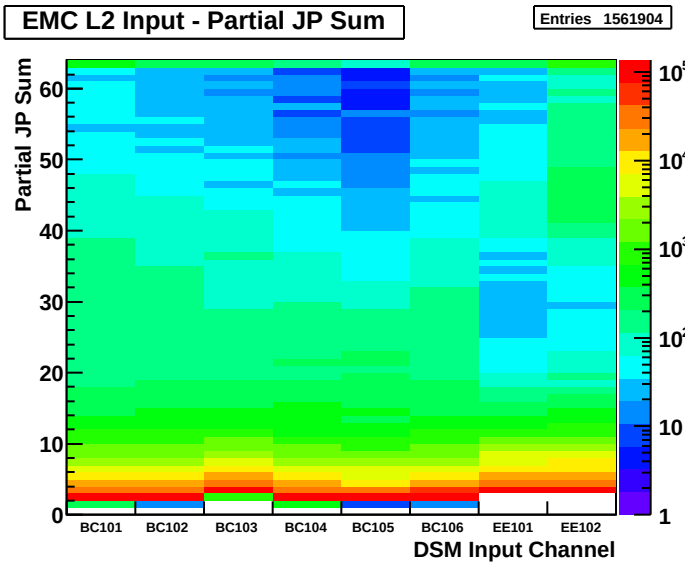

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

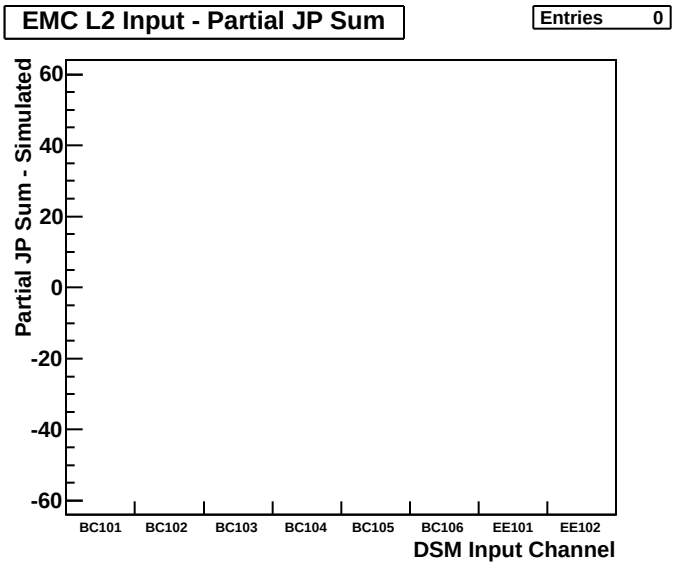

EMC L2 Input - JPY/JPB bits
Entries 1561904
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1625 776 575 248 165 676 527 1262
2981 1665 1265 811 518 1549 1155 2601
2999 1798 1536 1197 1165 1989 192 359
188033 190999 191862 192982 193390 191424 193364 191016

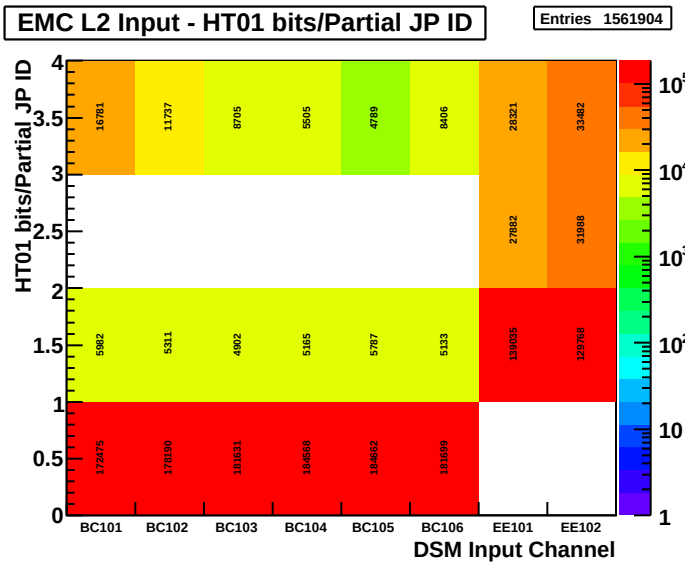

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

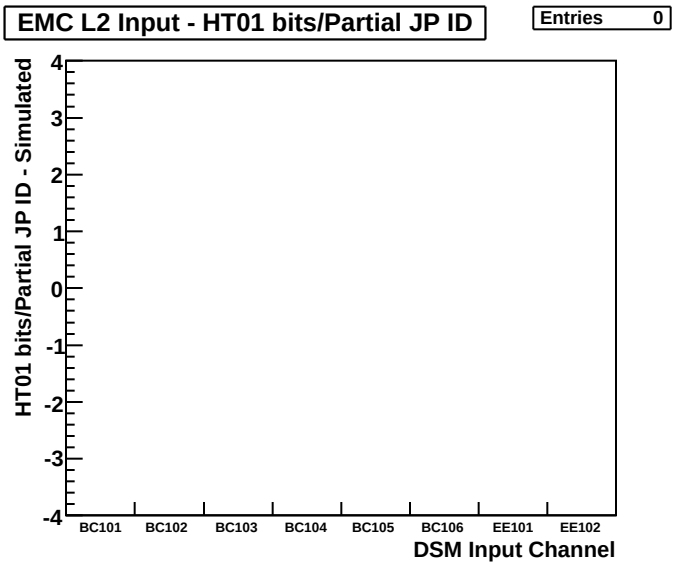

EMC L2 Input - JPZ/JPC bits
Entries 1561904
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1121 702 543 284 177 565 356 557
2228 1576 1222 869 594 1285 1164 1349
2362 1910 1687 1308 1358 1552 279 323
189527 191050 191786 192777 193109 191838 193439 193009

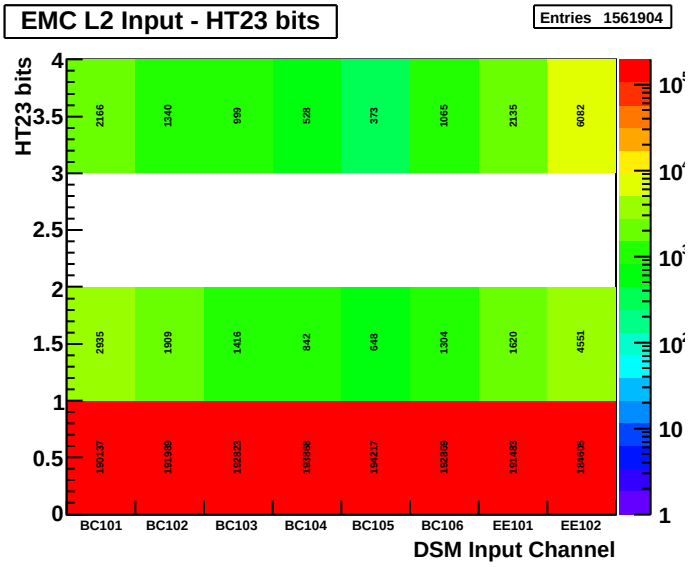

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

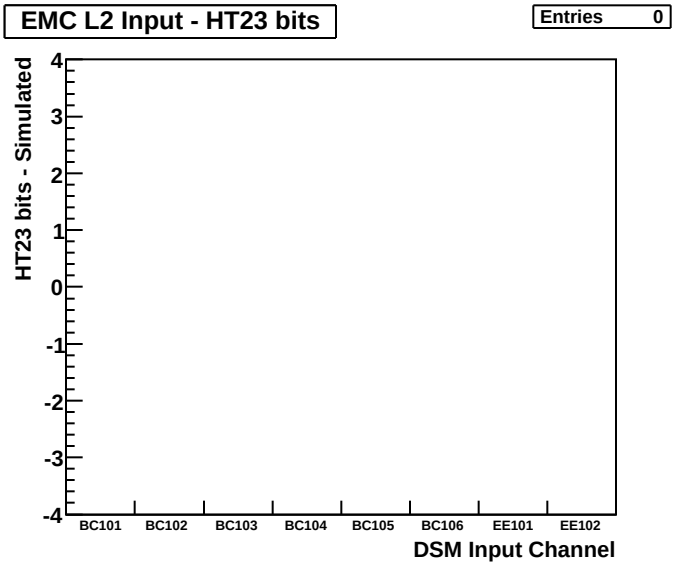

EMC L2 Input - Partial JP Sum
Entries 1561904
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

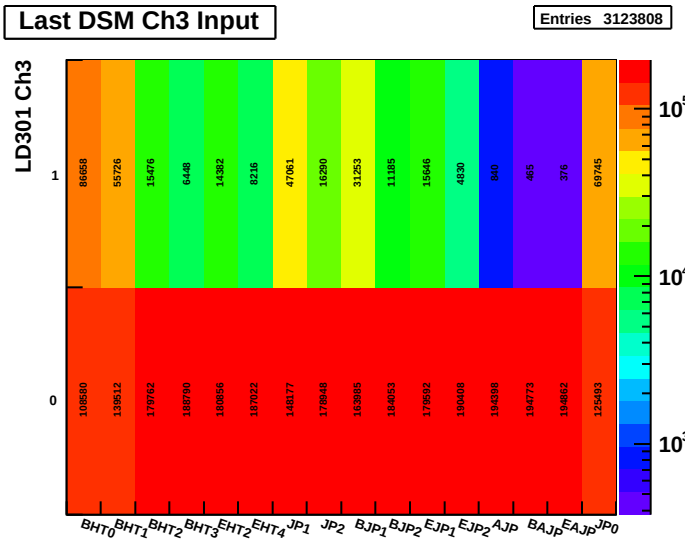

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

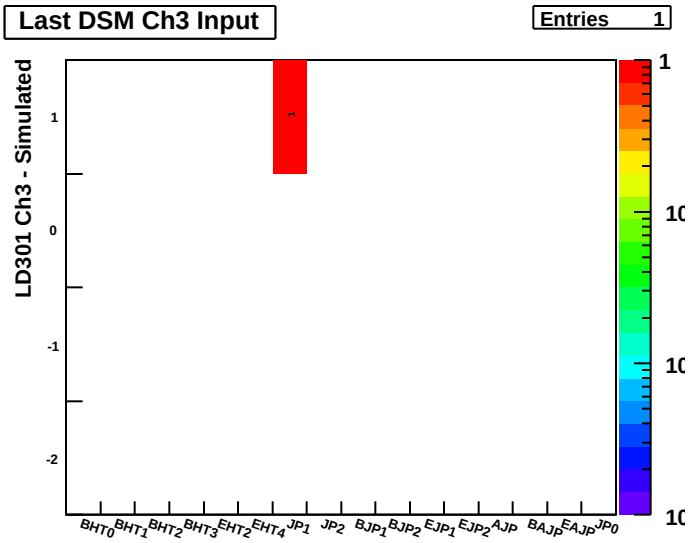

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1561904
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

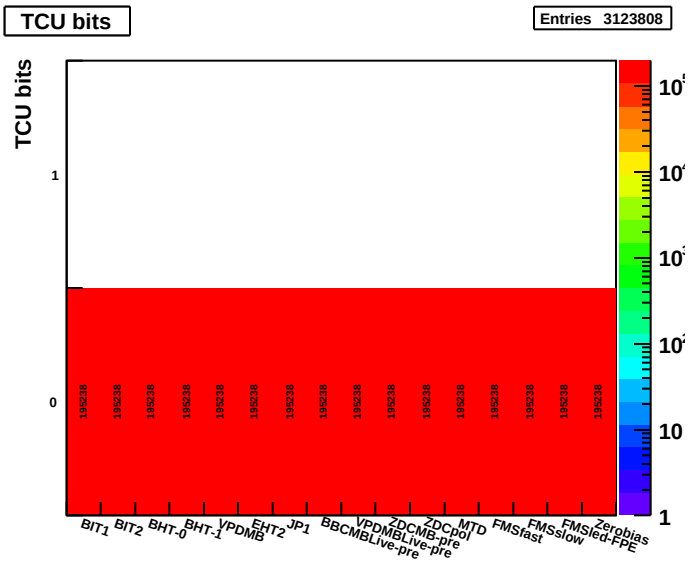

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

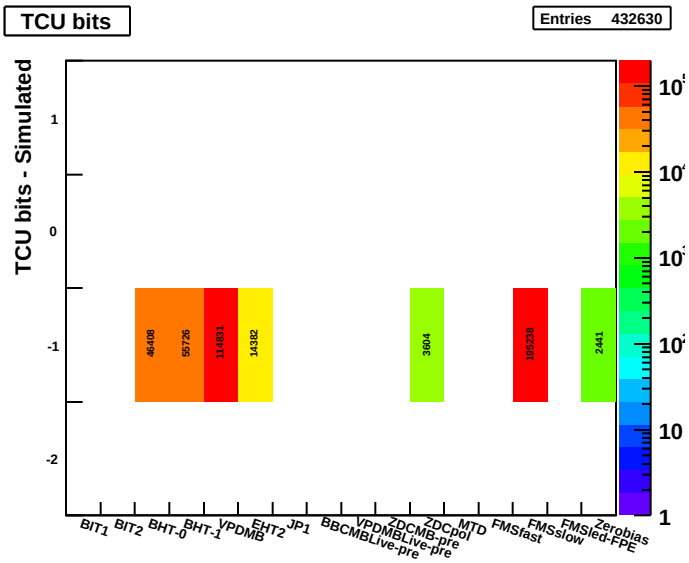

EMC L2 Input - HT23 bits
Entries 1561904
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

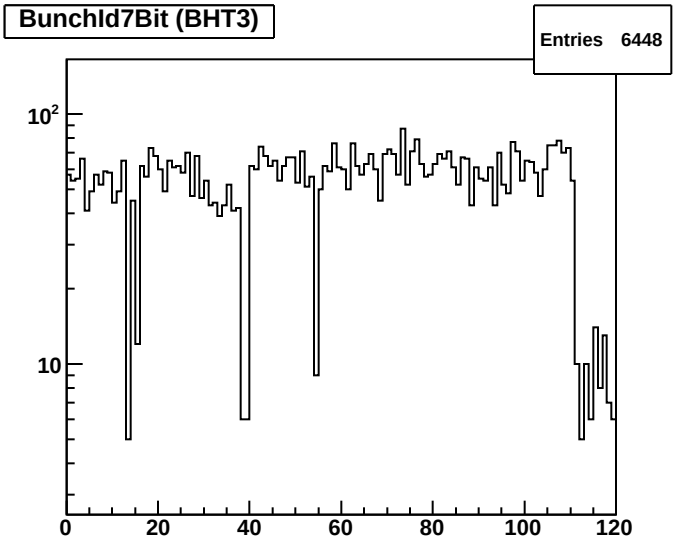

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

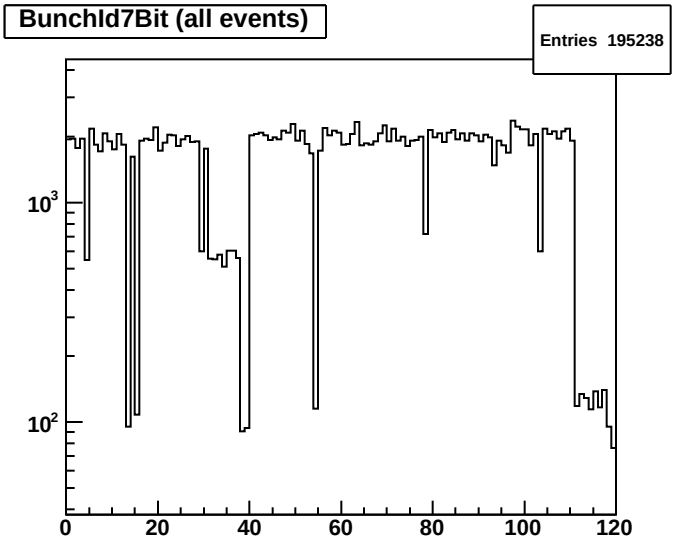

Last DSM Ch3 Input
Entries 3123808
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

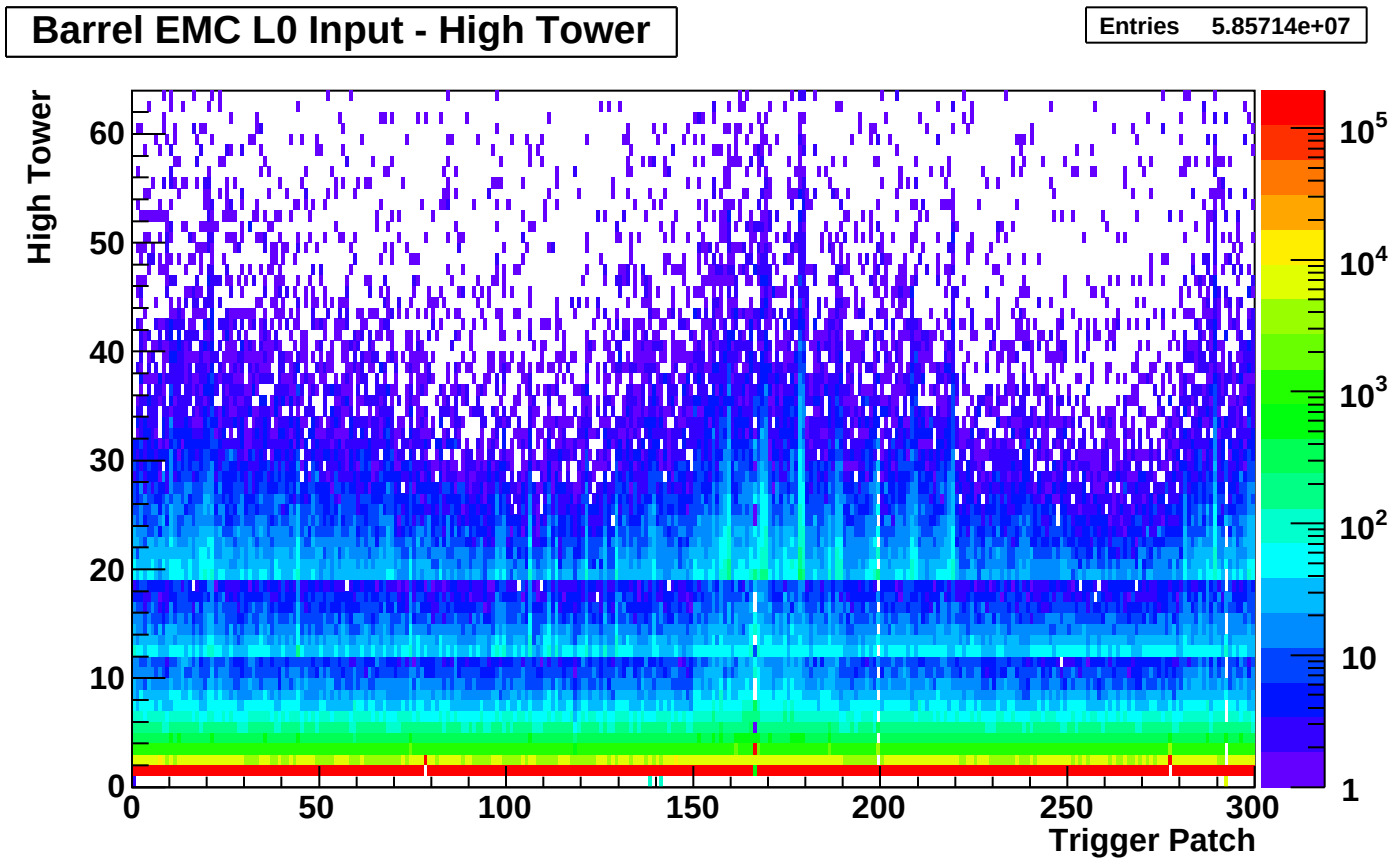

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

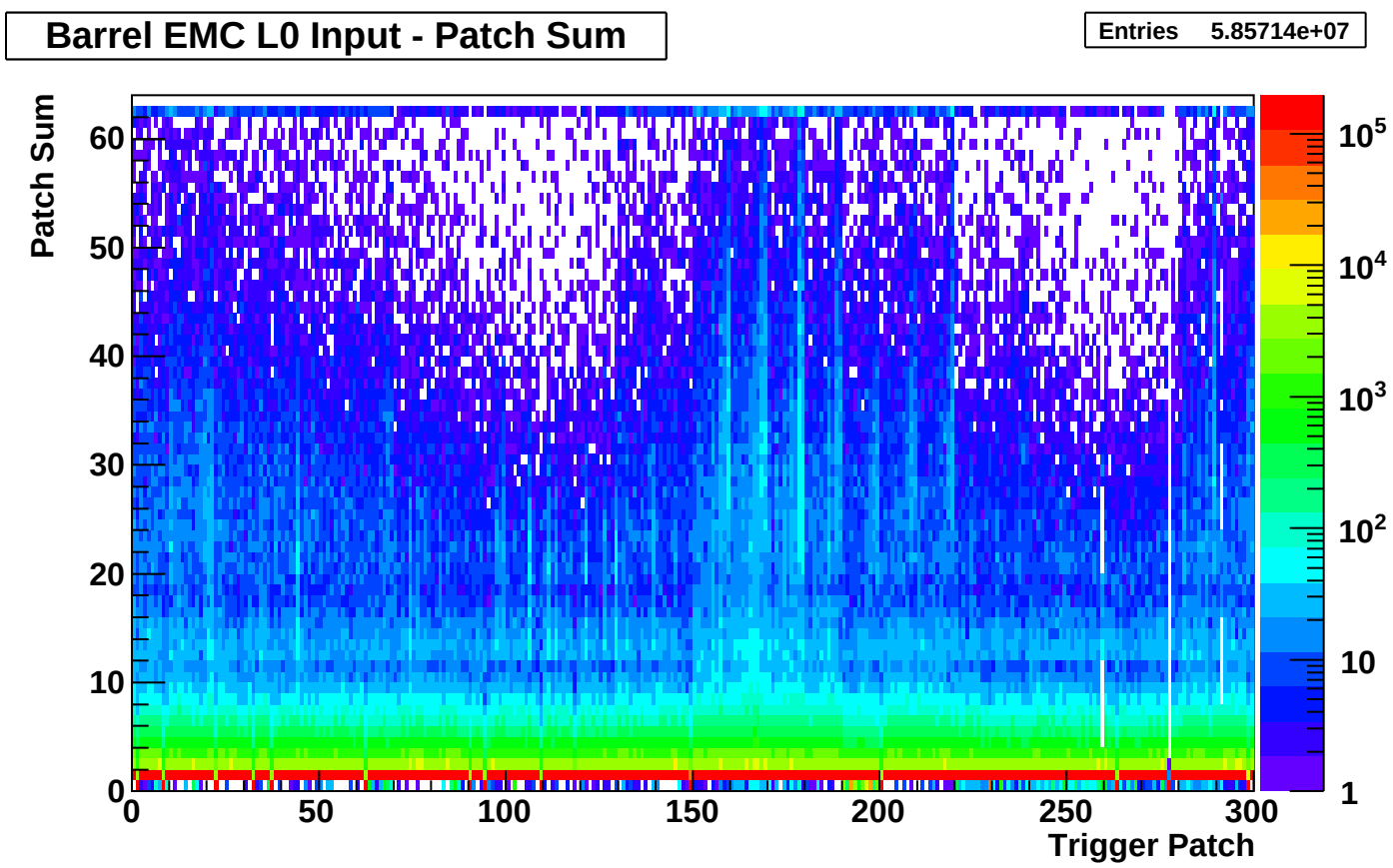

TCU bits
Entries 3123808
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

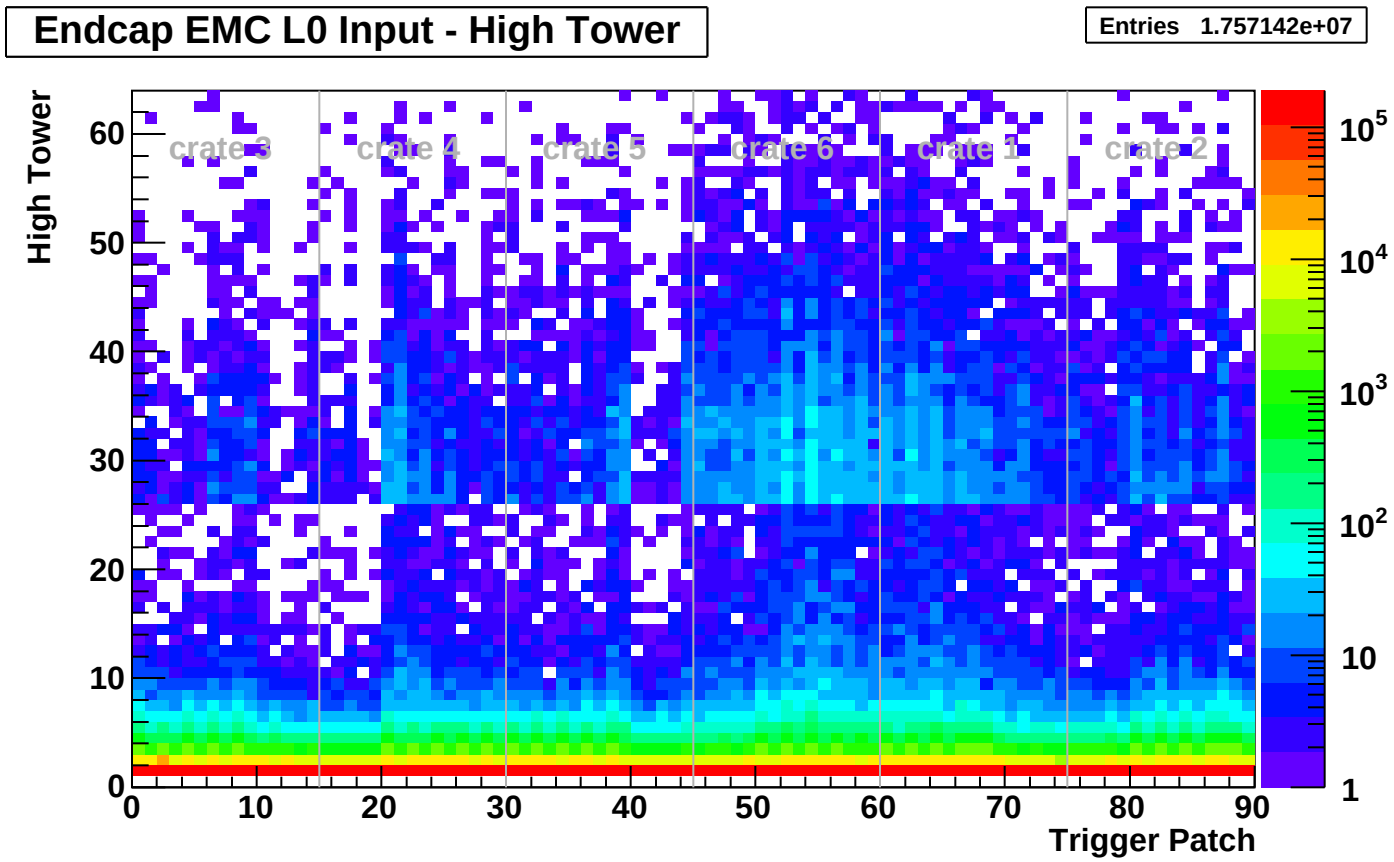

TCU bits
Entries 432630
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

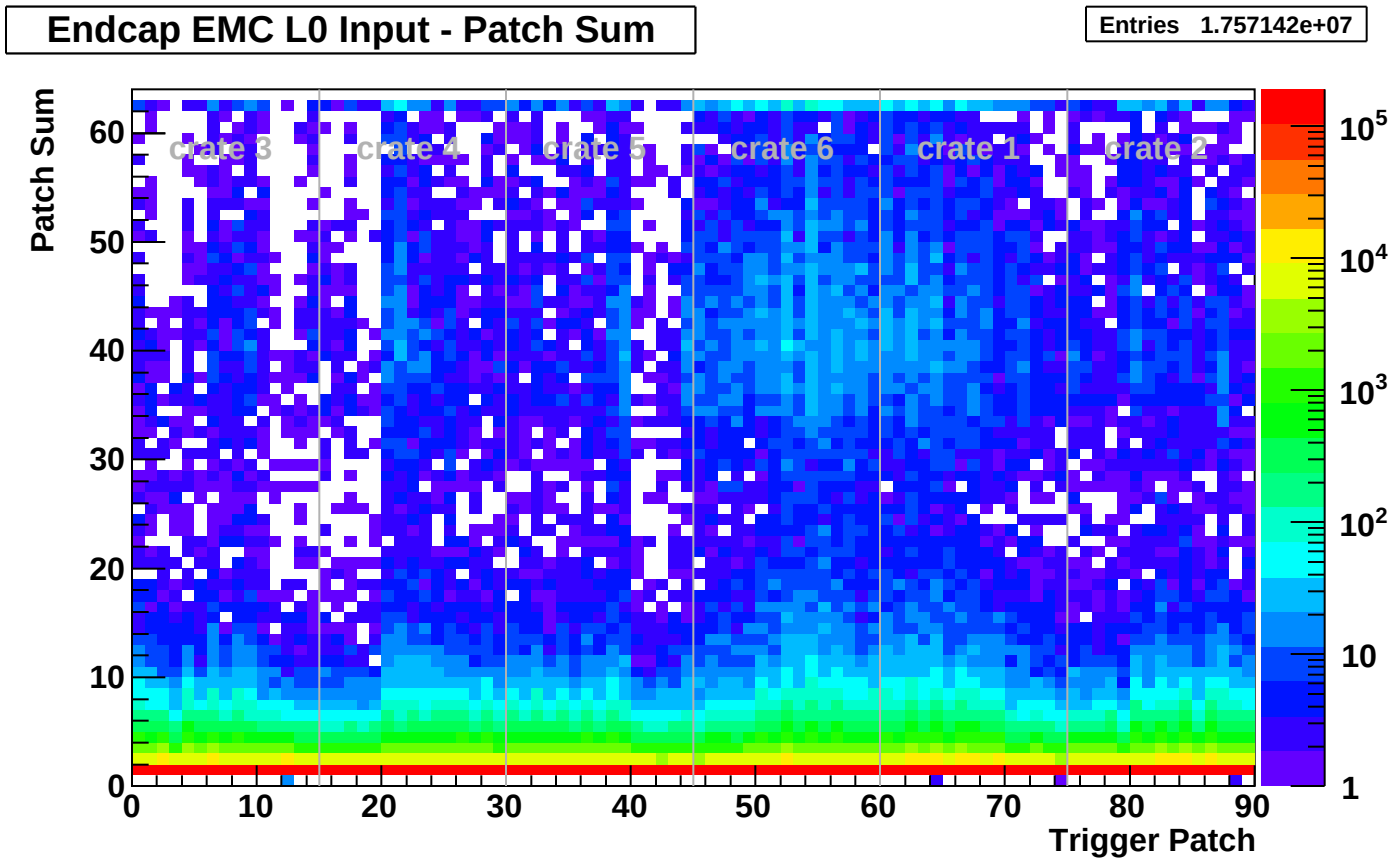

BunchId7Bit (BHT3)
Entries 6448

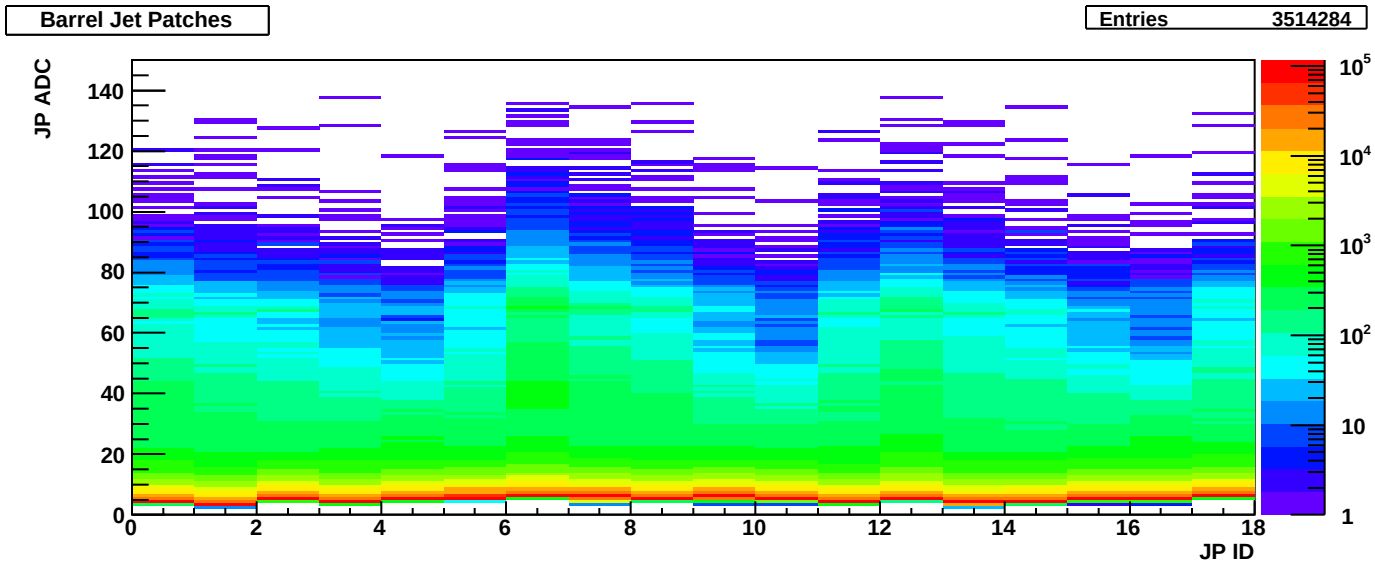

BunchId7Bit (all events)
Entries 195238

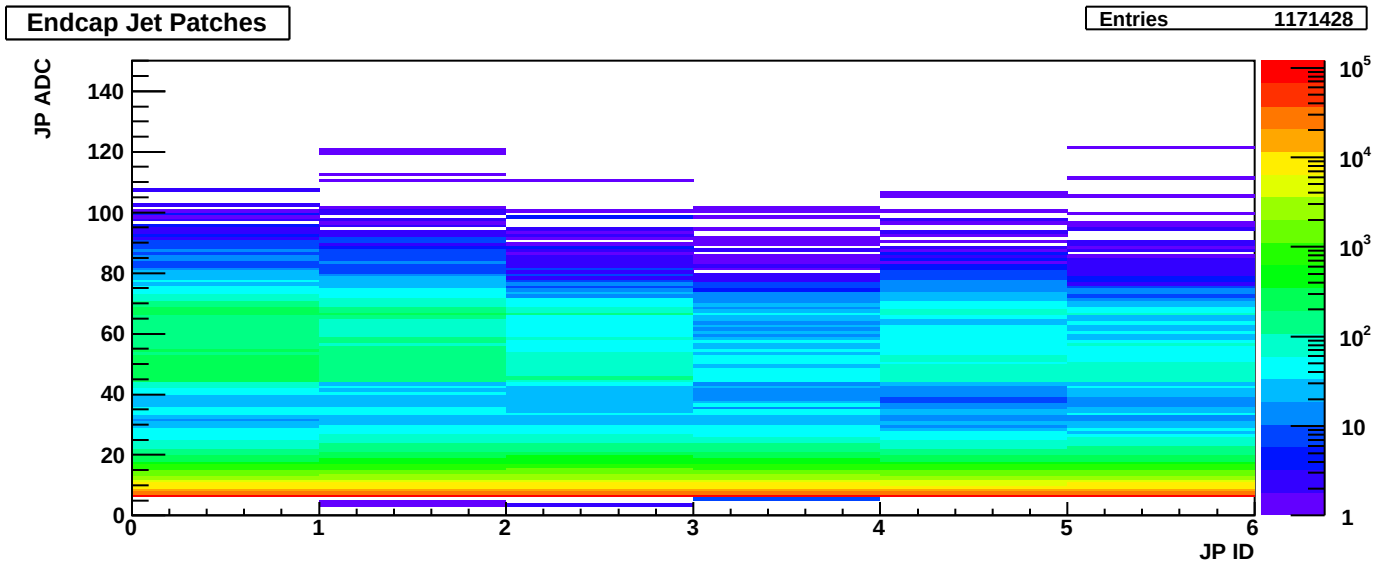

Barrel EMC L0 Input - High Tower
Entries 5.85714e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

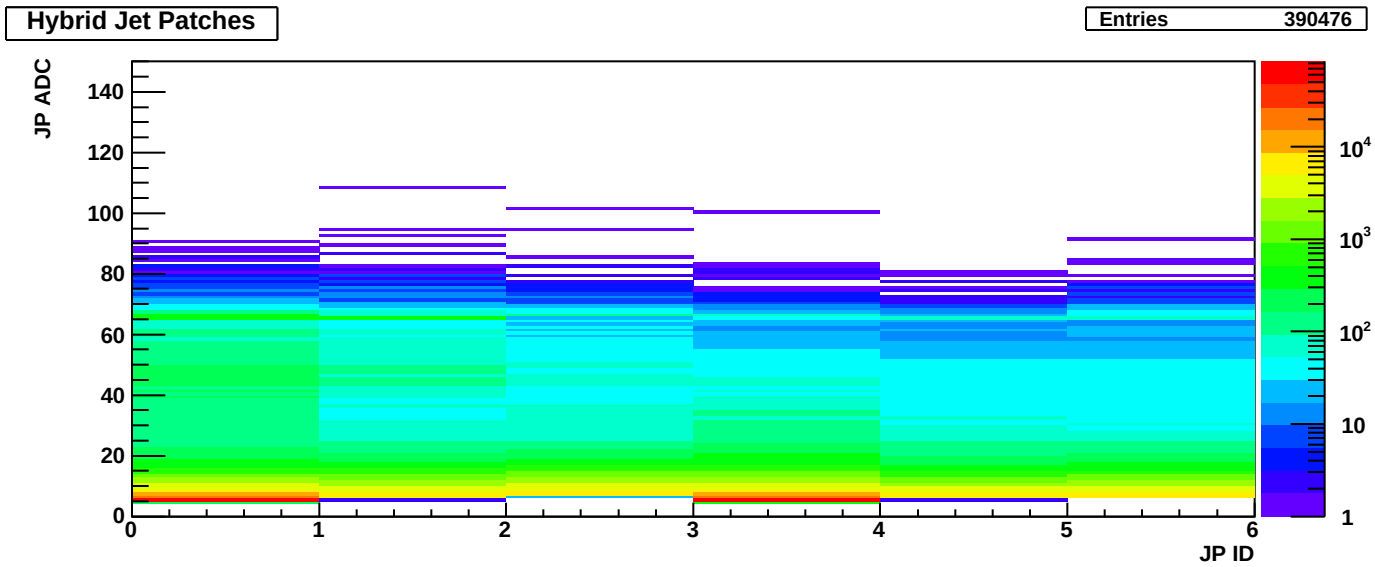

Barrel EMC L0 Input - Patch Sum
Entries 5.85714e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

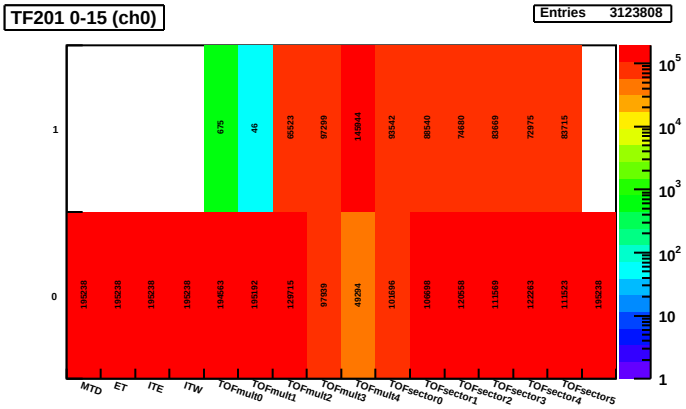

Endcap EMC L0 Input - High Tower
Entries 1.757142e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

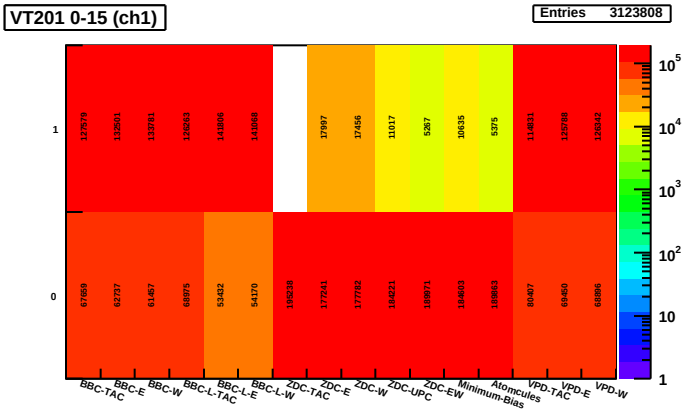

Endcap EMC L0 Input - Patch Sum
Entries 1.757142e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

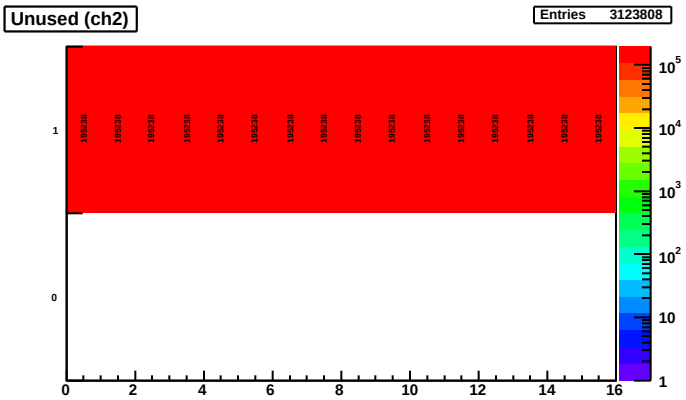

Barrel Jet Patches
Entries 3514284
JP ADC
JP ID

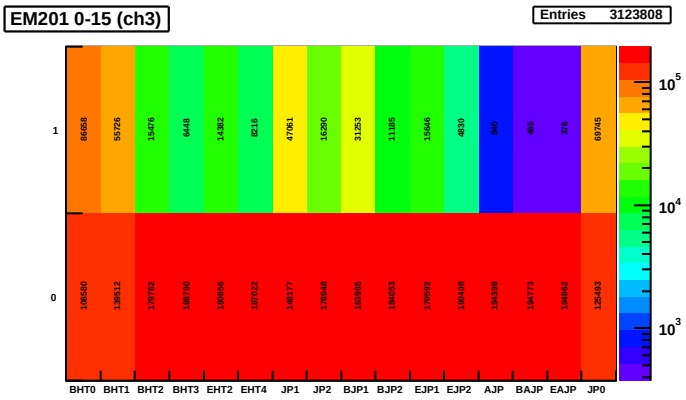

Endcap Jet Patches
Entries 1171428
JP ADC
JP ID

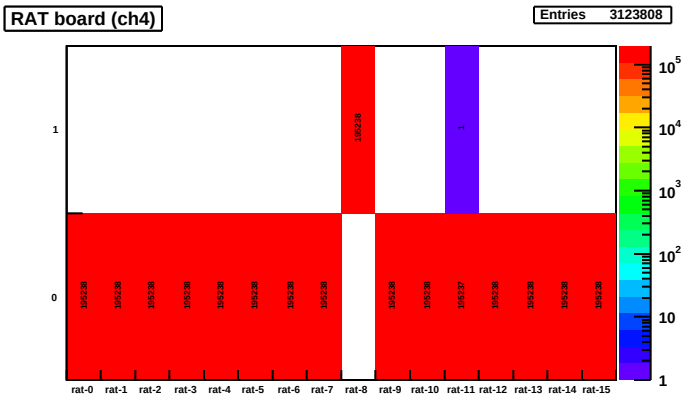

Hybrid Jet Patches
Entries 390476
JP ADC
JP ID

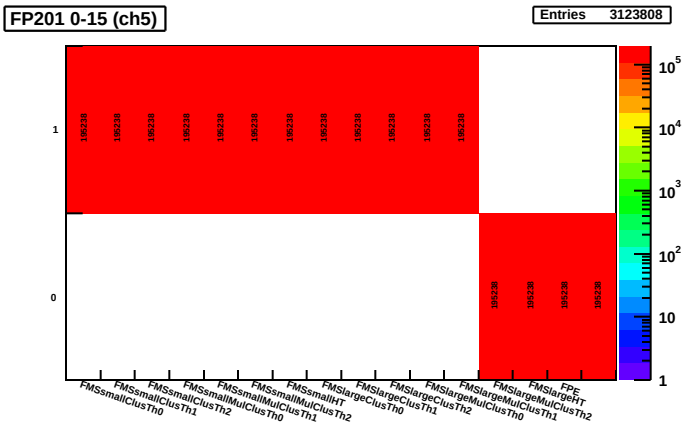

TF201 0-15 (ch0)
Entries 3123808
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

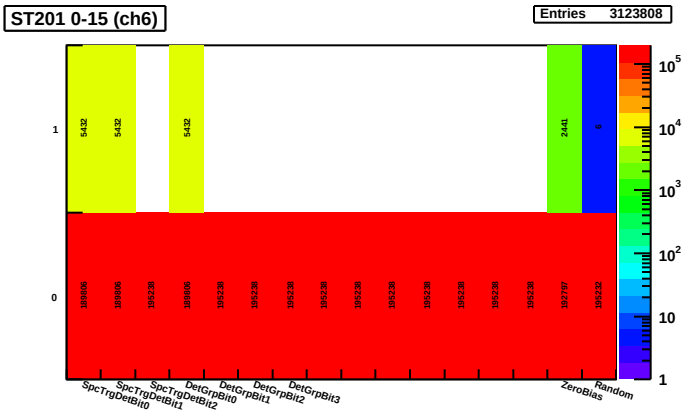

VT201 0-15 (ch1)
Entries 3123808
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

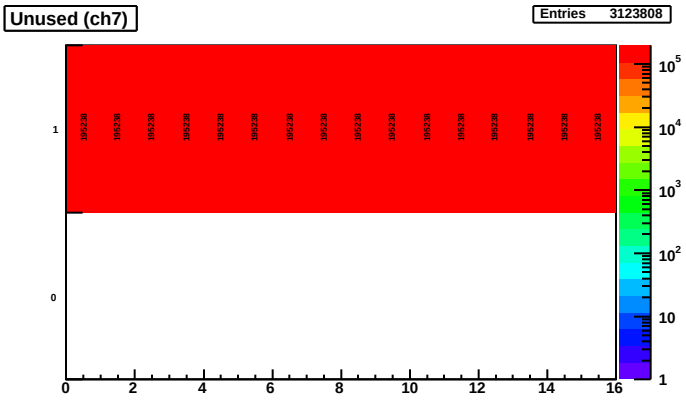

Unused (ch2)
Entries 3123808

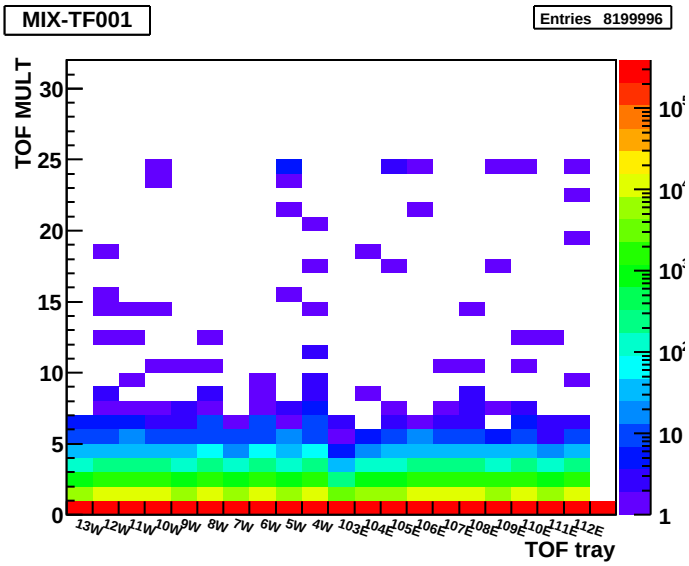

EM201 0-15 (ch3)
Entries 3123808
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

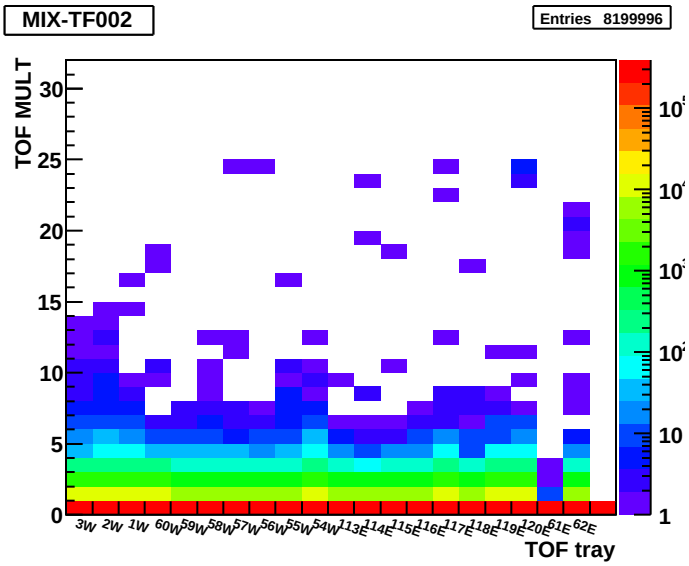

RAT board (ch4)
Entries 3123808
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

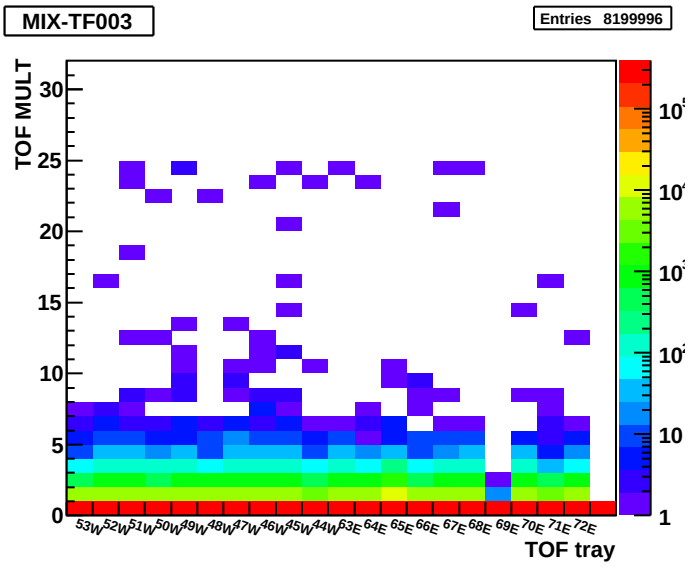

FP201 0-15 (ch5)
Entries 3123808
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

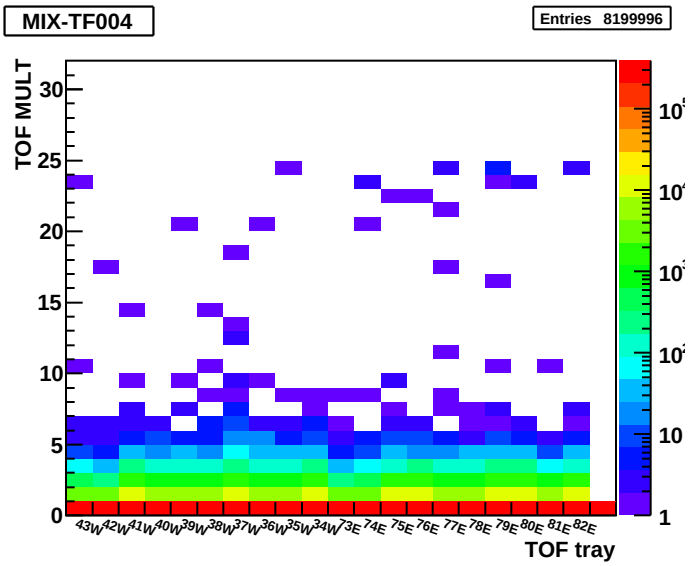

ST201 0-15 (ch6)
Entries 3123808
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

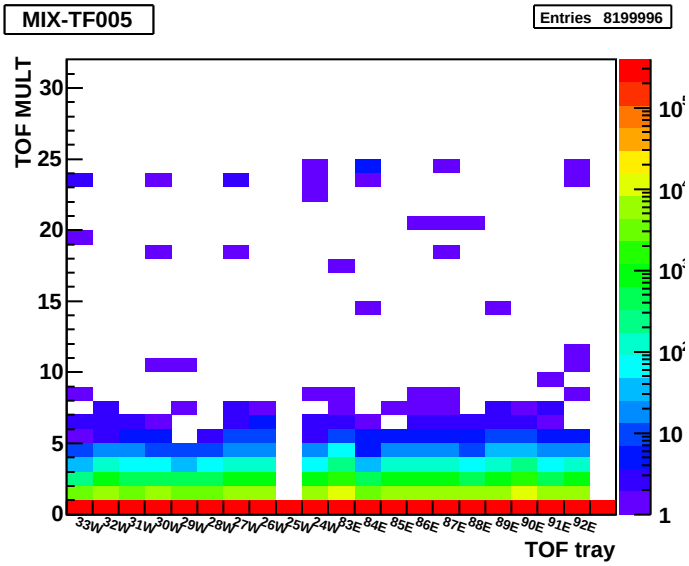

Unused (ch7)
Entries 3123808

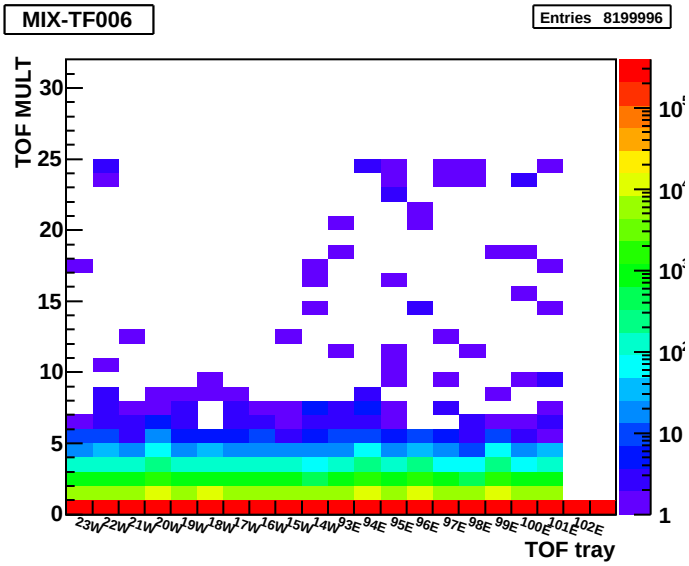

MIX-TF001
Entries 8199996
TOF MULT
TOF tray

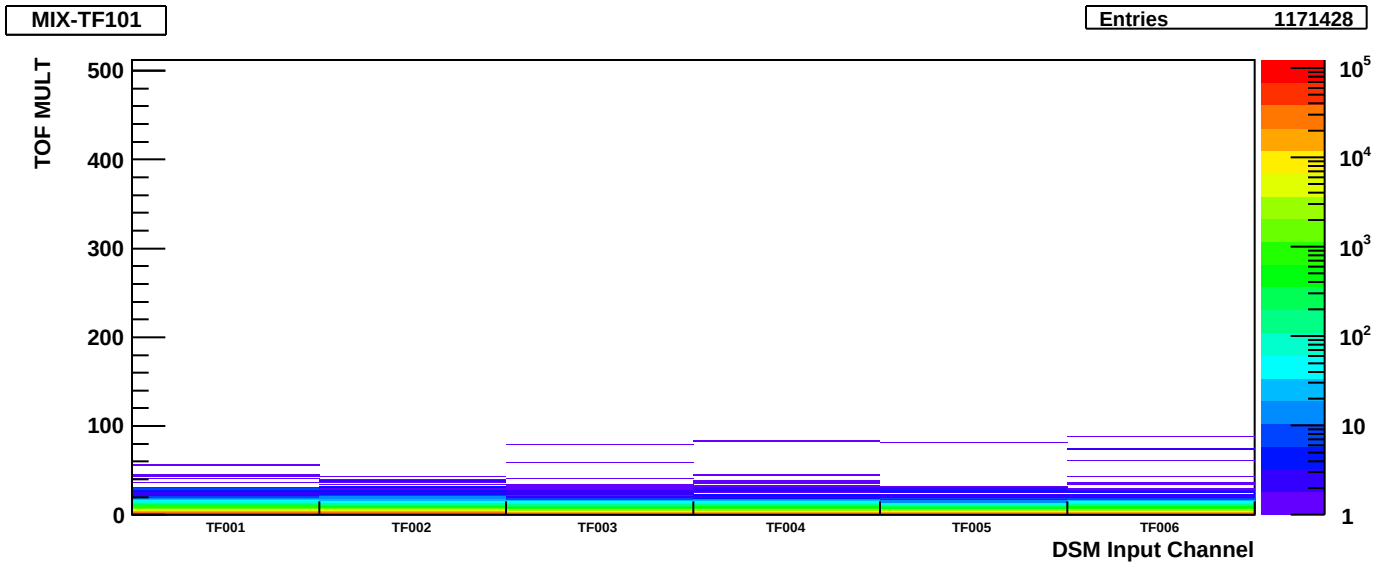

MIX-TF002
Entries 8199996
TOF MULT
TOF tray

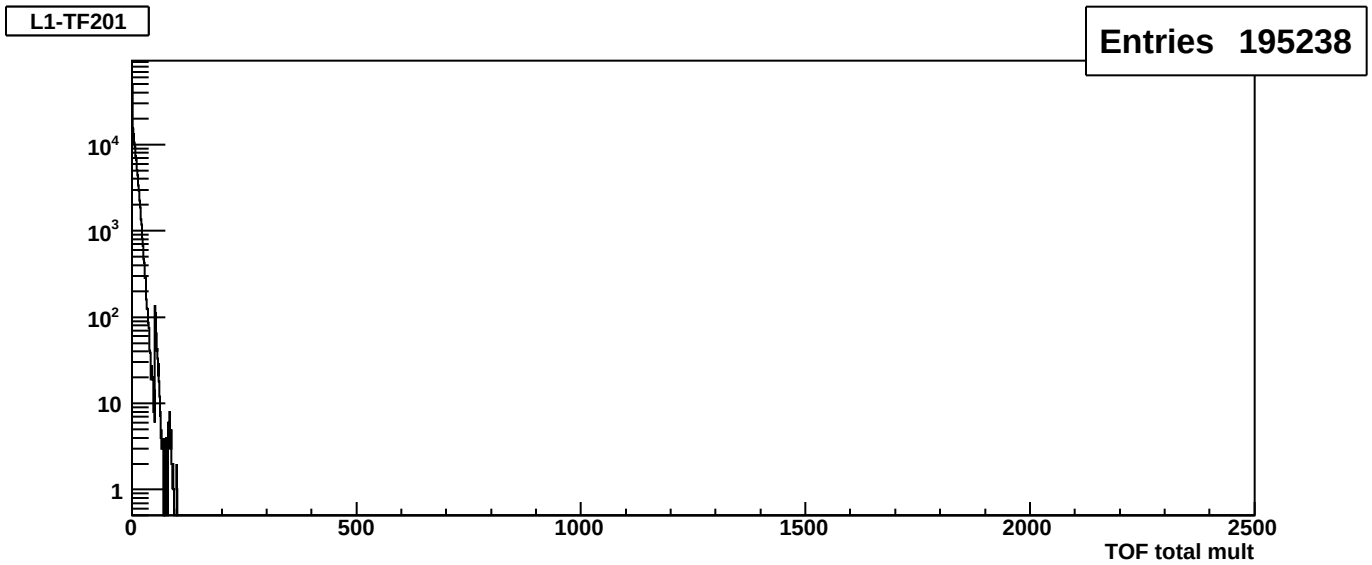

MIX-TF003
Entries 8199996
TOF MULT
TOF tray

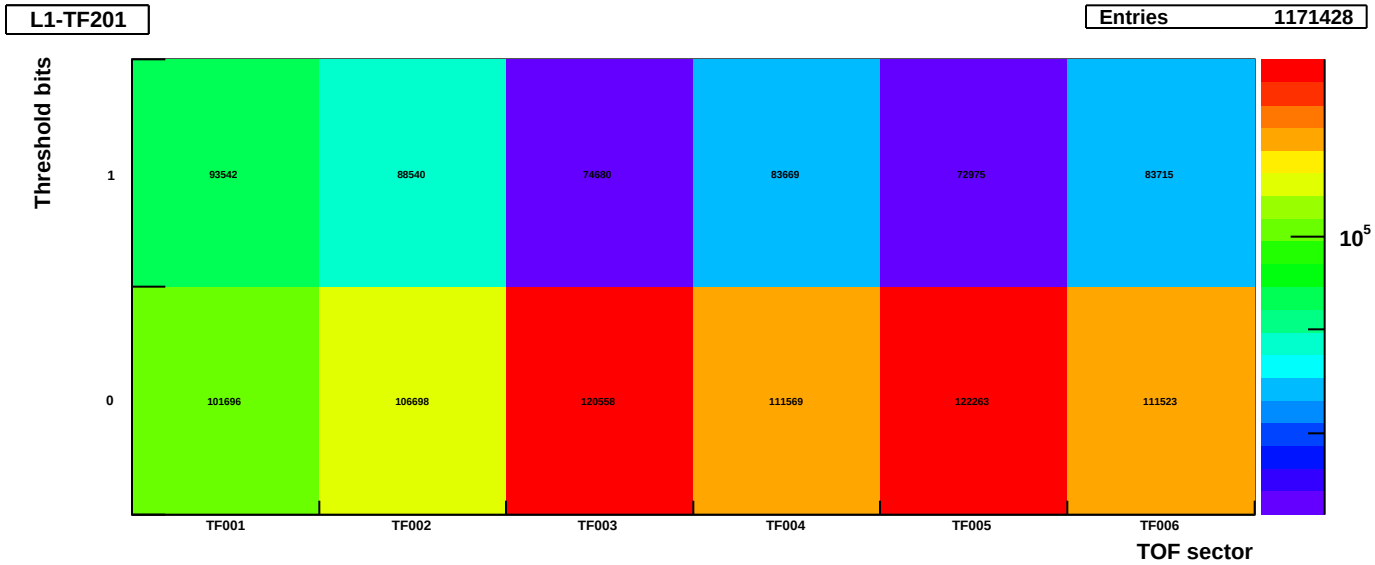

MIX-TF004
Entries 8199996
TOF MULT
TOF tray

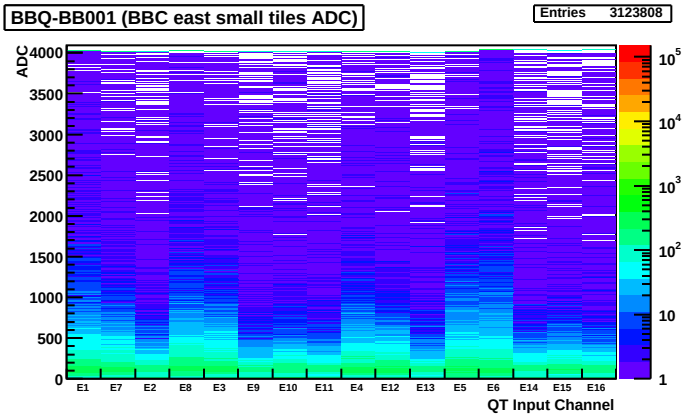

MIX-TF005
Entries 8199996
TOF MULT
TOF tray

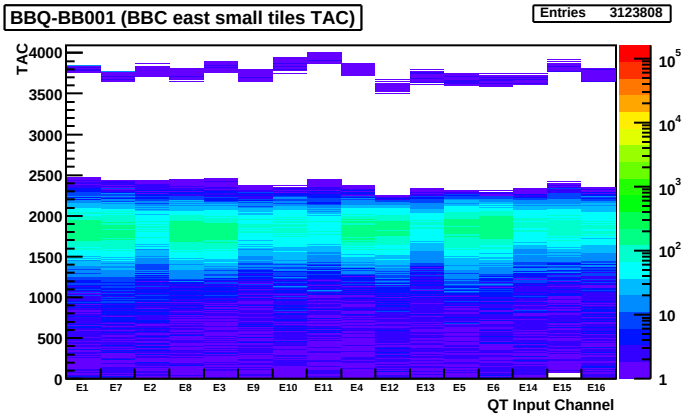

MIX-TF006
Entries 8199996
TOF MULT
TOF tray

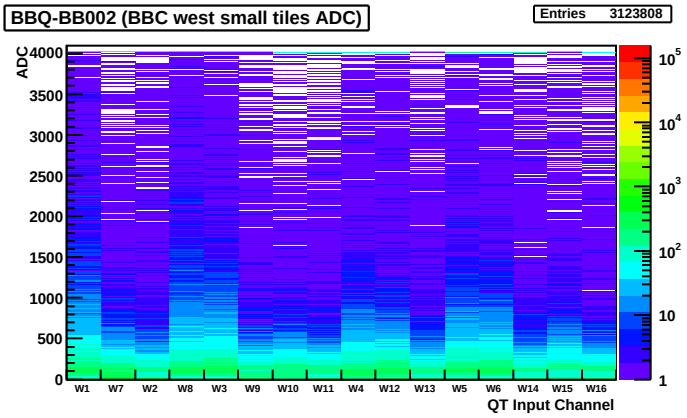

MIX-TF101
Entries 1171428
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

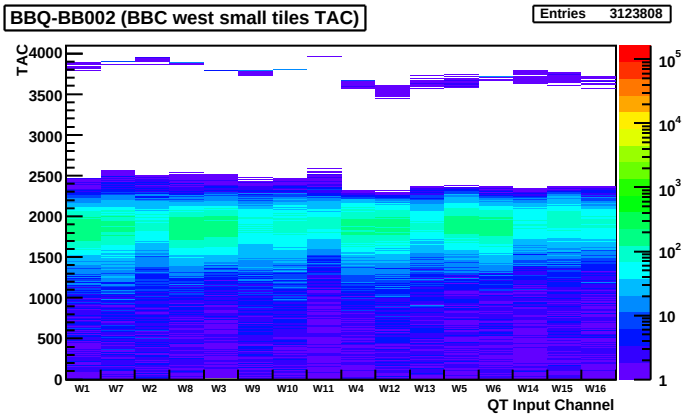

L1-TF201
Entries 195238
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

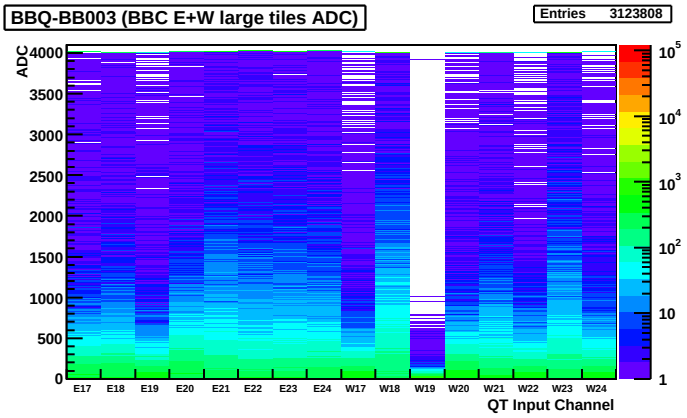

L1-TF201
Entries 1171428
Threshold bits
1
0
93542
88540
74680
83669
72975
83715
101696
106698
120558
111569
122263
111523
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

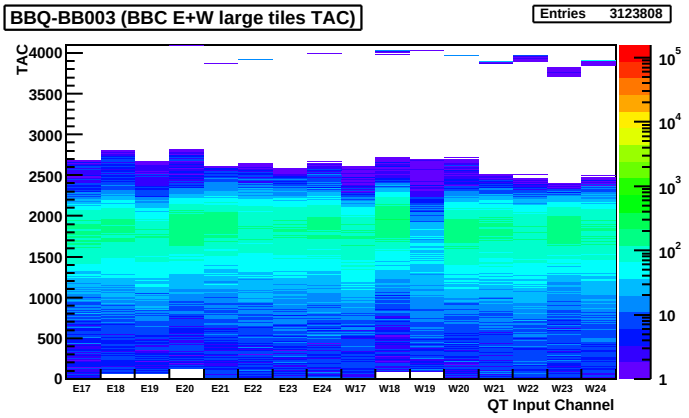

BBQ-BB001 (BBC east small tiles ADC)
Entries 3123808
ADC
QT Input Channel

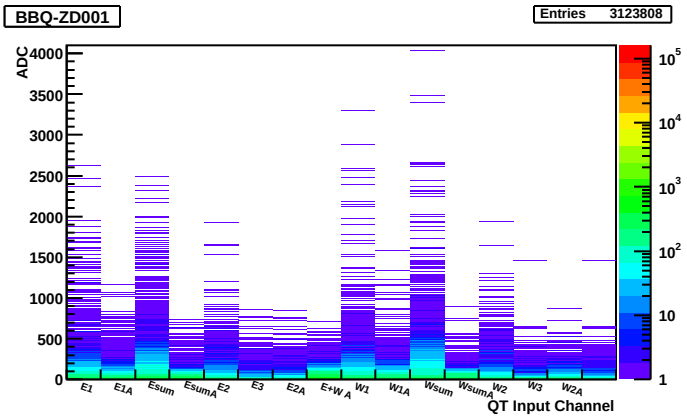

BBQ-BB001 (BBC east small tiles TAC)
Entries 3123808
TAC
QT Input Channel

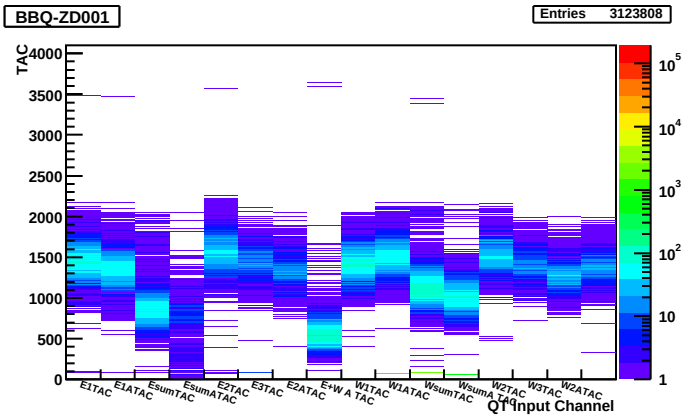

BBQ-BB002 (BBC west small tiles ADC)
Entries 3123808
ADC
QT Input Channel

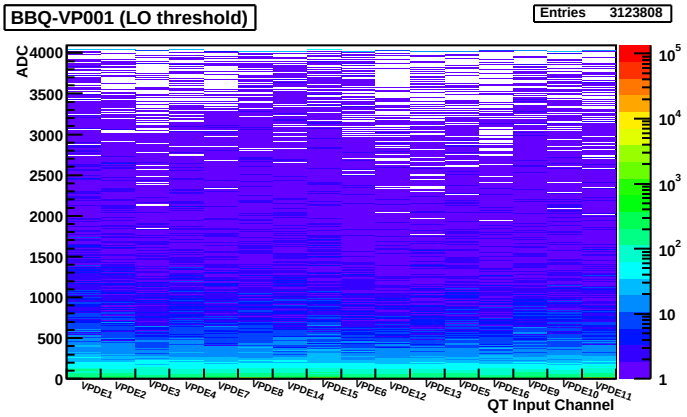

BBQ-BB002 (BBC west small tiles TAC)
Entries 3123808
TAC
QT Input Channel

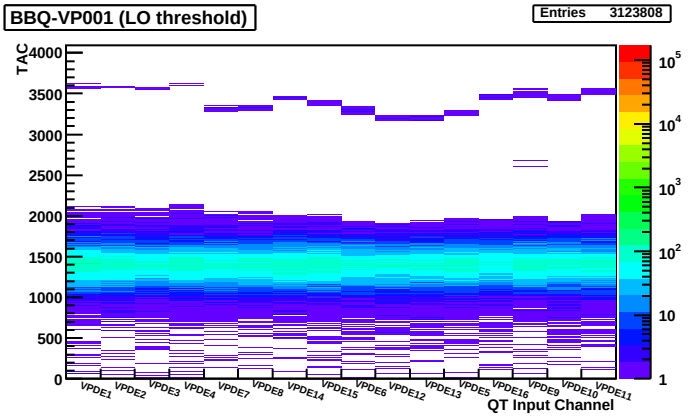

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3123808
ADC
QT Input Channel

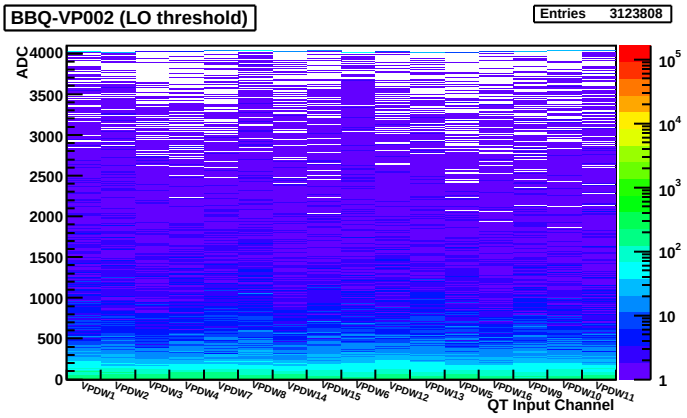

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3123808
TAC
QT Input Channel

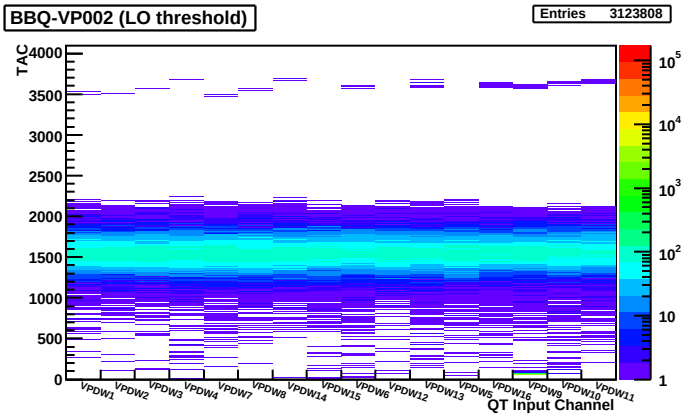

BBQ-ZD001
Entries 3123808
ADC
QT Input Channel

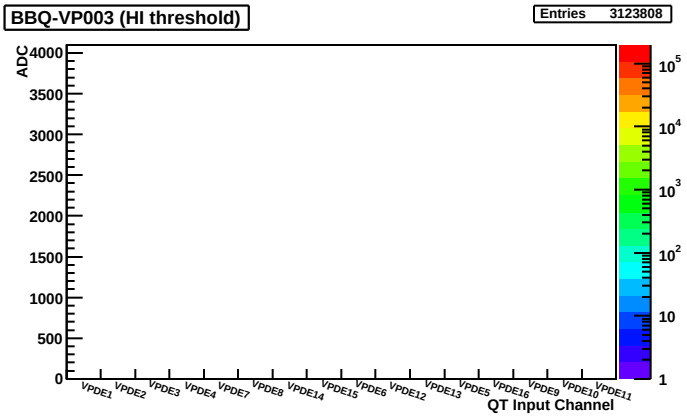

BBQ-ZD001
Entries 3123808
TAC
QT Input Channel

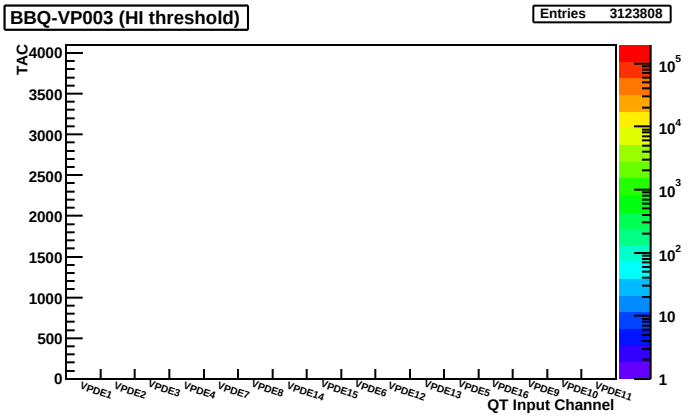

BBQ-VP001 (LO threshold)
Entries 3123808
ADC
QT Input Channel

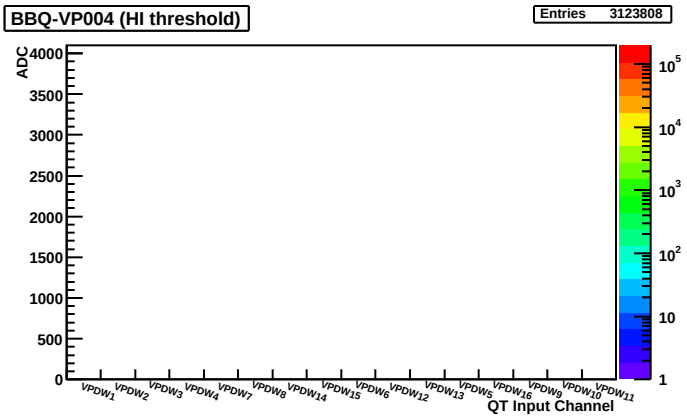

BBQ-VP001 (LO threshold)
Entries 3123808
TAC
QT Input Channel

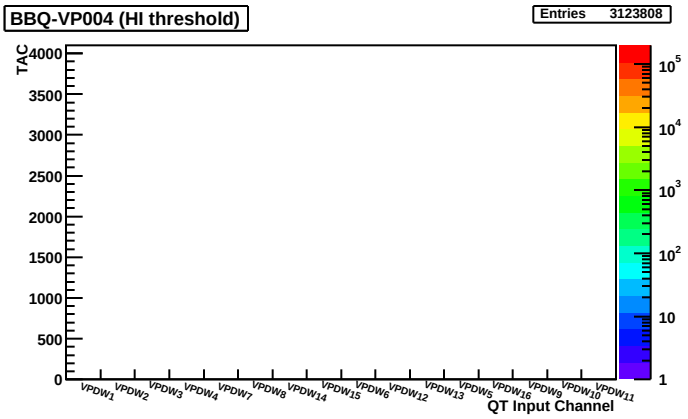

BBQ-VP002 (LO threshold)
Entries 3123808
ADC
QT Input Channel

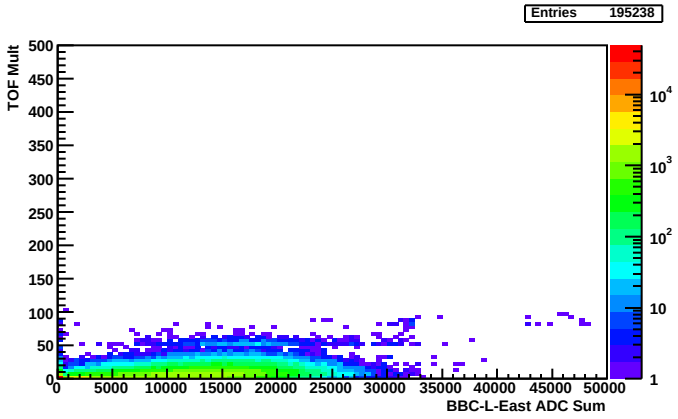

BBQ-VP002 (LO threshold)
Entries 3123808
TAC
QT Input Channel

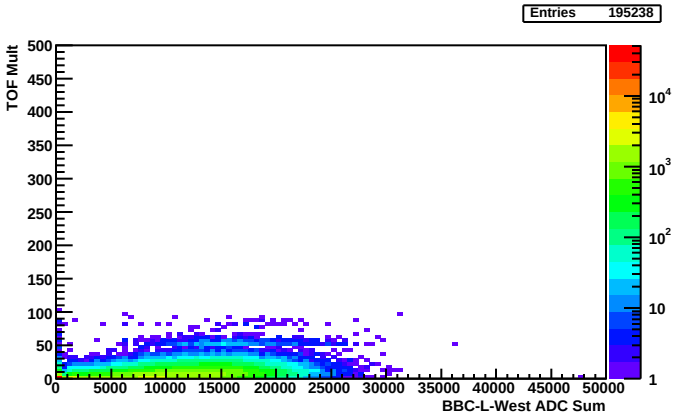

BBQ-VP003 (HI threshold)
Entries 3123808
ADC
QT Input Channel

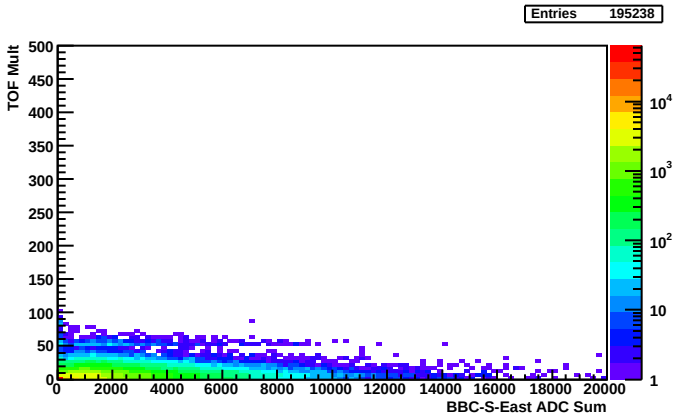

BBQ-VP003 (HI threshold)
Entries 3123808
TAC
QT Input Channel

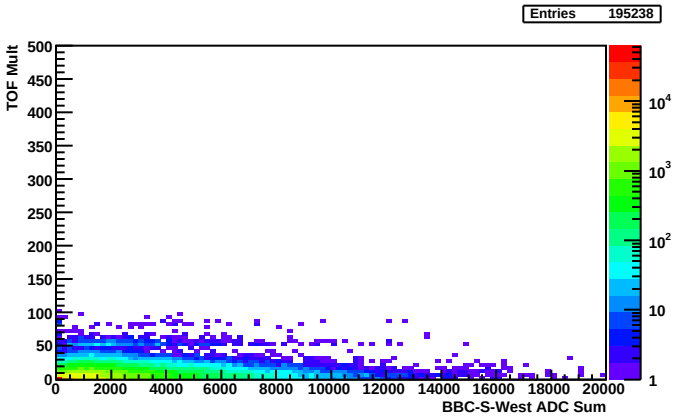

BBQ-VP004 (HI threshold)
Entries 3123808
ADC
QT Input Channel

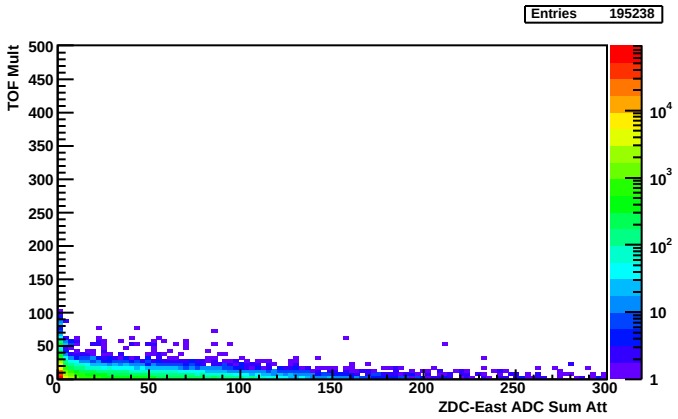

BBQ-VP004 (HI threshold)
Entries 3123808
TAC
QT Input Channel

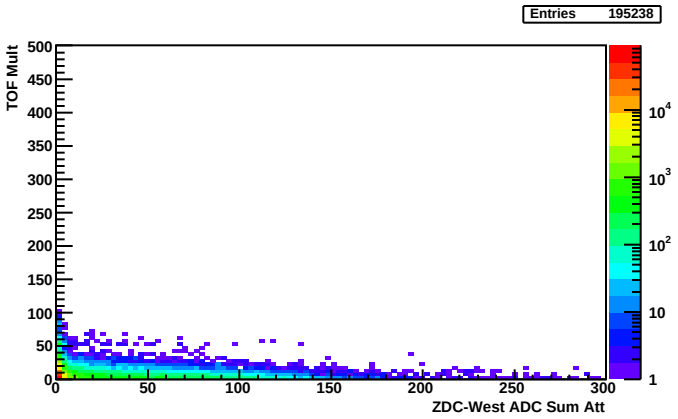

Entries 195238
TOF Mult
BBC-L-East ADC Sum

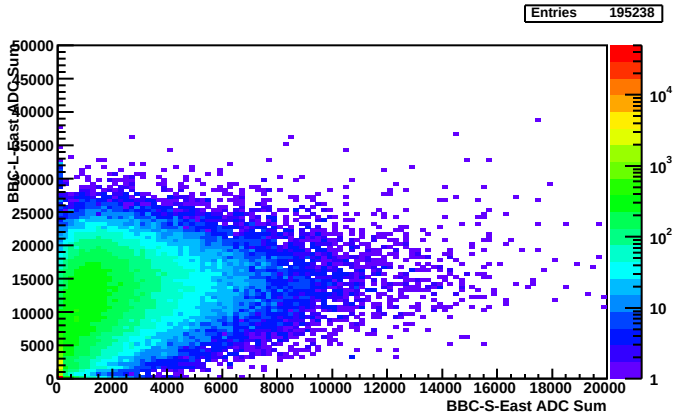

Entries 195238
TOF Mult
BBC-L-West ADC Sum

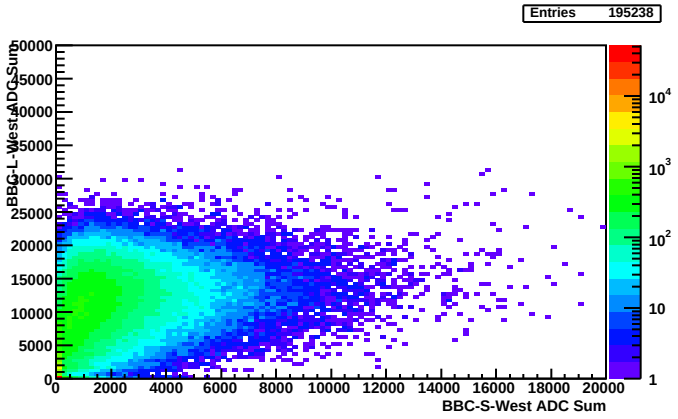

Entries 195238
TOF Mult
BBC-S-East ADC Sum

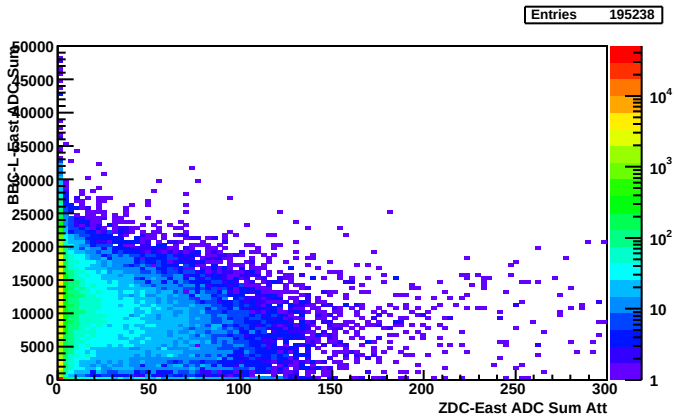

Entries 195238
TOF Mult
BBC-S-West ADC Sum

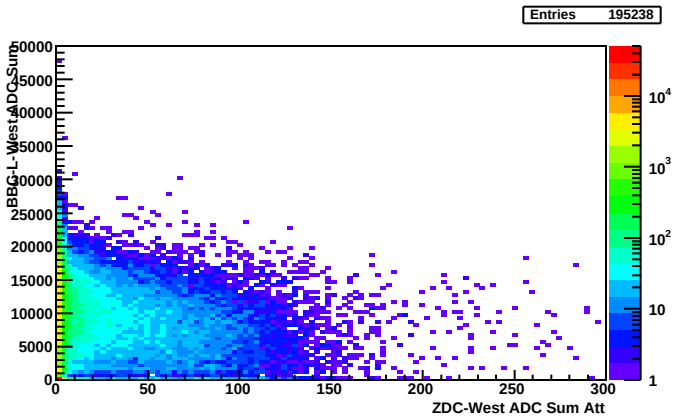

Entries 195238
TOF Mult
ZDC-East ADC Sum Att

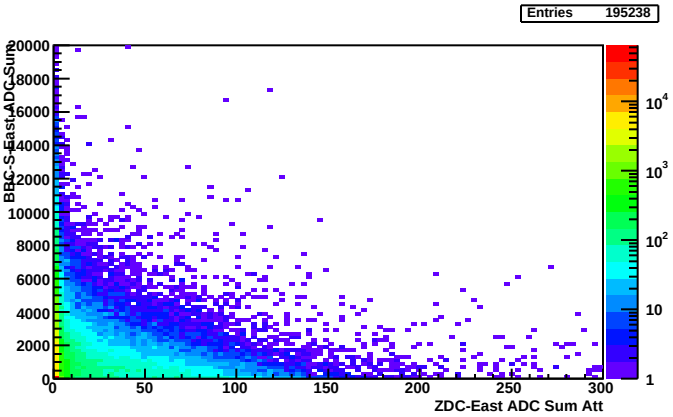

Entries 195238
TOF Mult
ZDC-West ADC Sum Att

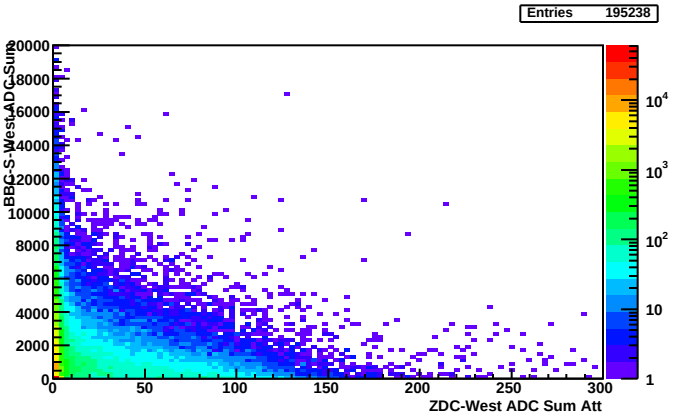

Entries 195238
BBC-L-East ADC Sum
BBC-S-East ADC Sum

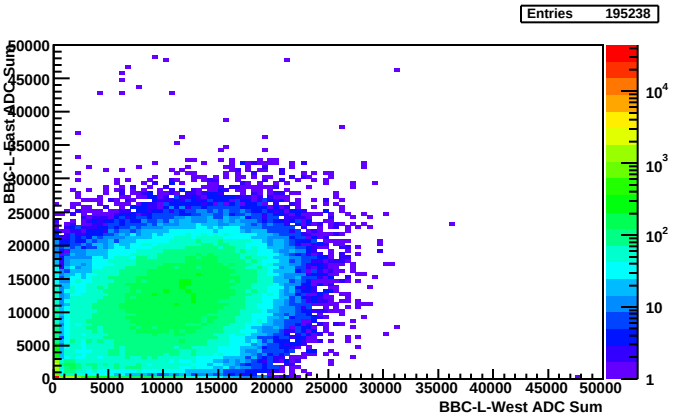

Entries 195238
BBC-L-West ADC Sum
BBC-S-West ADC Sum

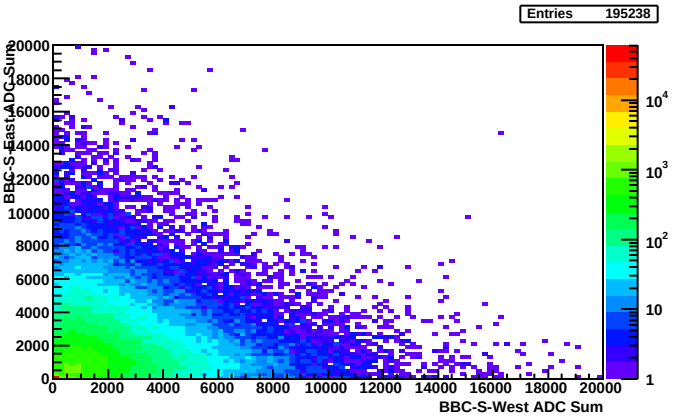

Entries 195238
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

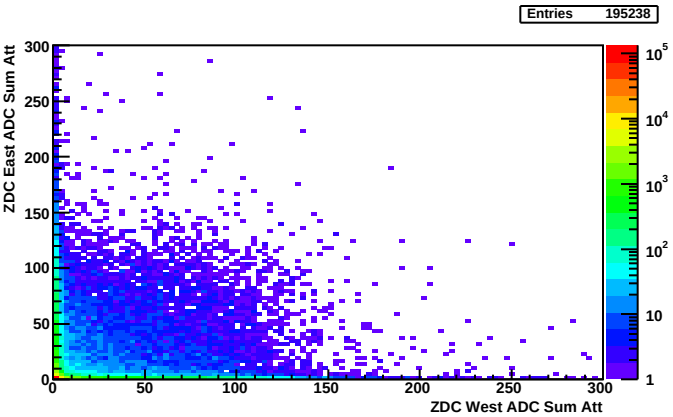

Entries 195238
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

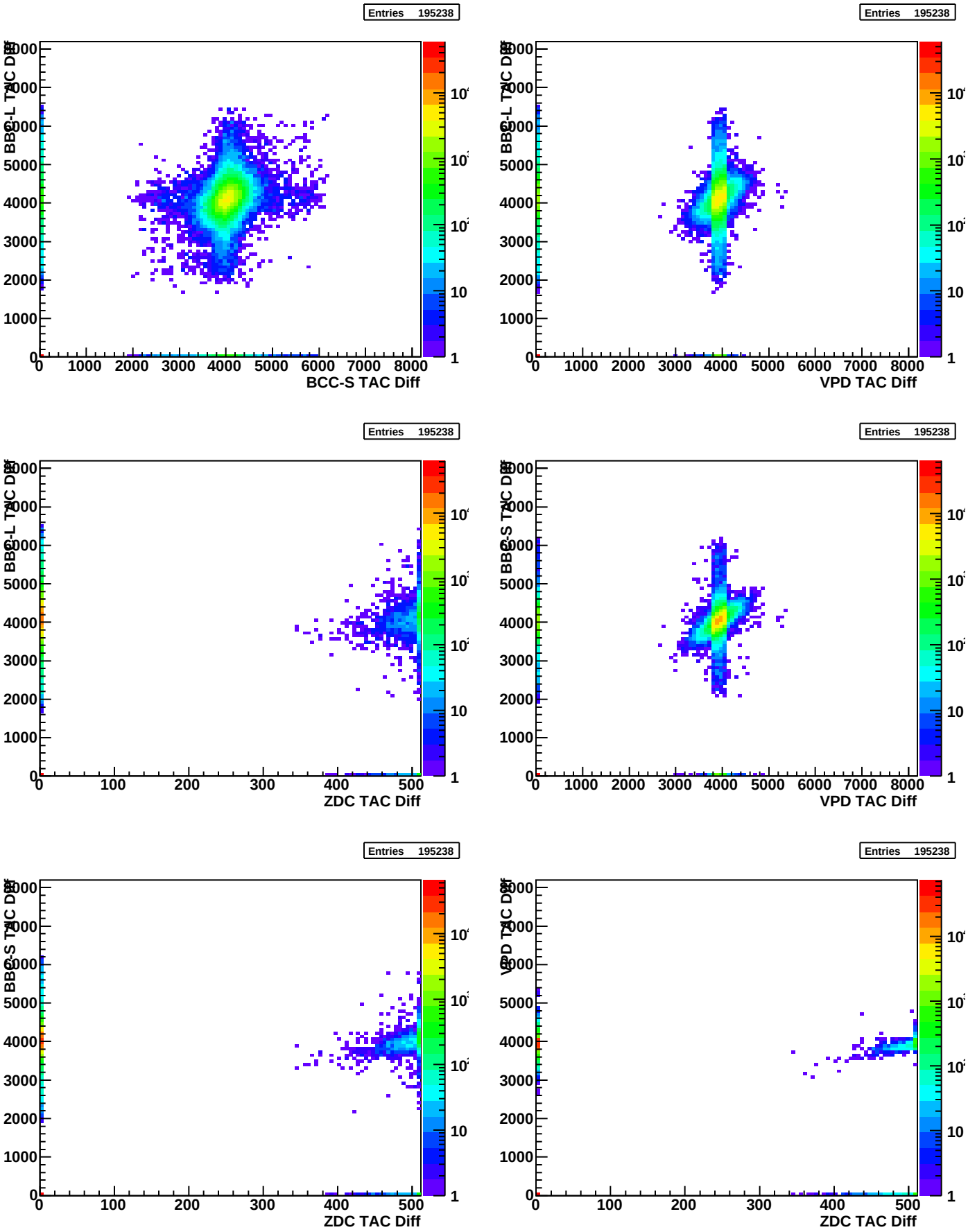

Entries 195238
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

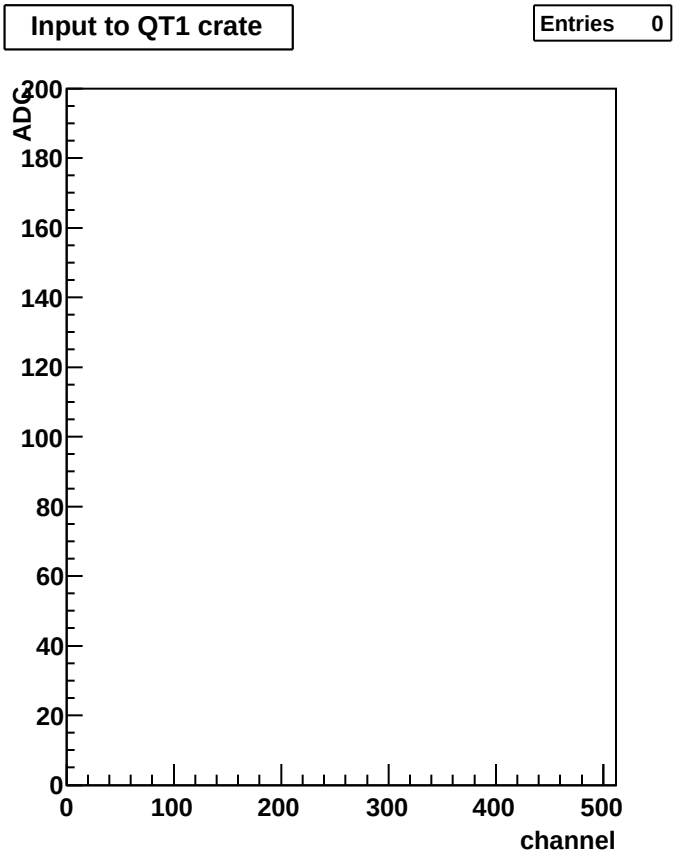

Entries 195238
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

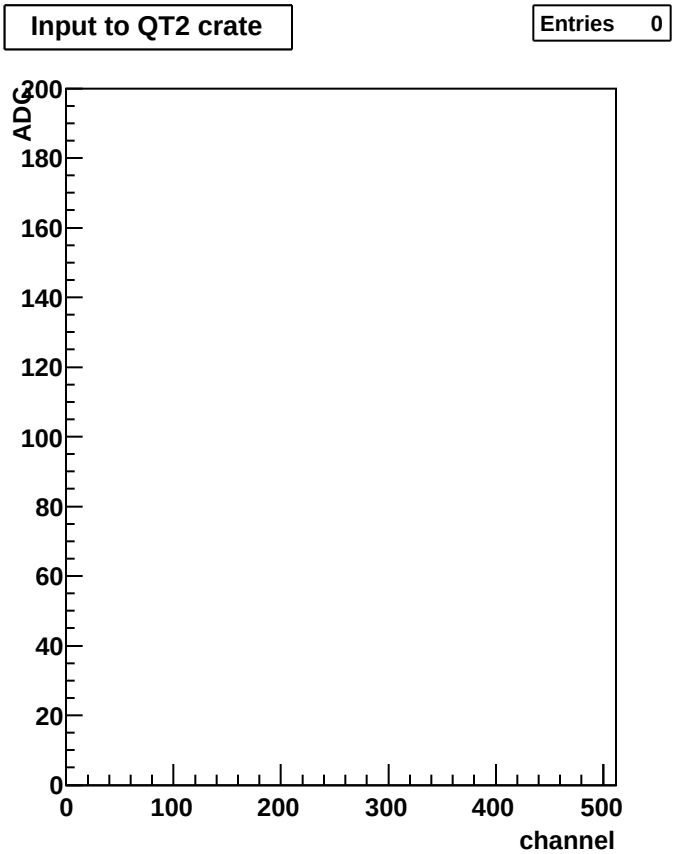

Entries 195238
BBC-L-East ADC Sum
BBC-L-West ADC Sum

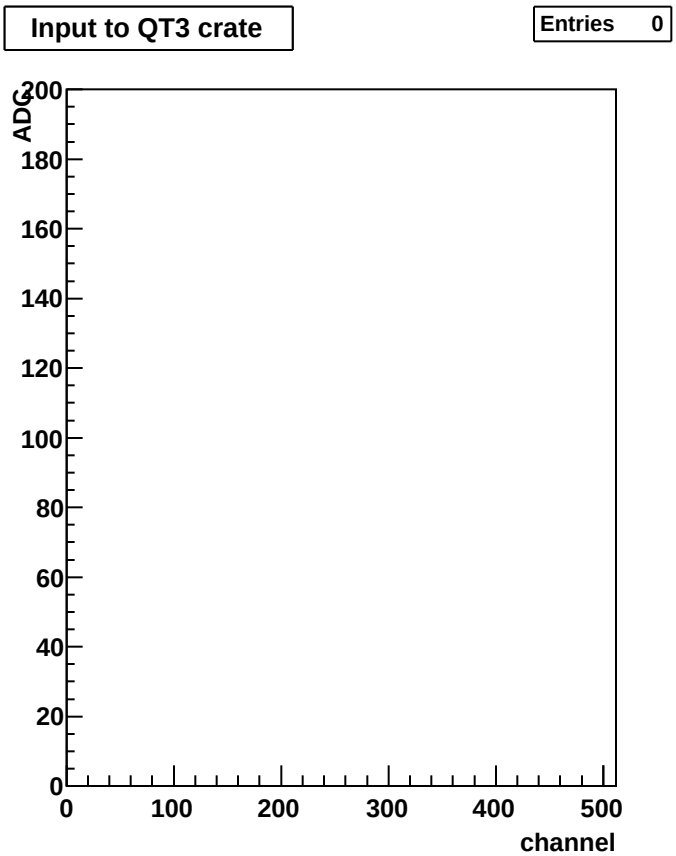

Entries 195238
BBC-S-East ADC Sum
BBC-S-West ADC Sum

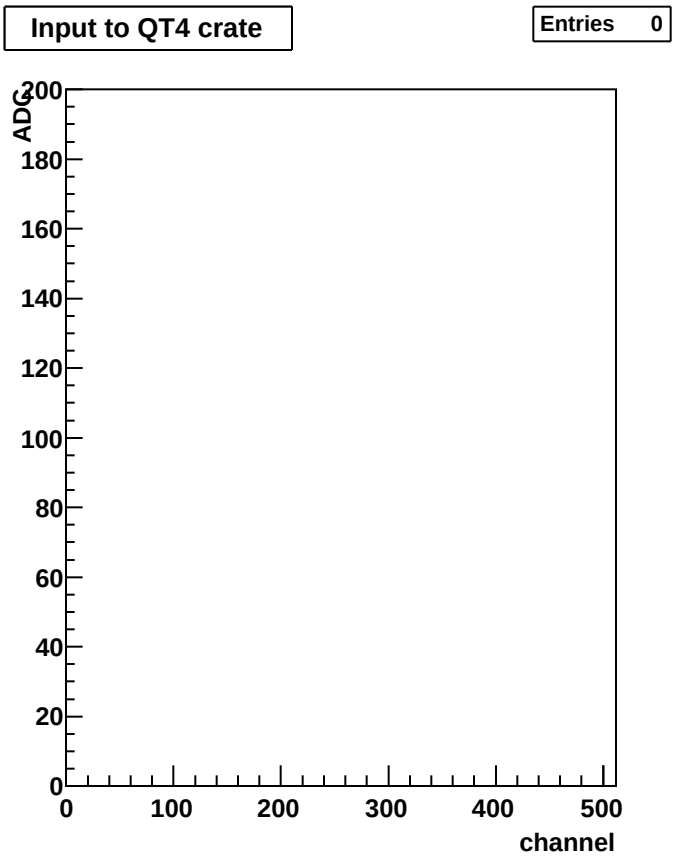

Entries 195238
ZDC East ADC Sum Att
ZDC West ADC Sum Att

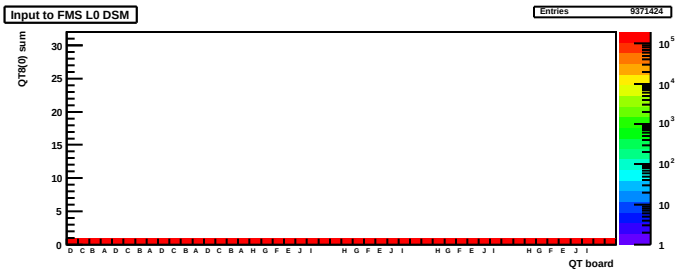

Entries 195238
BBC-L TAC Diff
BCC-S TAC Diff
Entries 195238
BBC-L TAC Diff
VPD TAC Diff
Entries 195238
BBC-L TAC Diff
ZDC TAC Diff
Entries 195238
BBC-S TAC Diff
VPD TAC Diff
Entries 195238
BBC-S TAC Diff
ZDC TAC Diff
Entries 195238
VPD TAC Diff
ZDC TAC Diff

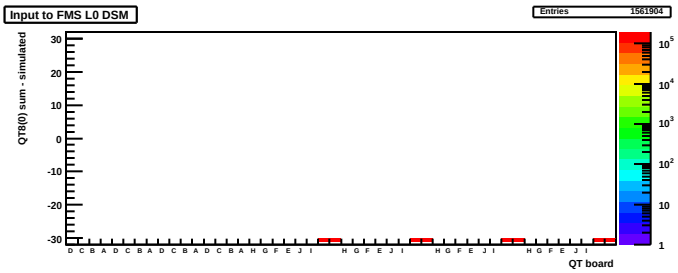

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

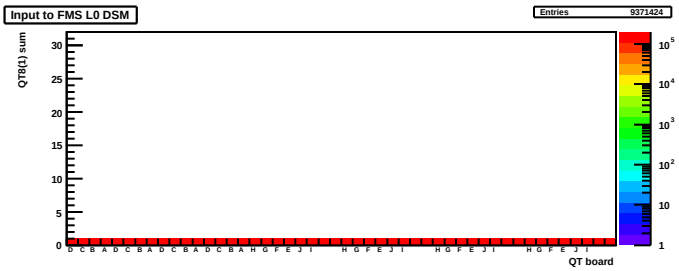

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

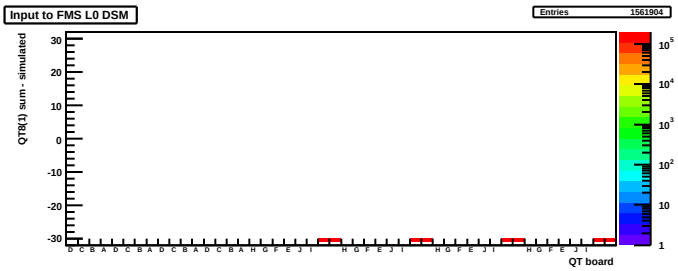

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

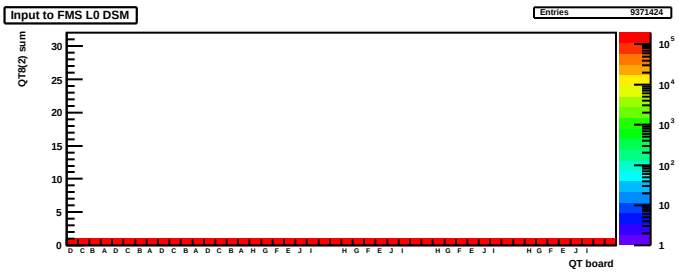

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

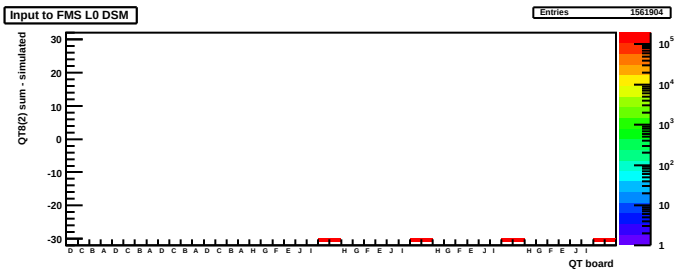

Input to FMS L0 DSM
Entries 9371424
QT8(0) sum
QT board

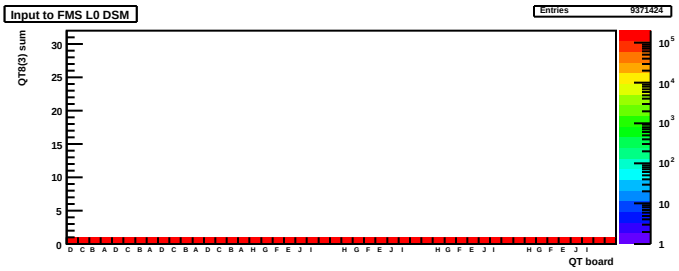

Input to FMS L0 DSM
Entries 1561904
QT8(0) sum - simulated
QT board

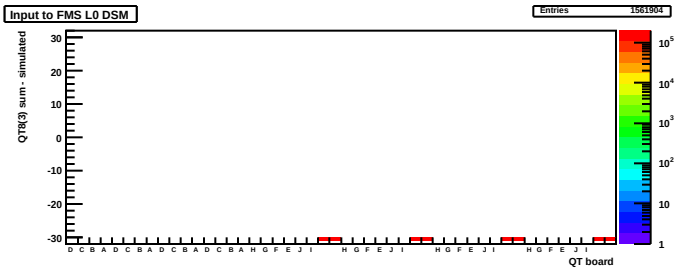

Input to FMS L0 DSM
Entries 9371424
QT8(1) sum
QT board

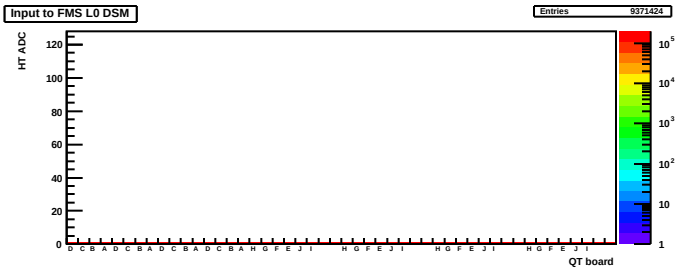

Input to FMS L0 DSM
Entries 1561904
QT8(1) sum - simulated
QT board

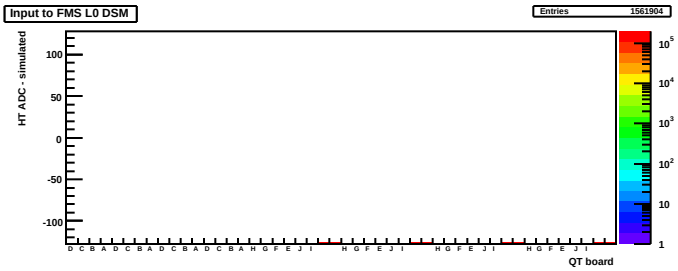

Input to FMS L0 DSM
Entries 9371424
QT8(2) sum
QT board

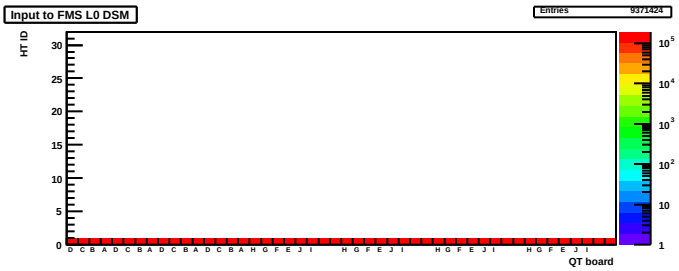

Input to FMS L0 DSM
Entries 1561904
QT8(2) sum - simulated
QT board

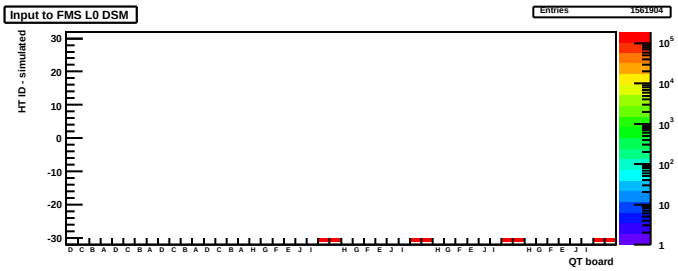

Input to FMS L0 DSM
Entries 9371424
QT8(3) sum
QT board

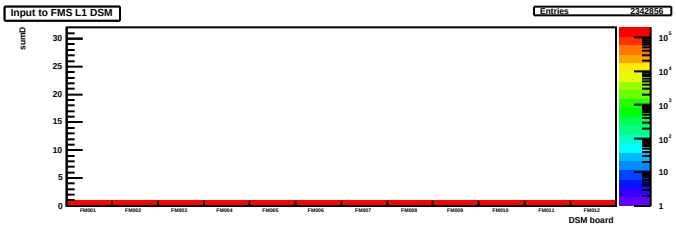

Input to FMS L0 DSM
Entries 1561904
QT8(3) sum - simulated
QT board

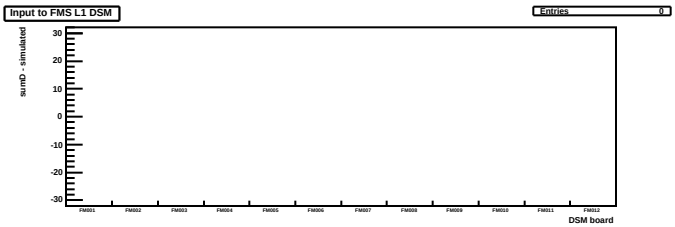

Input to FMS L0 DSM
Entries 9371424
HT ADC
QT board

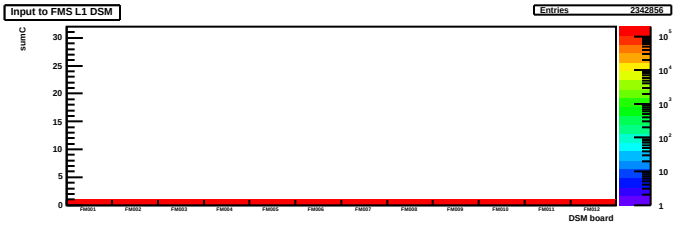

Input to FMS L0 DSM
Entries 1561904
HT ADC - simulated
QT board

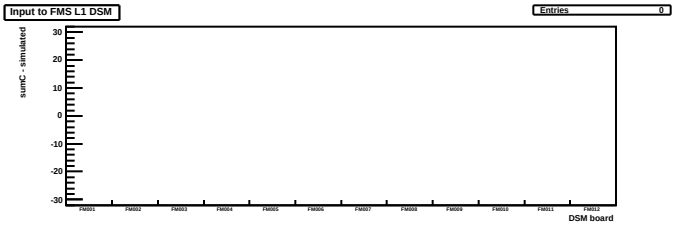

Input to FMS L0 DSM
Entries 9371424
HT ID
QT board

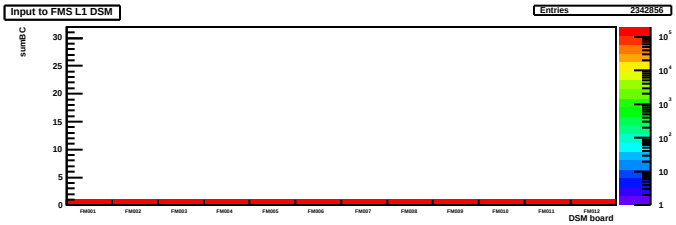

Input to FMS L0 DSM
Entries 1561904
HT ID - simulated
QT board

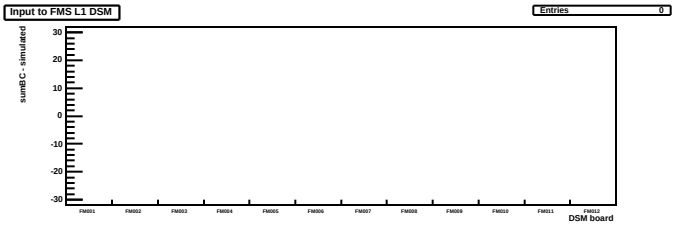

Input to FMS L1 DSM
Entries 2342856
sumD
DSM board

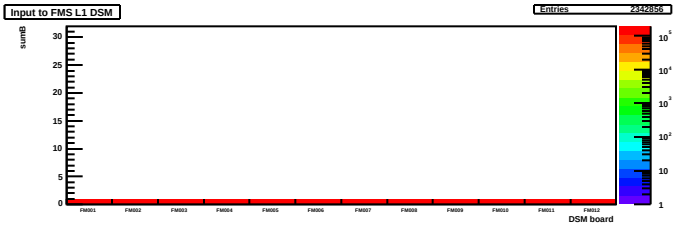

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

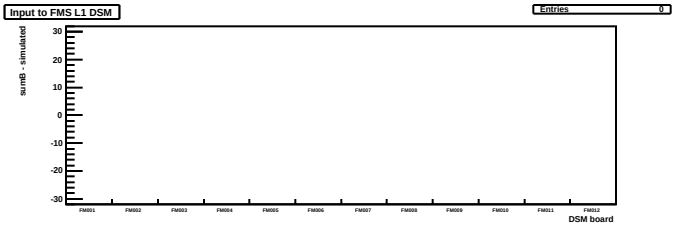

Input to FMS L1 DSM
Entries 2342856
sumC
DSM board

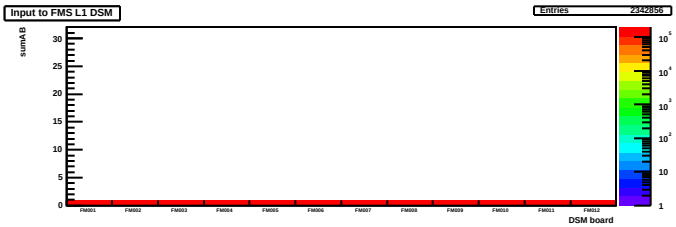

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

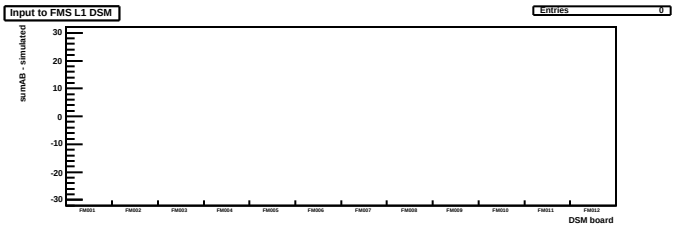

Input to FMS L1 DSM
Entries 2342856
sumBC
DSM board

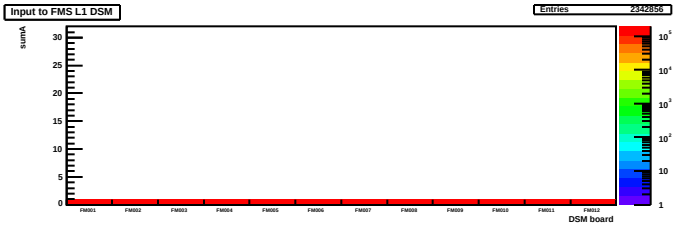

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

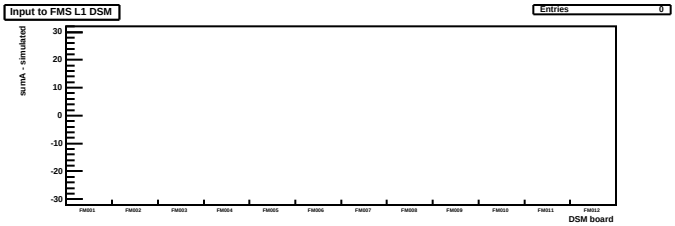

Input to FMS L1 DSM
Entries 2342856
sumB
DSM board

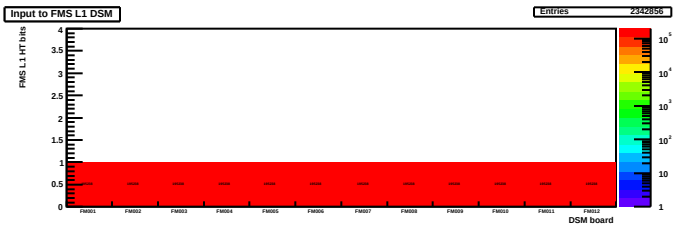

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

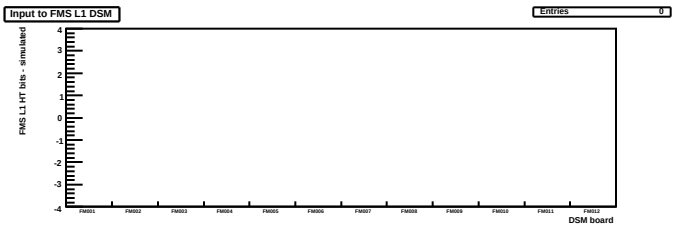

Input to FMS L1 DSM
Entries 2342856
sumAB
DSM board

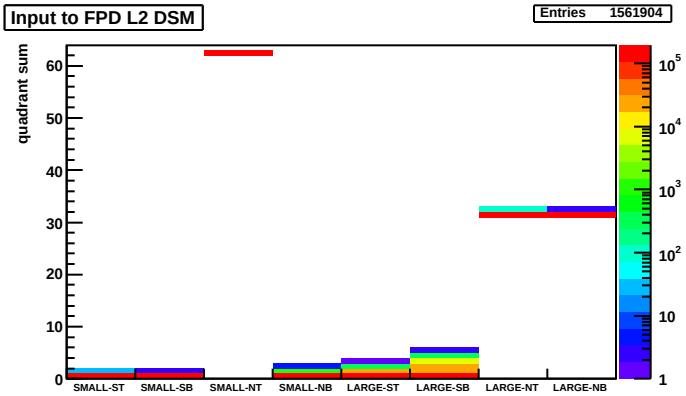

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

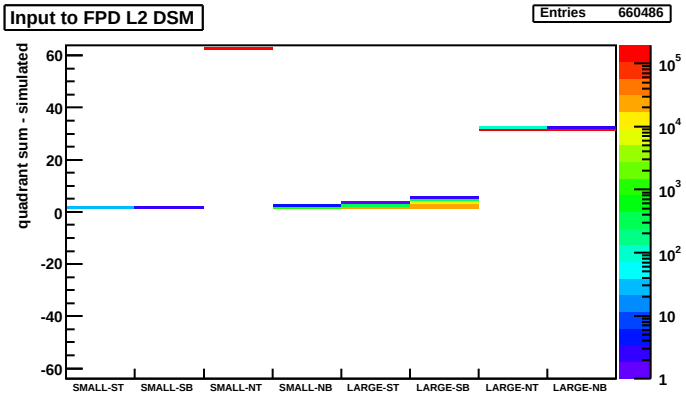

Input to FMS L1 DSM
Entries 2342856
sumA
DSM board

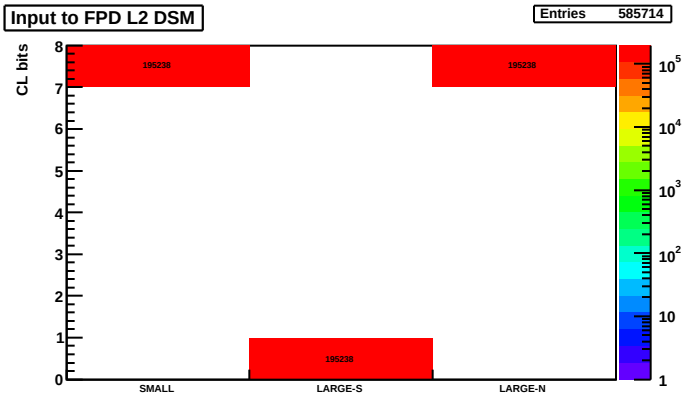

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

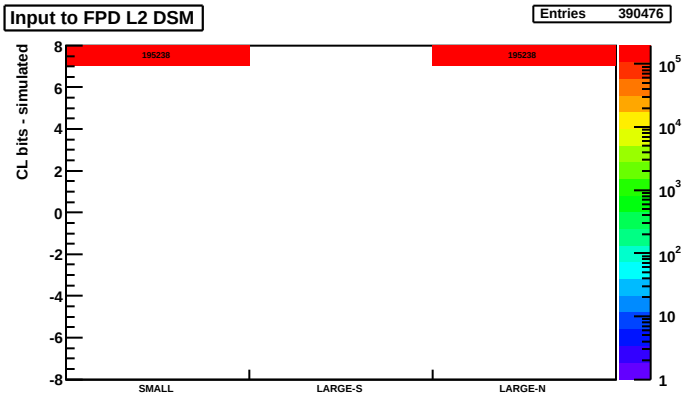

Input to FMS L1 DSM
Entries 2342856
FMS L1 HT bits
DSM board

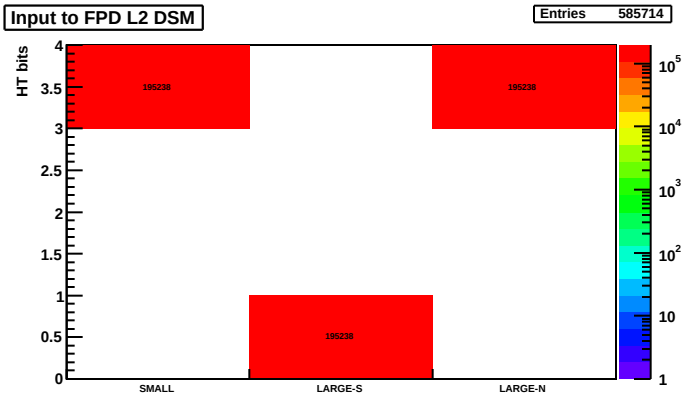

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

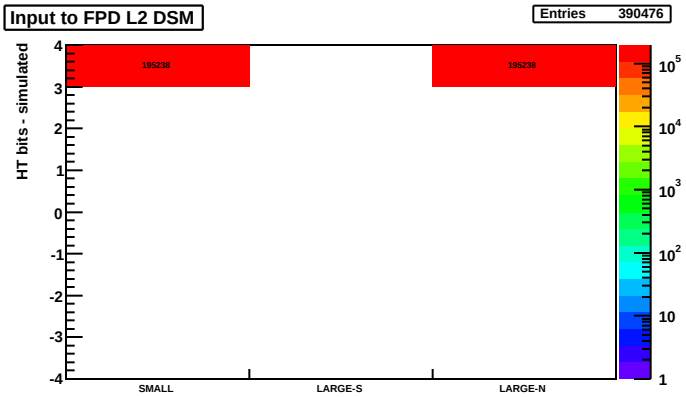

Input to FPD L2 DSM
Entries 1561904
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

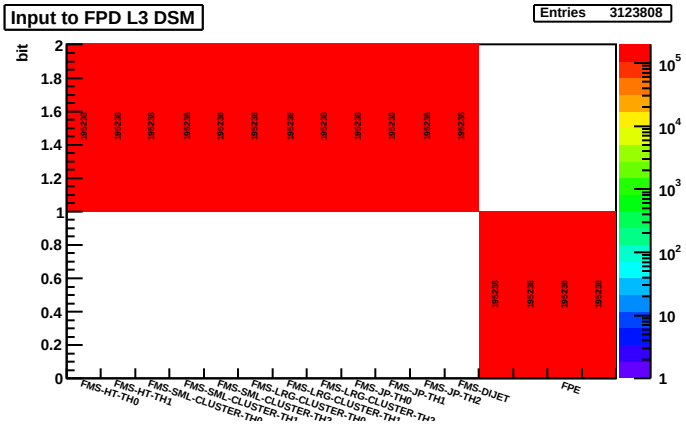

Input to FPD L2 DSM
Entries 660486
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

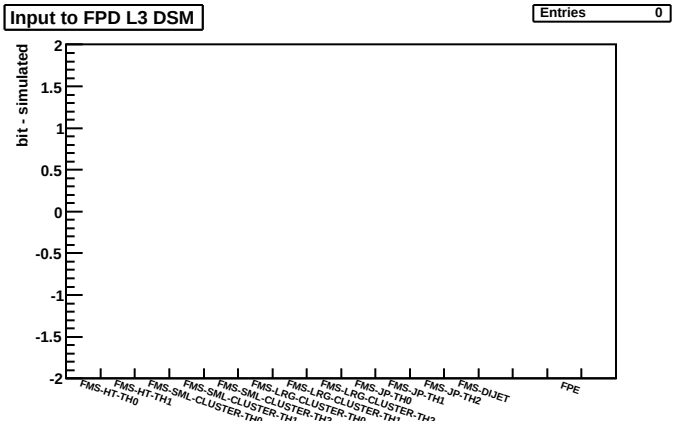

Input to FPD L2 DSM
Entries 585714
CL bits
195238
SMALL
LARGE-S
LARGE-N

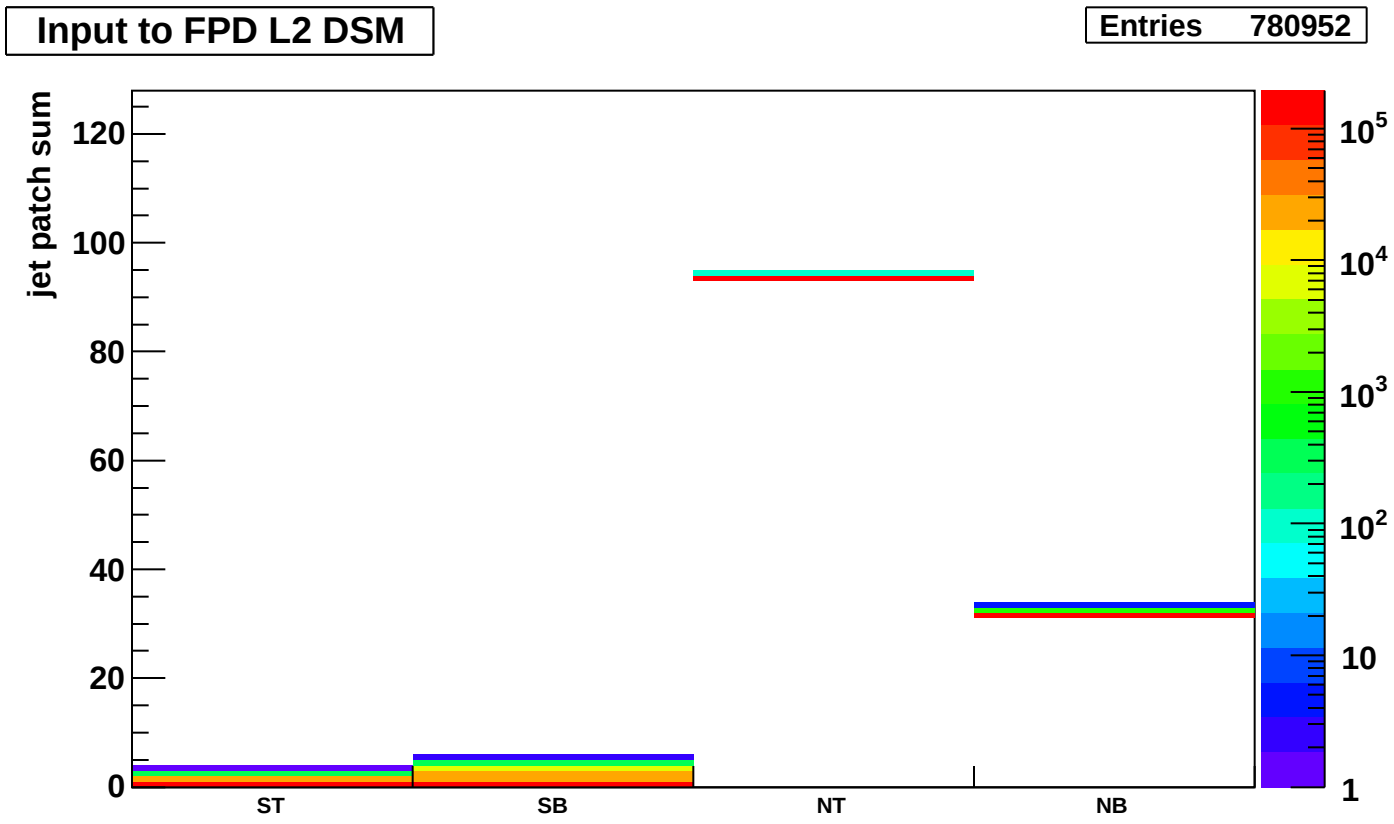

Input to FPD L2 DSM
Entries 390476
CL bits - simulated
195238
SMALL
LARGE-S
LARGE-N

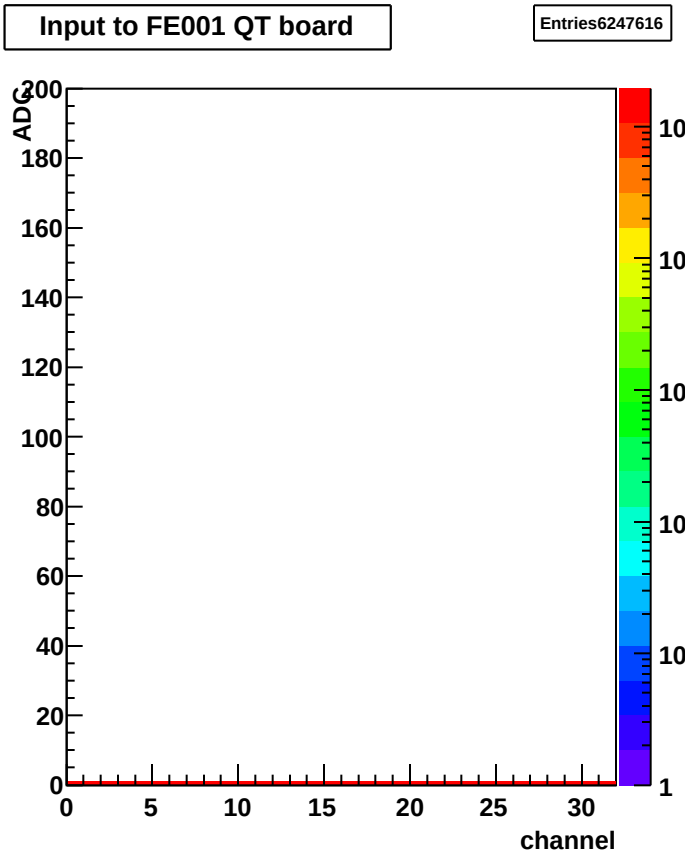

Input to FPD L2 DSM
Entries 585714
HT bits
195238
SMALL
LARGE-S
LARGE-N

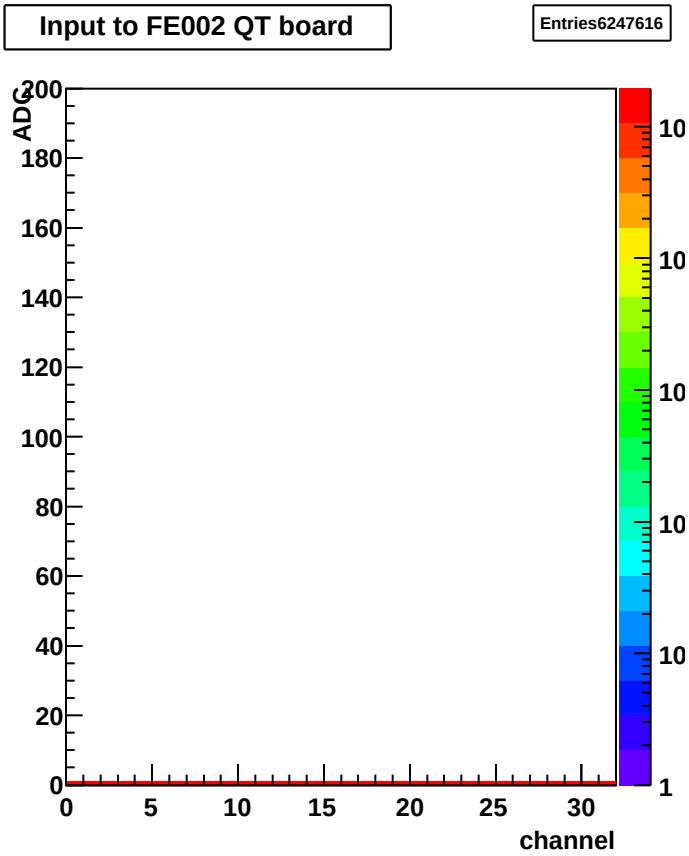

Input to FPD L2 DSM
Entries 390476
HT bits - simulated
195238
SMALL
LARGE-S
LARGE-N

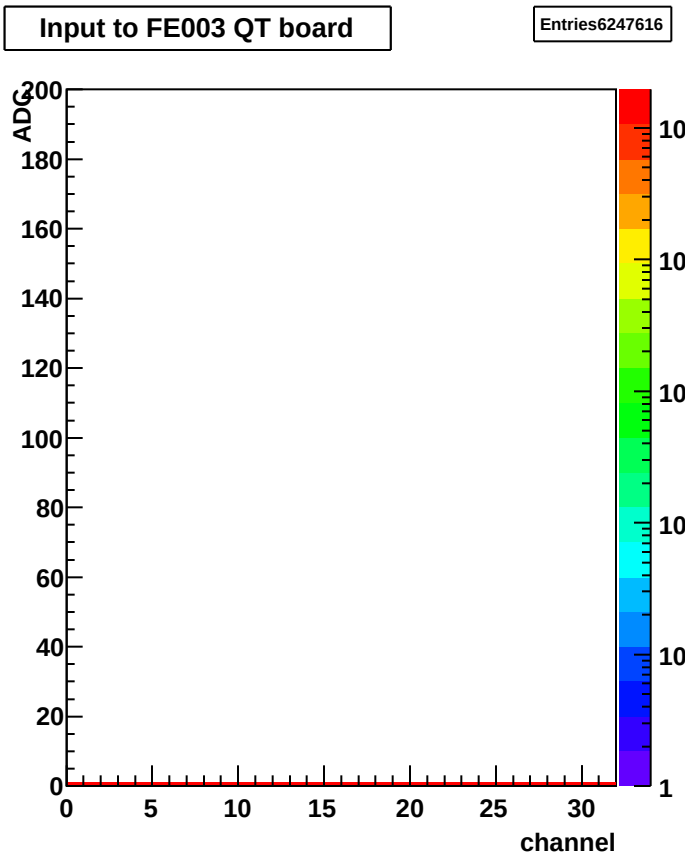

Input to FPD L3 DSM
Entries 3123808
bit
195238
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

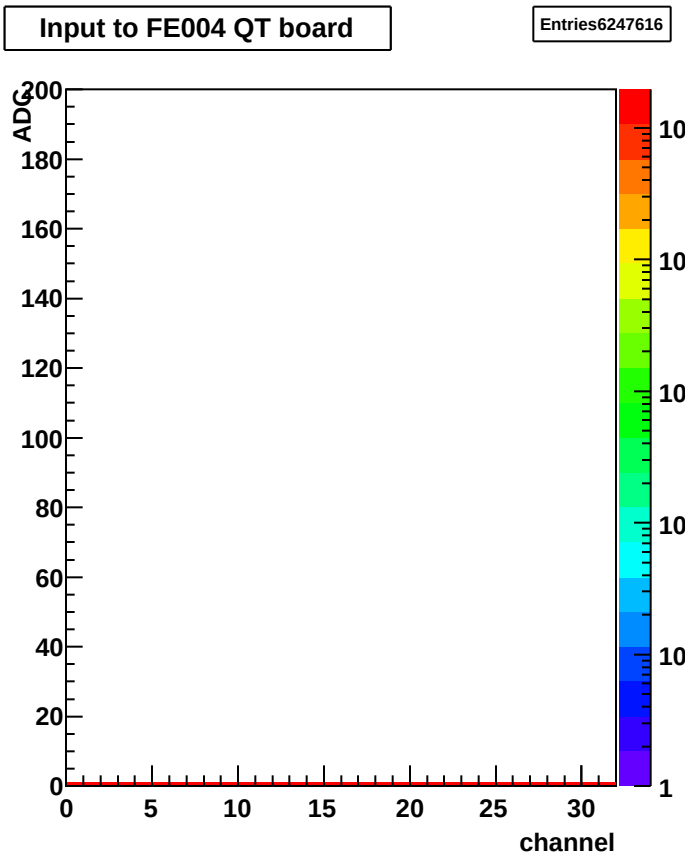

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

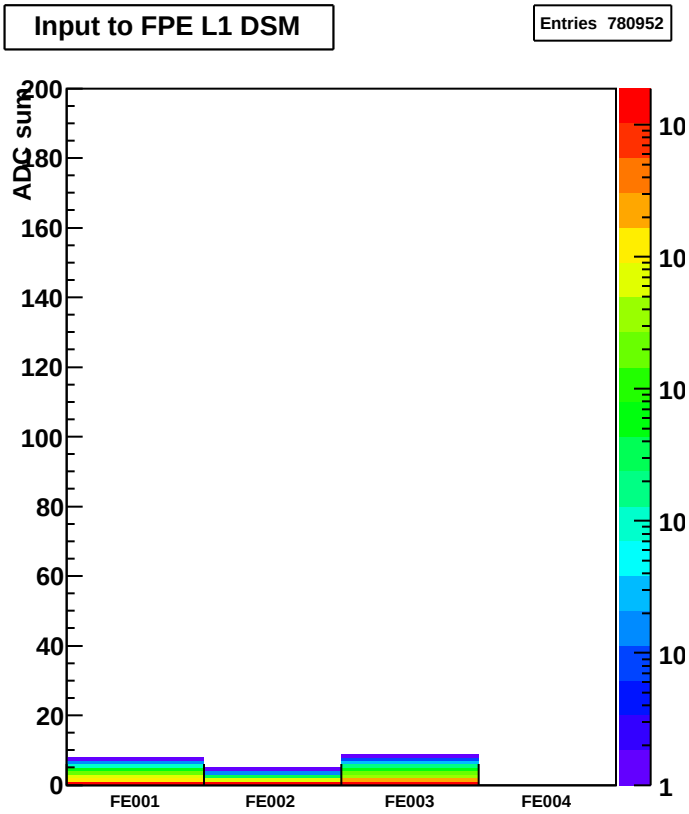

Input to FPD L2 DSM
Entries 780952
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

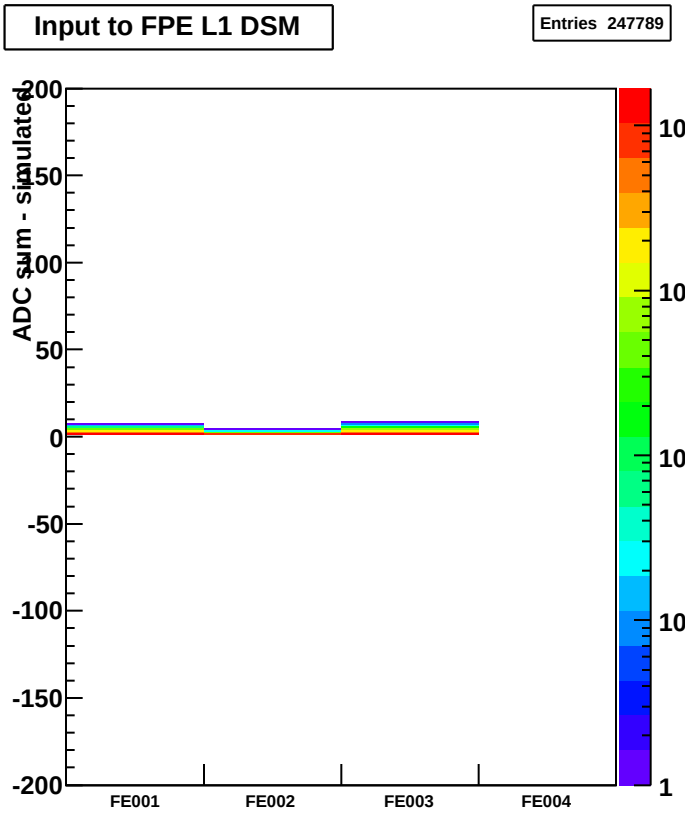

Input to FE001 QT board
Entries6247616
ADC
channel

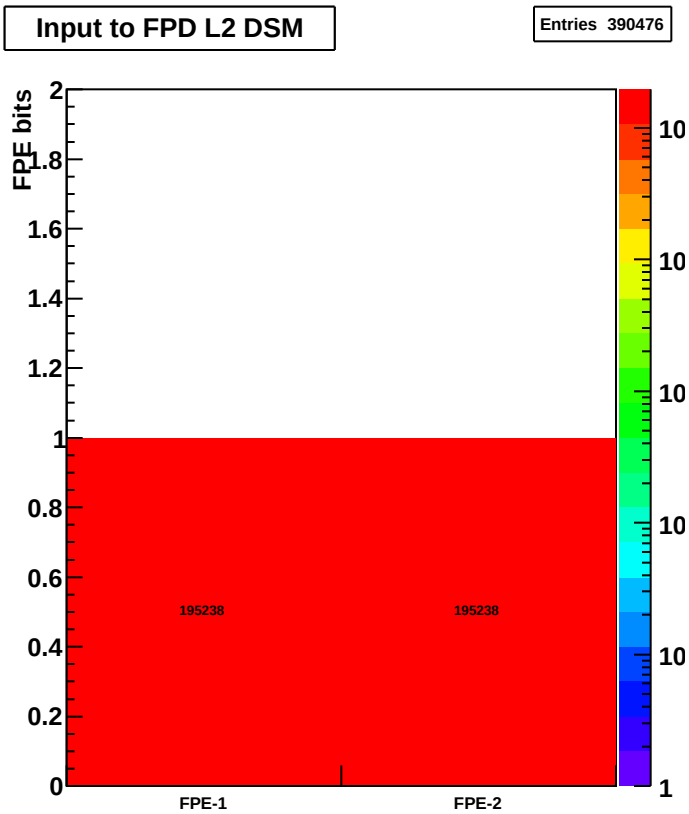

Input to FE002 QT board
Entries6247616
ADC
channel

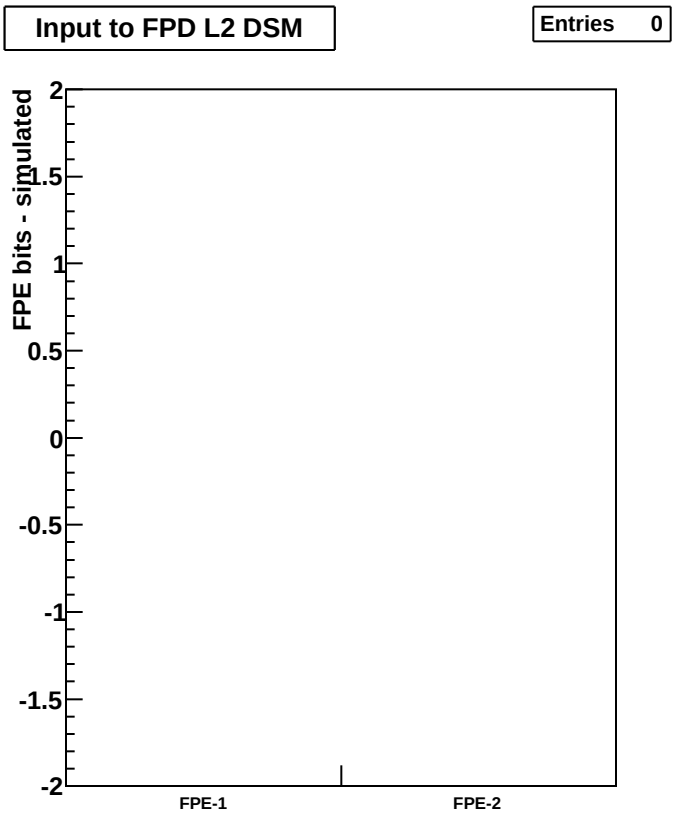

Input to FE003 QT board
Entries6247616
ADC
channel


Input to FE004 QT board
Entries6247616
ADC
channel


Input to FPE L1 DSM
Entries 780952
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 247789
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 390476
FPE bits
195238
195238
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2