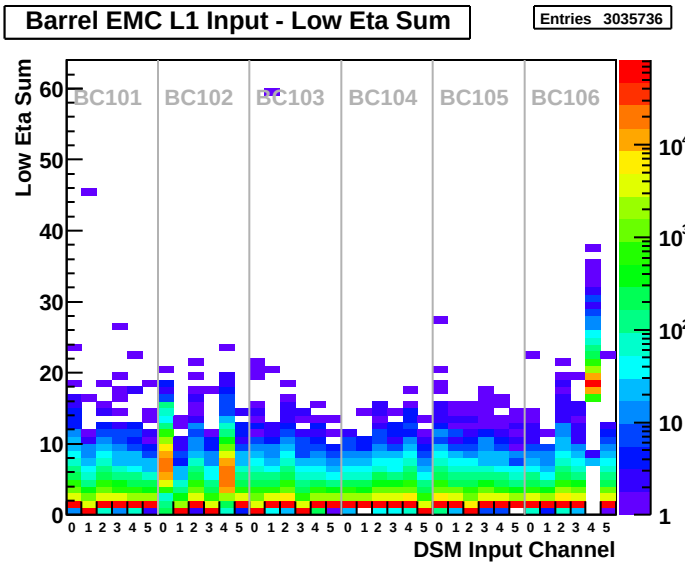
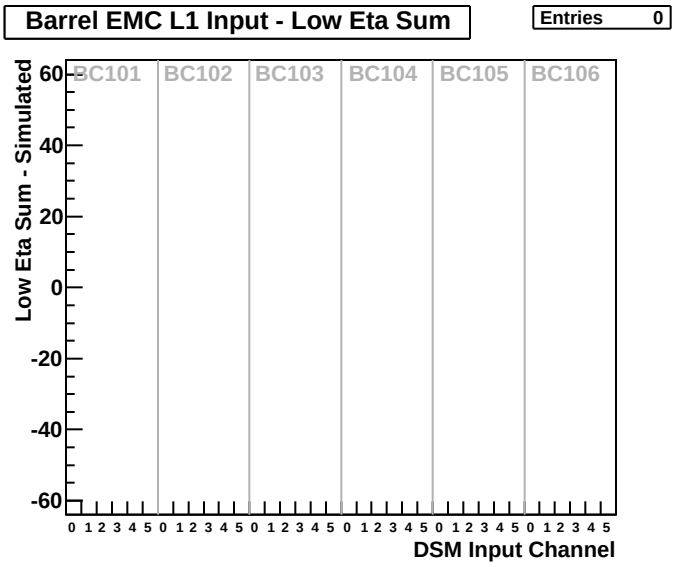
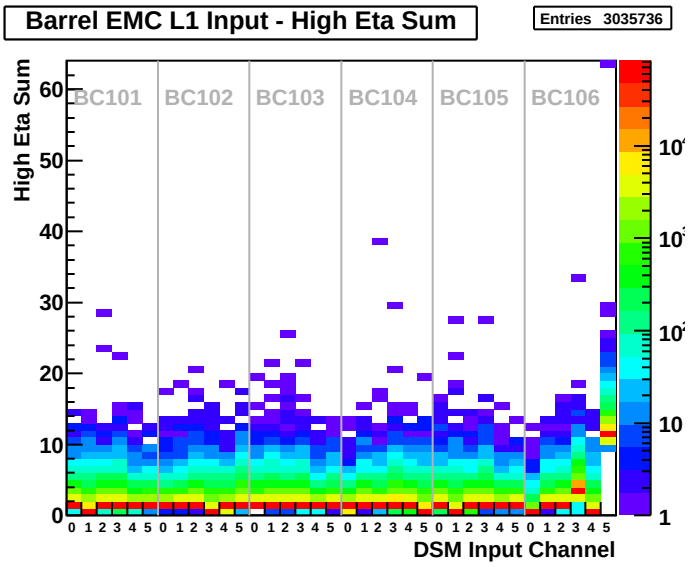
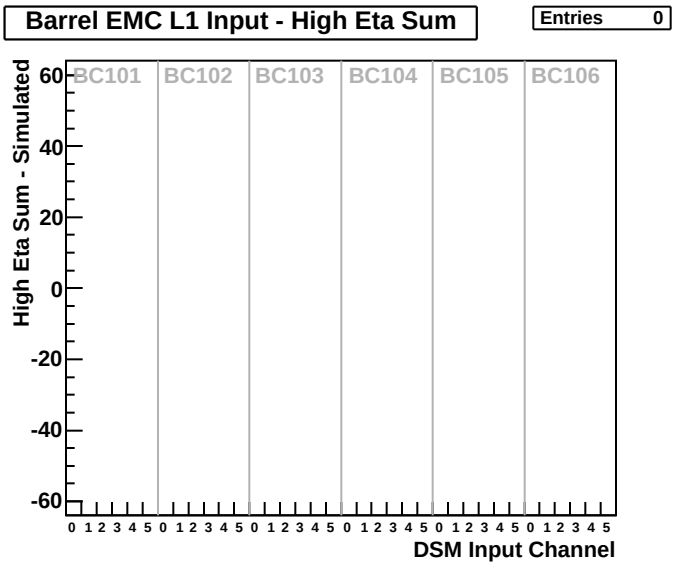
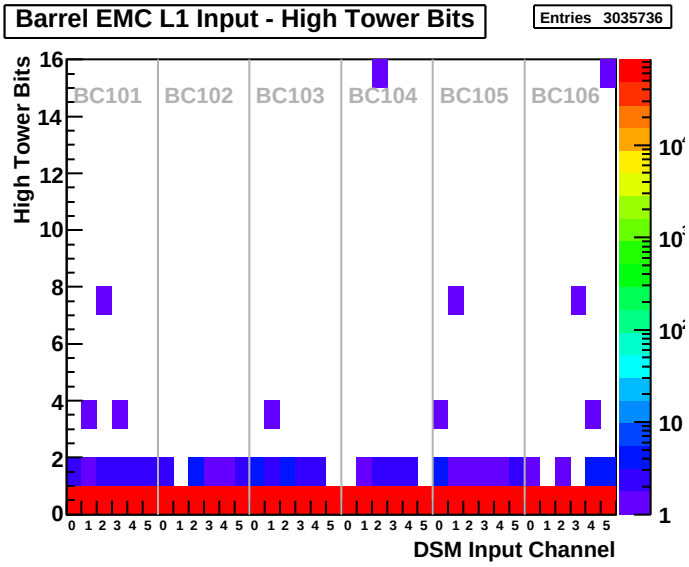
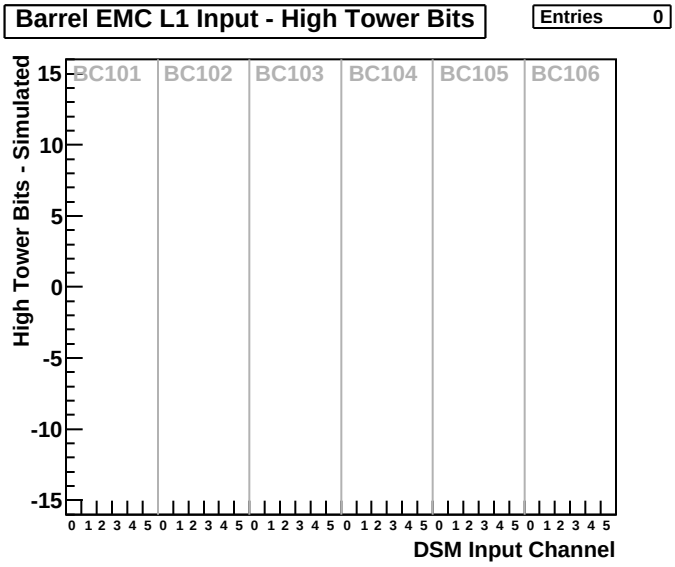

Barrel EMC L1 Input - Low Eta Sum
Entries 3035736
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

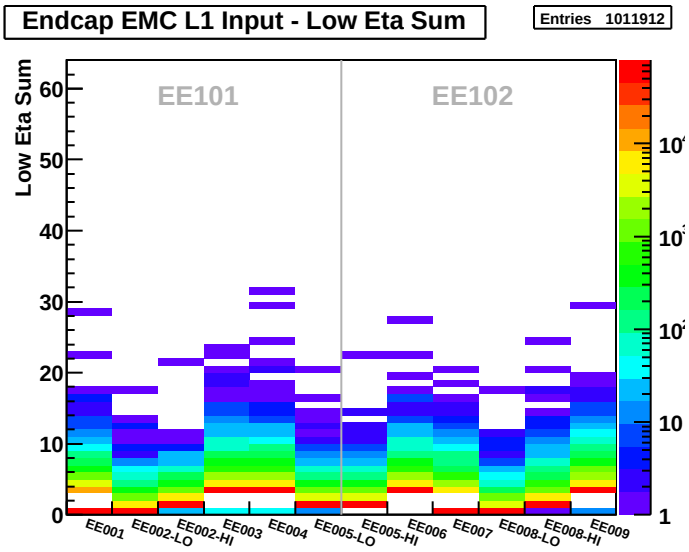

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

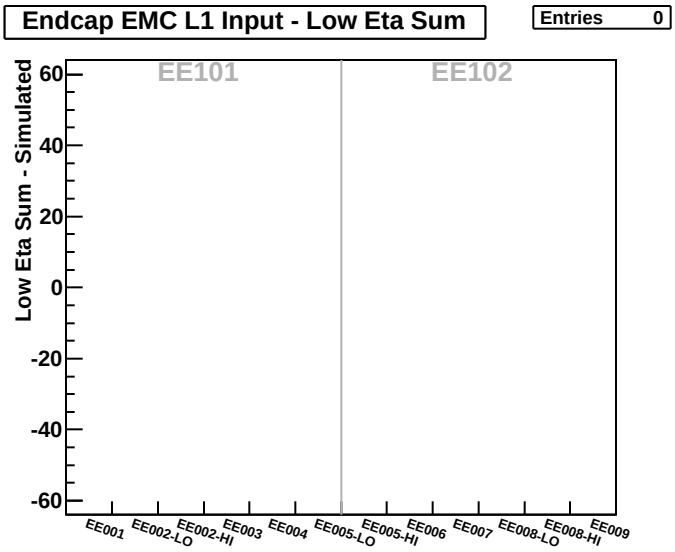

Barrel EMC L1 Input - High Eta Sum
Entries 3035736
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

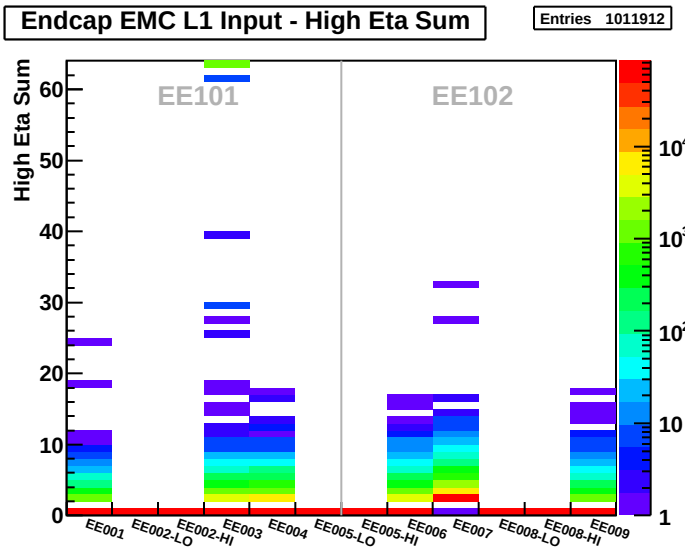

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

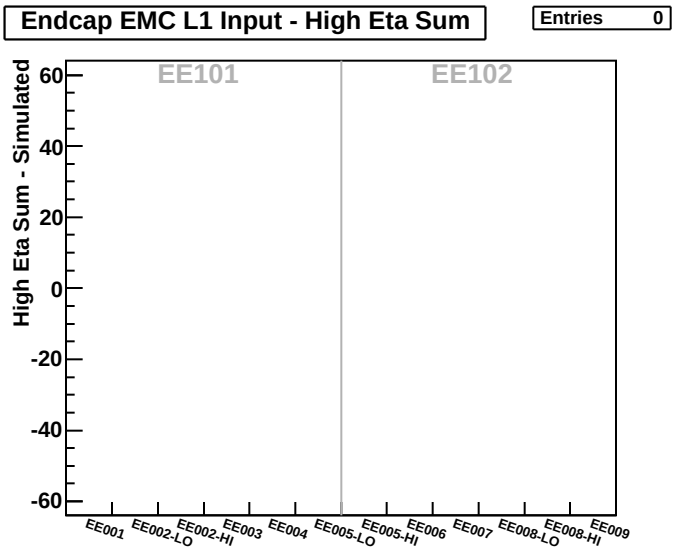

Barrel EMC L1 Input - High Tower Bits
Entries 3035736
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

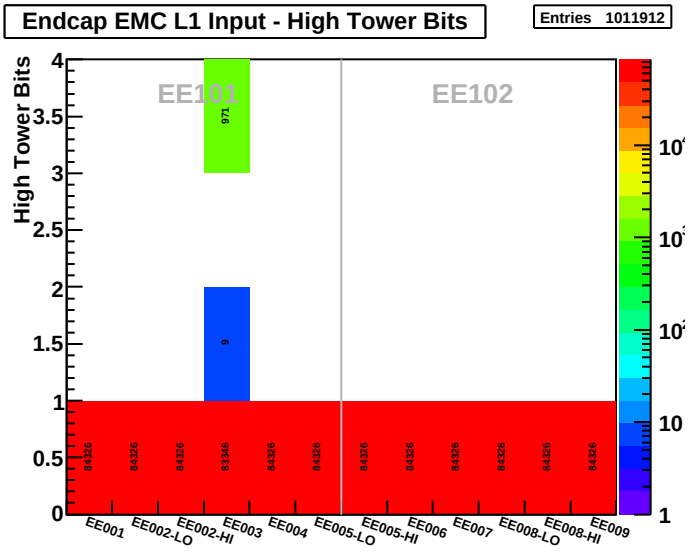

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

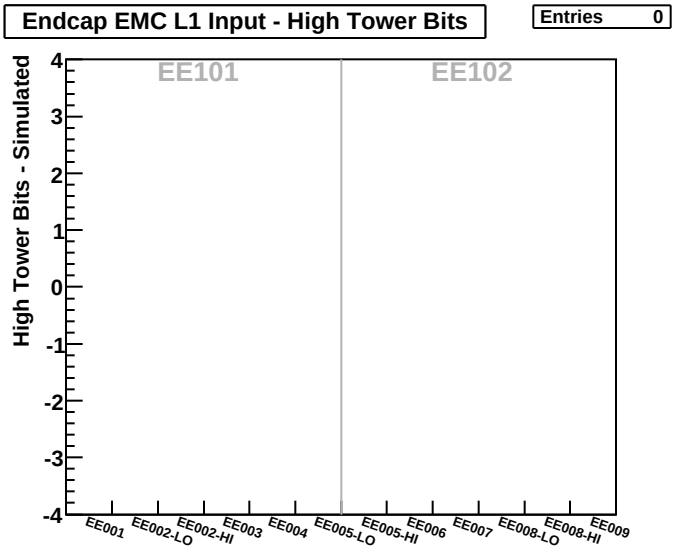

Endcap EMC L1 Input - Low Eta Sum
Entries 1011912
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

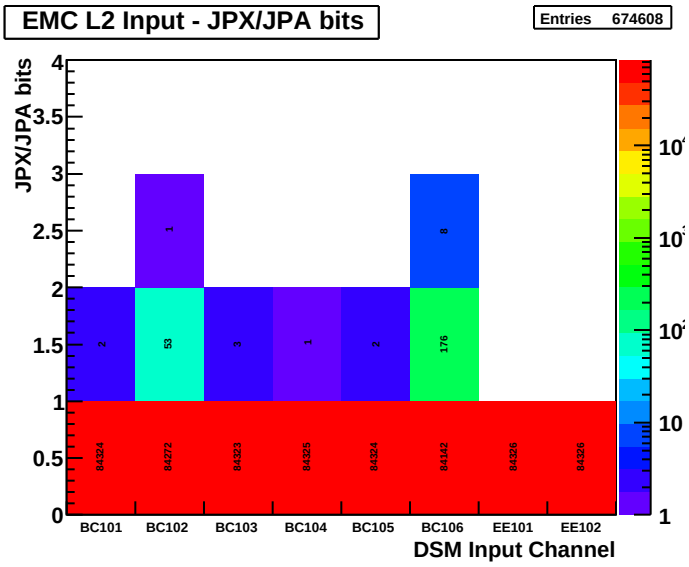

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

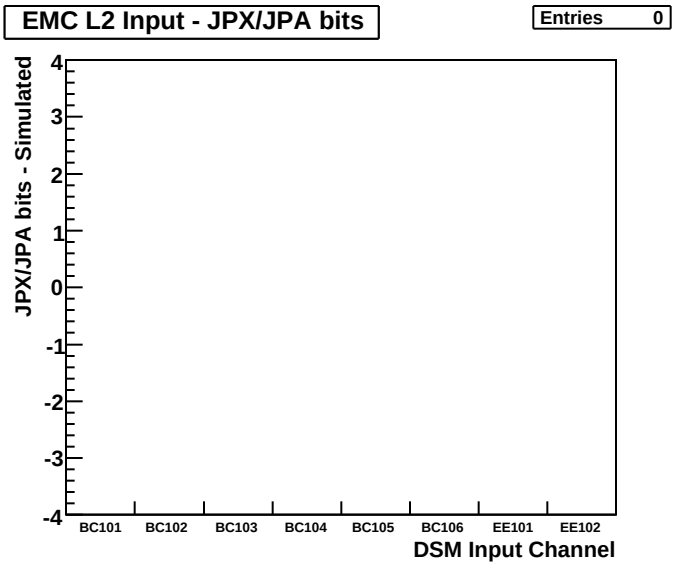

Endcap EMC L1 Input - High Eta Sum
Entries 1011912
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

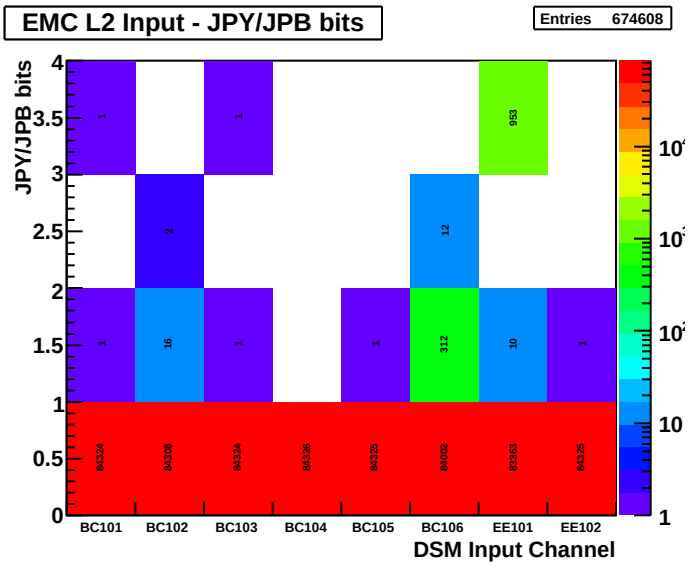

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

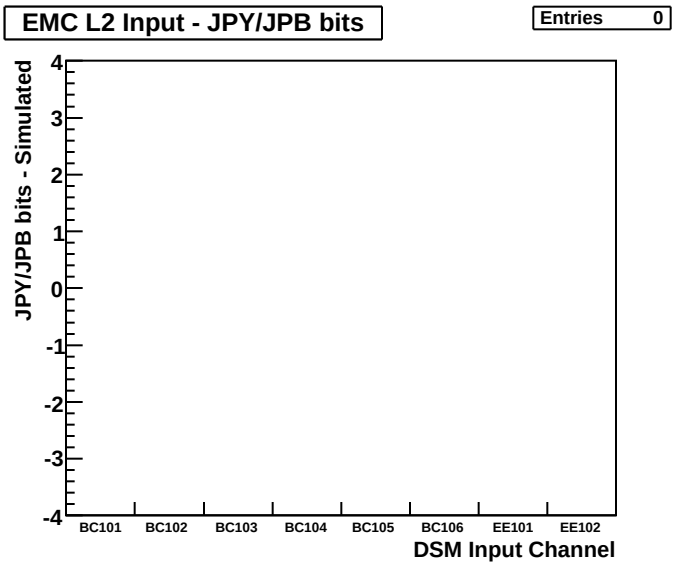

Endcap EMC L1 Input - High Tower Bits
Entries 1011912
High Tower Bits
EE101
EE102
971
9
84326
83346
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

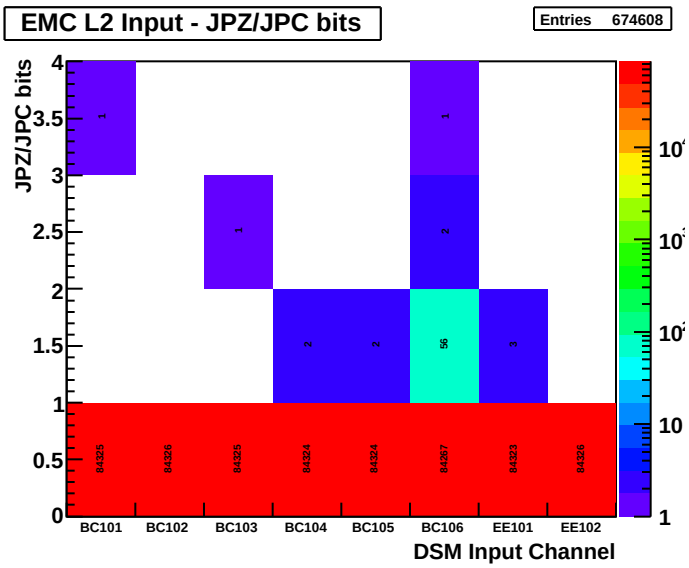

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

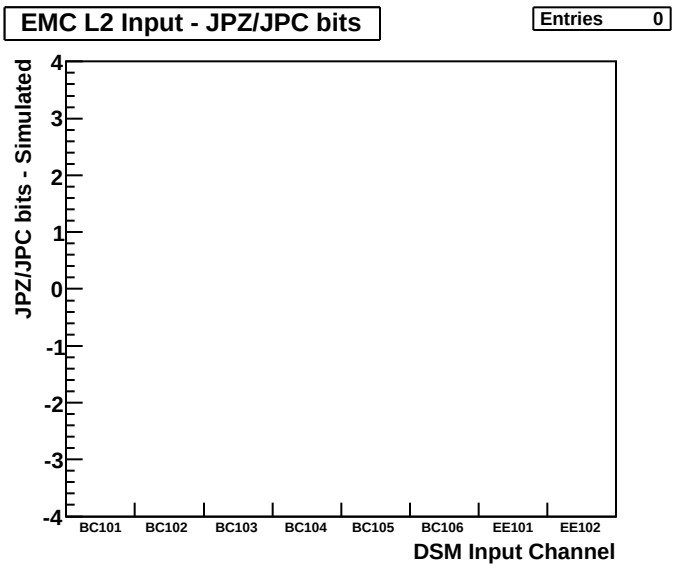

EMC L2 Input - JPX/JPA bits
Entries 674608
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

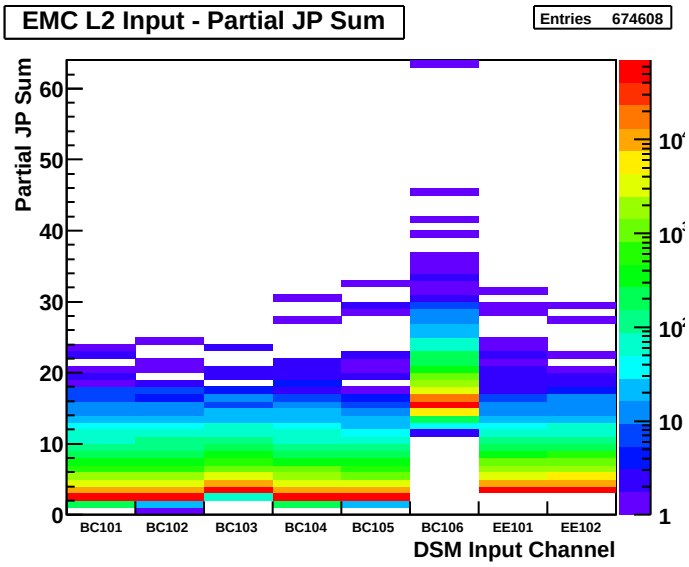

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

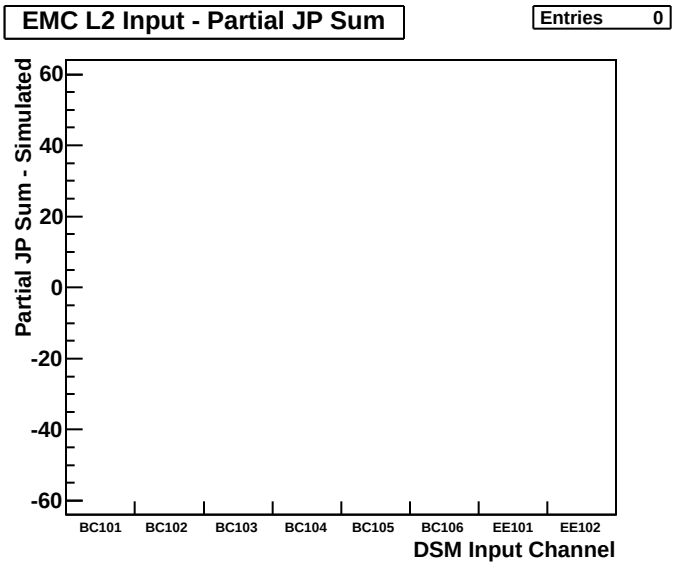

EMC L2 Input - JPY/JPB bits
Entries 674608
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

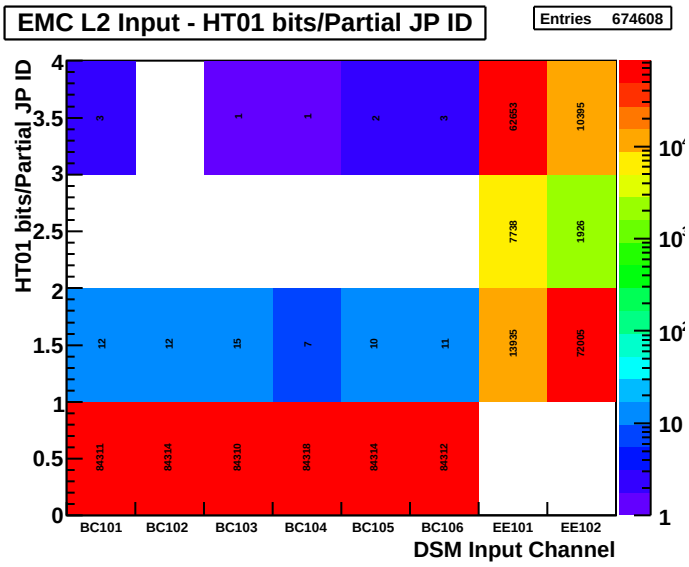

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

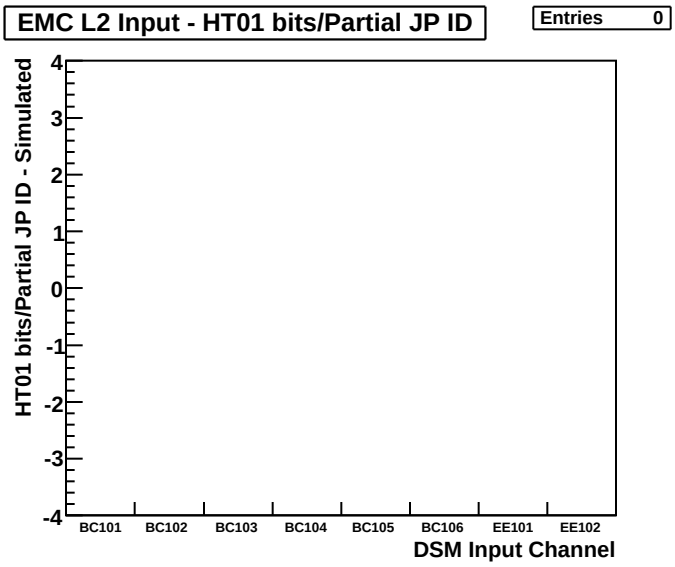

EMC L2 Input - JPZ/JPC bits
Entries 674608
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

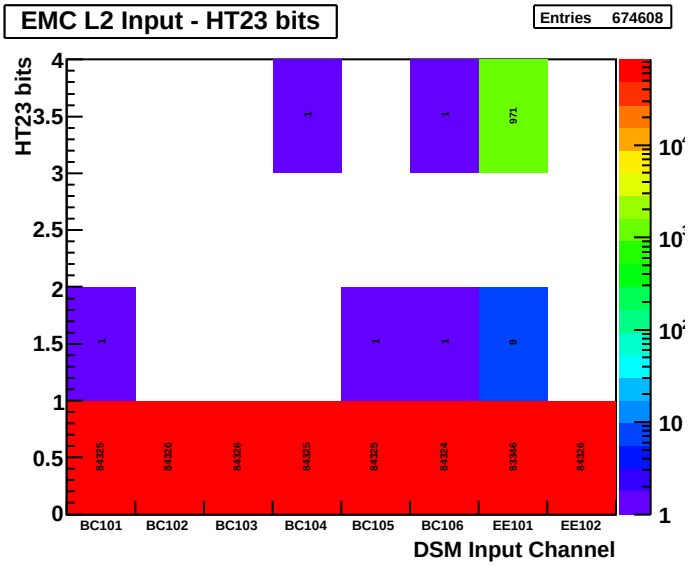

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

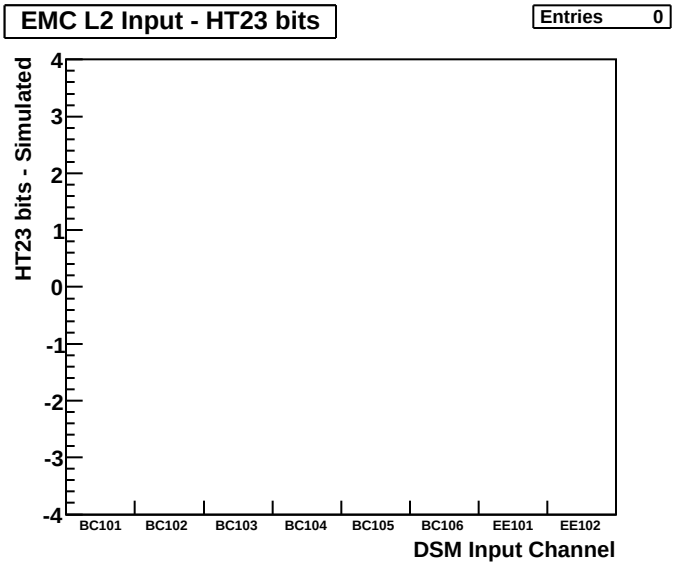

EMC L2 Input - Partial JP Sum
Entries 674608
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

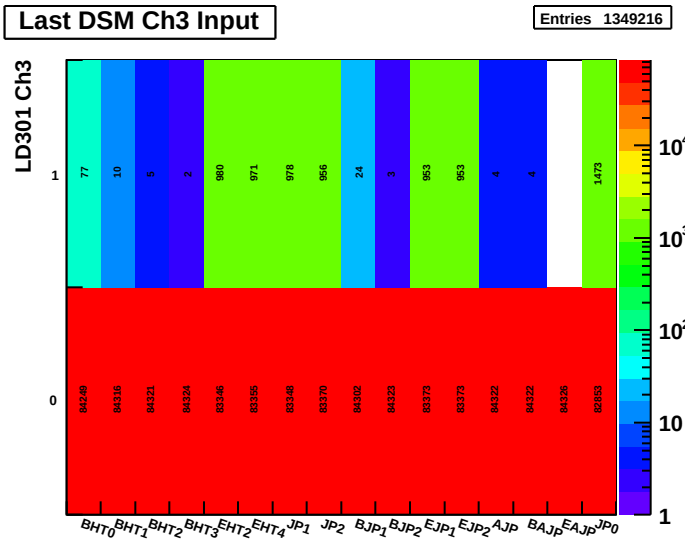

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

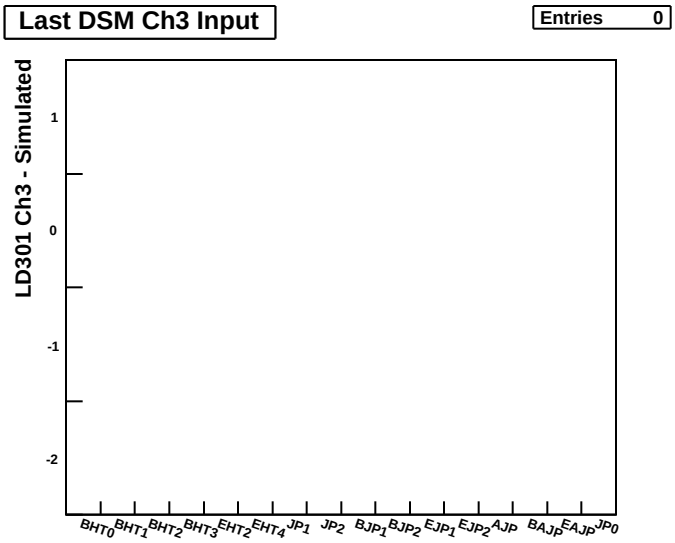

EMC L2 Input - HT01 bits/Partial JP ID
Entries 674608
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

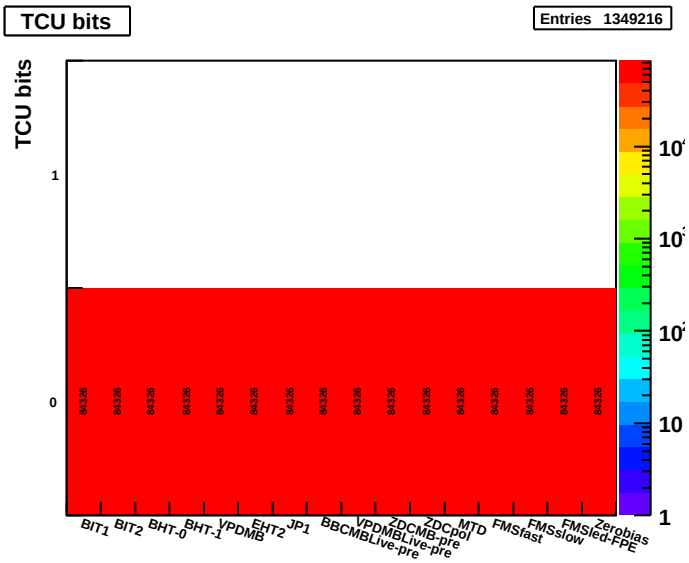

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

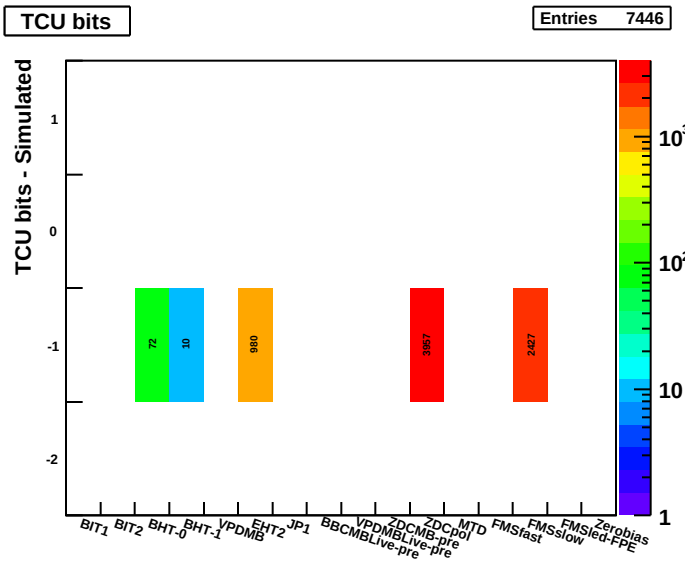

EMC L2 Input - HT23 bits
Entries 674608
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

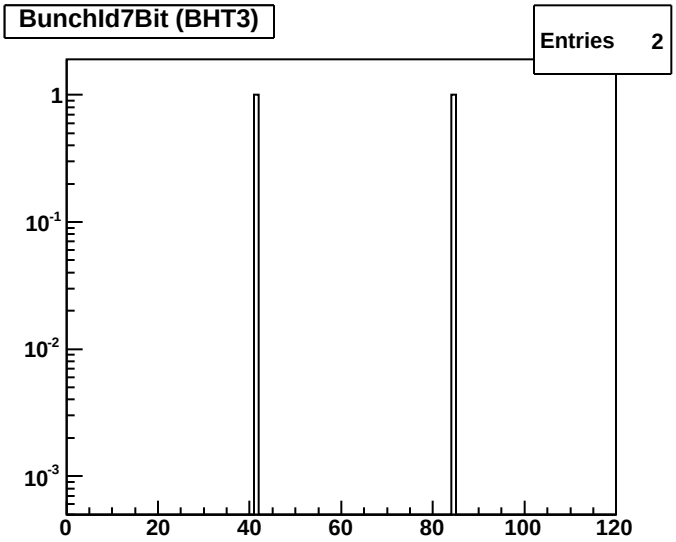

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

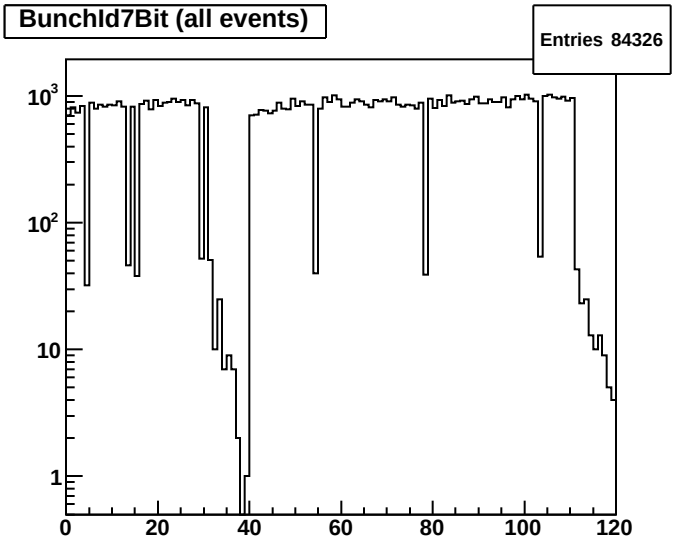

Last DSM Ch3 Input
Entries 1349216
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

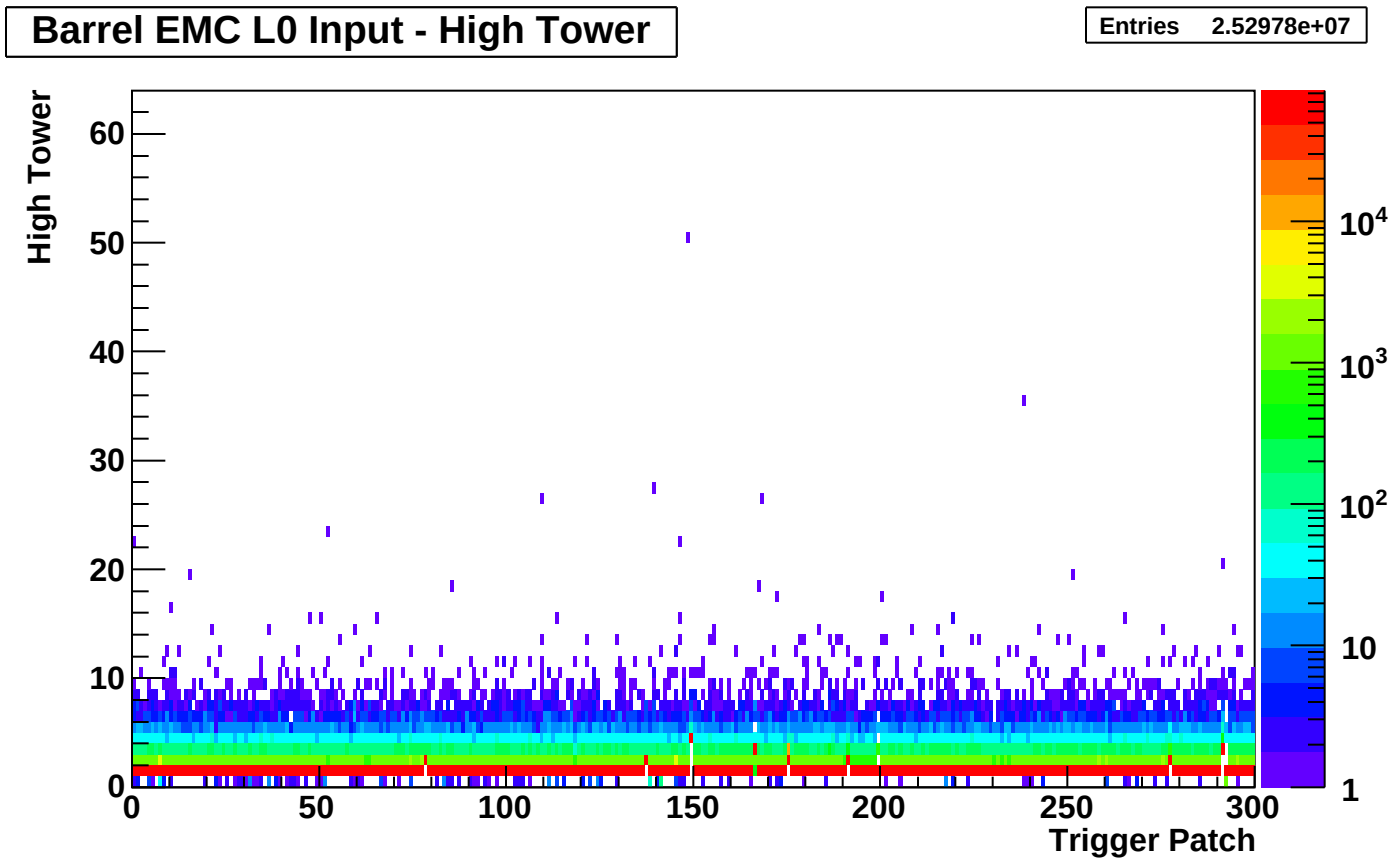

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

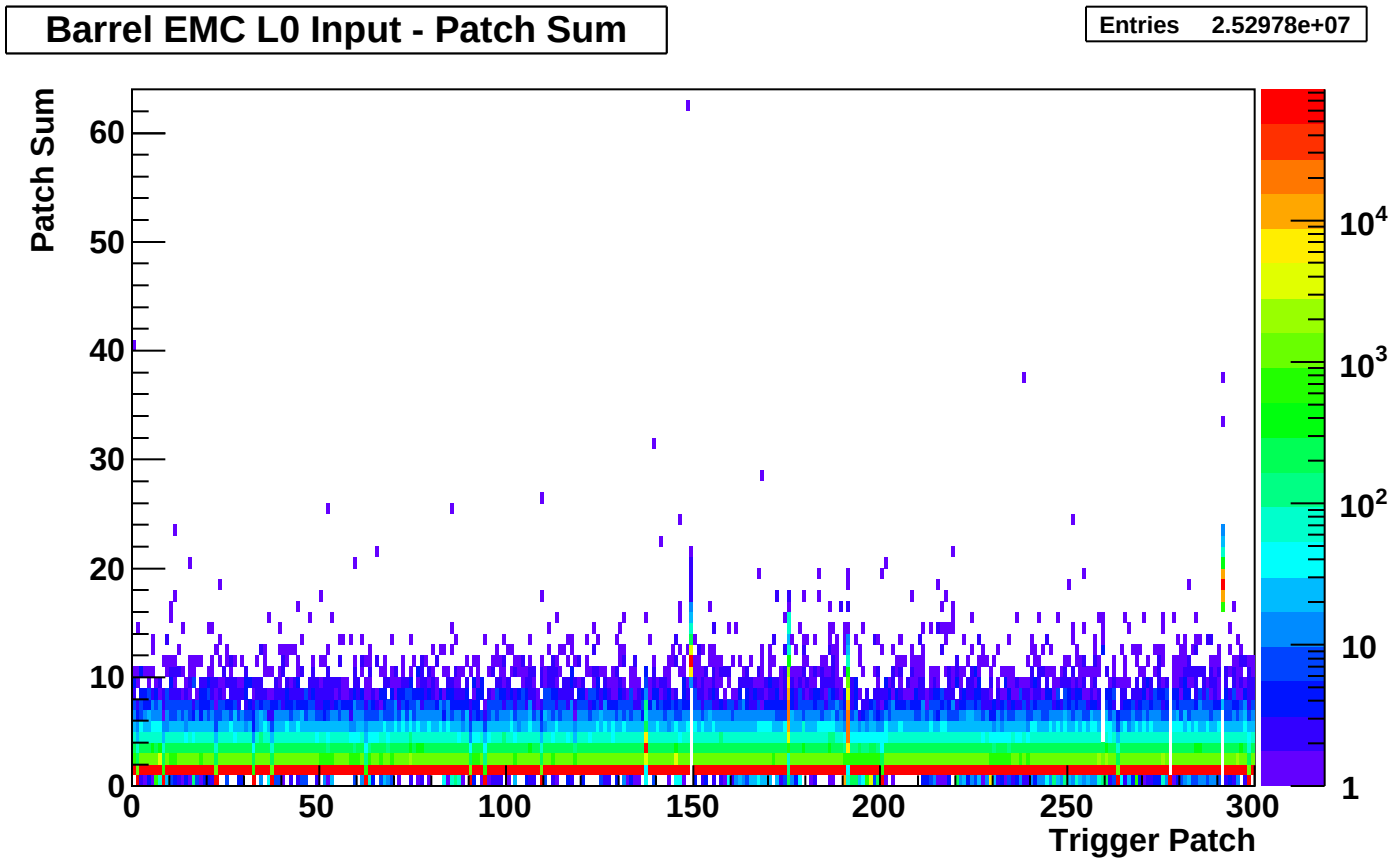

TCU bits
Entries 1349216
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

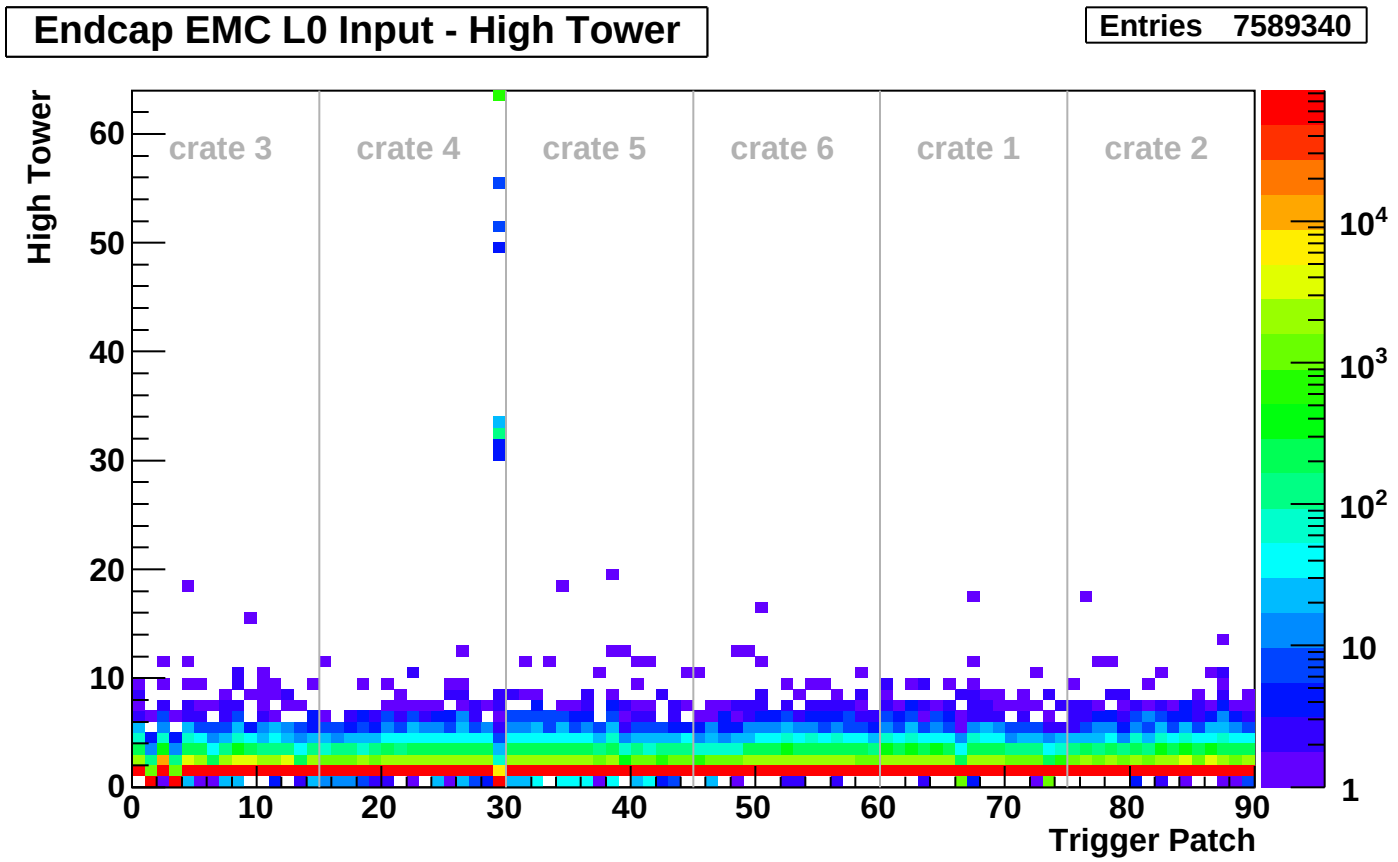

TCU bits
Entries 7446
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

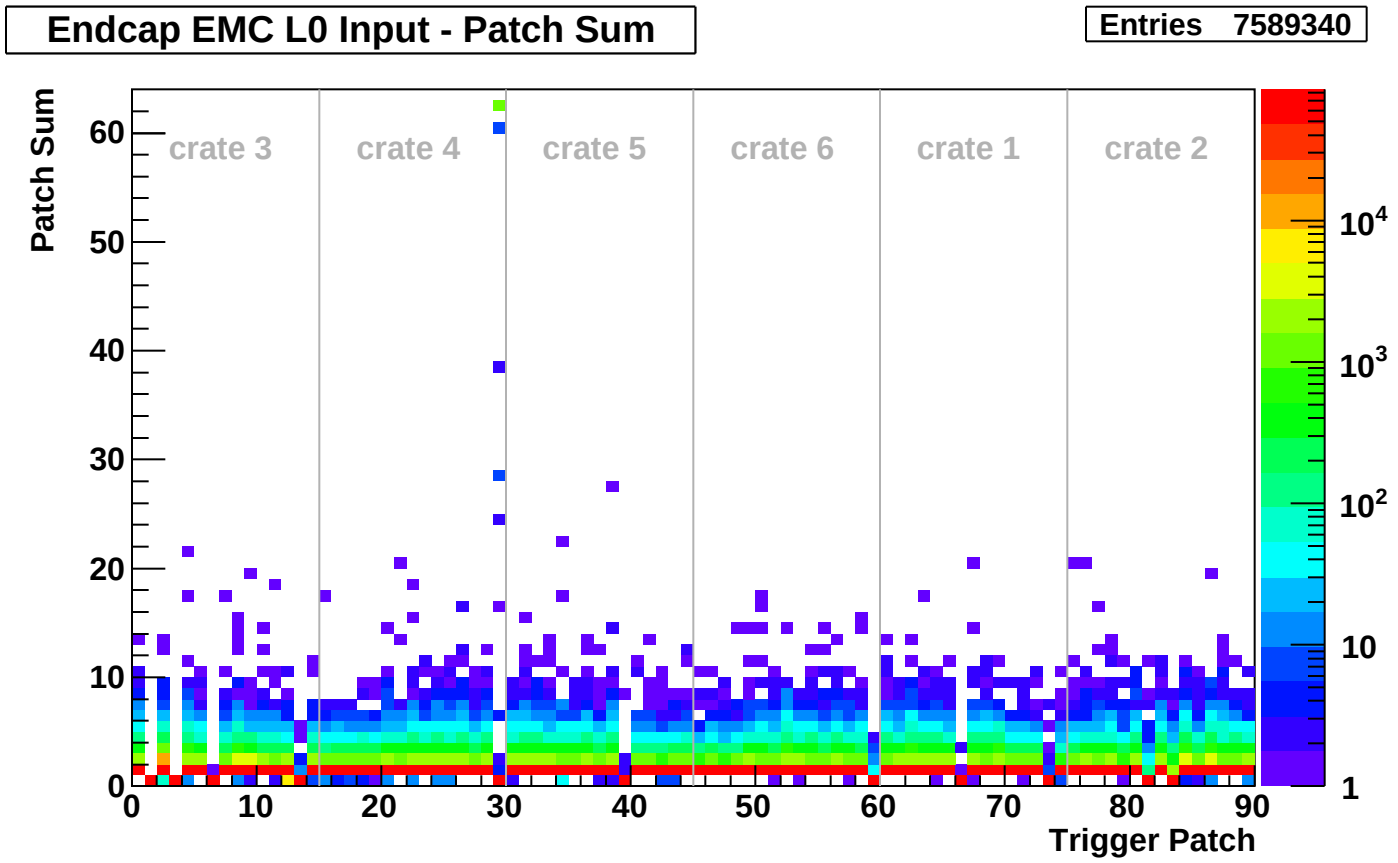

BunchId7Bit (BHT3)
Entries 2

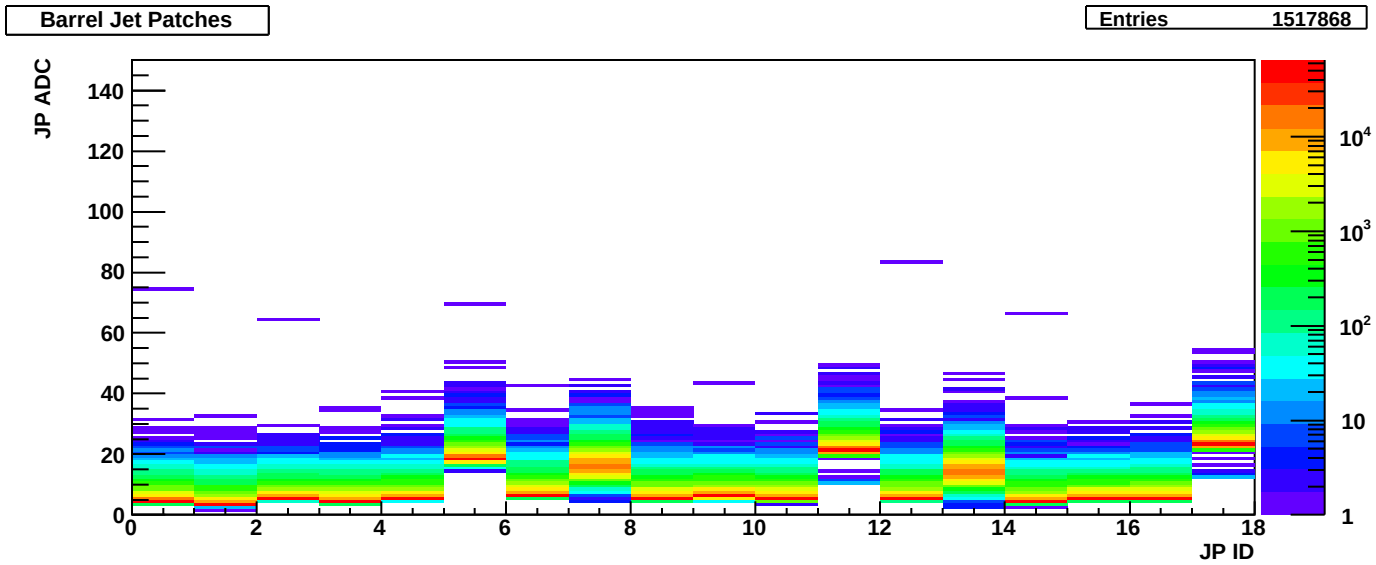

BunchId7Bit (all events)
Entries 84326

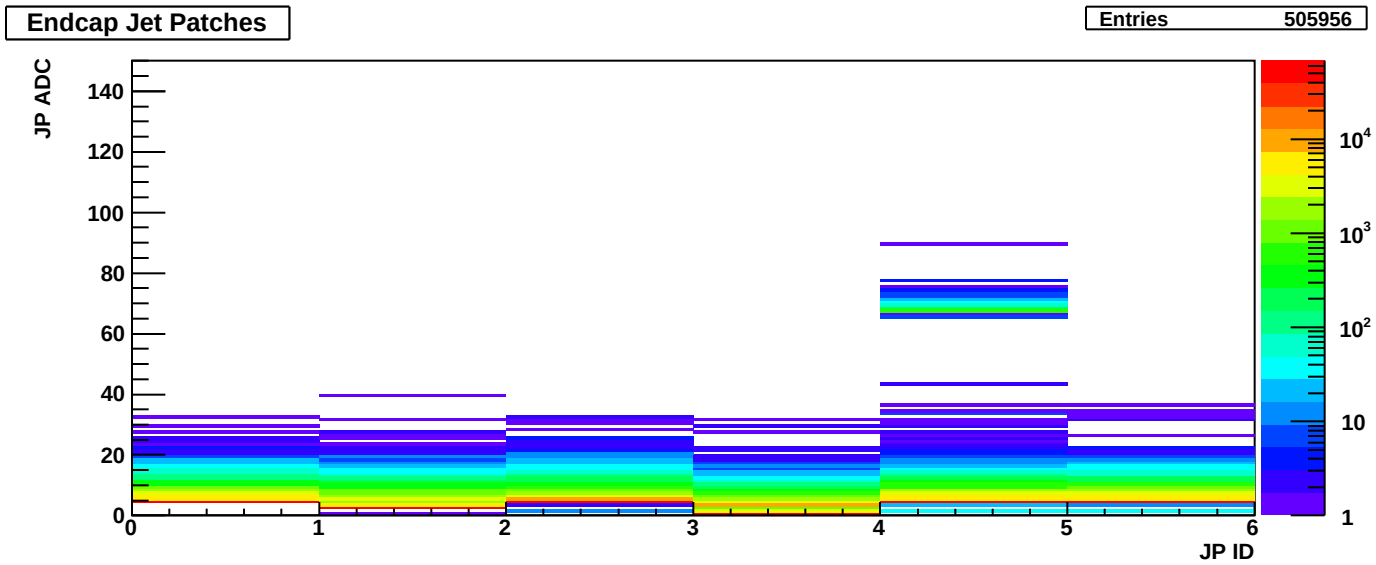

Barrel EMC L0 Input - High Tower
Entries 2.52978e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

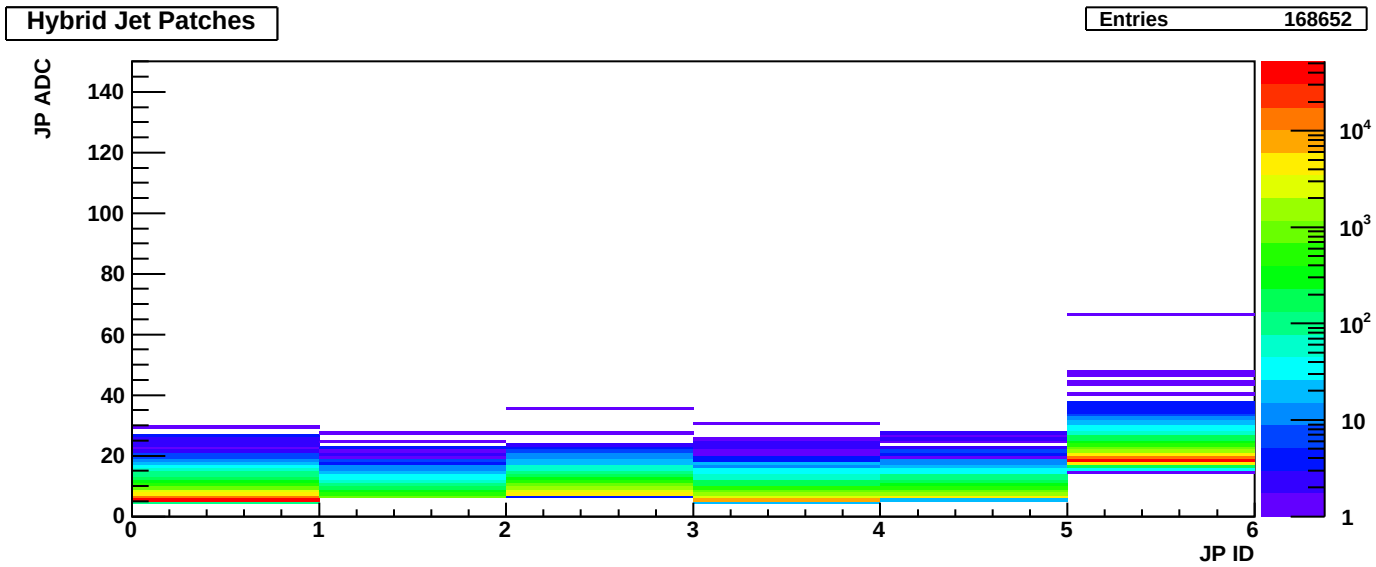

Barrel EMC L0 Input - Patch Sum
Entries 2.52978e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

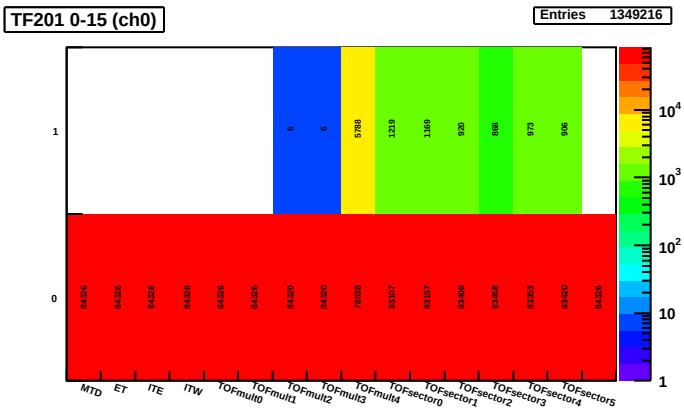

Endcap EMC L0 Input - High Tower
Entries 7589340
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

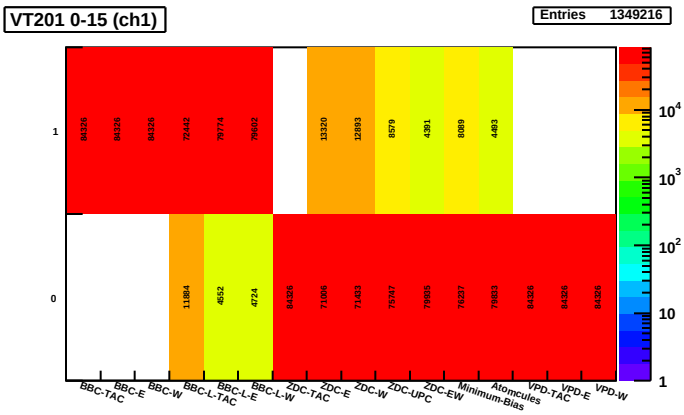

Endcap EMC L0 Input - Patch Sum
Entries 7589340
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

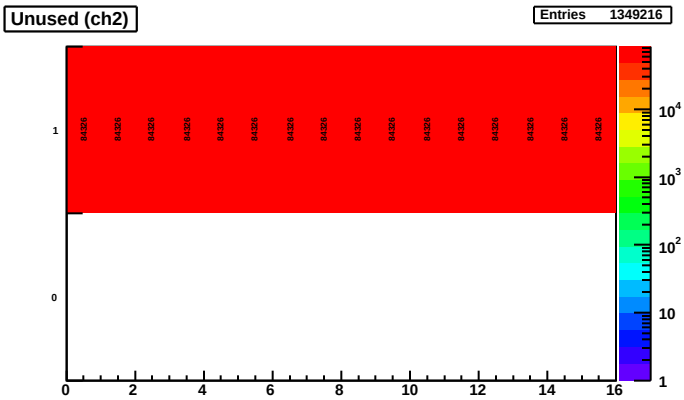

Barrel Jet Patches
Entries 1517868
JP ADC
JP ID

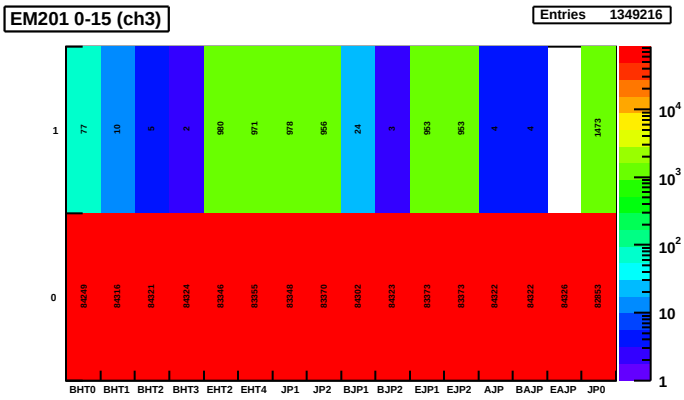

Endcap Jet Patches
Entries 505956
JP ADC
JP ID

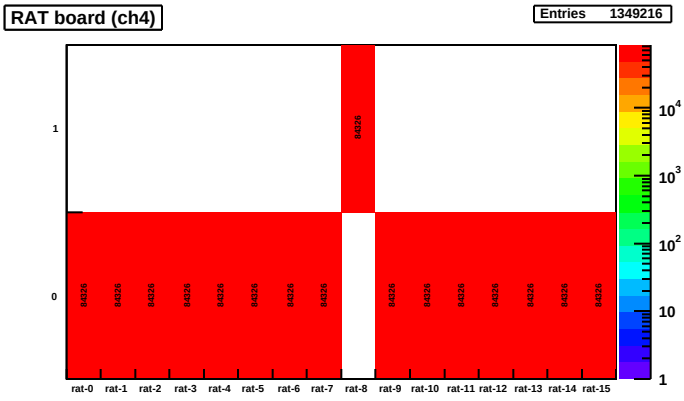

Hybrid Jet Patches
Entries 168652
JP ADC
JP ID

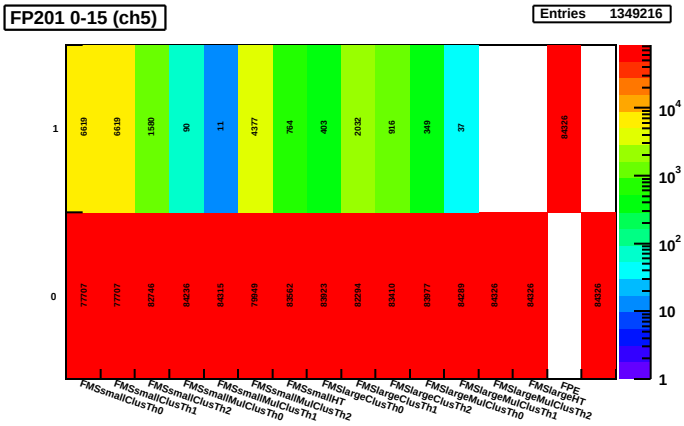

TF201 0-15 (ch0)
Entries 1349216
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

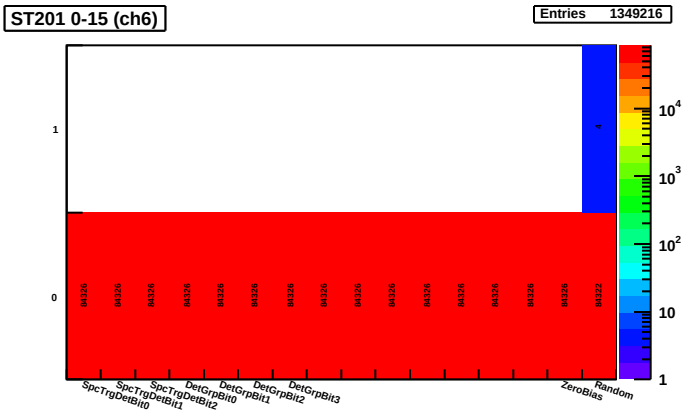

VT201 0-15 (ch1)
Entries 1349216
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

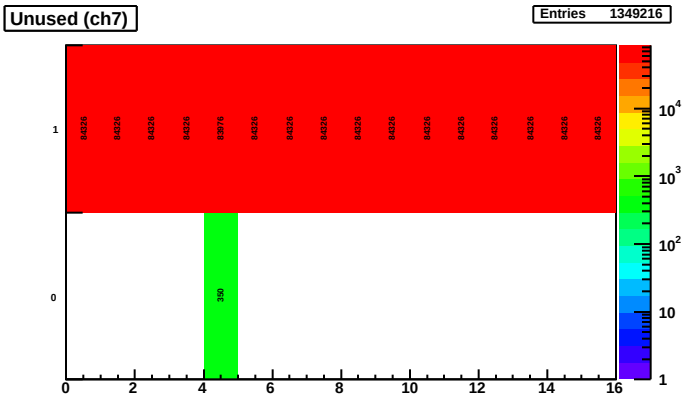

Unused (ch2)
Entries 1349216

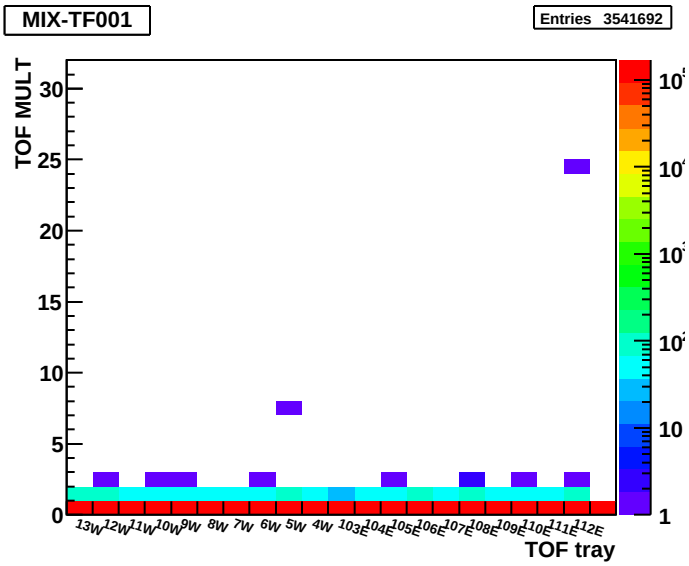

EM201 0-15 (ch3)
Entries 1349216
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

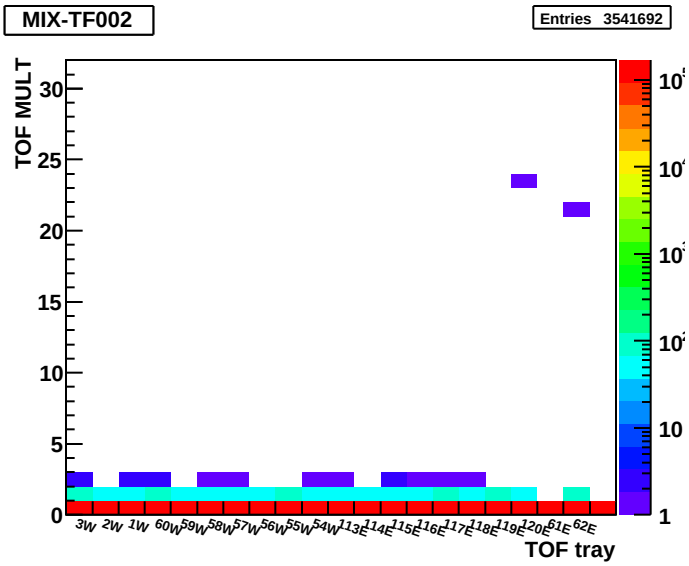

RAT board (ch4)
Entries 1349216
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

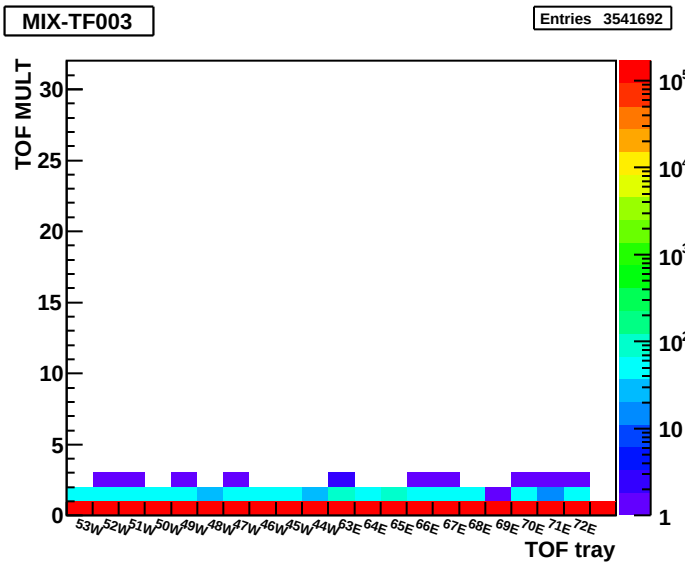

FP201 0-15 (ch5)
Entries 1349216
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

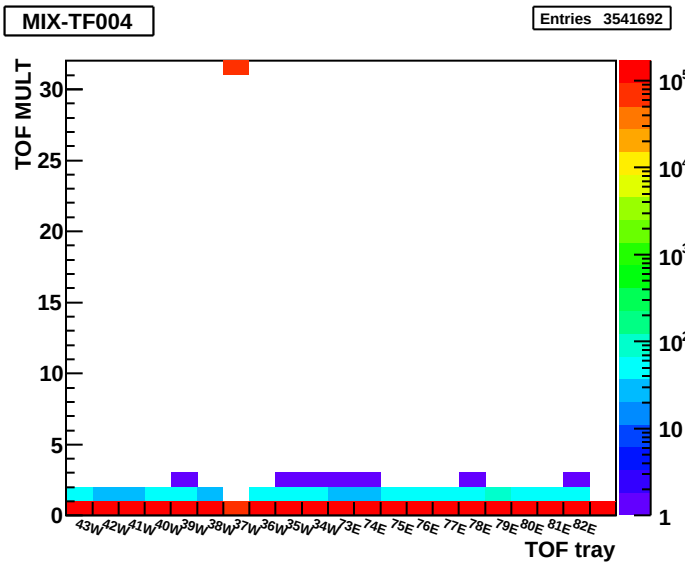

ST201 0-15 (ch6)
Entries 1349216
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

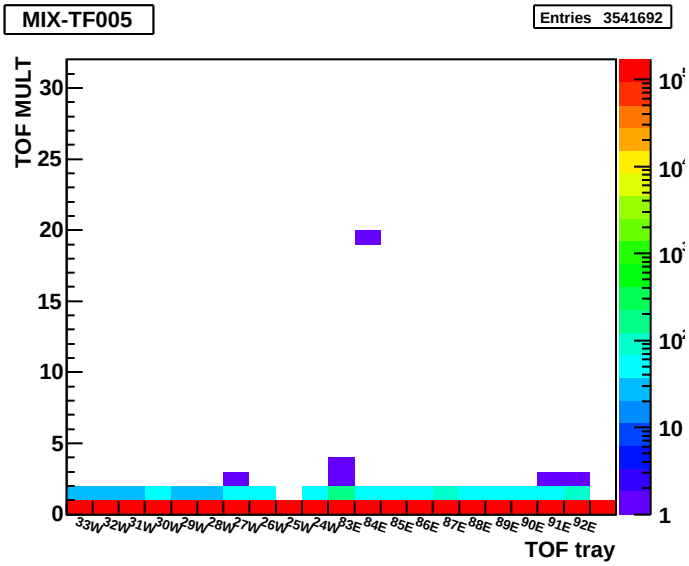

Unused (ch7)
Entries 1349216

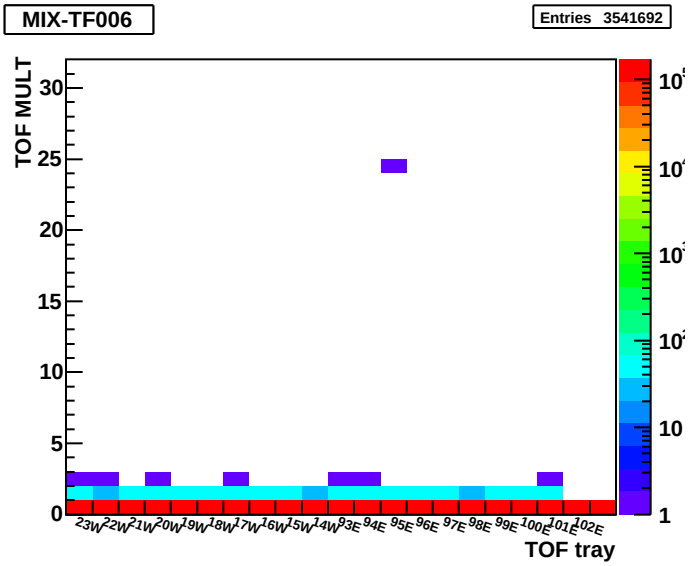

MIX-TF001
Entries 3541692
TOF MULT
TOF tray

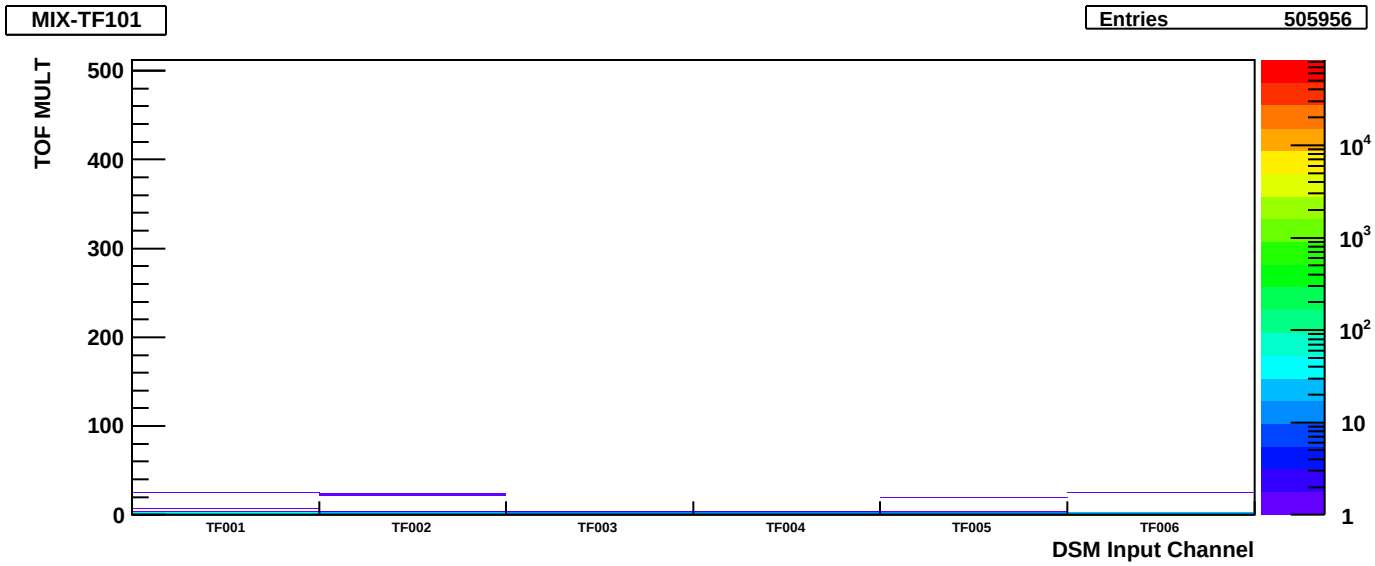

MIX-TF002
Entries 3541692
TOF MULT
TOF tray

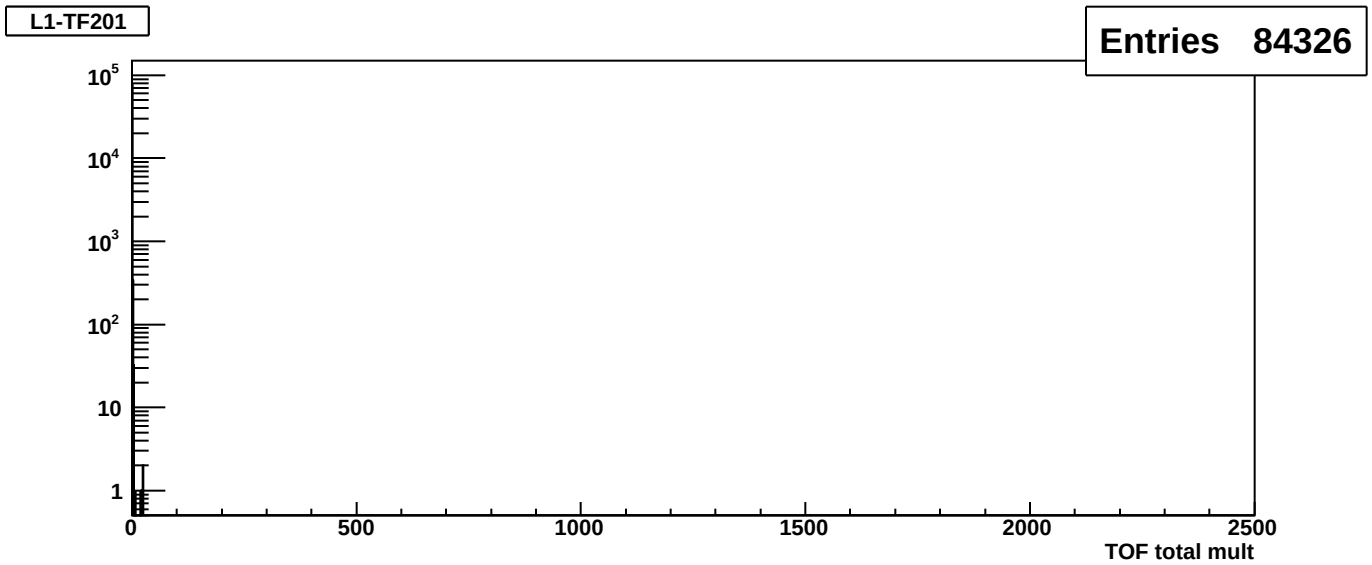

MIX-TF003
Entries 3541692
TOF MULT
TOF tray

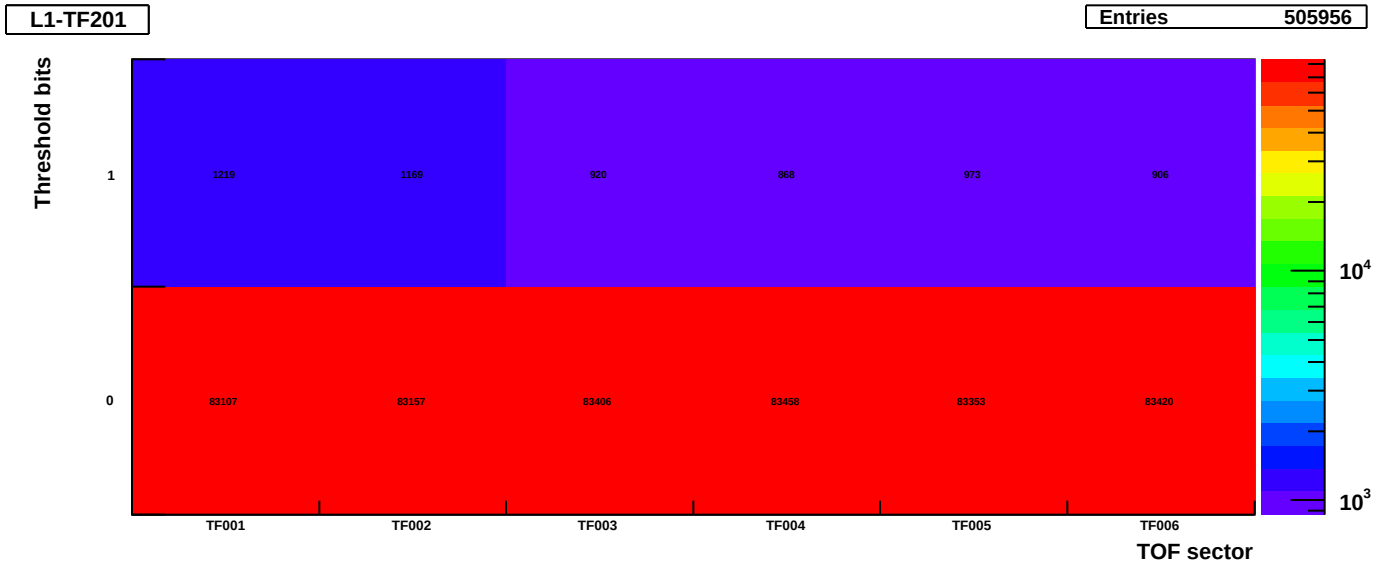

MIX-TF004
Entries 3541692
TOF MULT
TOF tray

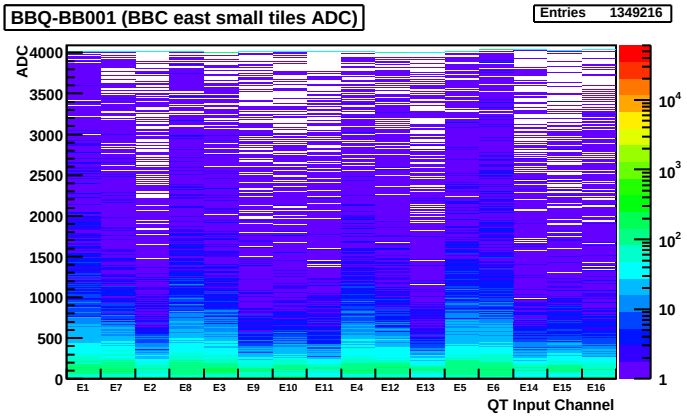

MIX-TF005
Entries 3541692
TOF MULT
TOF tray

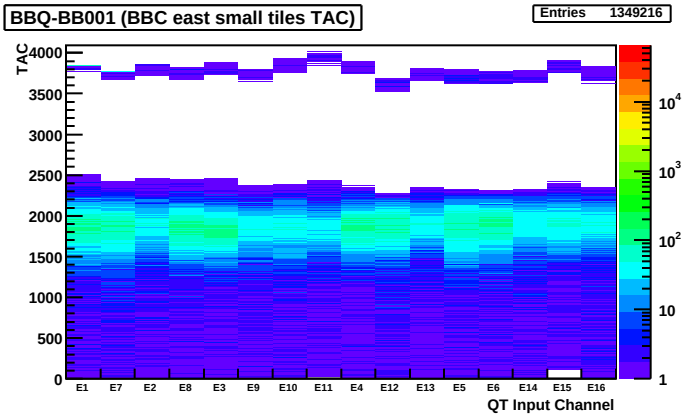

MIX-TF006
Entries 3541692
TOF MULT
TOF tray

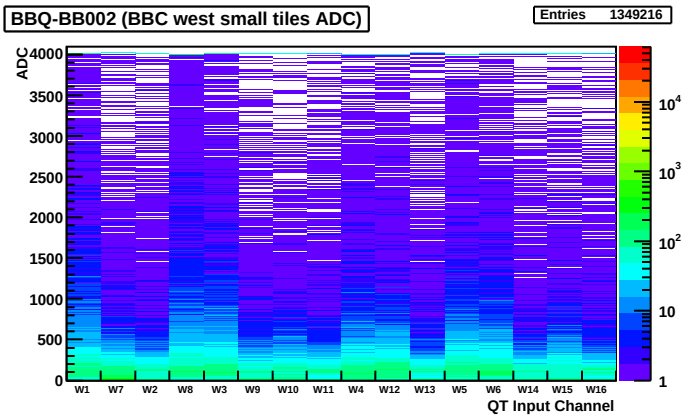

MIX-TF101
Entries 505956
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

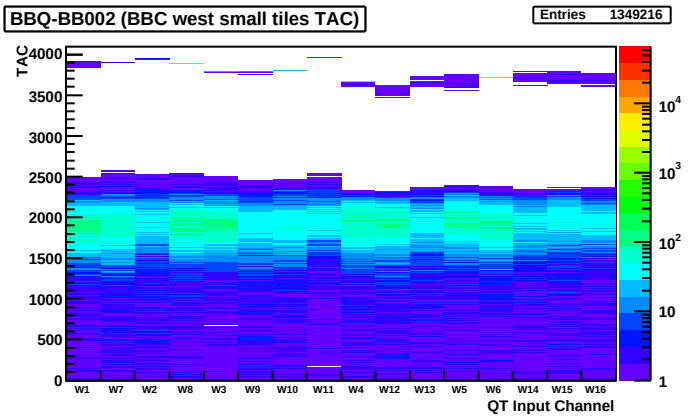

L1-TF201
Entries 84326
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

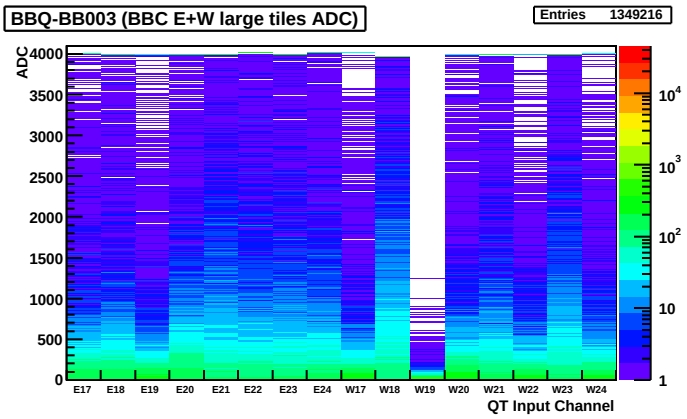

L1-TF201
Entries 505956
Threshold bits
1
0
1219
1169
920
868
973
906
83107
83157
83406
83458
83353
83420
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

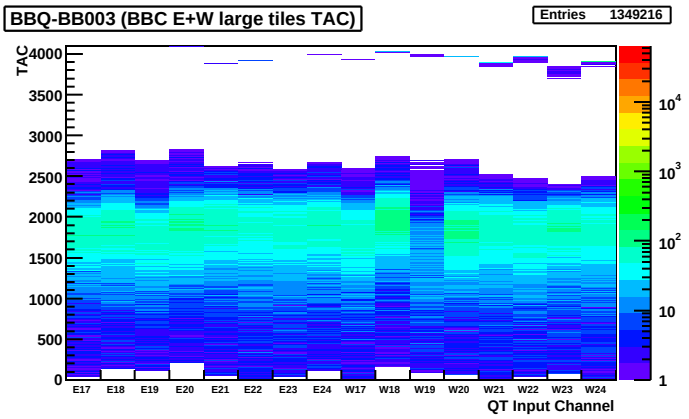

BBQ-BB001 (BBC east small tiles ADC)
Entries 1349216
ADC
QT Input Channel

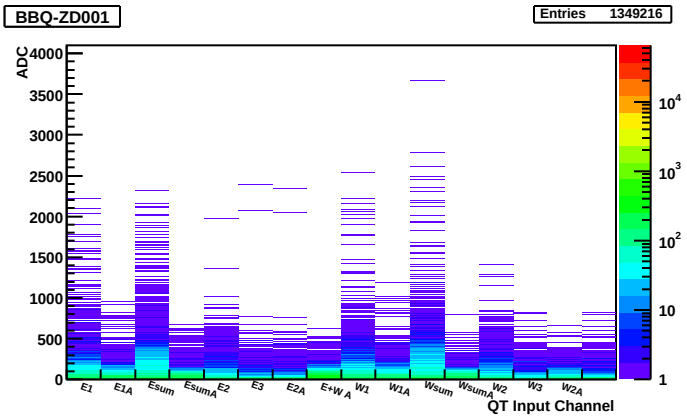

BBQ-BB001 (BBC east small tiles TAC)
Entries 1349216
TAC
QT Input Channel

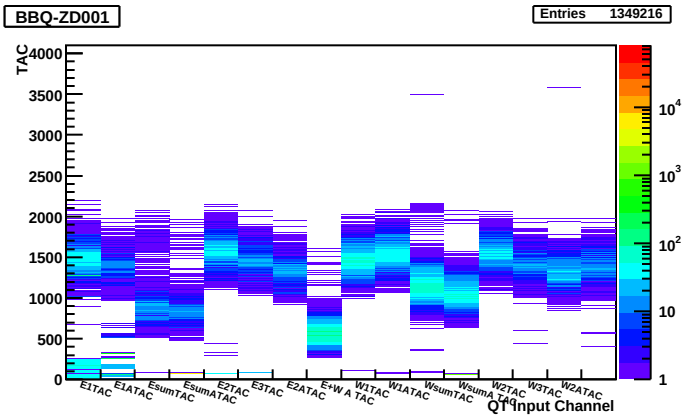

BBQ-BB002 (BBC west small tiles ADC)
Entries 1349216
ADC
QT Input Channel

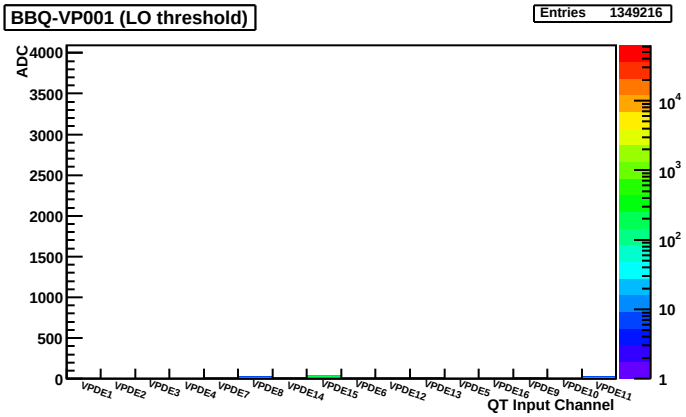

BBQ-BB002 (BBC west small tiles TAC)
Entries 1349216
TAC
QT Input Channel

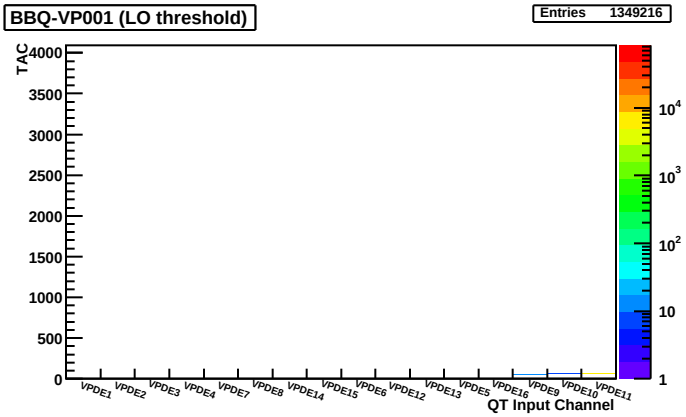

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1349216
ADC
QT Input Channel

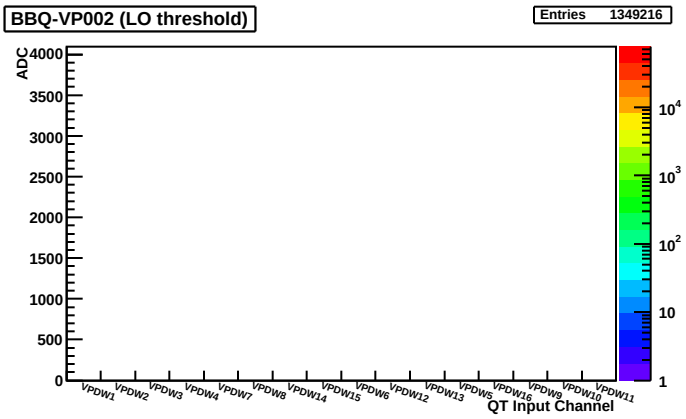

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1349216
TAC
QT Input Channel

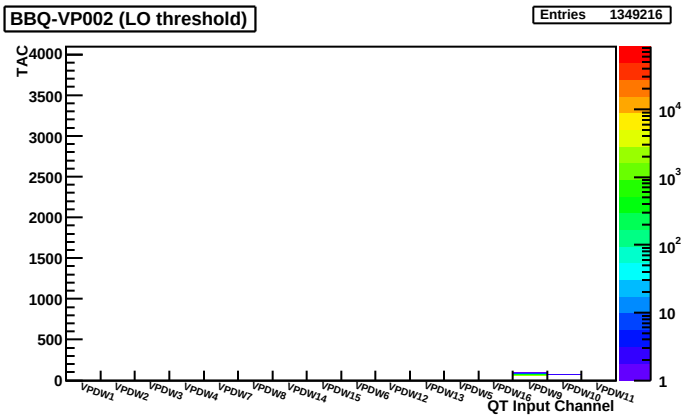

BBQ-ZD001
Entries 1349216
ADC
QT Input Channel

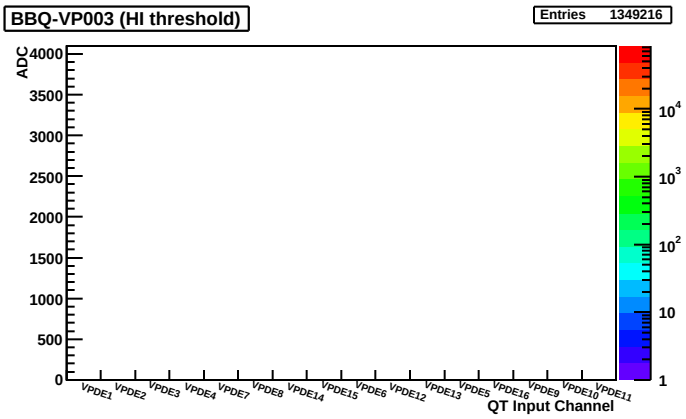

BBQ-ZD001
Entries 1349216
TAC
QT Input Channel

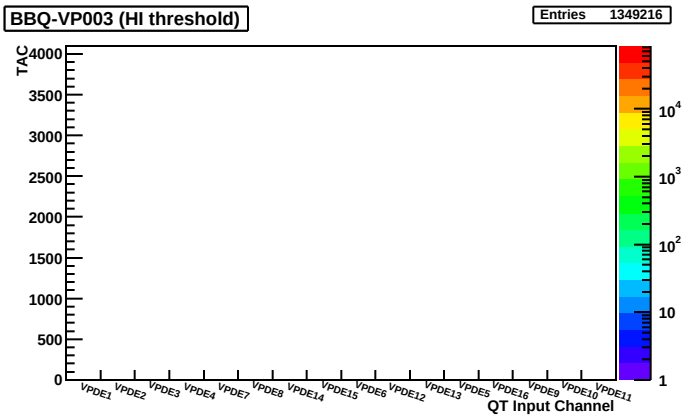

BBQ-VP001 (LO threshold)
Entries 1349216
ADC
QT Input Channel

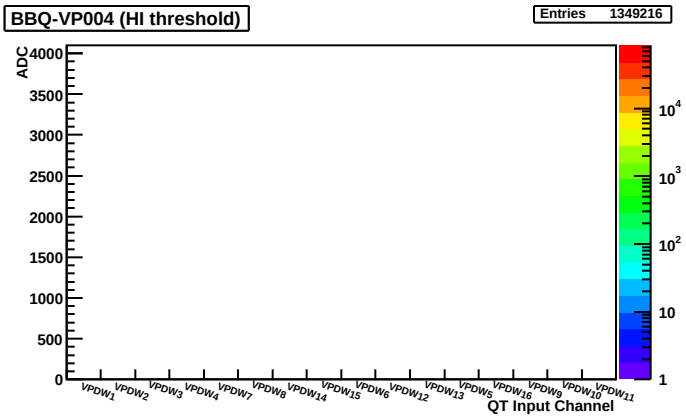

BBQ-VP001 (LO threshold)
Entries 1349216
TAC
QT Input Channel

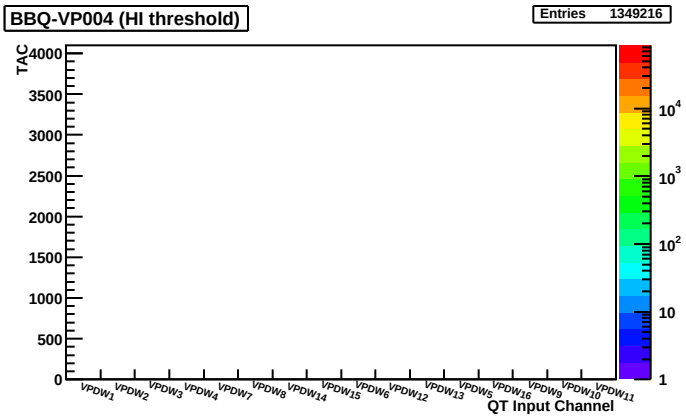

BBQ-VP002 (LO threshold)
Entries 1349216
ADC
QT Input Channel

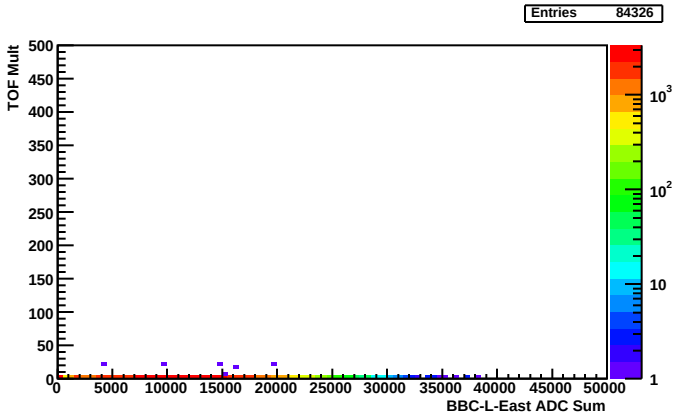

BBQ-VP002 (LO threshold)
Entries 1349216
TAC
QT Input Channel

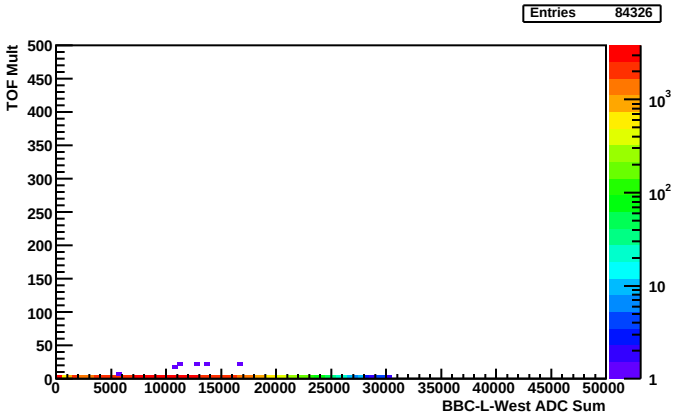

BBQ-VP003 (HI threshold)
Entries 1349216
ADC
QT Input Channel

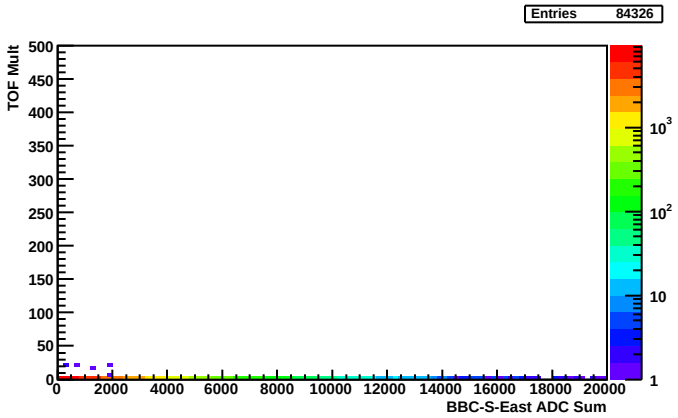

BBQ-VP003 (HI threshold)
Entries 1349216
TAC
QT Input Channel

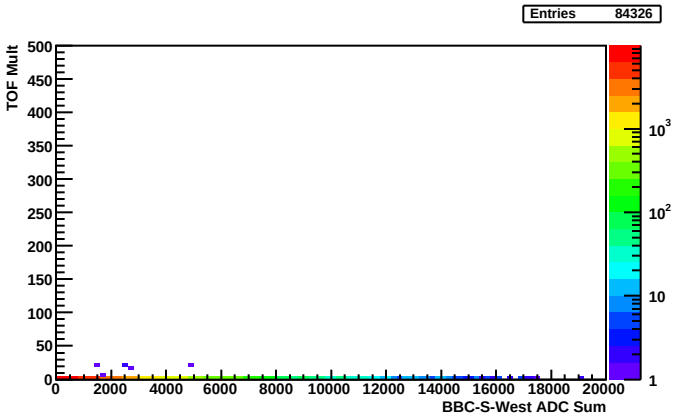

BBQ-VP004 (HI threshold)
Entries 1349216
ADC
QT Input Channel

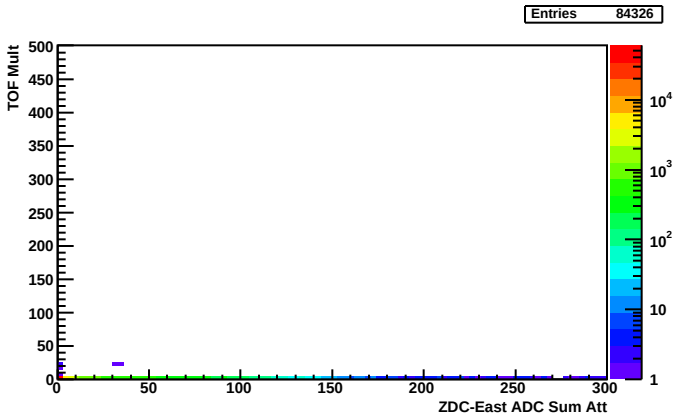

BBQ-VP004 (HI threshold)
Entries 1349216
TAC
QT Input Channel

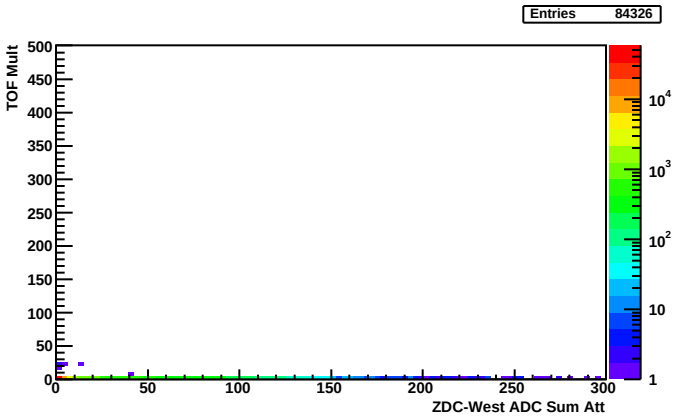

Entries 84326
TOF Mult
BBC-L-East ADC Sum

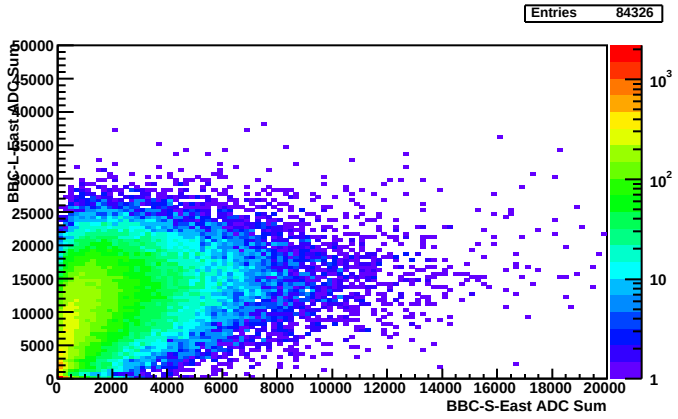

Entries 84326
TOF Mult
BBC-L-West ADC Sum

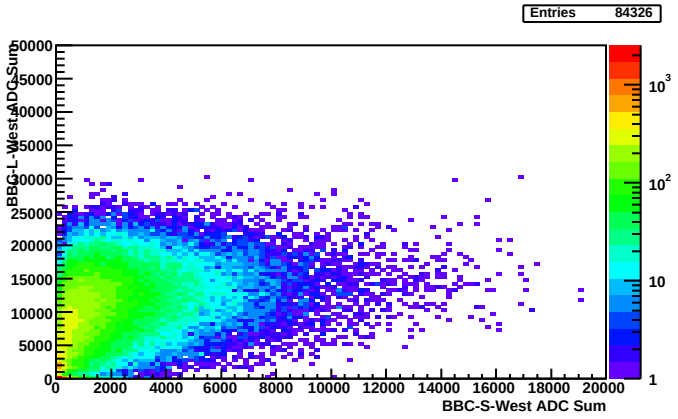

Entries 84326
TOF Mult
BBC-S-East ADC Sum

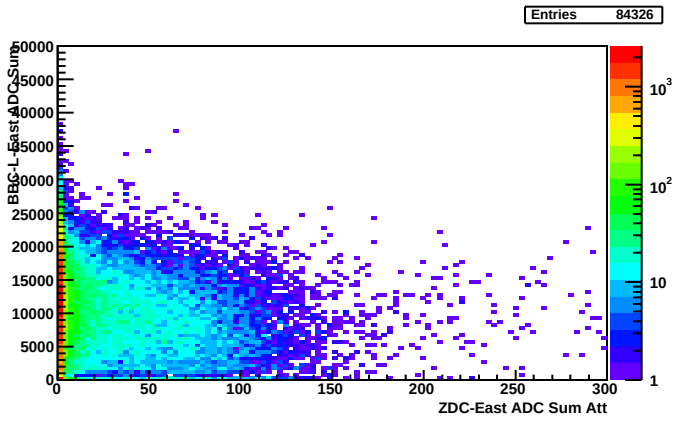

Entries 84326
TOF Mult
BBC-S-West ADC Sum

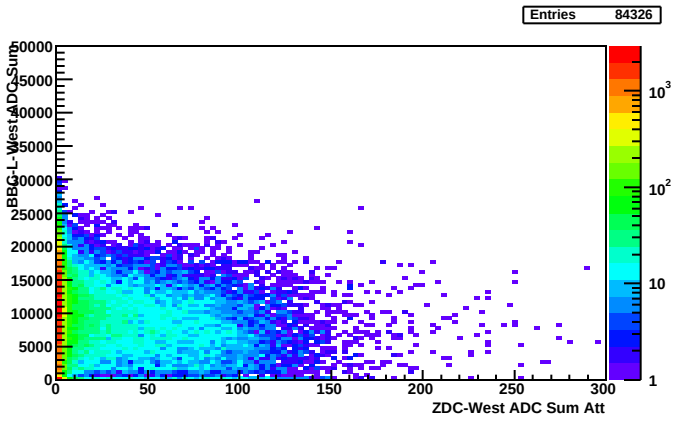

Entries 84326
TOF Mult
ZDC-East ADC Sum Att

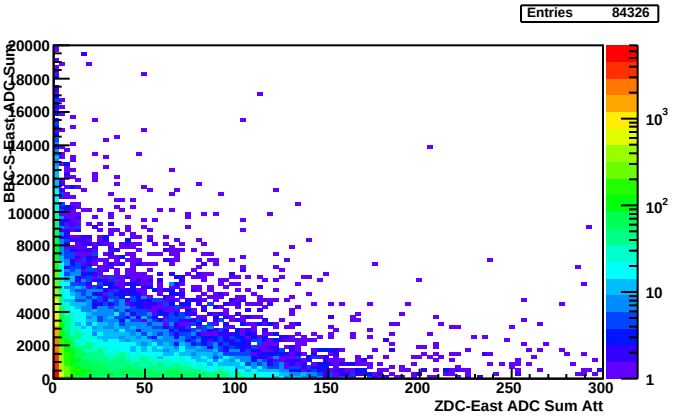

Entries 84326
TOF Mult
ZDC-West ADC Sum Att

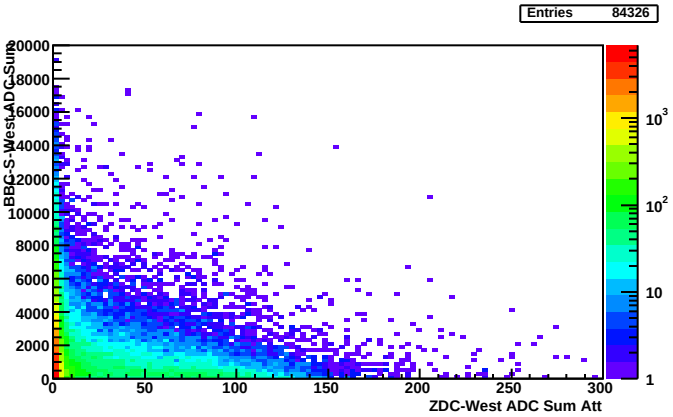

Entries 84326
BBC-L-East ADC Sum
BBC-S-East ADC Sum

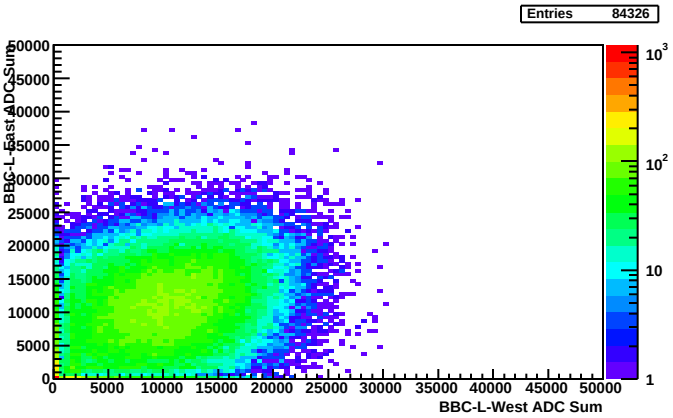

Entries 84326
BBC-L-West ADC Sum
BBC-S-West ADC Sum

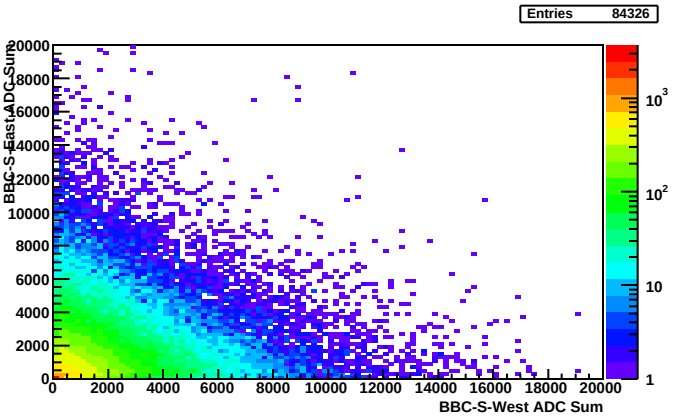

Entries 84326
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

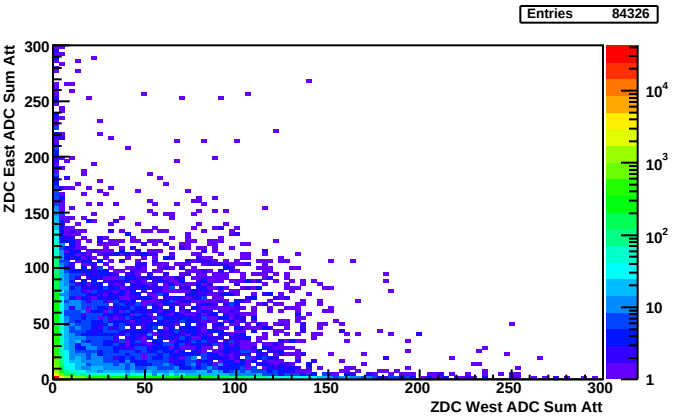

Entries 84326
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

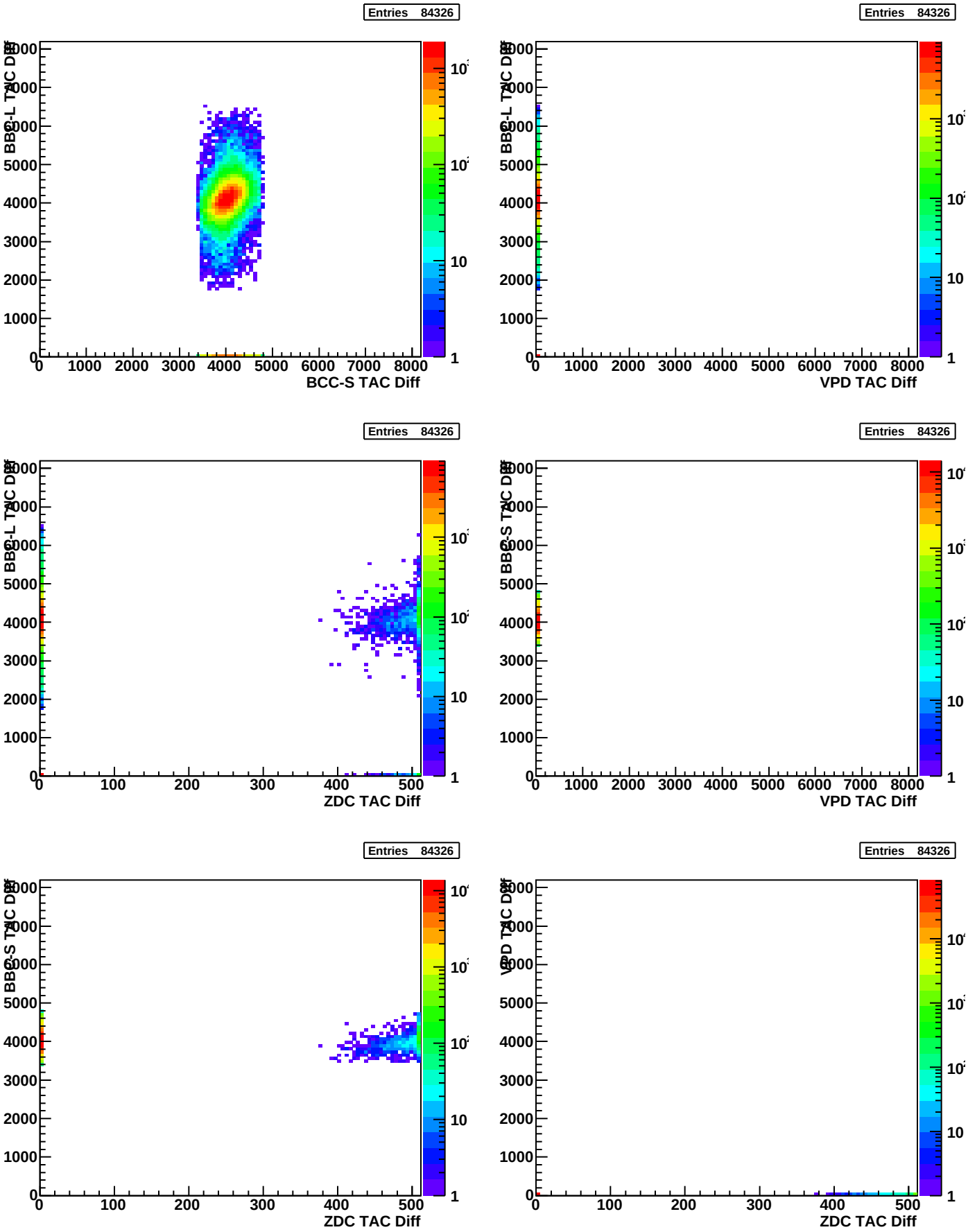

Entries 84326
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

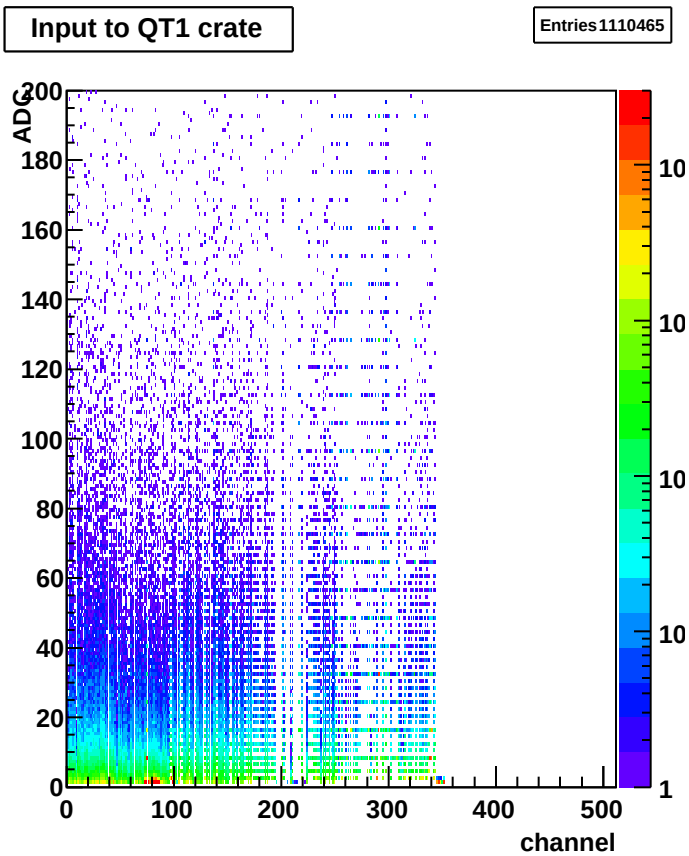

Entries 84326
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

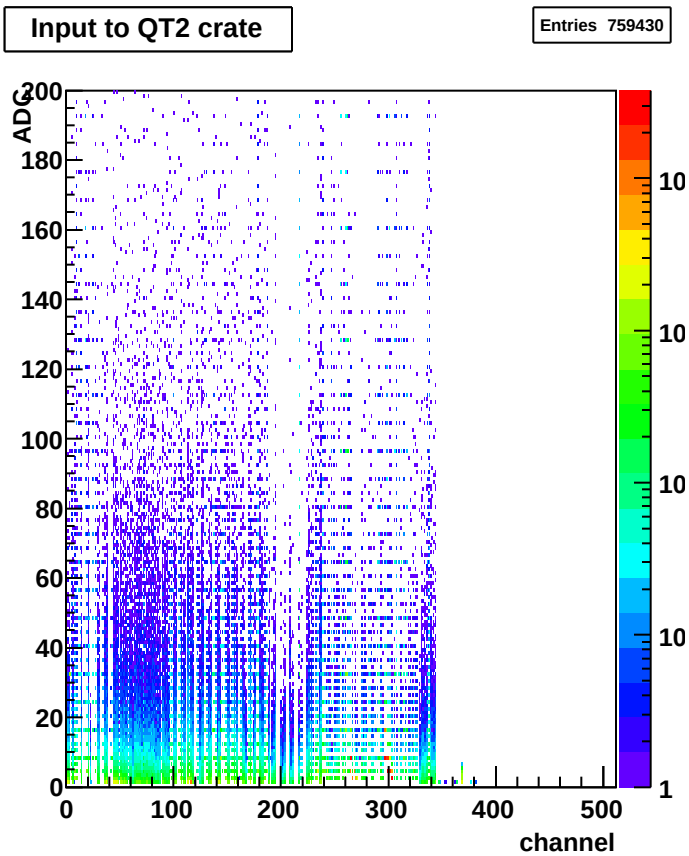

Entries 84326
BBC-L-East ADC Sum
BBC-L-West ADC Sum

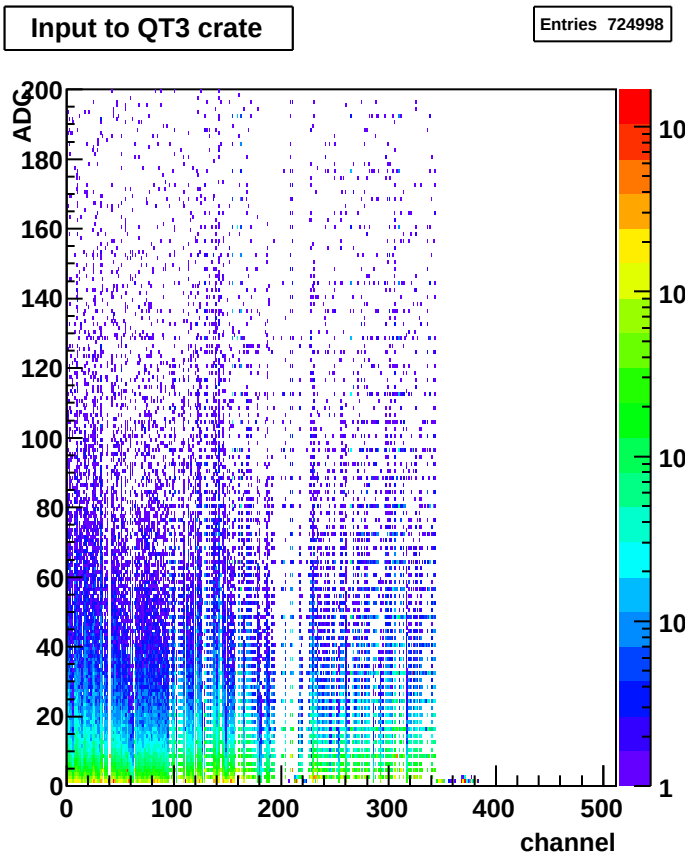

Entries 84326
BBC-S-East ADC Sum
BBC-S-West ADC Sum

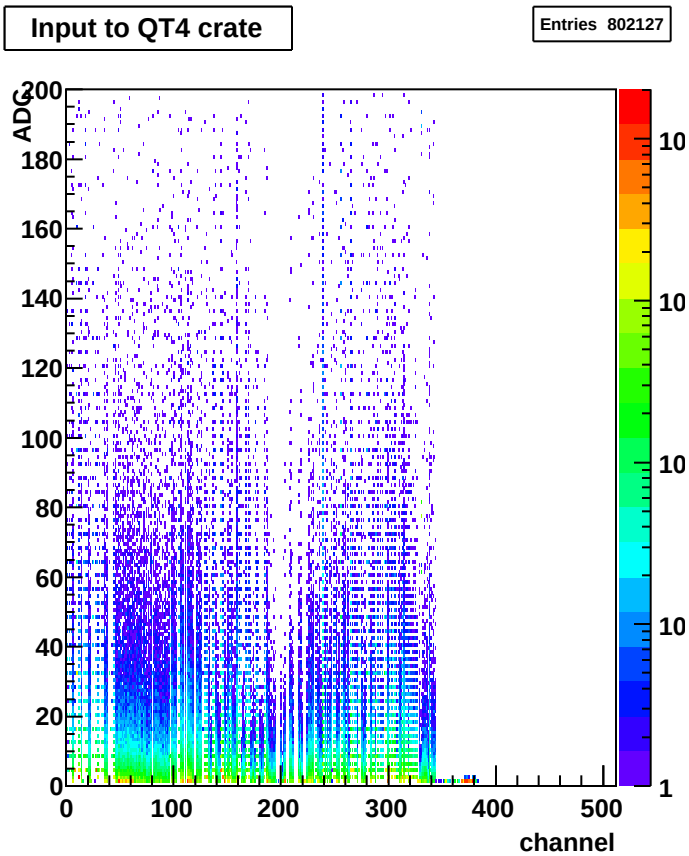

Entries 84326
ZDC East ADC Sum Att
ZDC West ADC Sum Att

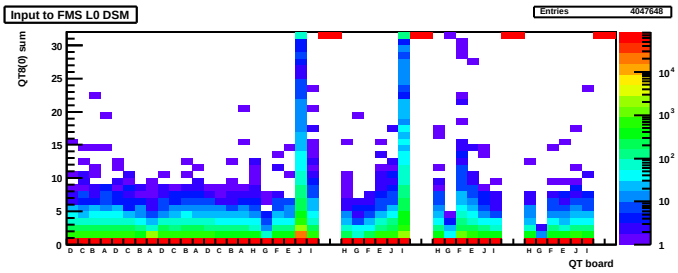

Entries 84326
BBC-L TAC Diff
BCC-S TAC Diff
Entries 84326
BBC-L TAC Diff
VPD TAC Diff
Entries 84326
BBC-L TAC Diff
ZDC TAC Diff
Entries 84326
BBC-S TAC Diff
VPD TAC Diff
Entries 84326
BBC-S TAC Diff
ZDC TAC Diff
Entries 84326
VPD TAC Diff
ZDC TAC Diff

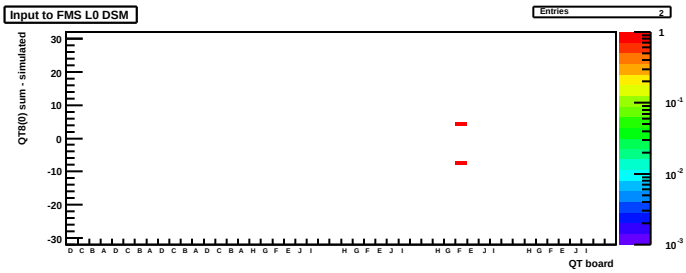

Input to QT1 crate
Entries 1110465
ADC
channel

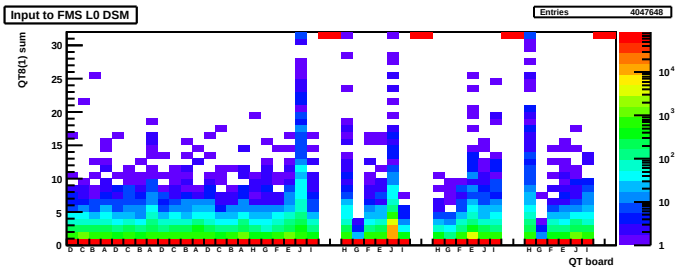

Input to QT2 crate
Entries 759430
ADC
channel

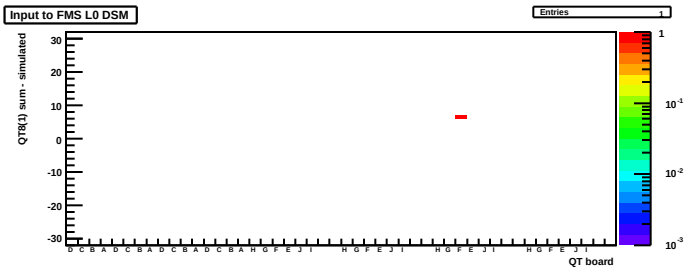

Input to QT3 crate
Entries 724998
ADC
channel

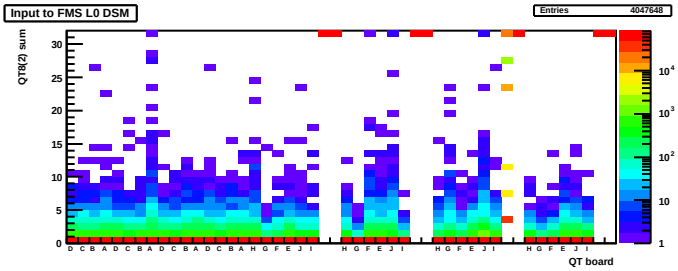

Input to QT4 crate
Entries 802127
ADC
channel

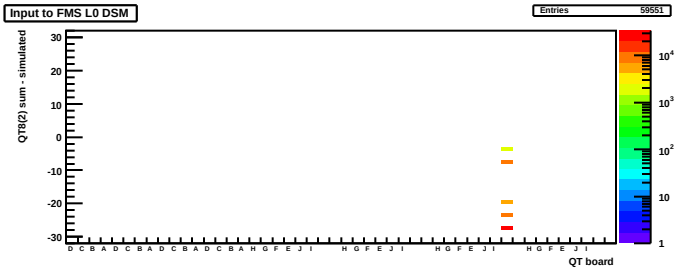

Input to FMS L0 DSM
Entries 4047648
QT8(0) sum
QT board

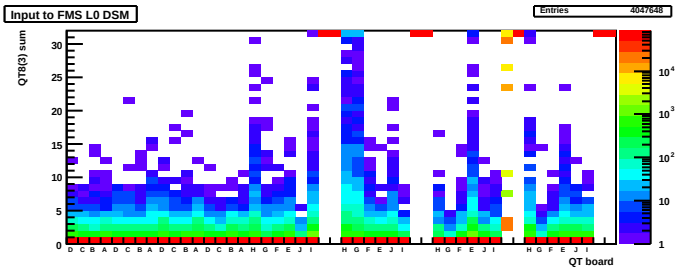

Input to FMS L0 DSM
Entries 2
QT8(0) sum - simulated
QT board

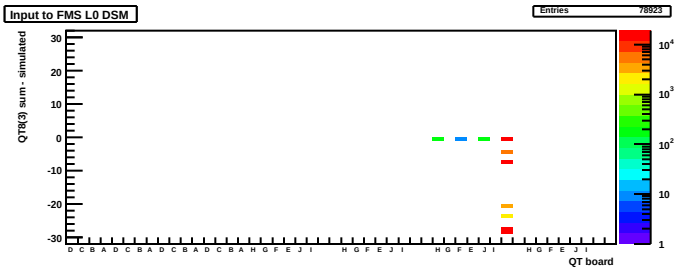

Input to FMS L0 DSM
Entries 4047648
QT8(1) sum
QT board

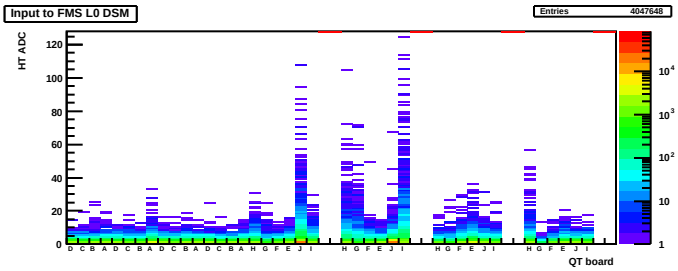

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

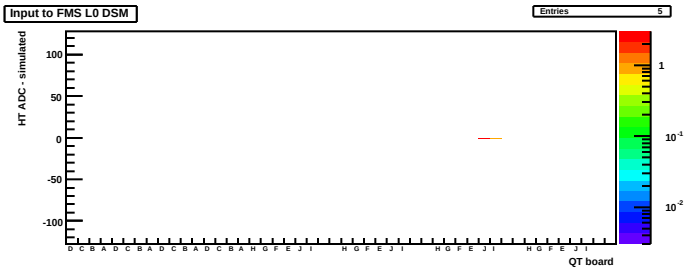

Input to FMS L0 DSM
Entries 4047648
QT8(2) sum
QT board

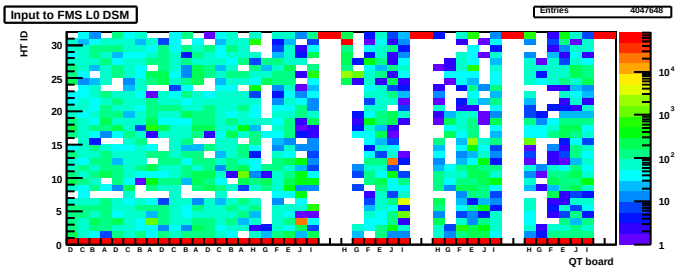

Input to FMS L0 DSM
Entries 59551
QT8(2) sum - simulated
QT board

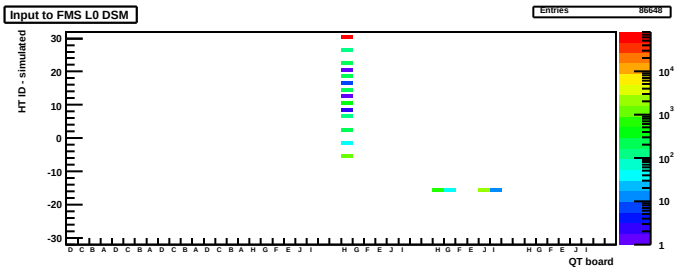

Input to FMS L0 DSM
Entries 4047648
QT8(3) sum
QT board

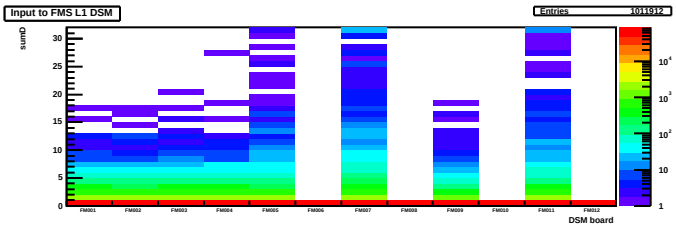

Input to FMS L0 DSM
Entries 78923
QT8(3) sum - simulated
QT board

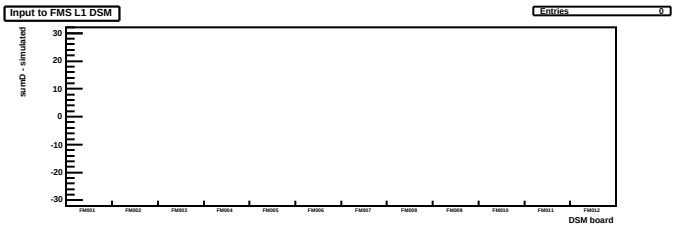

Input to FMS L0 DSM
Entries 4047648
HT ADC
QT board

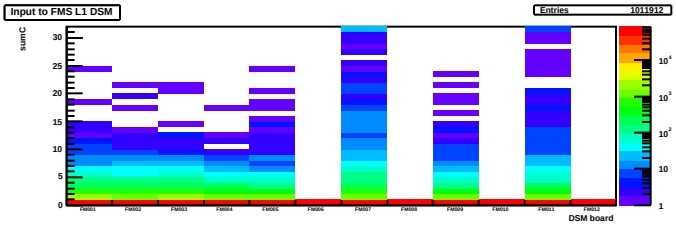

Input to FMS L0 DSM
Entries 5
HT ADC - simulated
QT board

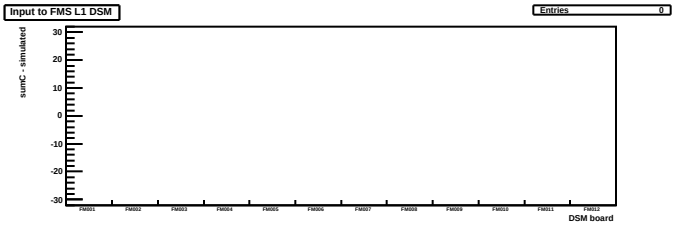

Input to FMS L0 DSM
Entries 4047648
HT ID
QT board

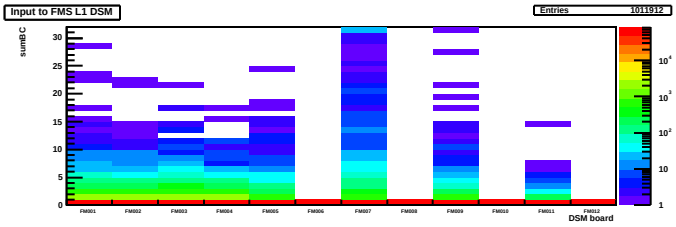

Input to FMS L0 DSM
Entries 86648
HT ID - simulated
QT board

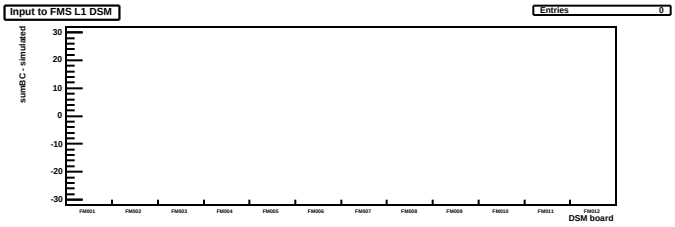

Input to FMS L1 DSM
Entries 1011912
sumD
DSM board

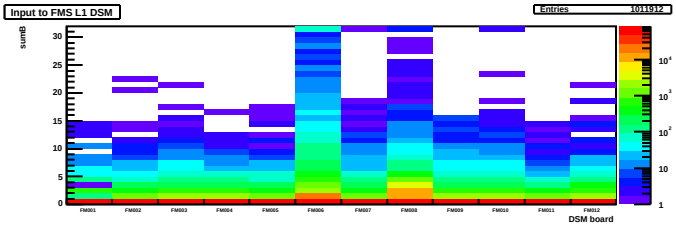

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

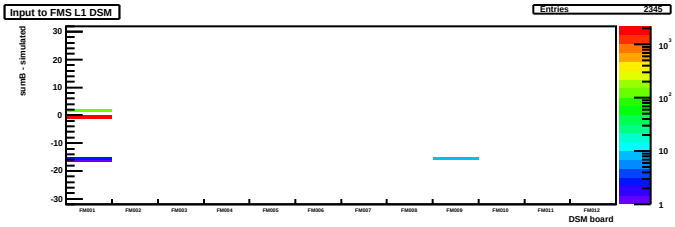

Input to FMS L1 DSM
Entries 1011912
sumC
DSM board

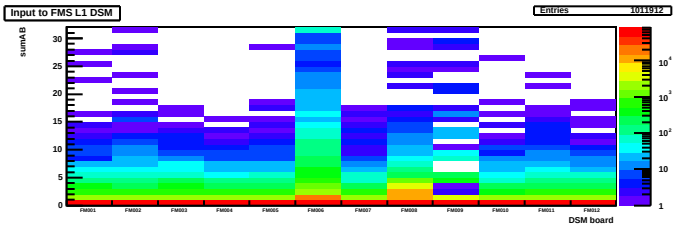

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

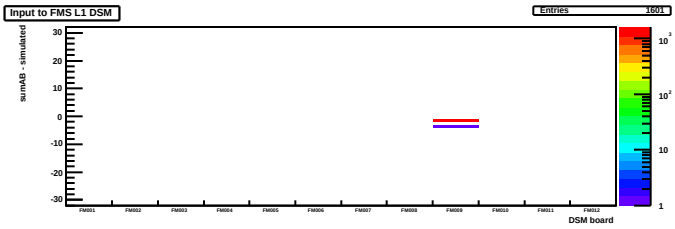

Input to FMS L1 DSM
Entries 1011912
sumBC
DSM board

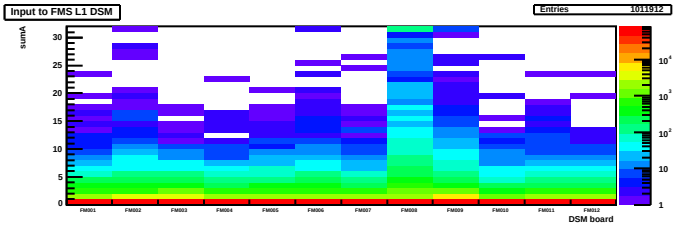

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

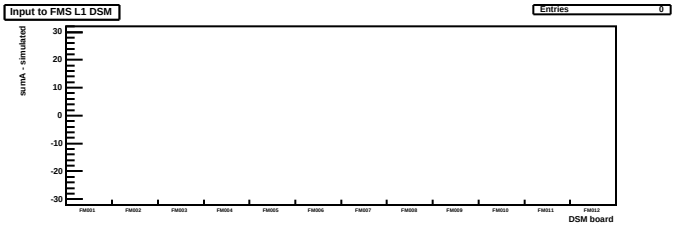

Input to FMS L1 DSM
Entries 1011912
sumB
DSM board

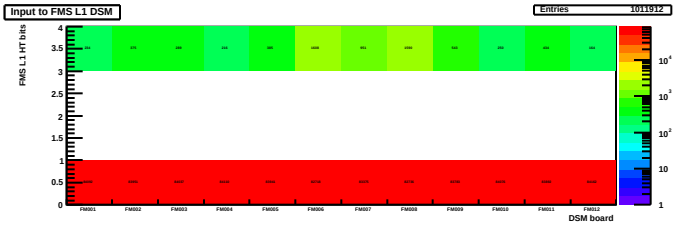

Input to FMS L1 DSM
Entries 2345
sumB - simulated
DSM board

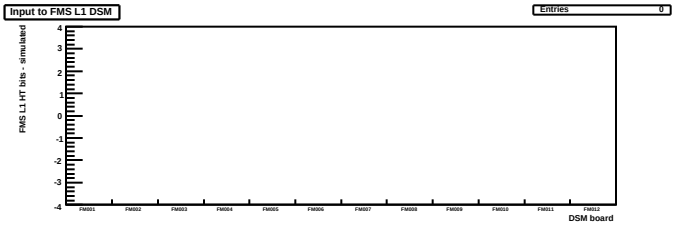

Input to FMS L1 DSM
Entries 1011912
sumAB
DSM board

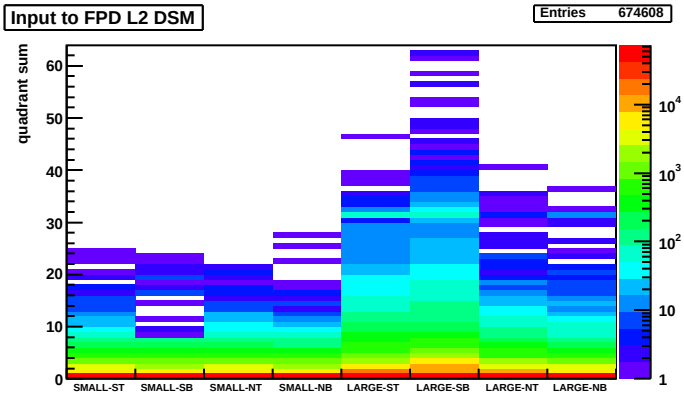

Input to FMS L1 DSM
Entries 1601
sumAB - simulated
DSM board

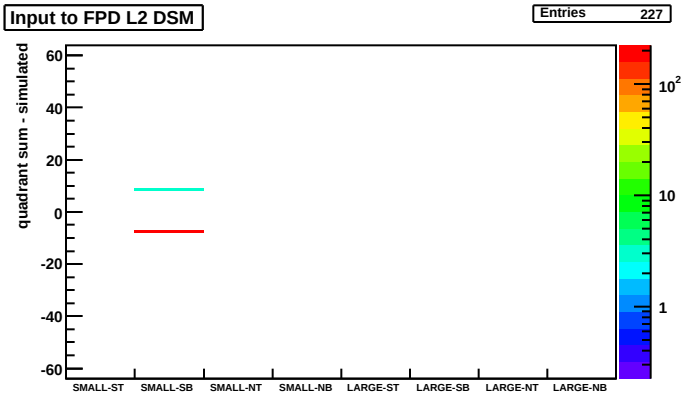

Input to FMS L1 DSM
Entries 1011912
sumA
DSM board

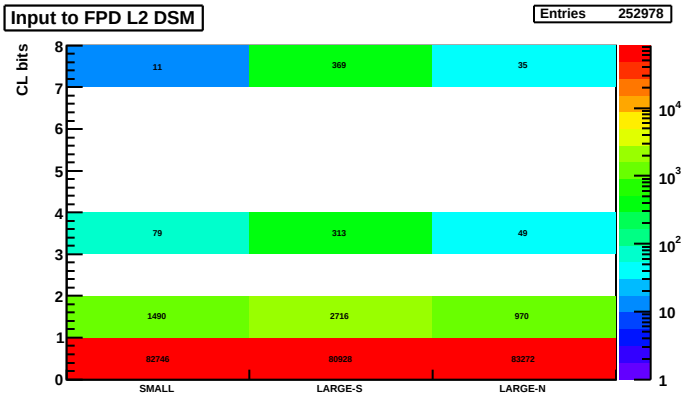

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

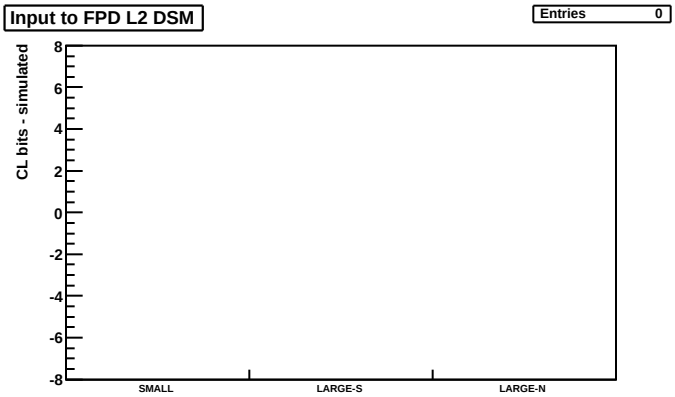

Input to FMS L1 DSM
Entries 1011912
FMS L1 HT bits
DSM board

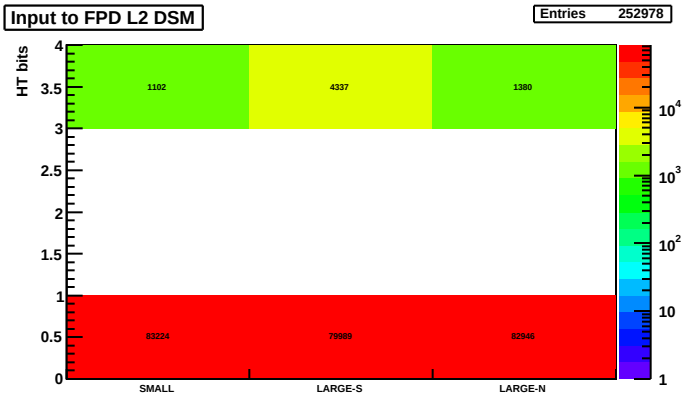

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

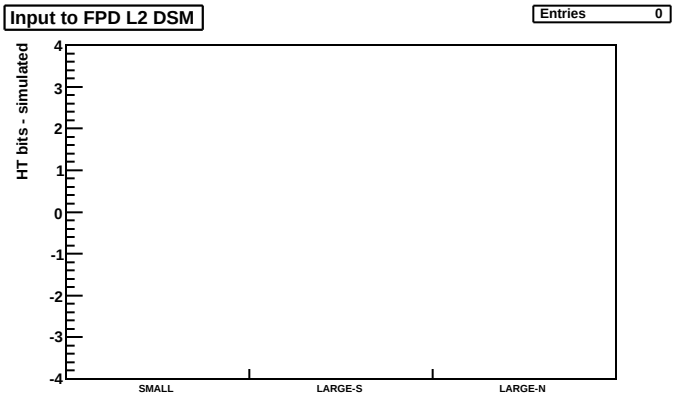

Input to FPD L2 DSM
Entries 674608
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

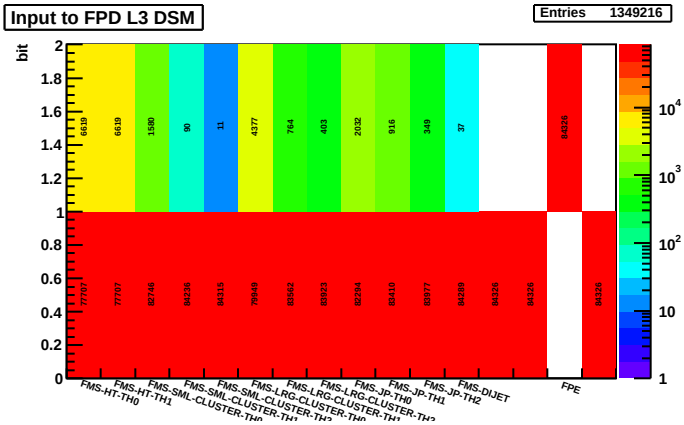

Input to FPD L2 DSM
Entries 227
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

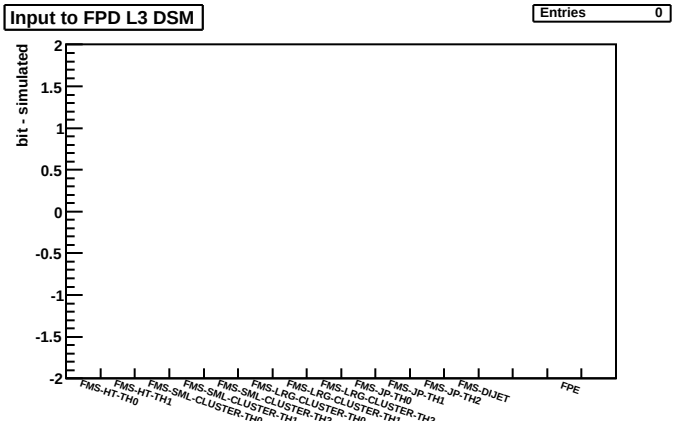

Input to FPD L2 DSM
Entries 252978
CL bits
11
369
35
79
313
49
1490
2716
970
82746
80928
83272
SMALL
LARGE-S
LARGE-N

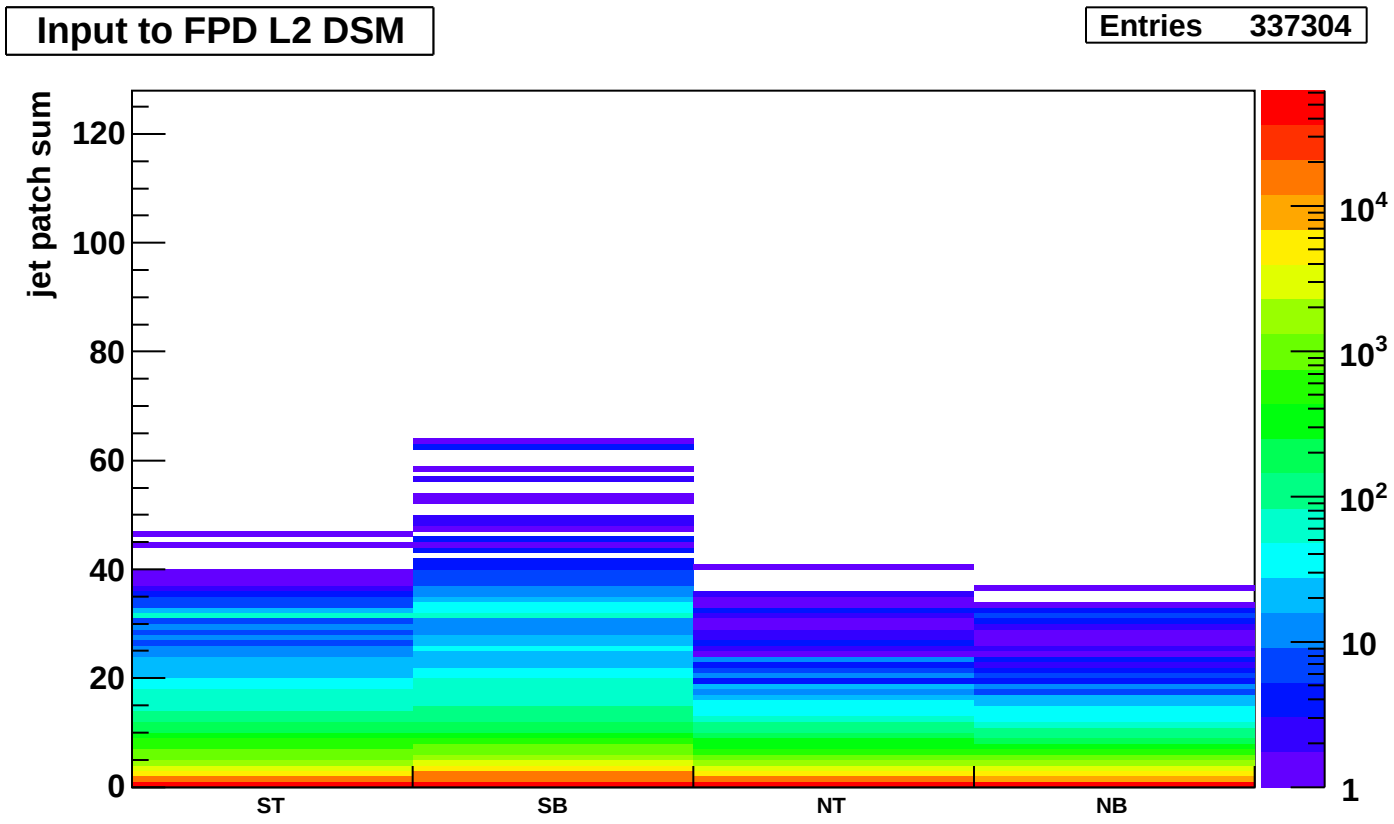

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

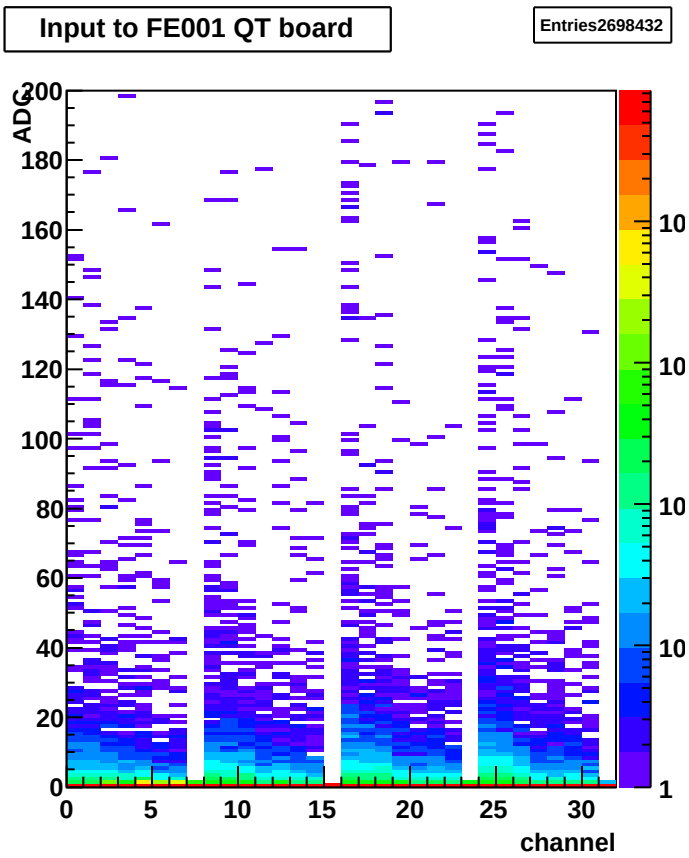

Input to FPD L2 DSM
Entries 252978
HT bits
1102
4337
1380
83224
79989
82946
SMALL
LARGE-S
LARGE-N

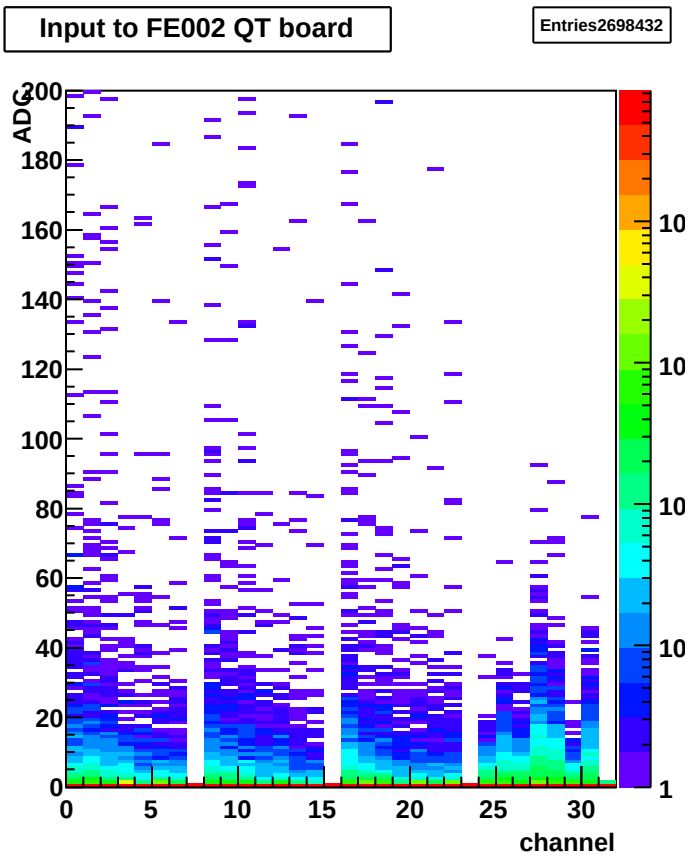

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

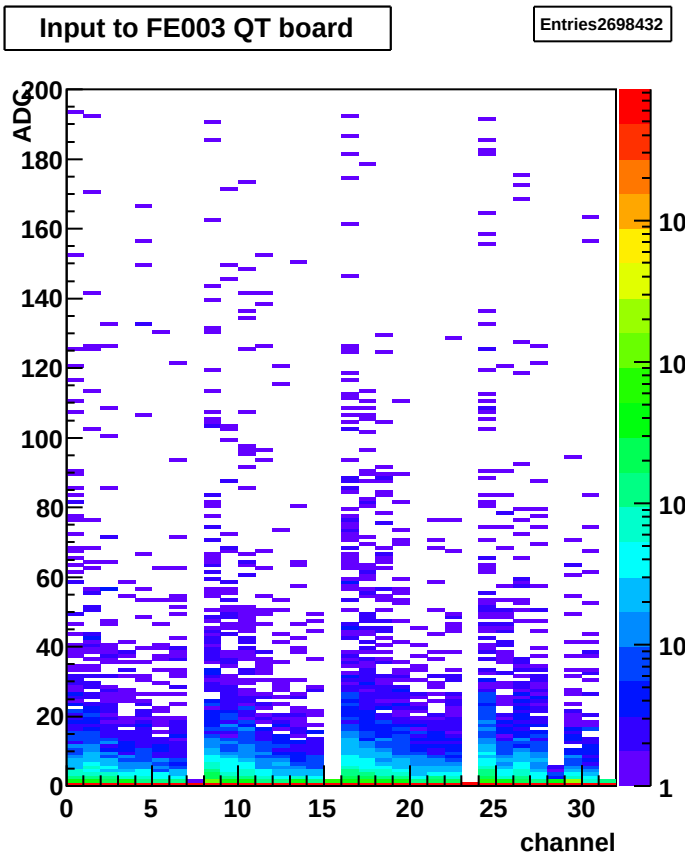

Input to FPD L3 DSM
Entries 1349216
bit
6619
6619
1590
90
11
4377
764
403
2032
916
348
37
84326
77707
77707
82746
84236
84315
79949
83962
83923
82294
82410
83977
84289
84326
84326
84326
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

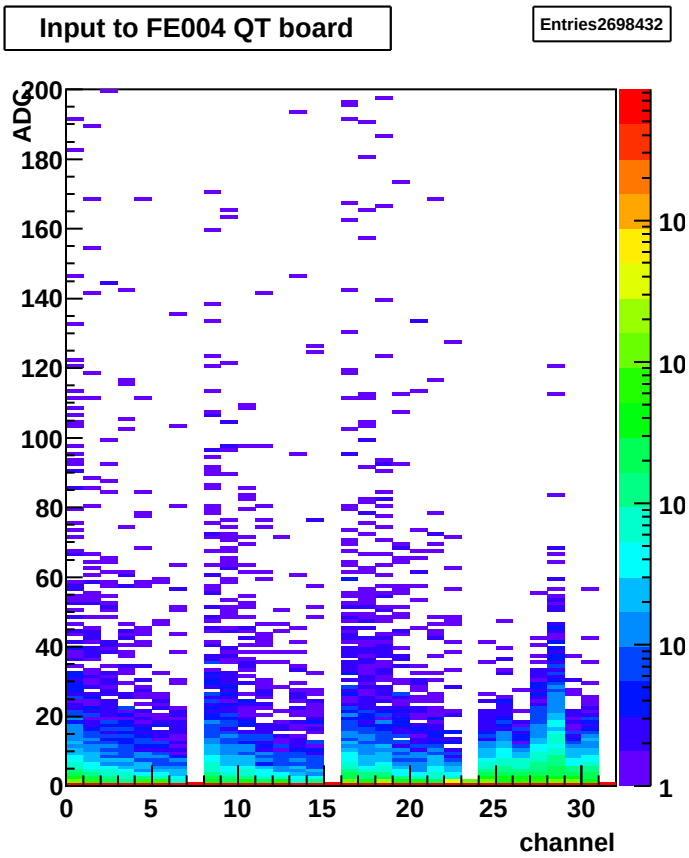

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

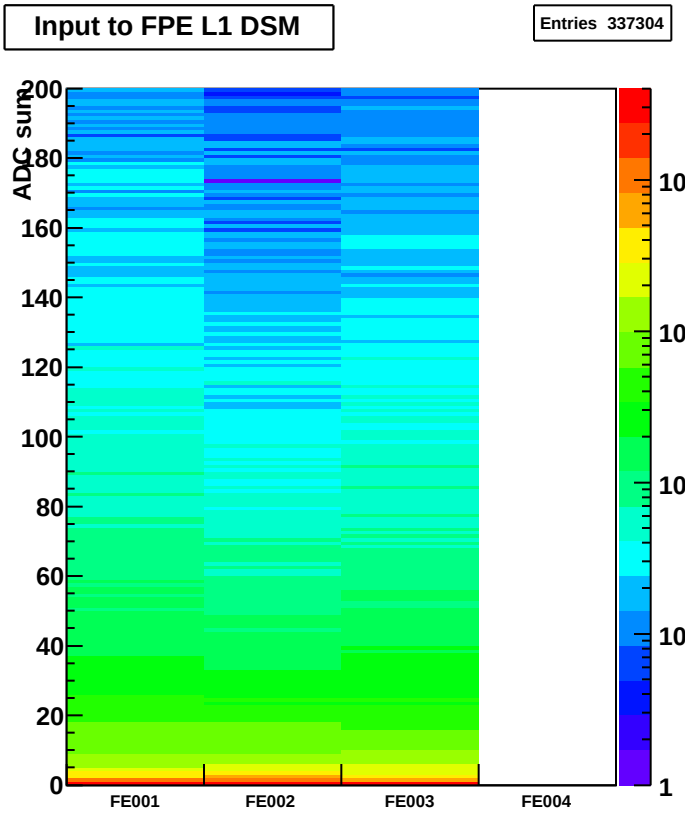

Input to FPD L2 DSM
Entries 337304
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

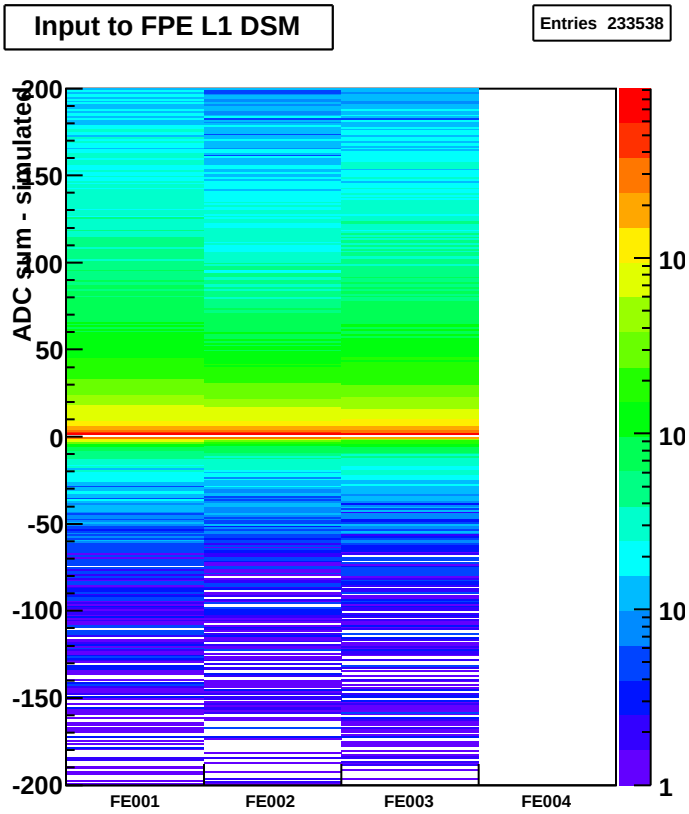

Input to FE001 QT board
Entries2698432
ADC
channel

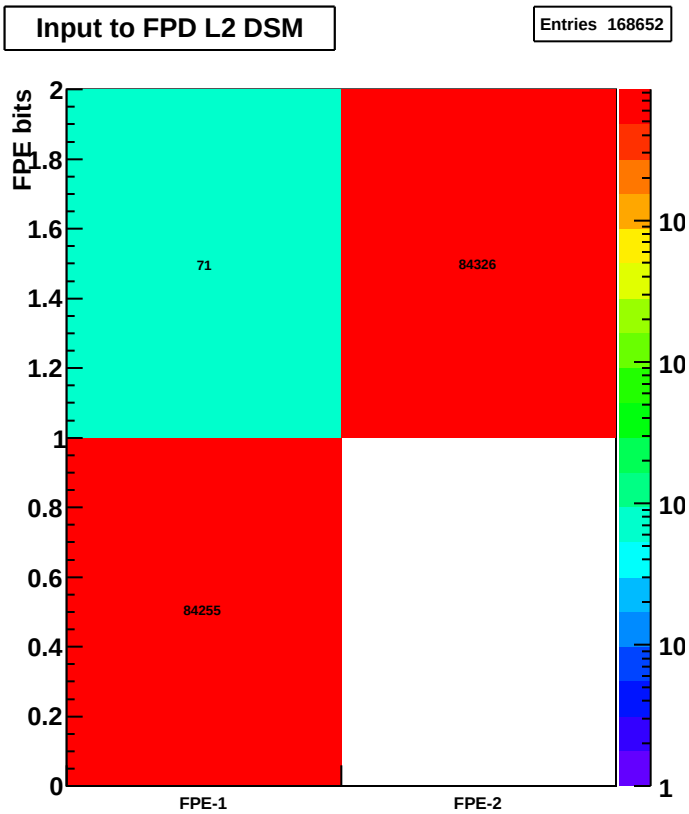

Input to FE002 QT board
Entries2698432
ADC
channel

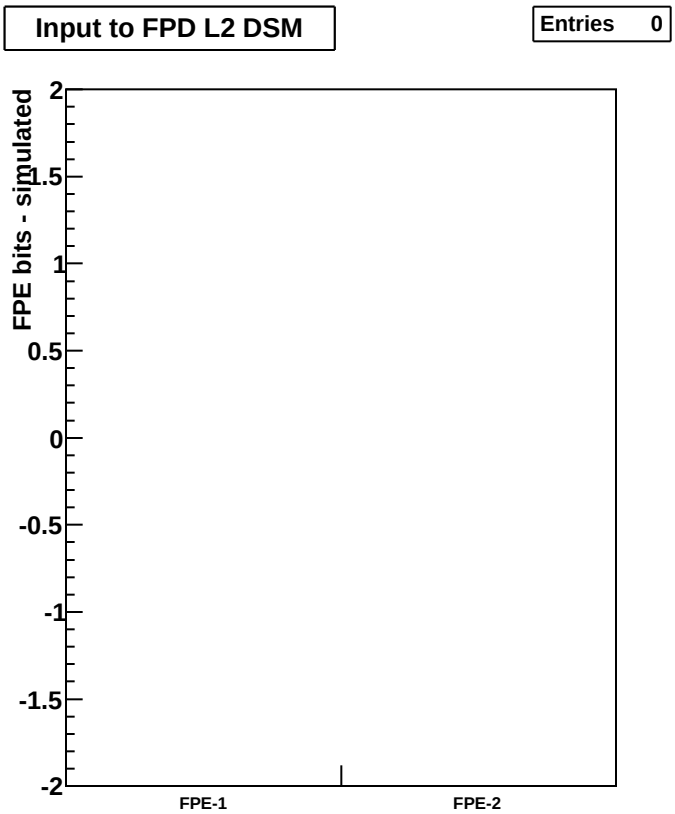

Input to FE003 QT board
Entries2698432
ADC
channel


Input to FE004 QT board
Entries2698432
ADC
channel


Input to FPE L1 DSM
Entries 337304
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 233538
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 168652
FPE bits
71
84326
84255
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2