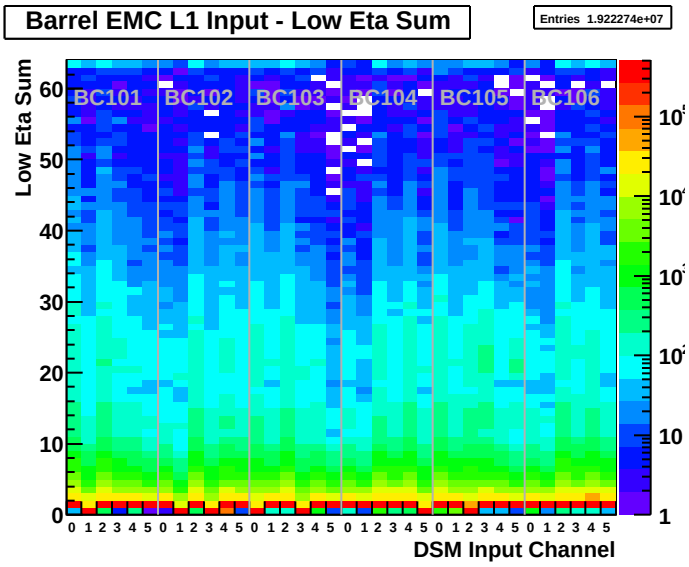
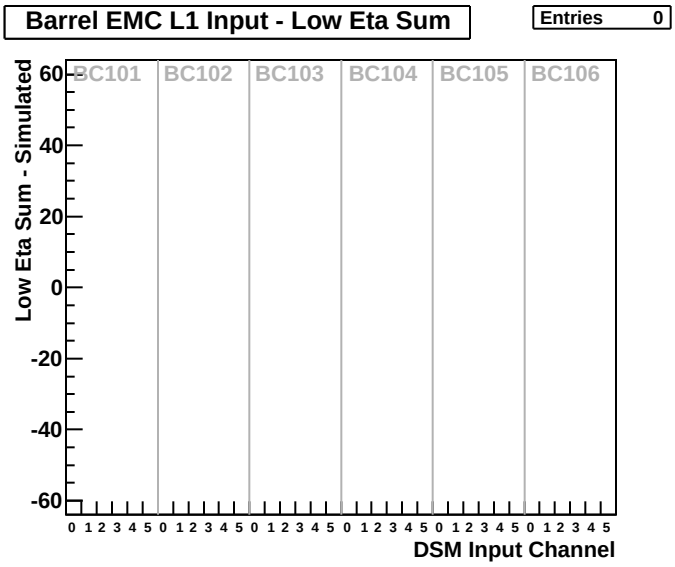
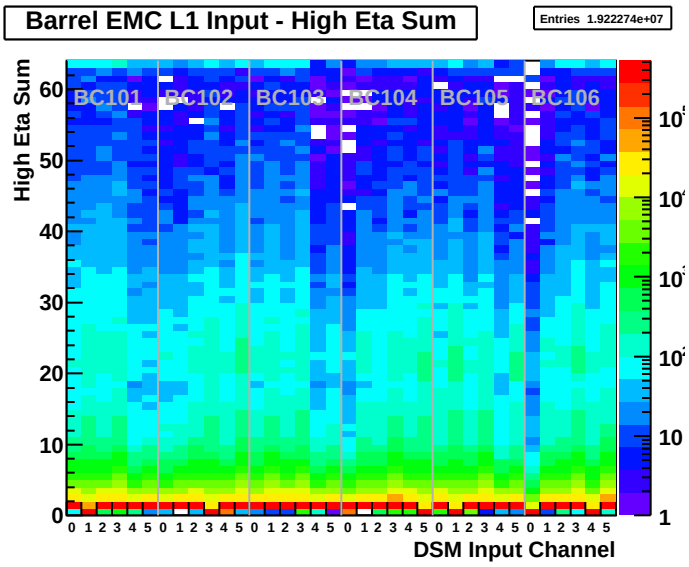
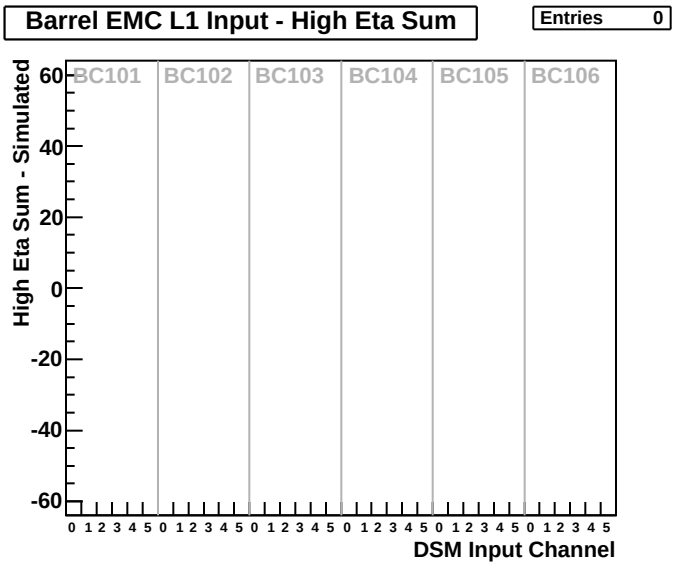
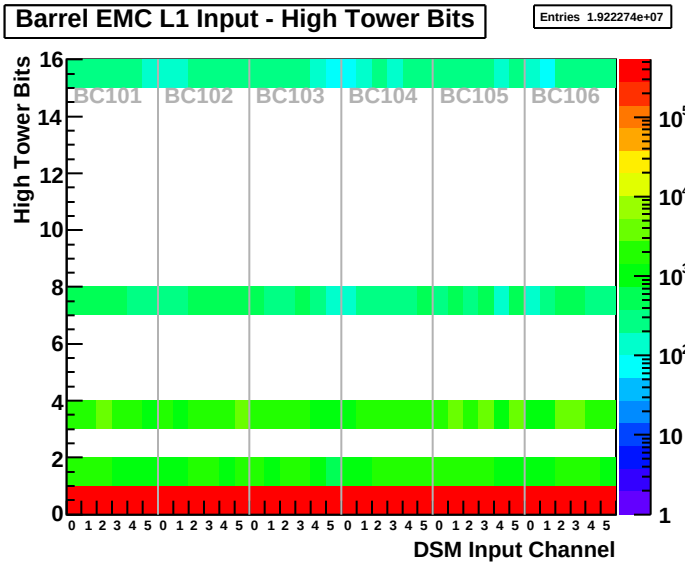
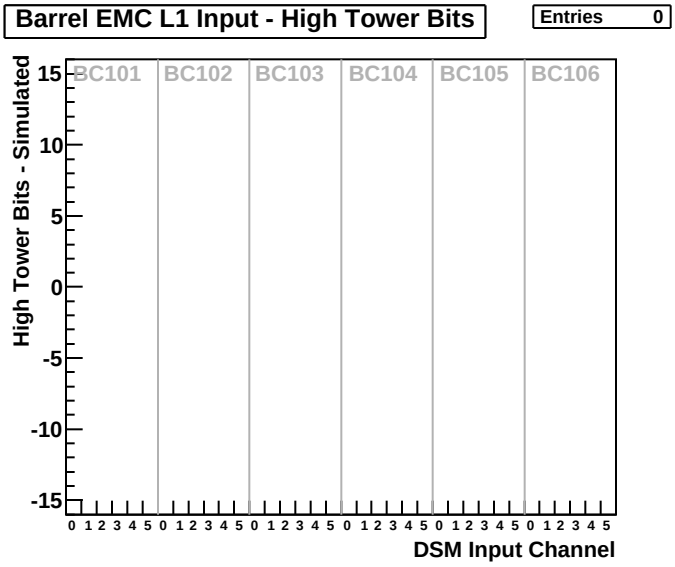

Barrel EMC L1 Input - Low Eta Sum
Entries 1.922274e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

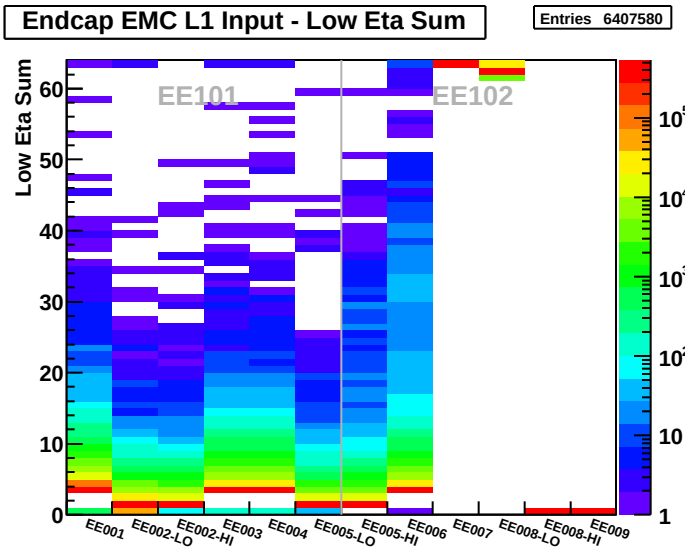

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

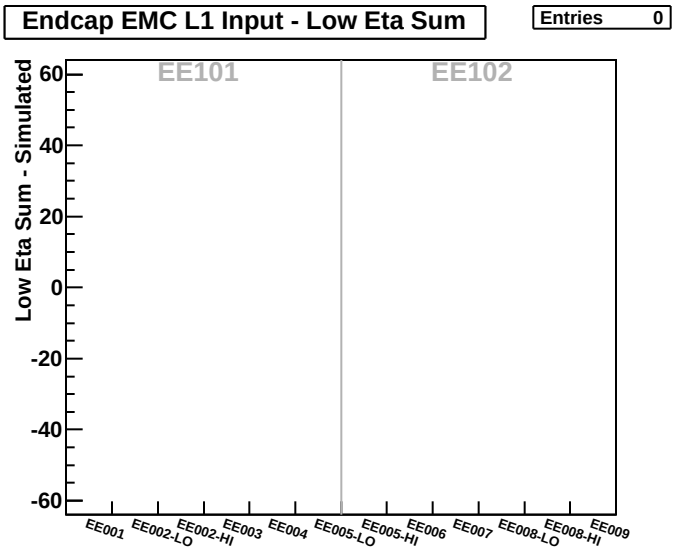

Barrel EMC L1 Input - High Eta Sum
Entries 1.922274e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

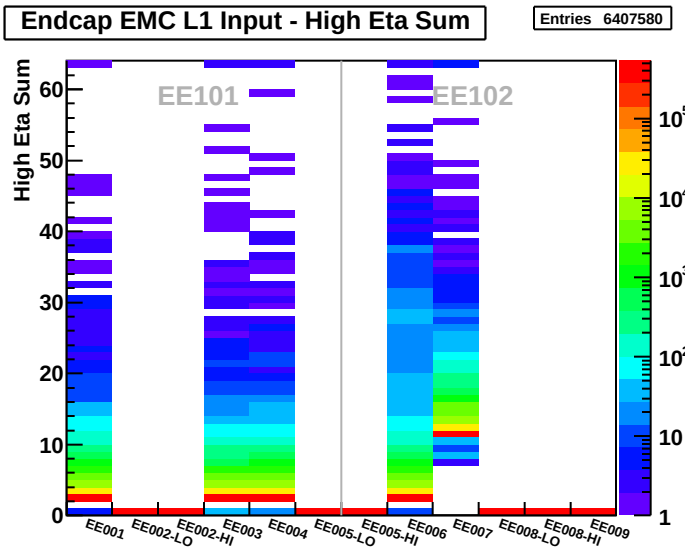

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

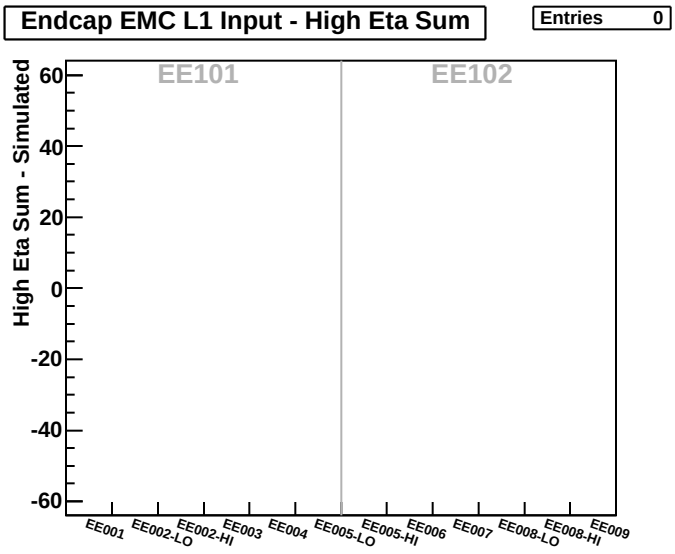

Barrel EMC L1 Input - High Tower Bits
Entries 1.922274e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

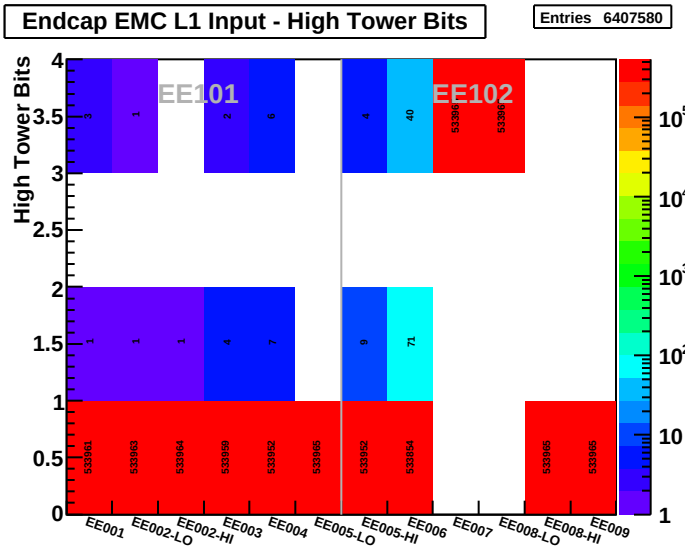

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

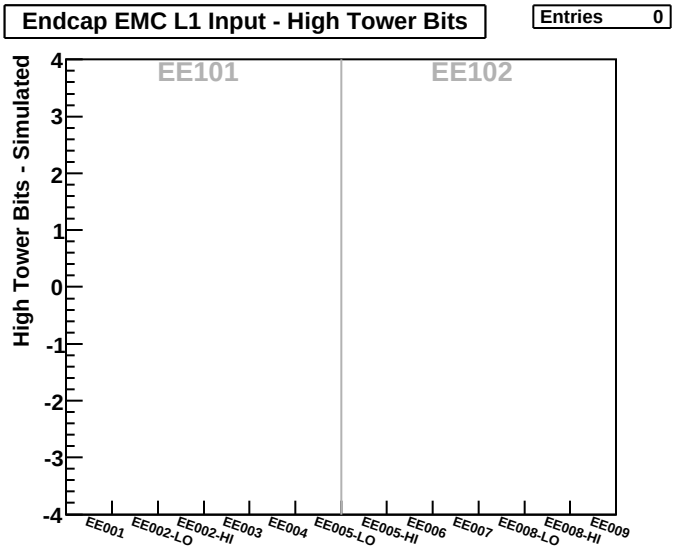

Endcap EMC L1 Input - Low Eta Sum
Entries 6407580
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

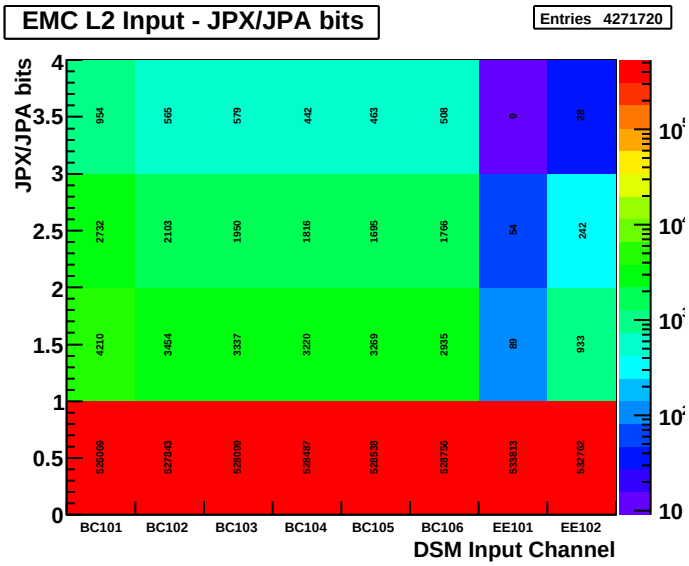

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

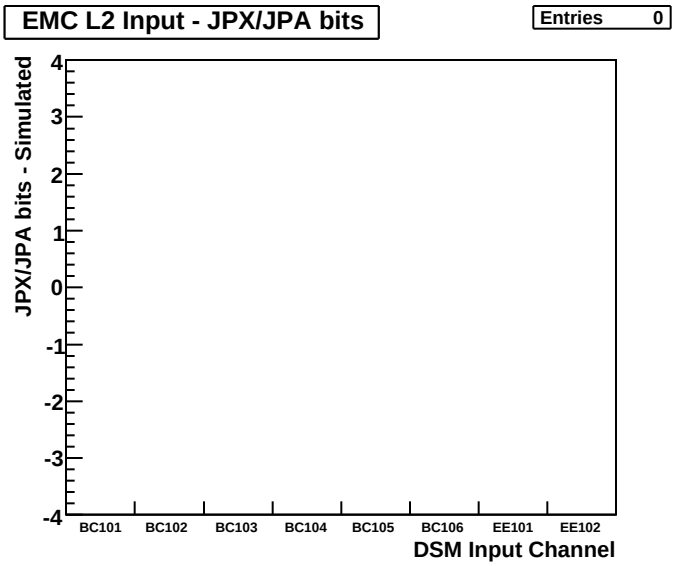

Endcap EMC L1 Input - High Eta Sum
Entries 6407580
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

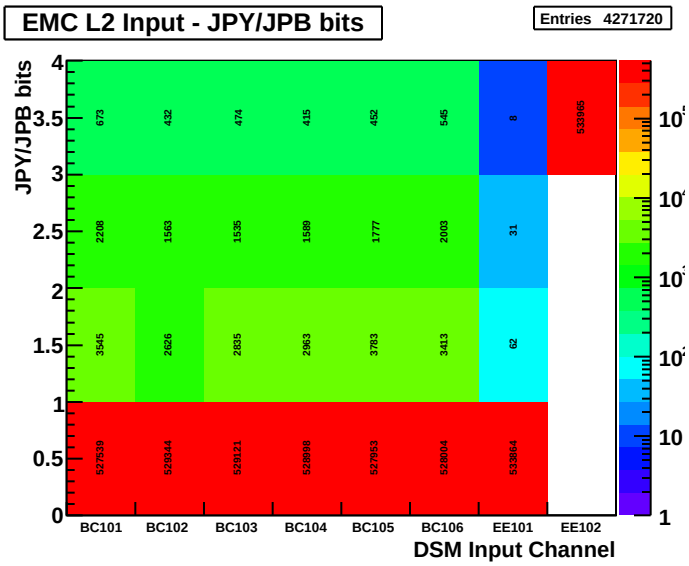

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

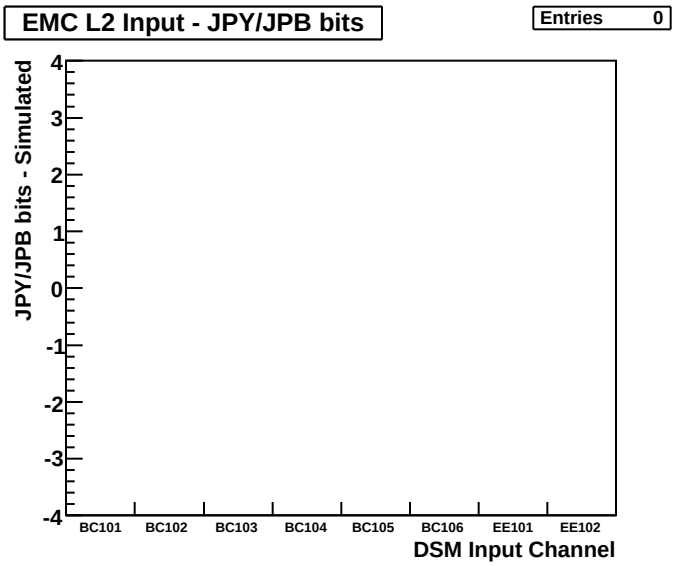

Endcap EMC L1 Input - High Tower Bits
Entries 6407580
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

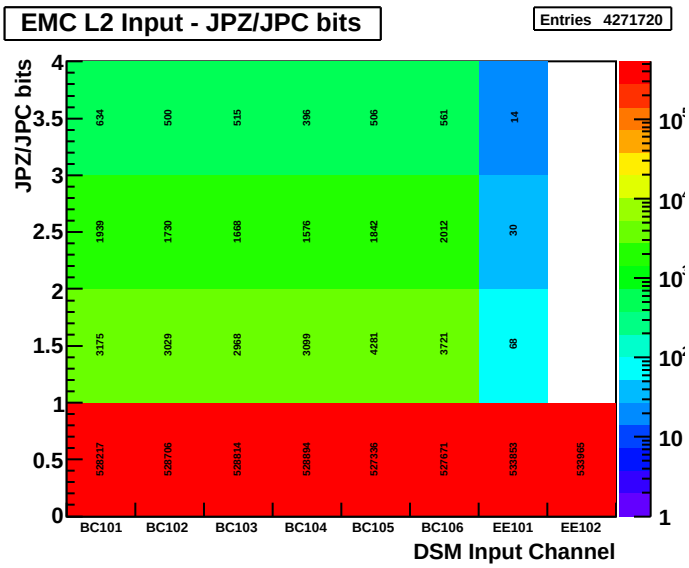

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

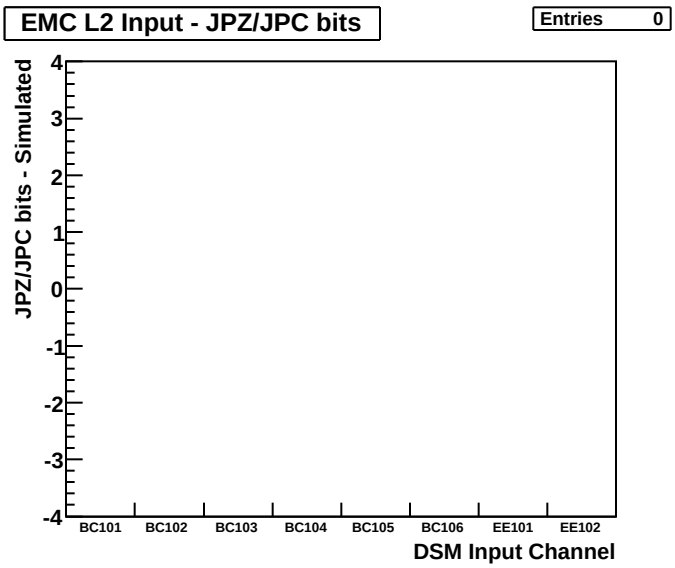

EMC L2 Input - JPX/JPA bits
Entries 4271720
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
954 565 579 442 463 508 9 28
2732 2103 1950 1816 1695 1766 54 242
4210 3454 3337 3220 3269 2935 89 933
528069 527843 528099 528487 528538 528756 533813 532762

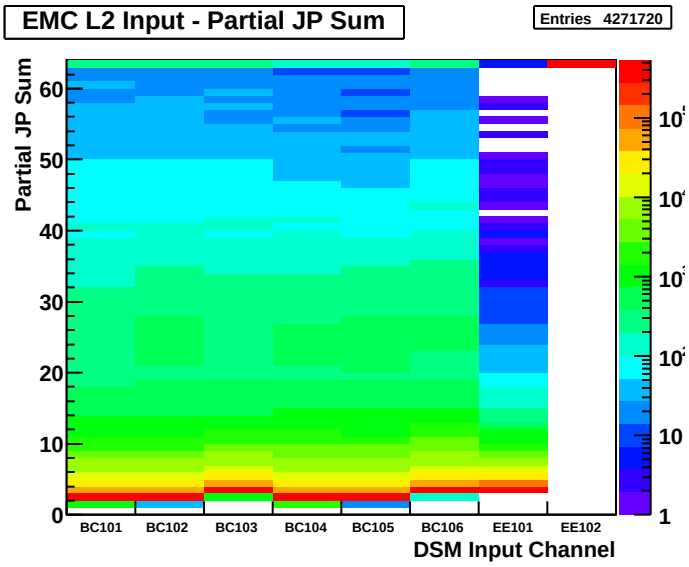

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

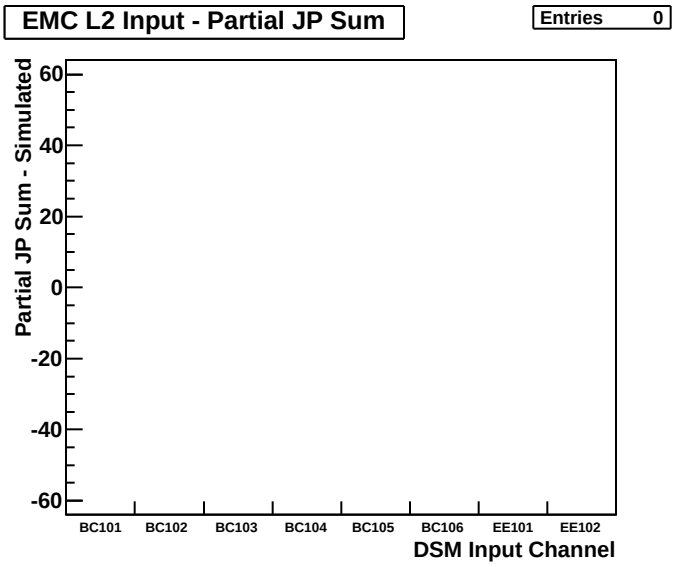

EMC L2 Input - JPY/JPB bits
Entries 4271720
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
673 432 474 415 452 545 8 533965
2208 1563 1535 1580 1777 2003 31
3545 2626 2835 2963 3783 3413 62
527539 528344 528121 528998 527953 528004 533864

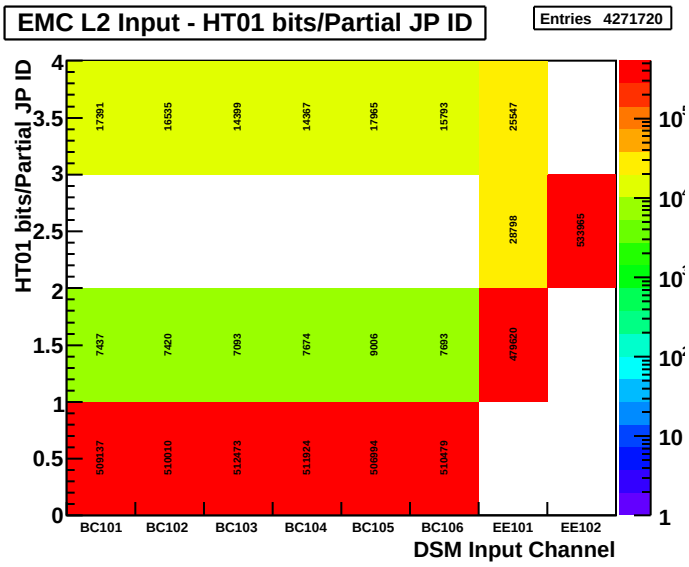

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

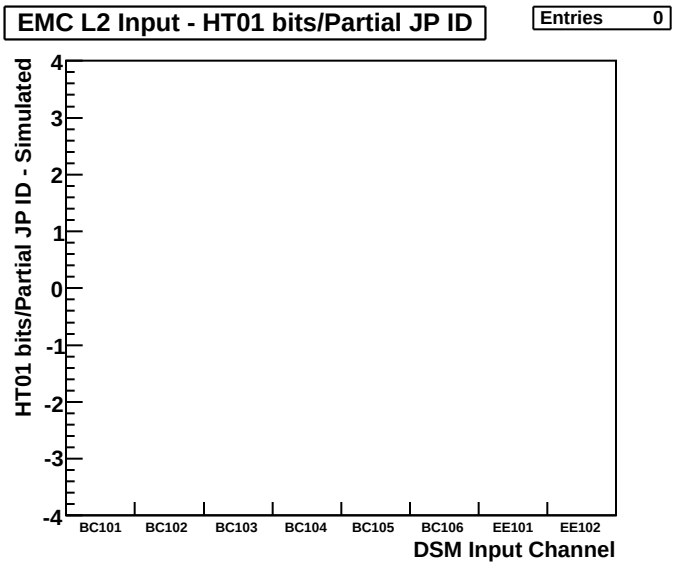

EMC L2 Input - JPZ/JPC bits
Entries 4271720
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
634 500 515 396 506 561 14
1939 1730 1668 1576 1842 2012 30
3175 3029 2968 3099 4281 3721 68
528217 528706 528814 528394 527338 527671 533853 533865

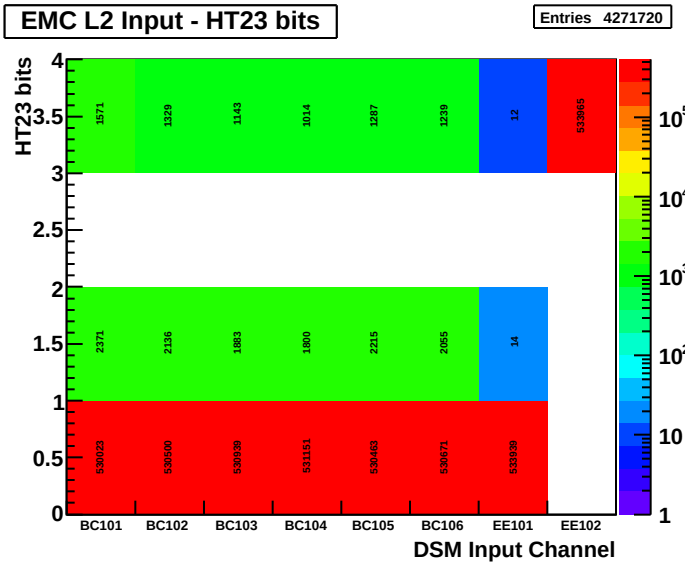

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

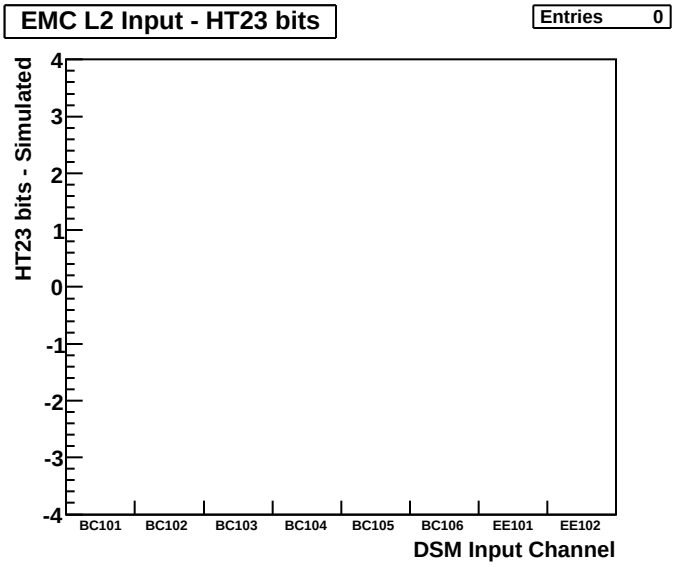

EMC L2 Input - Partial JP Sum
Entries 4271720
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

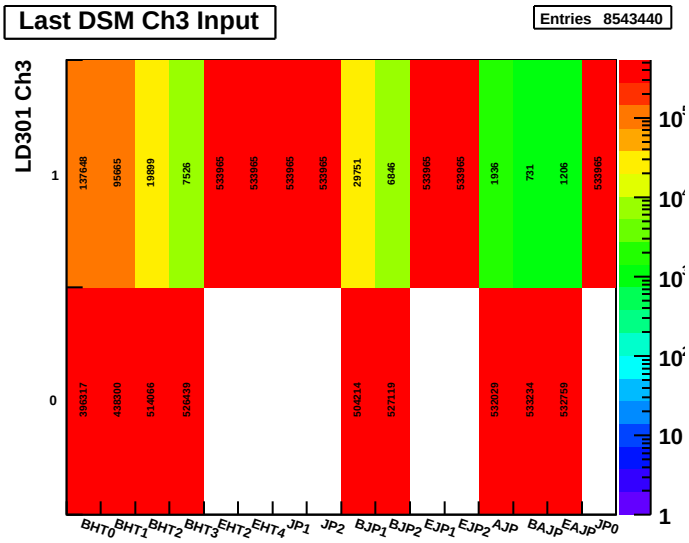

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

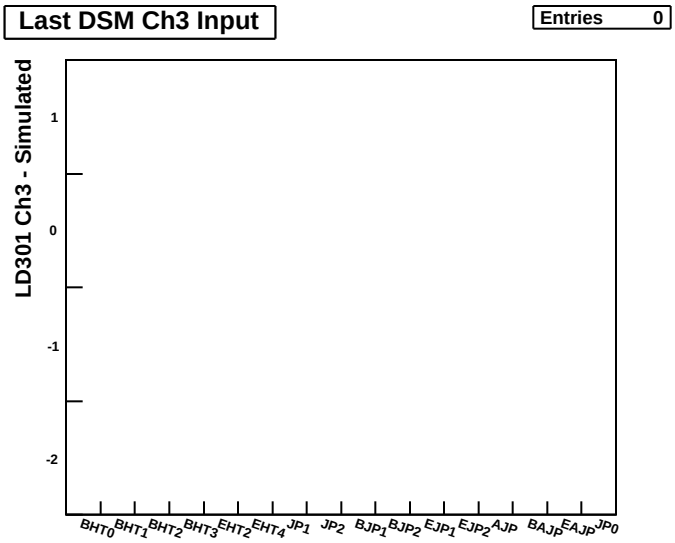

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4271720
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

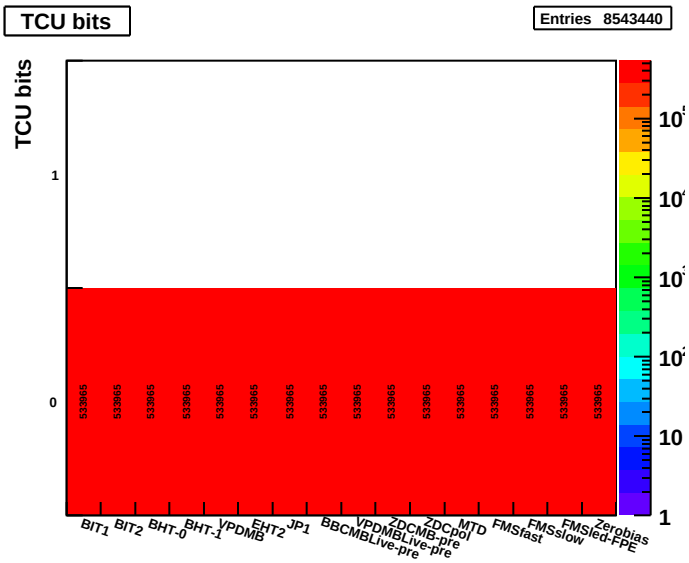

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

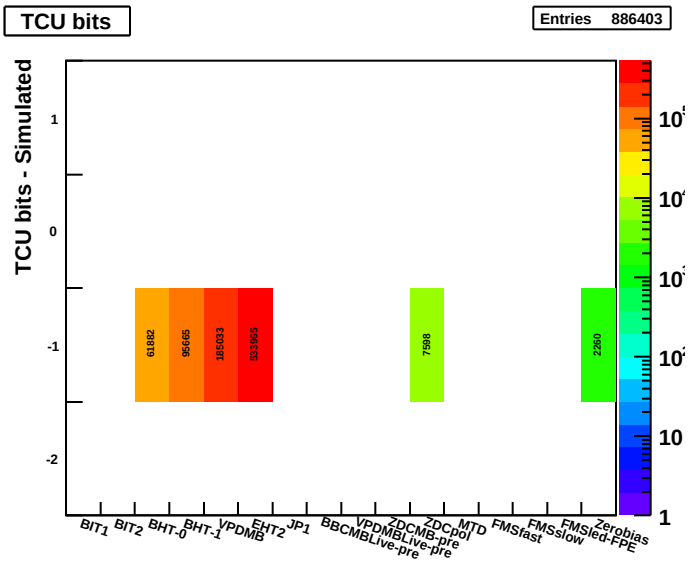

EMC L2 Input - HT23 bits
Entries 4271720
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

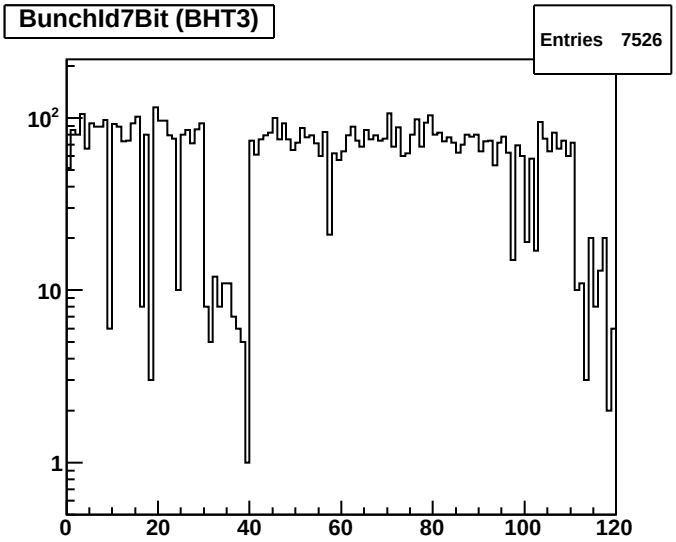

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

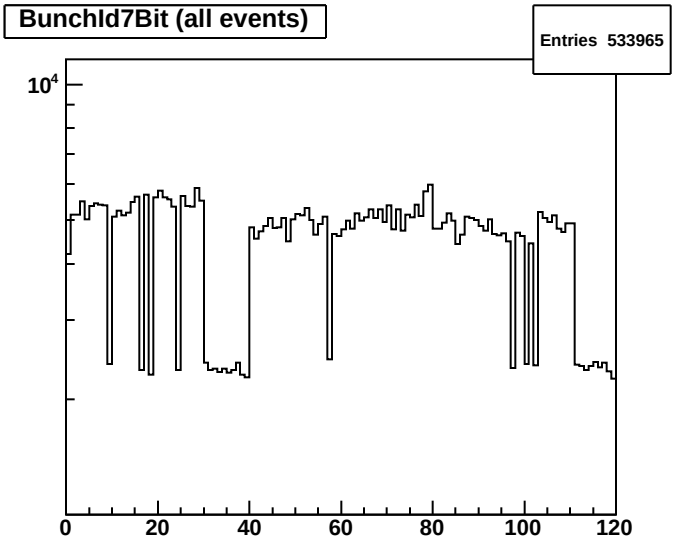

Last DSM Ch3 Input
Entries 8543440
LD301 Ch3
137648
95665
19899
7526
533965
29751
6846
1936
731
1206
396317
438300
514066
526439
504214
527119
532029
533234
532759
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

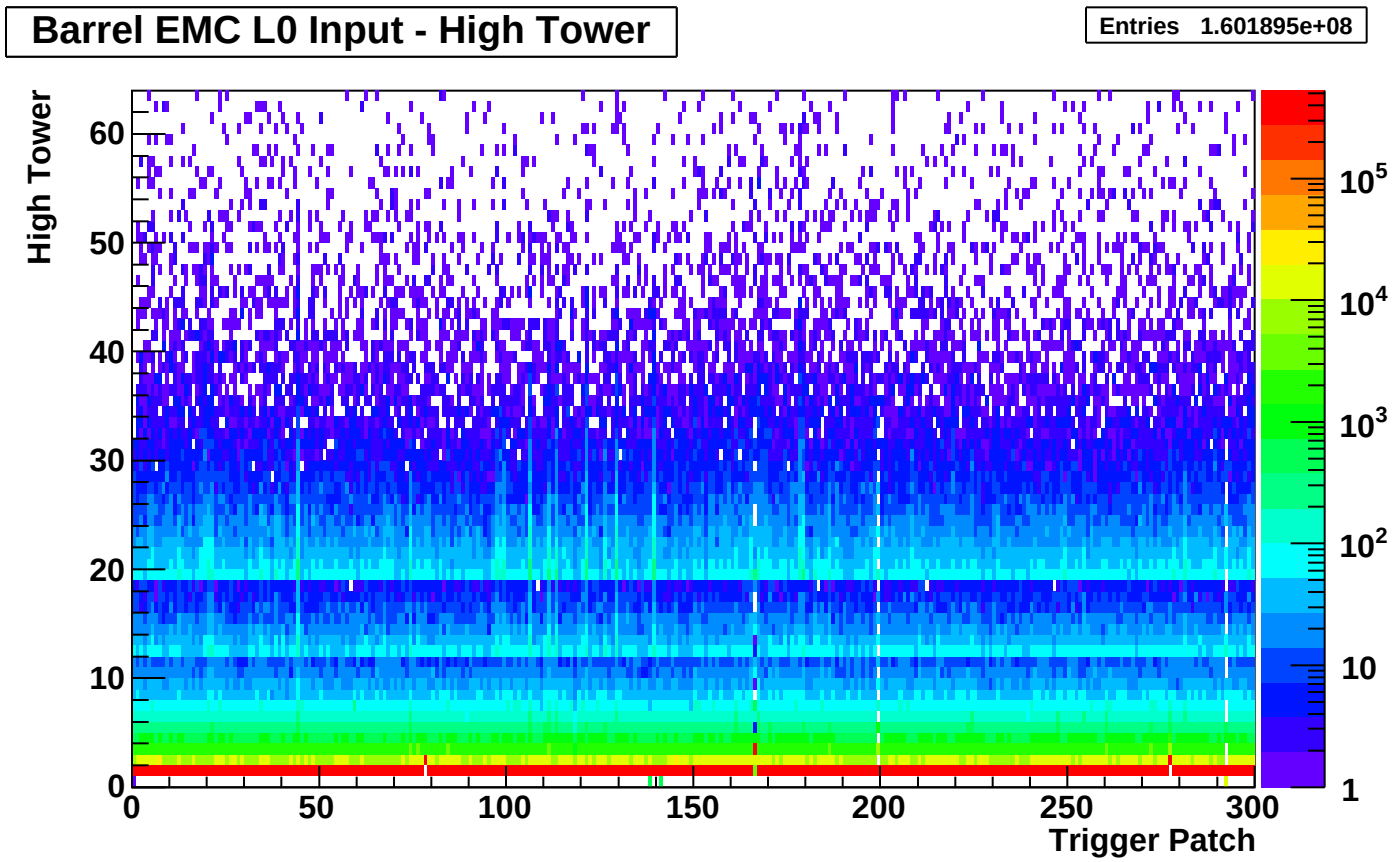

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

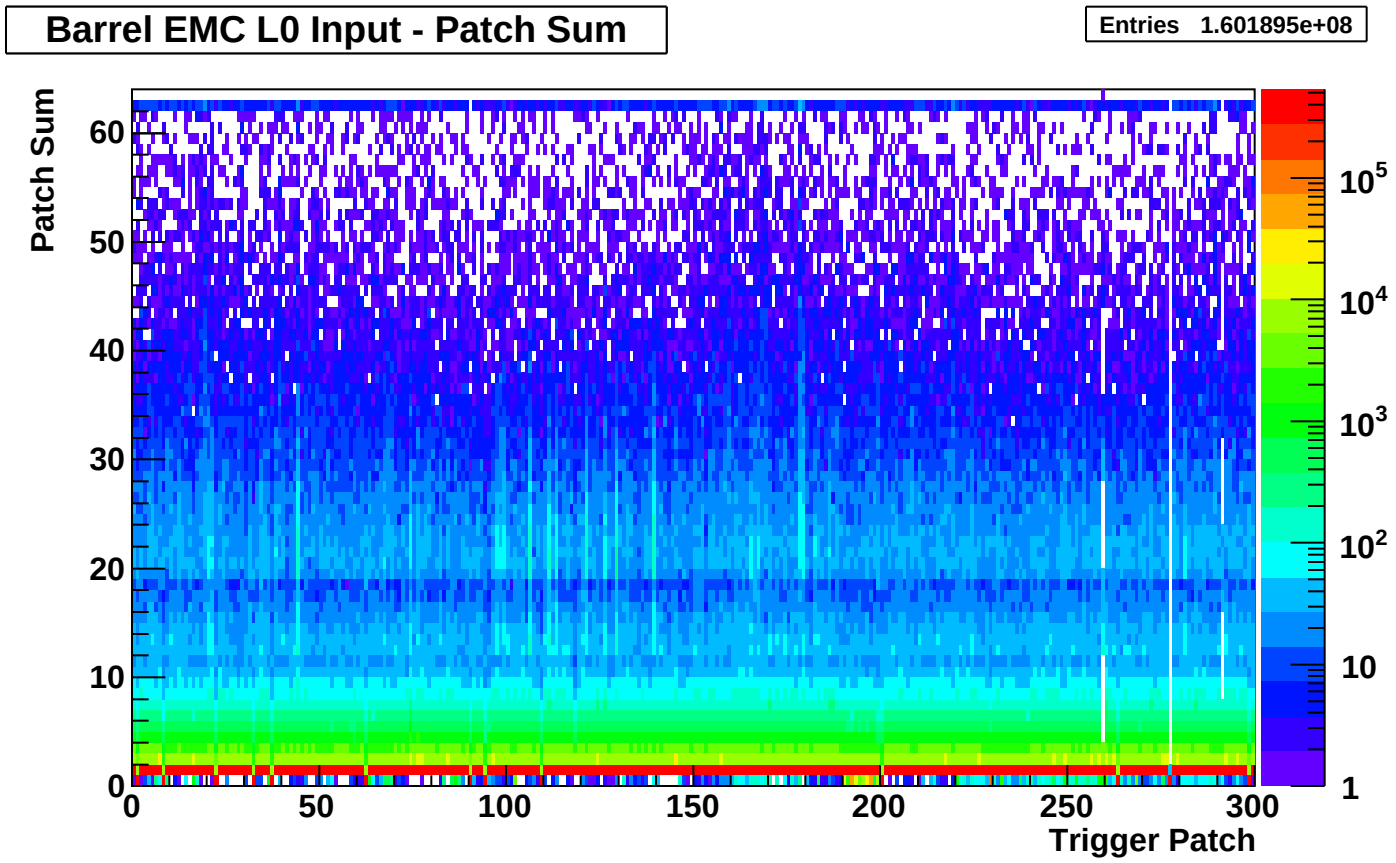

TCU bits
Entries 8543440
533965
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

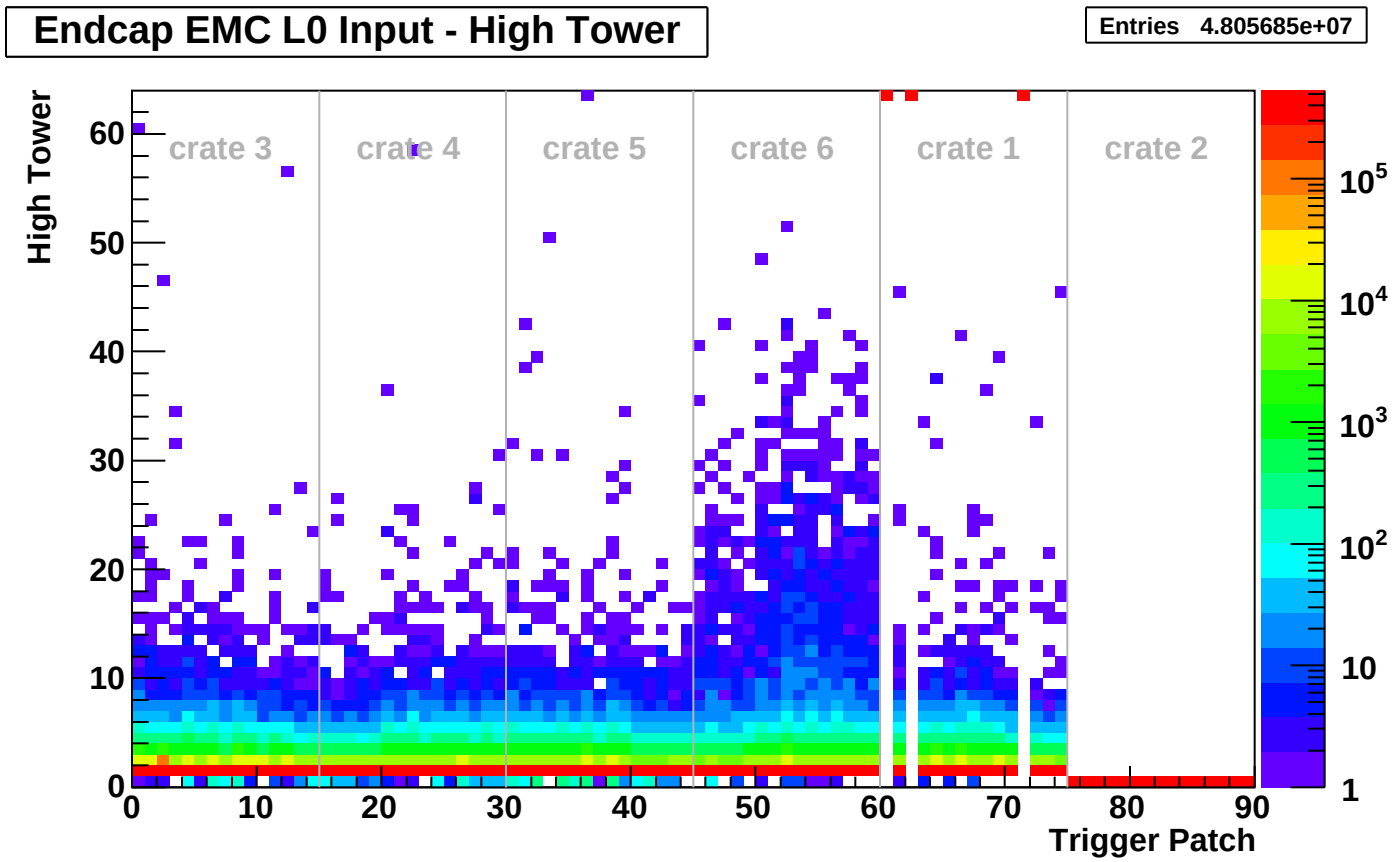

TCU bits
Entries 886403
TCU bits - Simulated
61882
95665
185033
533965
7588
2360
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

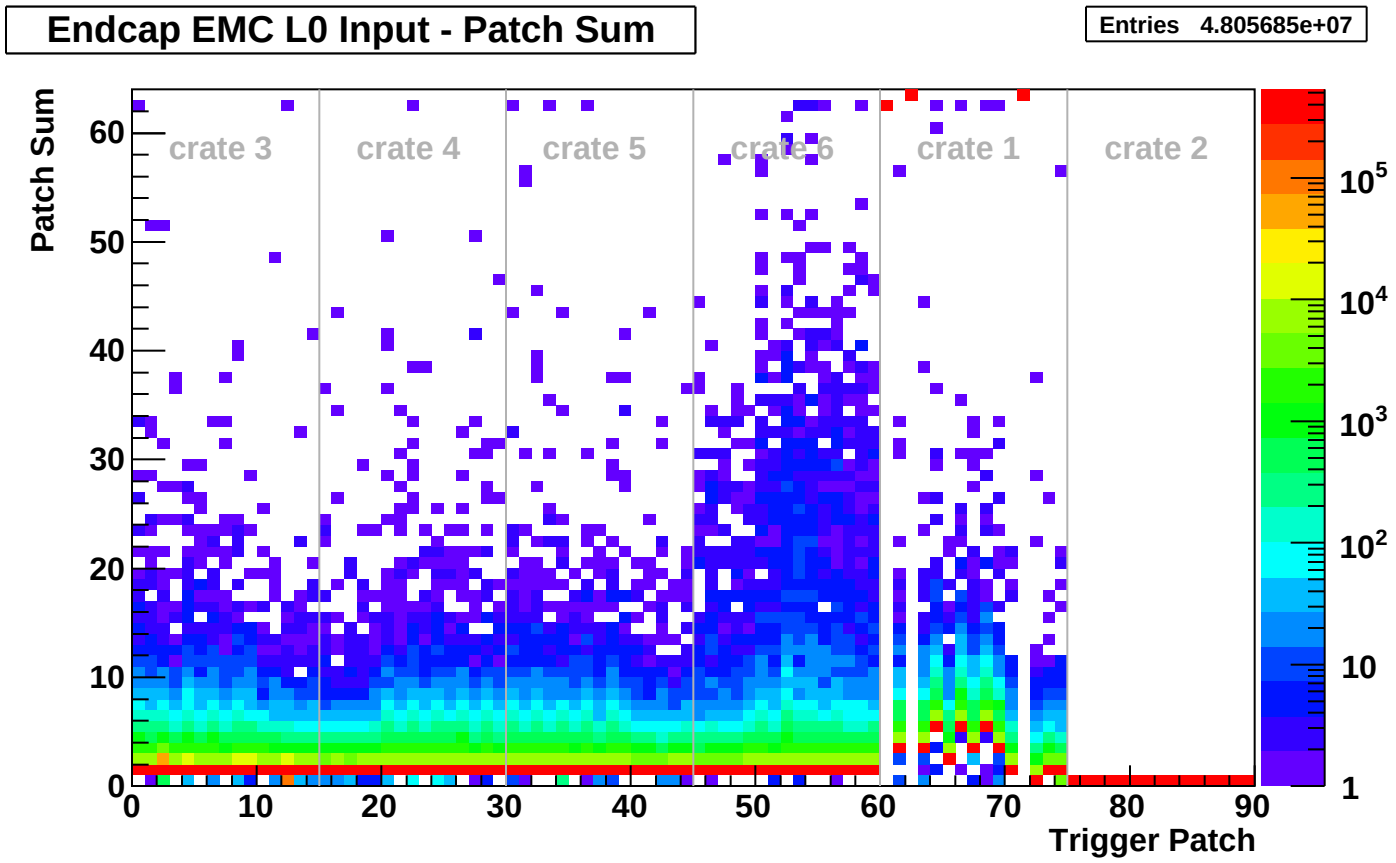

BunchId7Bit (BHT3)
Entries 7526

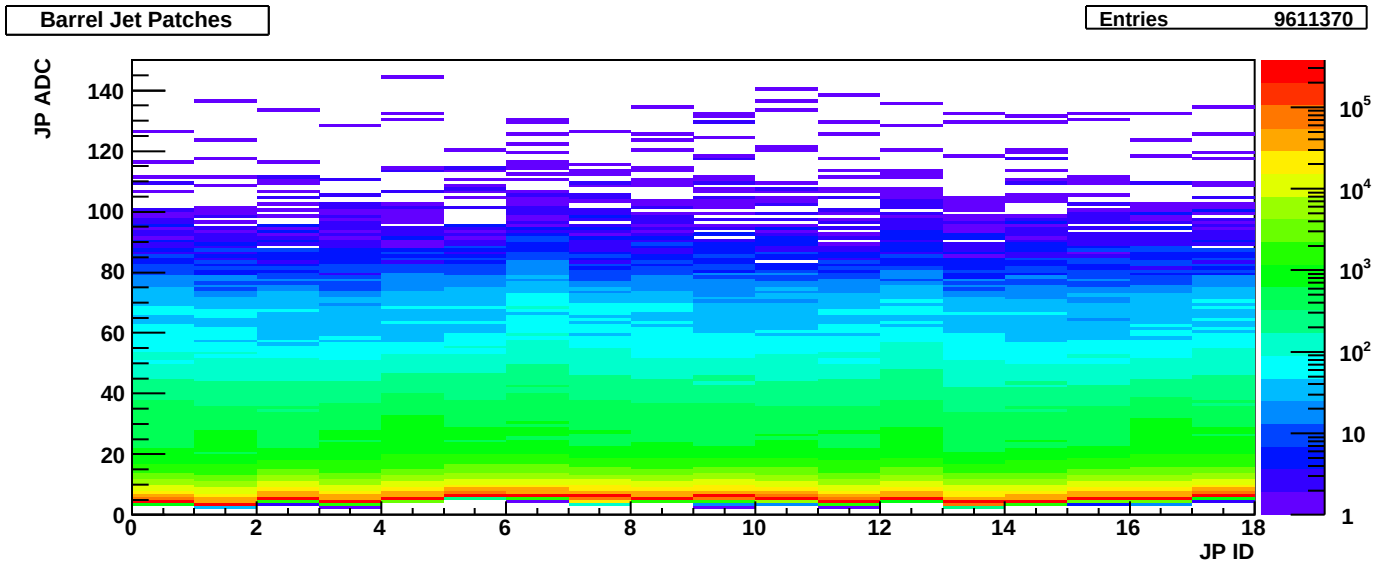

BunchId7Bit (all events)
Entries 533965

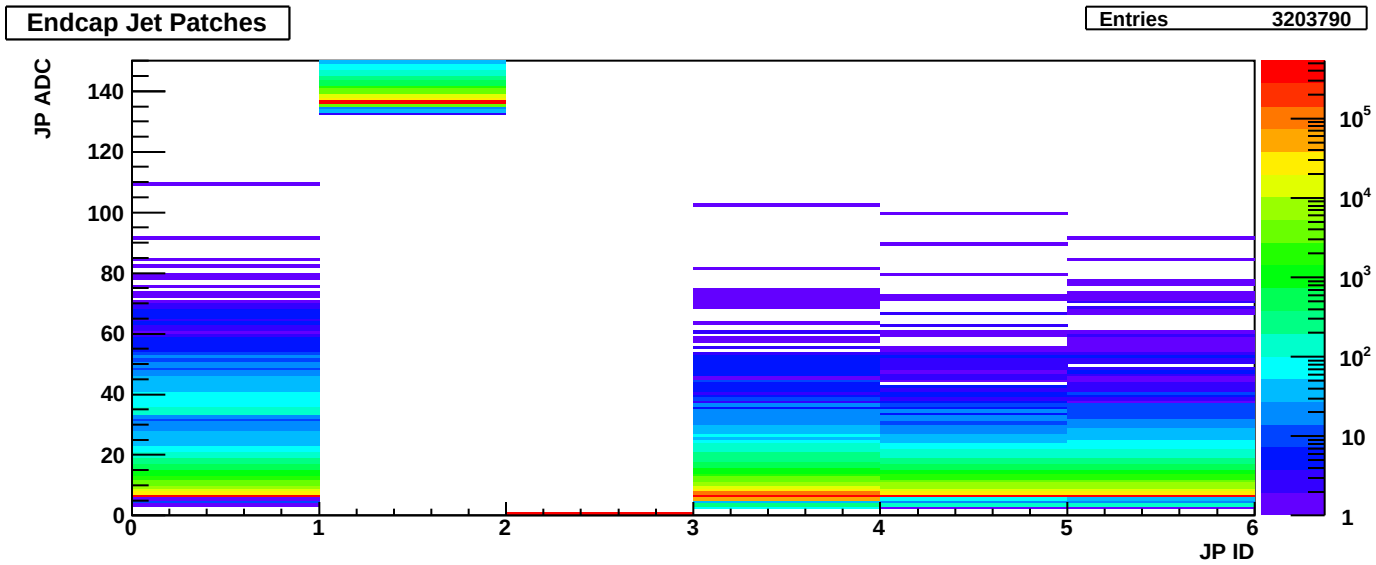

Barrel EMC L0 Input - High Tower
Entries 1.601895e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

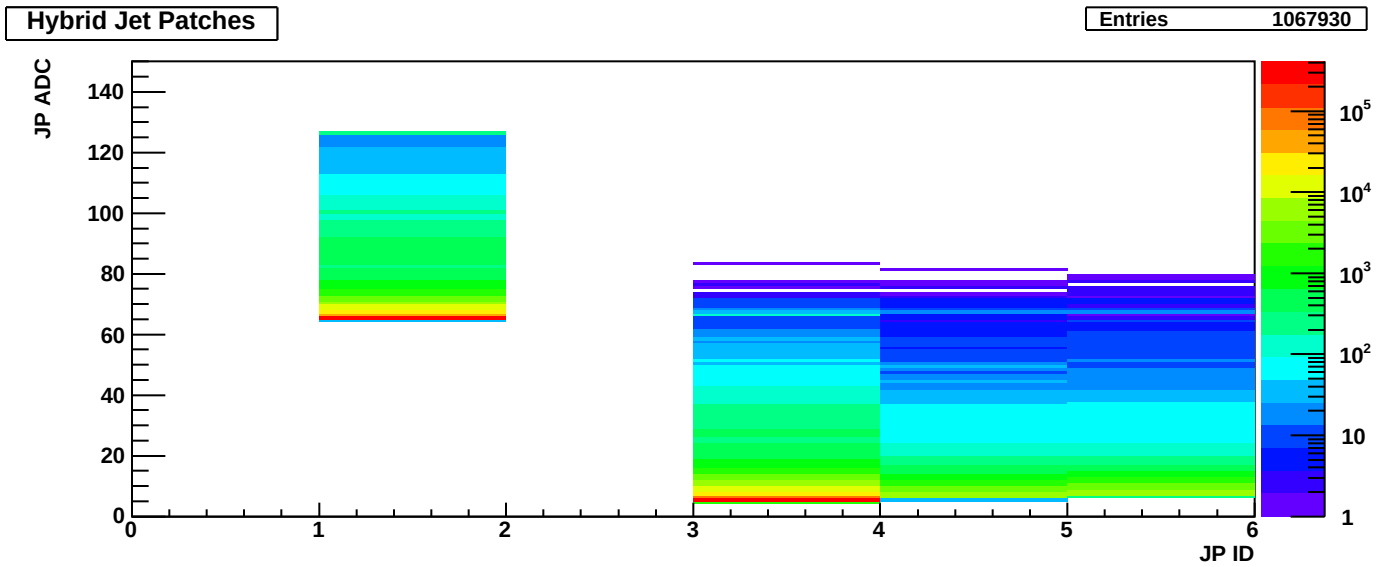

Barrel EMC L0 Input - Patch Sum
Entries 1.601895e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

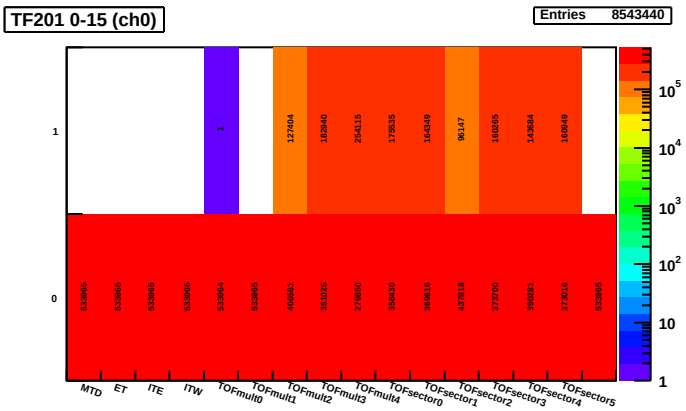

Endcap EMC L0 Input - High Tower
Entries 4.805685e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

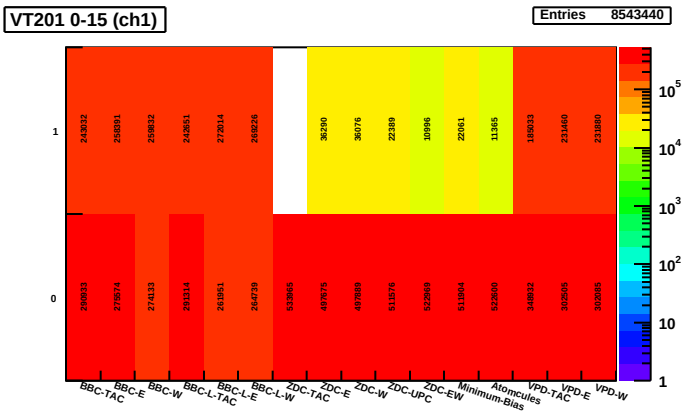

Endcap EMC L0 Input - Patch Sum
Entries 4.805685e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

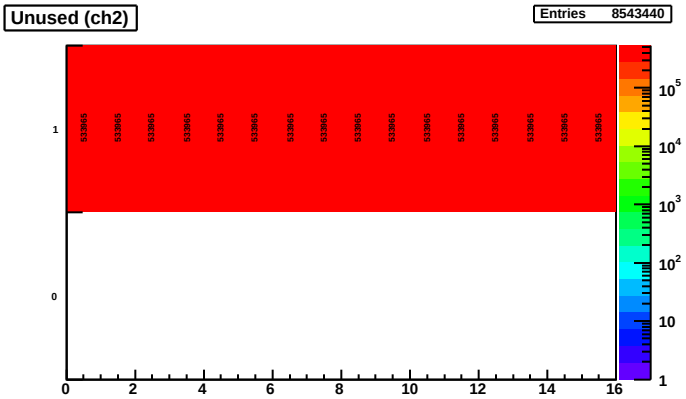

Barrel Jet Patches
Entries 9611370
JP ADC
JP ID

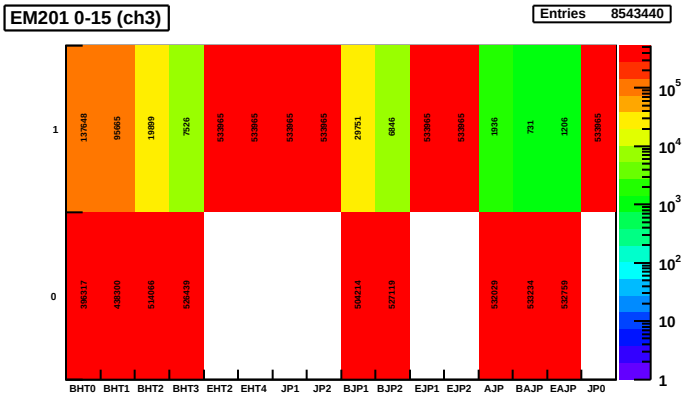

Endcap Jet Patches
Entries 3203790
JP ADC
JP ID

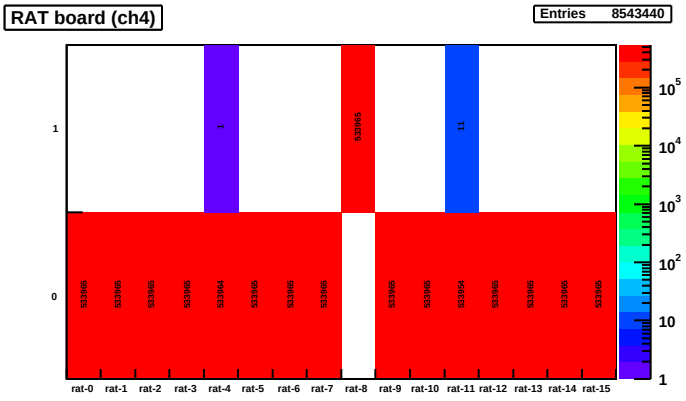

Hybrid Jet Patches
Entries 1067930
JP ADC
JP ID

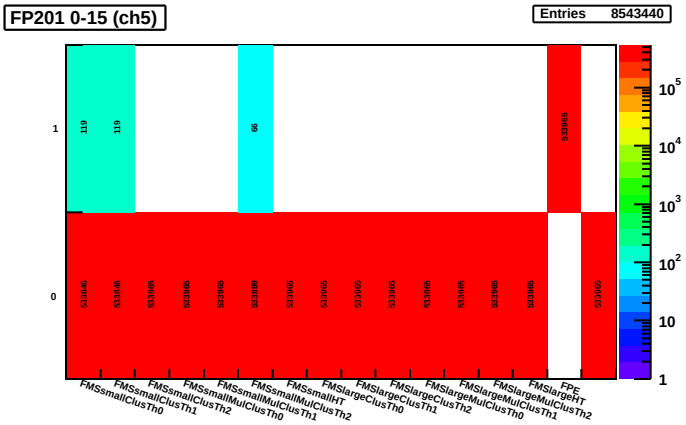

TF201 0-15 (ch0)
Entries 8543440
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

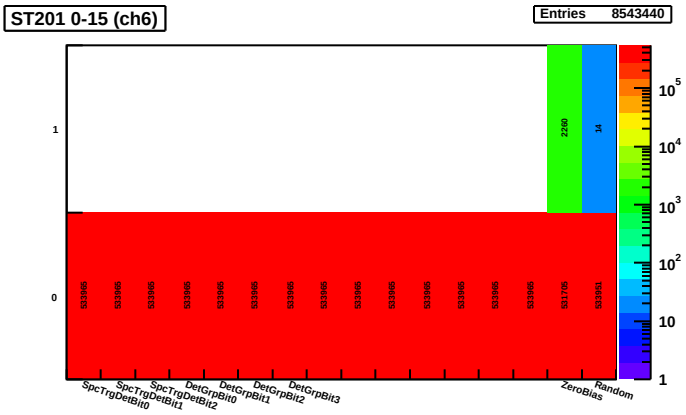

VT201 0-15 (ch1)
Entries 8543440
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

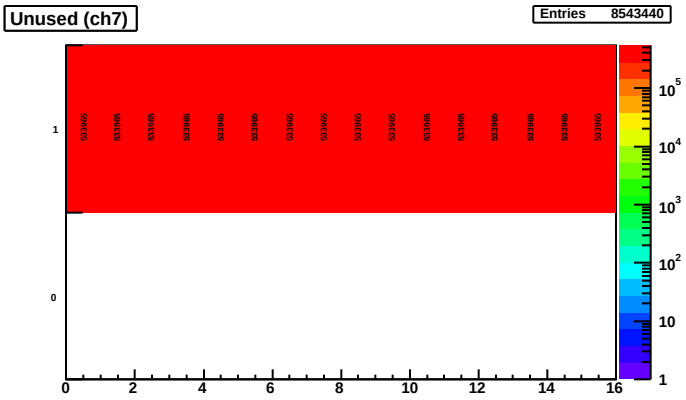

Unused (ch2)
Entries 8543440

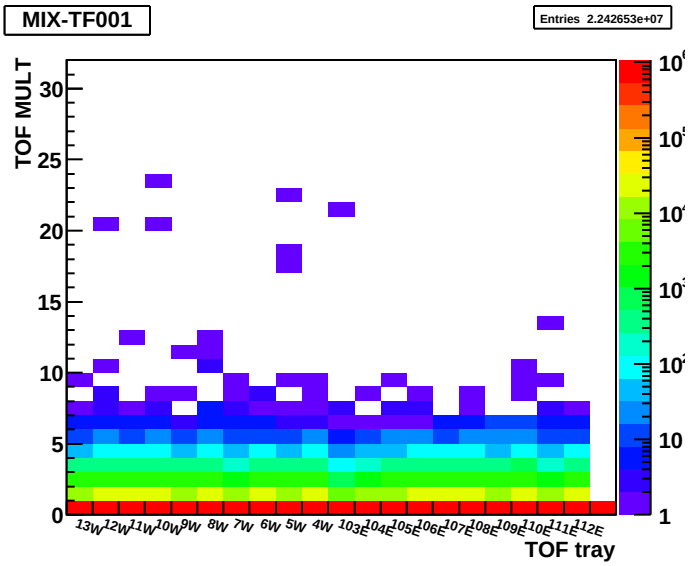

EM201 0-15 (ch3)
Entries 8543440
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

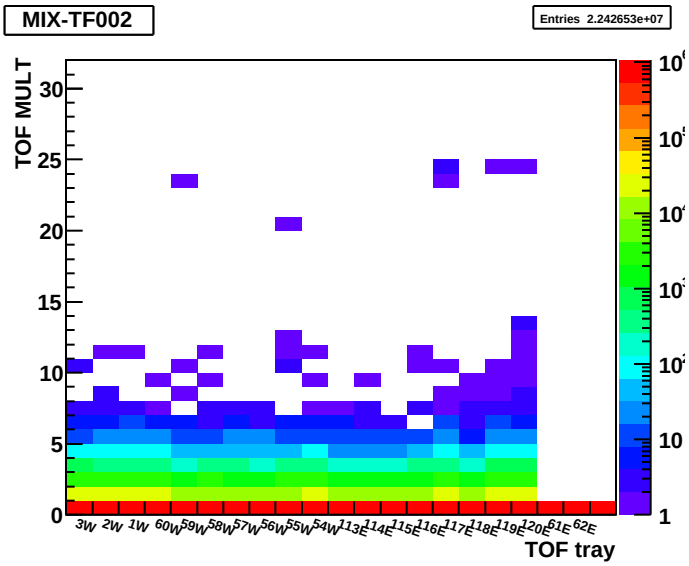

RAT board (ch4)
Entries 8543440
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

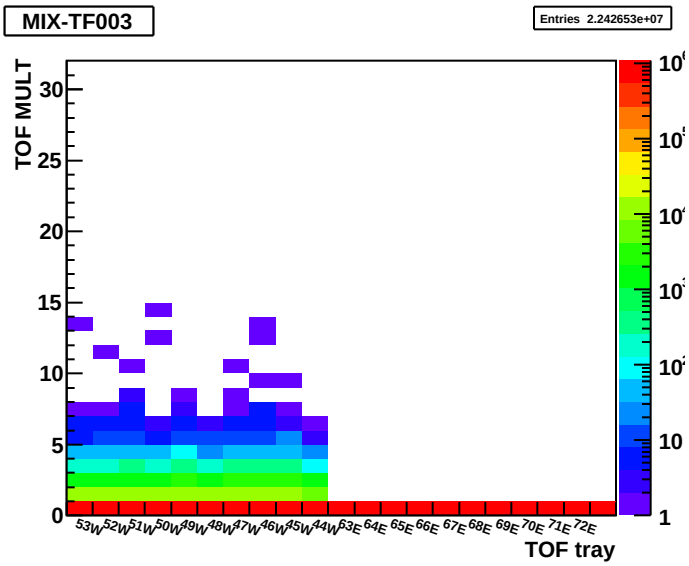

FP201 0-15 (ch5)
Entries 8543440
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

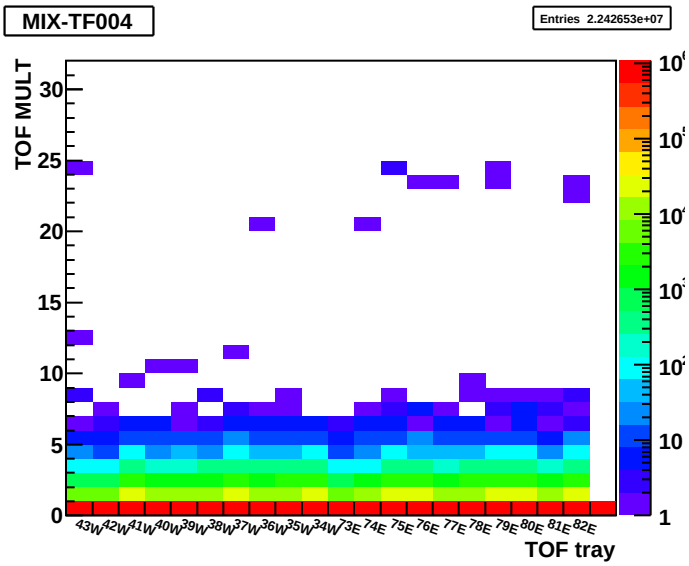

ST201 0-15 (ch6)
Entries 8543440
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

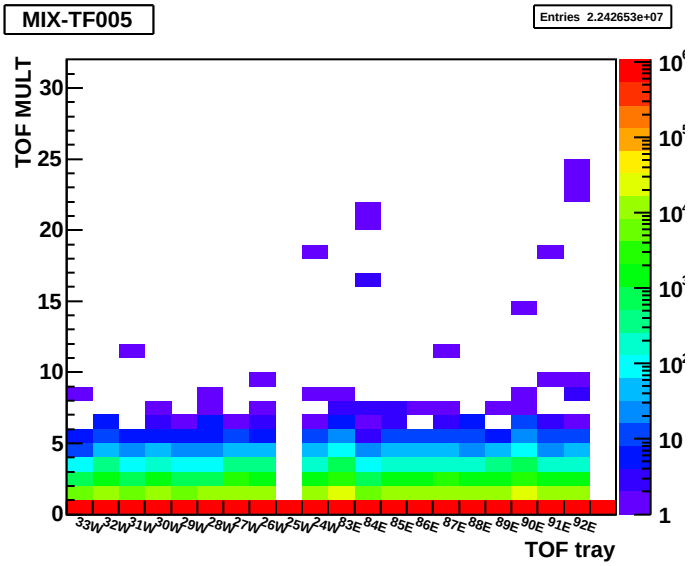

Unused (ch7)
Entries 8543440

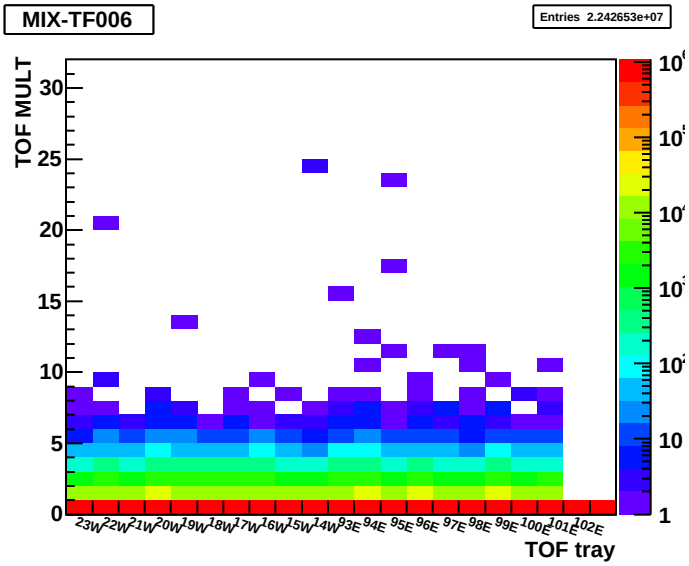

MIX-TF001
Entries 2.242653e+07
TOF MULT
TOF tray

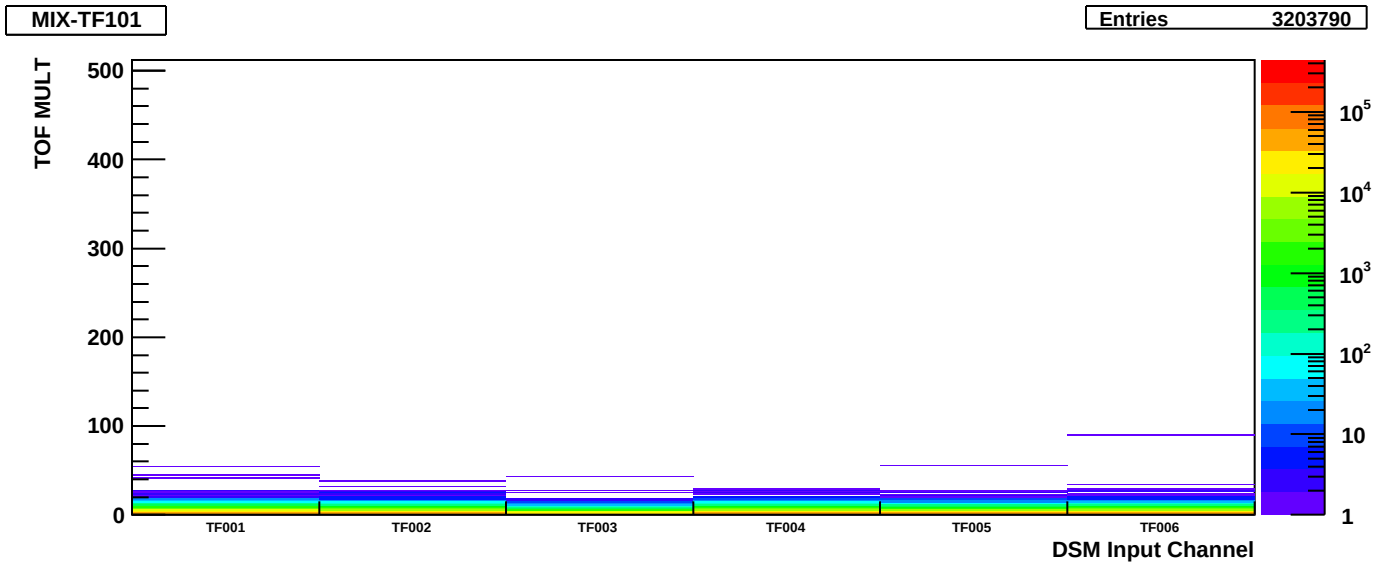

MIX-TF002
Entries 2.242653e+07
TOF MULT
TOF tray

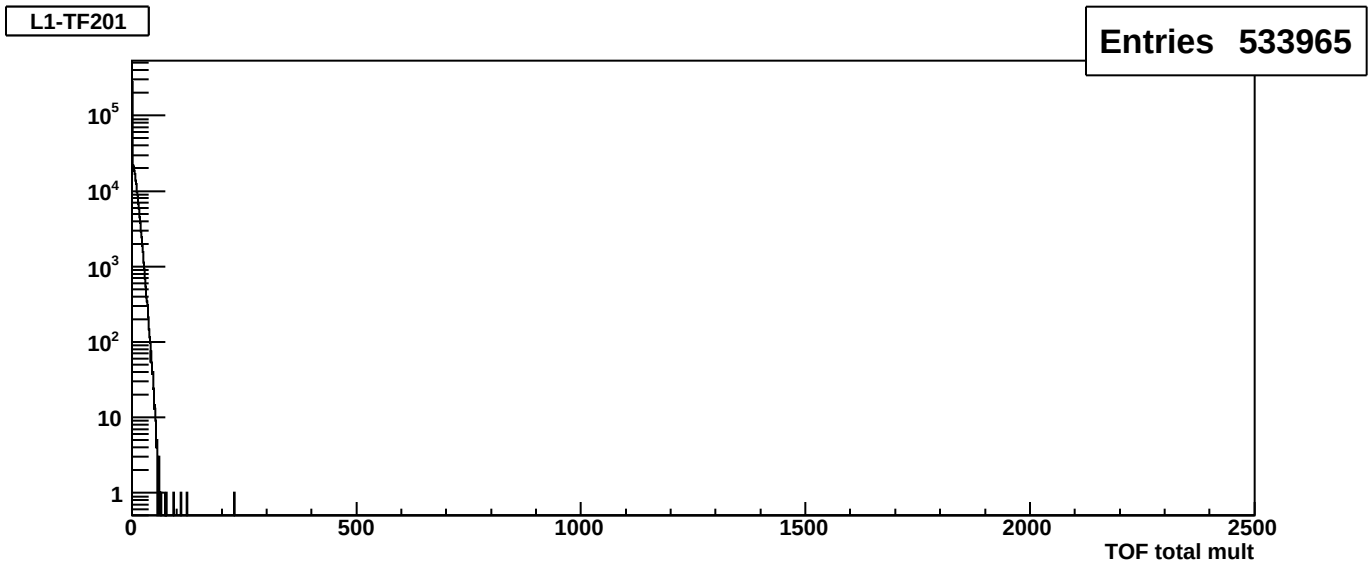

MIX-TF003
Entries 2.242653e+07
TOF MULT
TOF tray

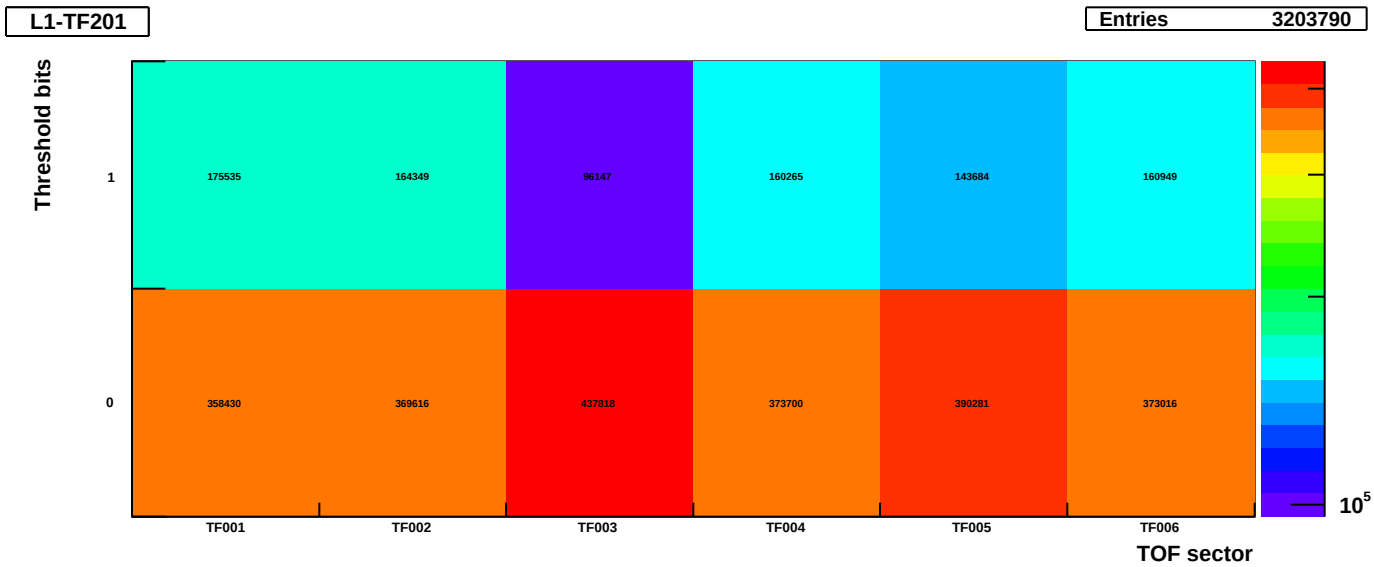

MIX-TF004
Entries 2.242653e+07
TOF MULT
TOF tray

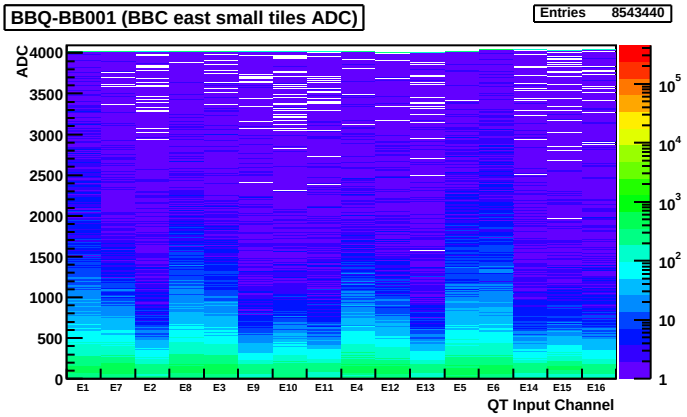

MIX-TF005
Entries 2.242653e+07
TOF MULT
TOF tray

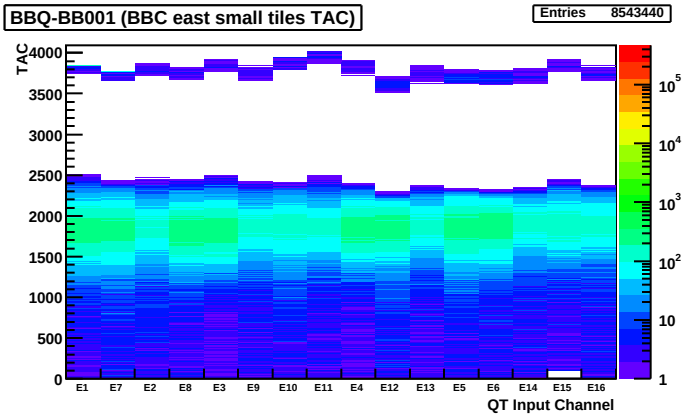

MIX-TF006
Entries 2.242653e+07
TOF MULT
TOF tray

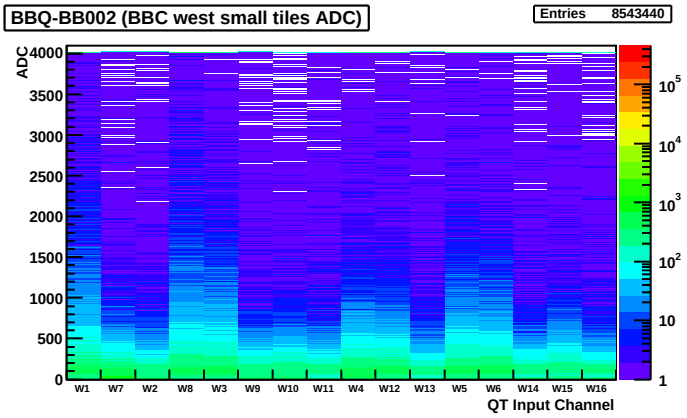

MIX-TF101
Entries 3203790
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

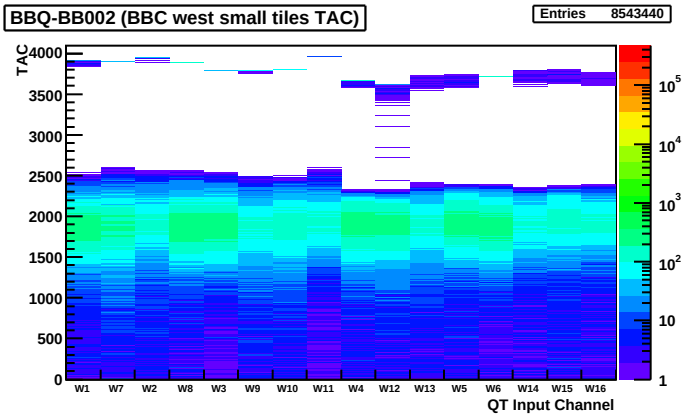

L1-TF201
Entries 533965
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

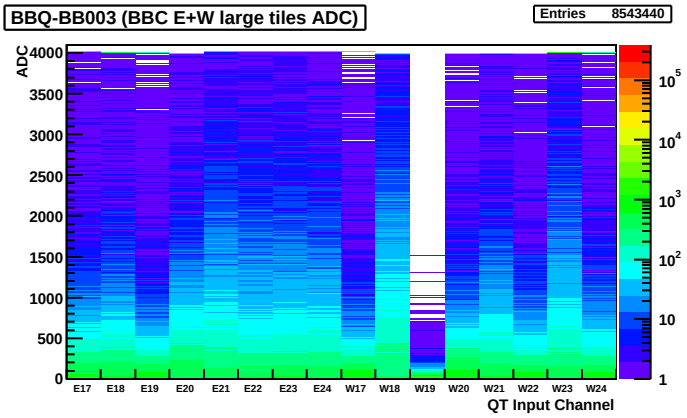

L1-TF201
Entries 3203790
Threshold bits
1
0
175535
164349
96147
160265
143684
160949
358430
369616
437818
373700
390281
373016
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

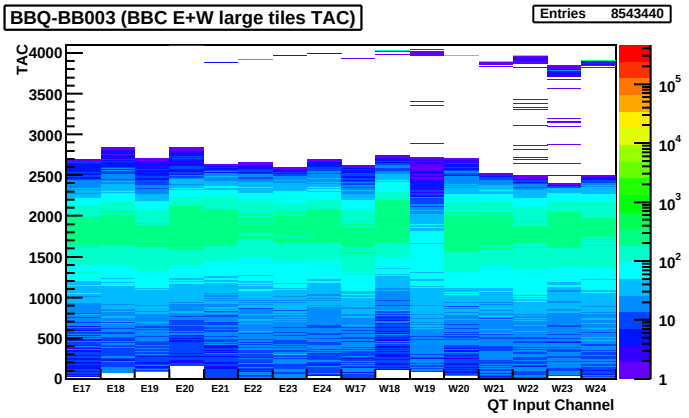

BBQ-BB001 (BBC east small tiles ADC)
Entries 8543440
ADC
QT Input Channel

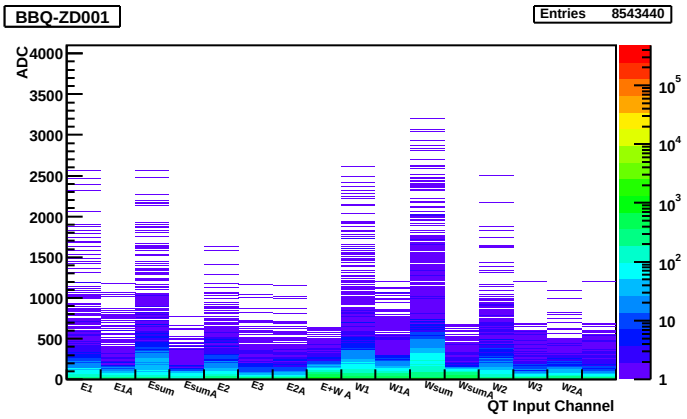

BBQ-BB001 (BBC east small tiles TAC)
Entries 8543440
TAC
QT Input Channel

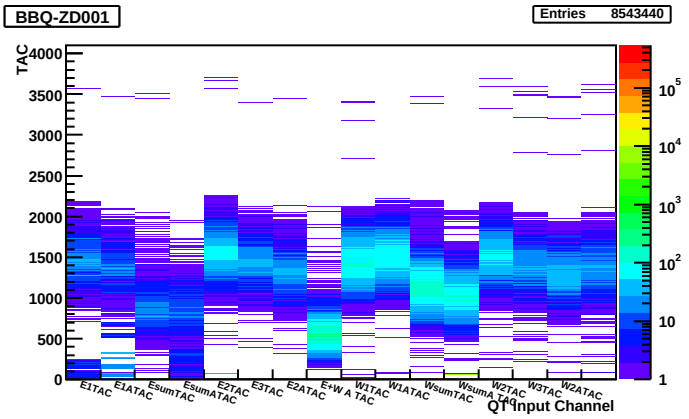

BBQ-BB002 (BBC west small tiles ADC)
Entries 8543440
ADC
QT Input Channel

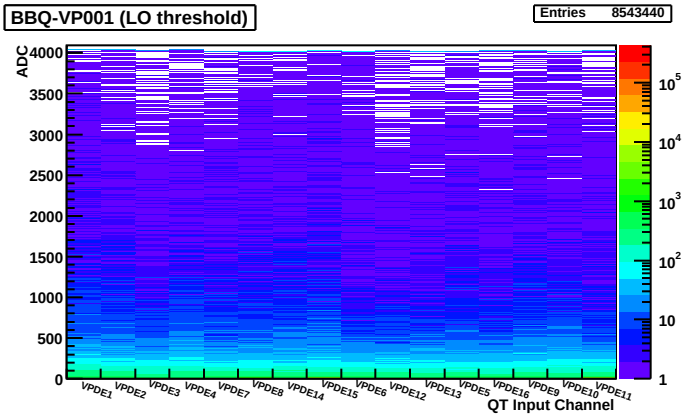

BBQ-BB002 (BBC west small tiles TAC)
Entries 8543440
TAC
QT Input Channel

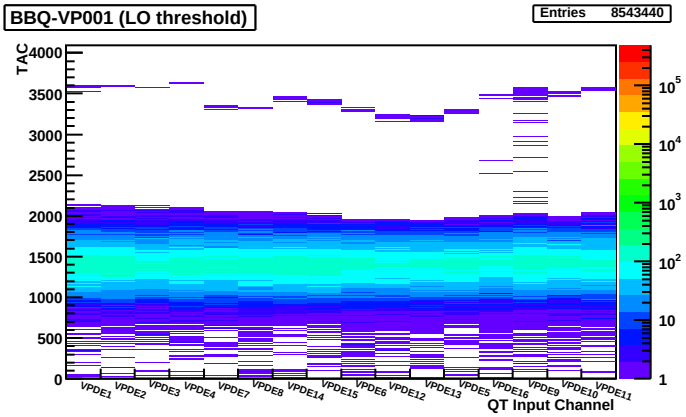

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8543440
ADC
QT Input Channel

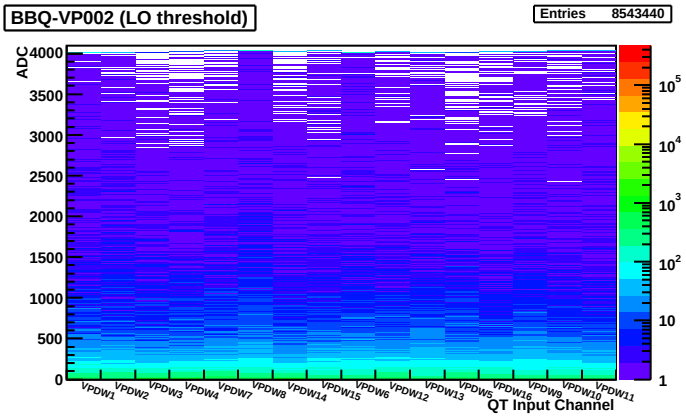

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8543440
TAC
QT Input Channel

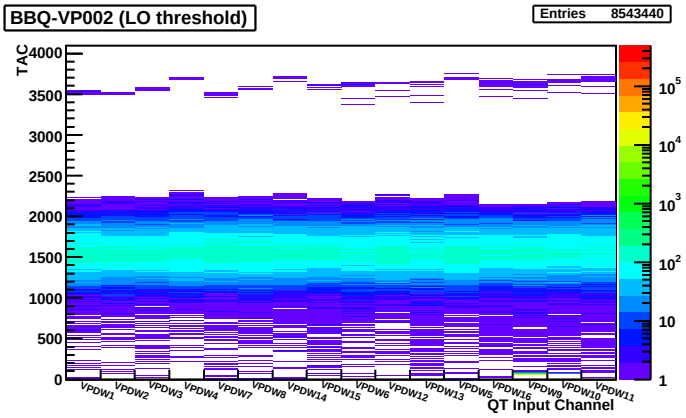

BBQ-ZD001
Entries 8543440
ADC
QT Input Channel

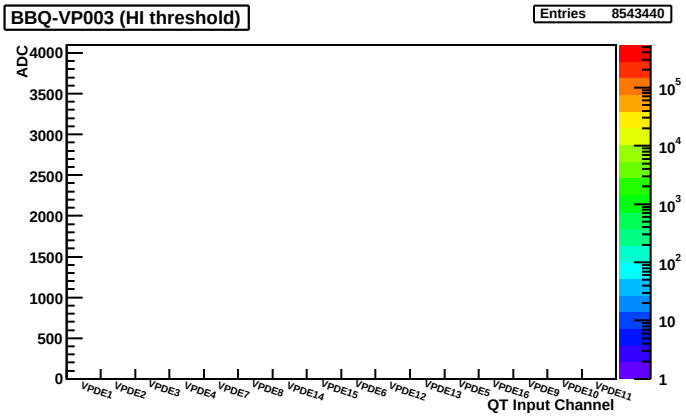

BBQ-ZD001
Entries 8543440
TAC
QT Input Channel

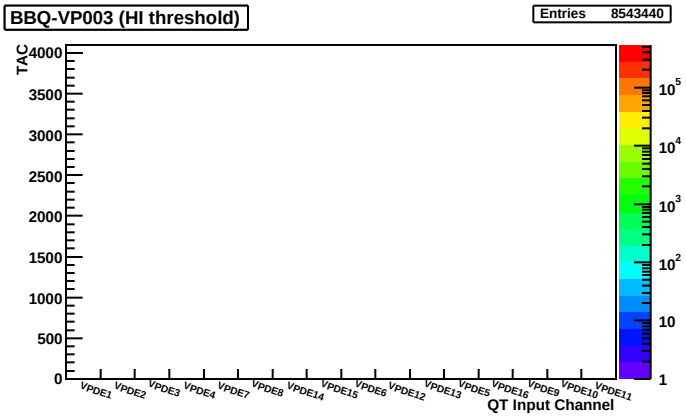

BBQ-VP001 (LO threshold)
Entries 8543440
ADC
QT Input Channel

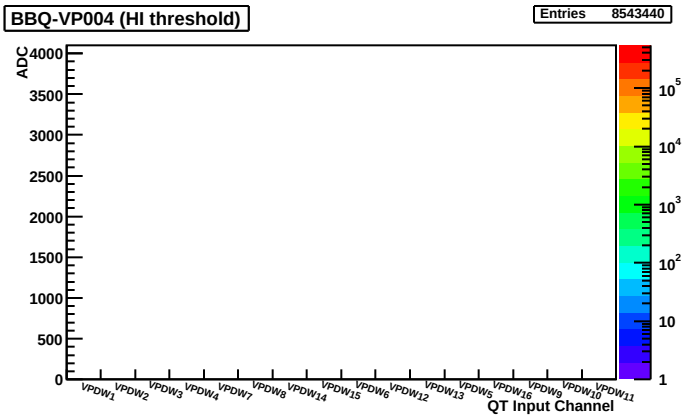

BBQ-VP001 (LO threshold)
Entries 8543440
TAC
QT Input Channel

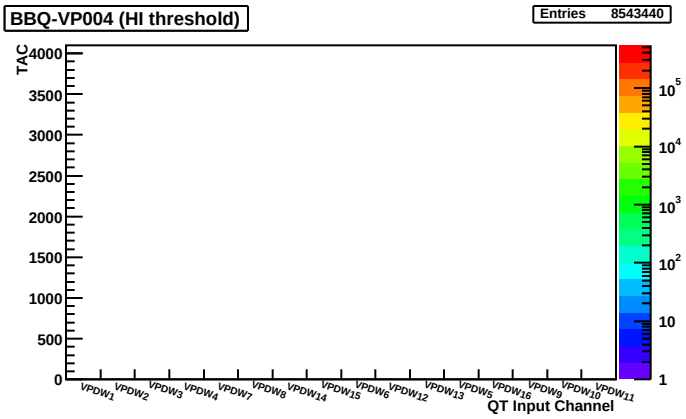

BBQ-VP002 (LO threshold)
Entries 8543440
ADC
QT Input Channel

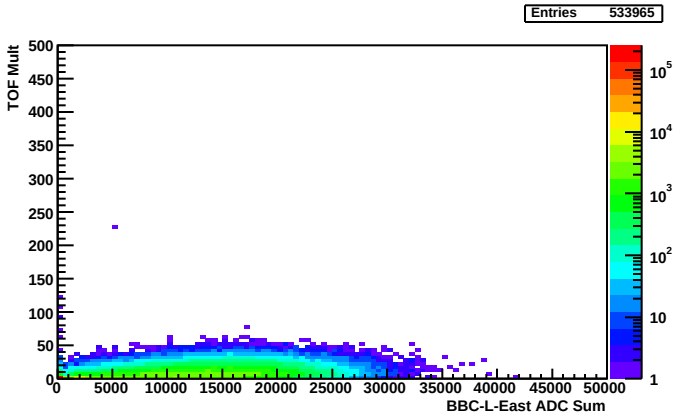

BBQ-VP002 (LO threshold)
Entries 8543440
TAC
QT Input Channel

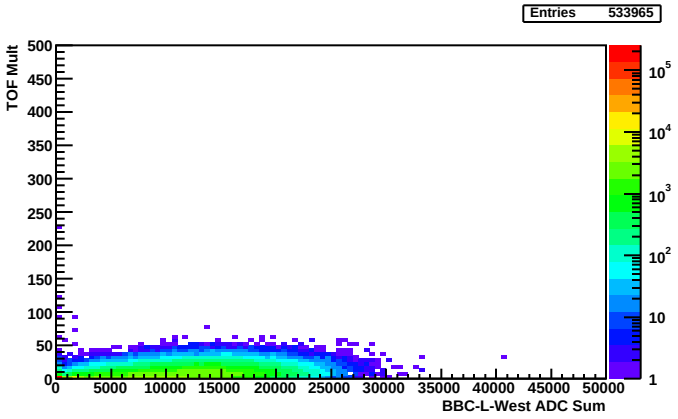

BBQ-VP003 (HI threshold)
Entries 8543440
ADC
QT Input Channel

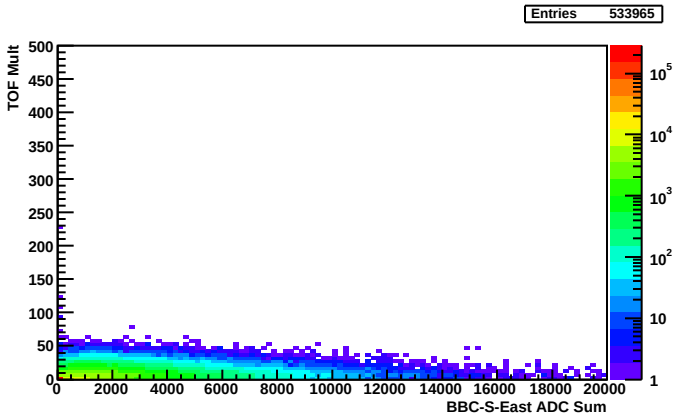

BBQ-VP003 (HI threshold)
Entries 8543440
TAC
QT Input Channel

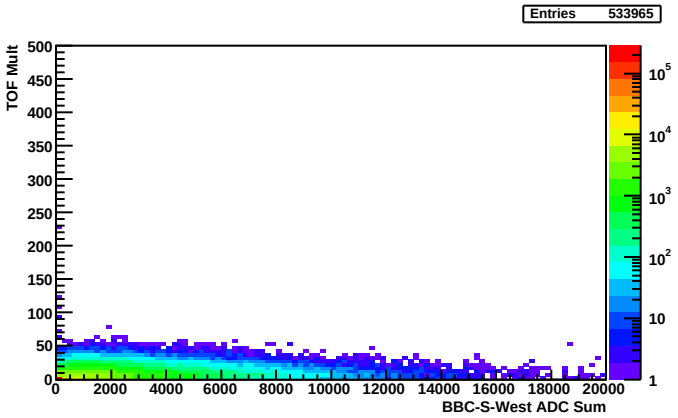

BBQ-VP004 (HI threshold)
Entries 8543440
ADC
QT Input Channel

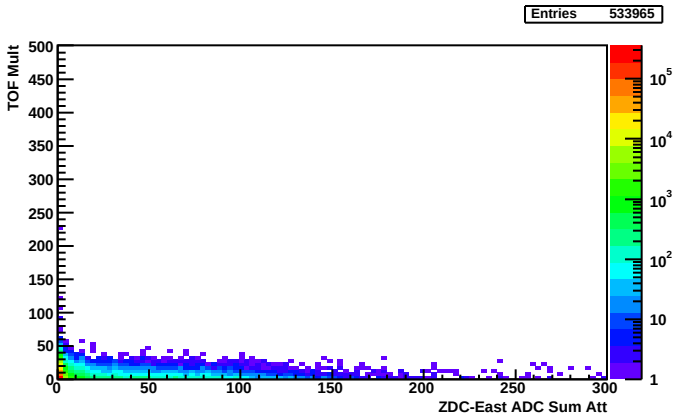

BBQ-VP004 (HI threshold)
Entries 8543440
TAC
QT Input Channel

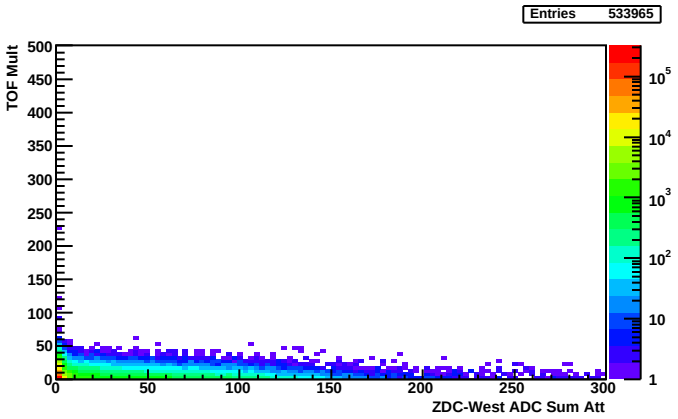

Entries 533965
TOF Mult
BBC-L-East ADC Sum

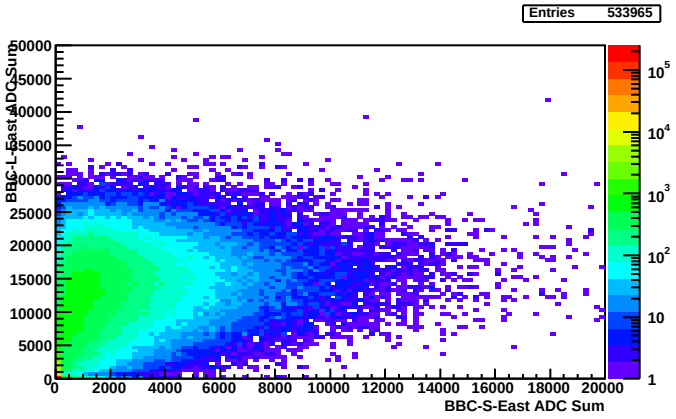

Entries 533965
TOF Mult
BBC-L-West ADC Sum

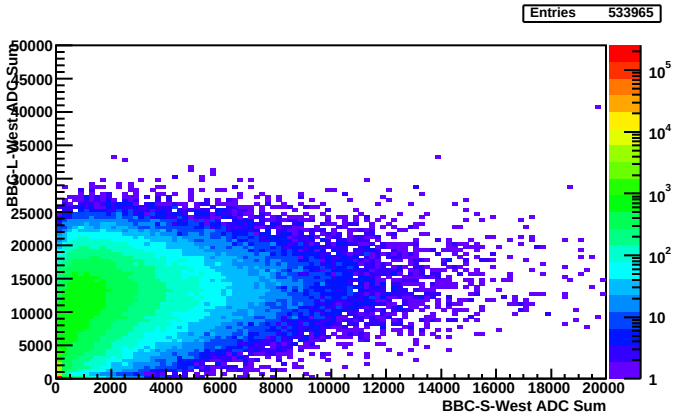

Entries 533965
TOF Mult
BBC-S-East ADC Sum

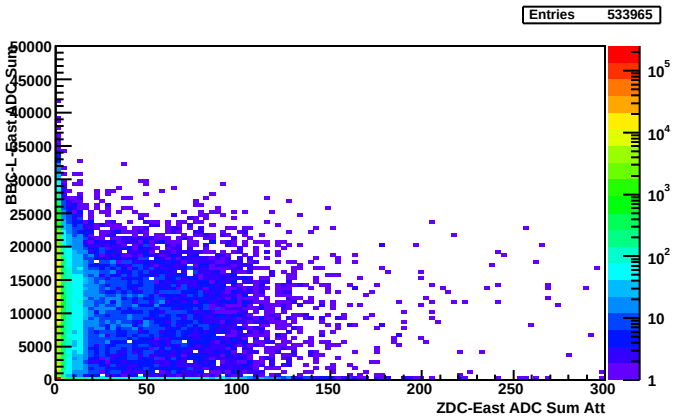

Entries 533965
TOF Mult
BBC-S-West ADC Sum

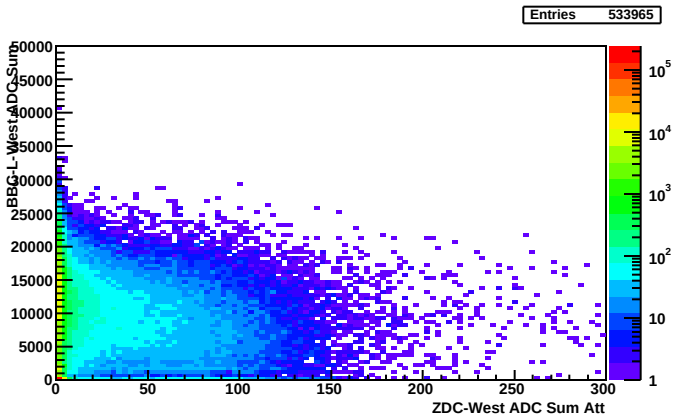

Entries 533965
TOF Mult
ZDC-East ADC Sum Att

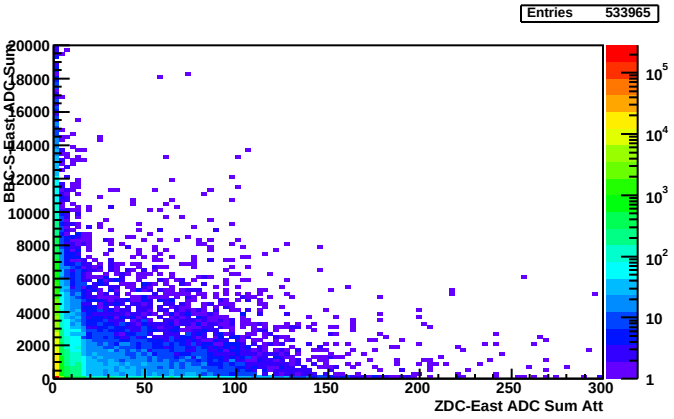

Entries 533965
TOF Mult
ZDC-West ADC Sum Att

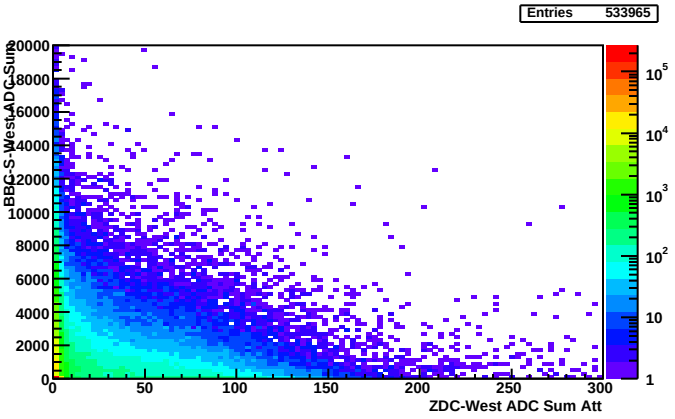

Entries 533965
BBC-L-East ADC Sum
BBC-S-East ADC Sum

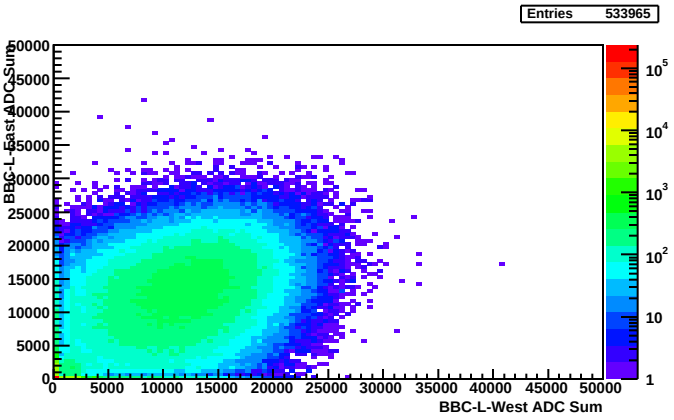

Entries 533965
BBC-L-West ADC Sum
BBC-S-West ADC Sum

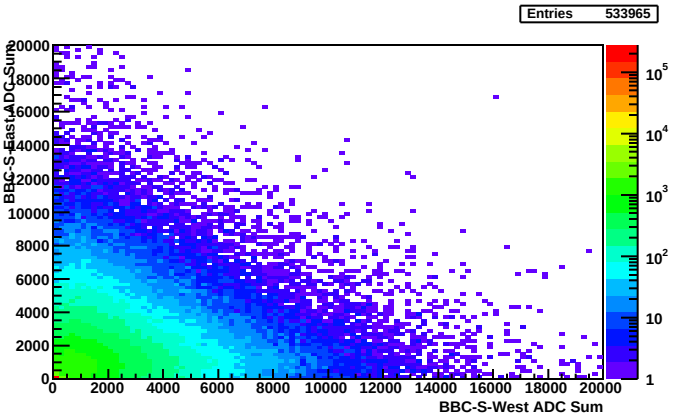

Entries 533965
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

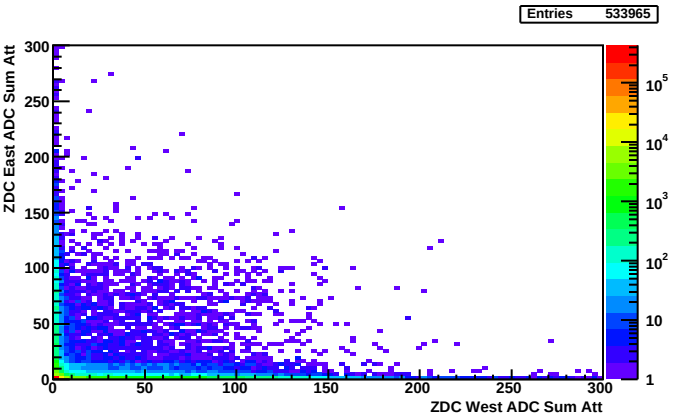

Entries 533965
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

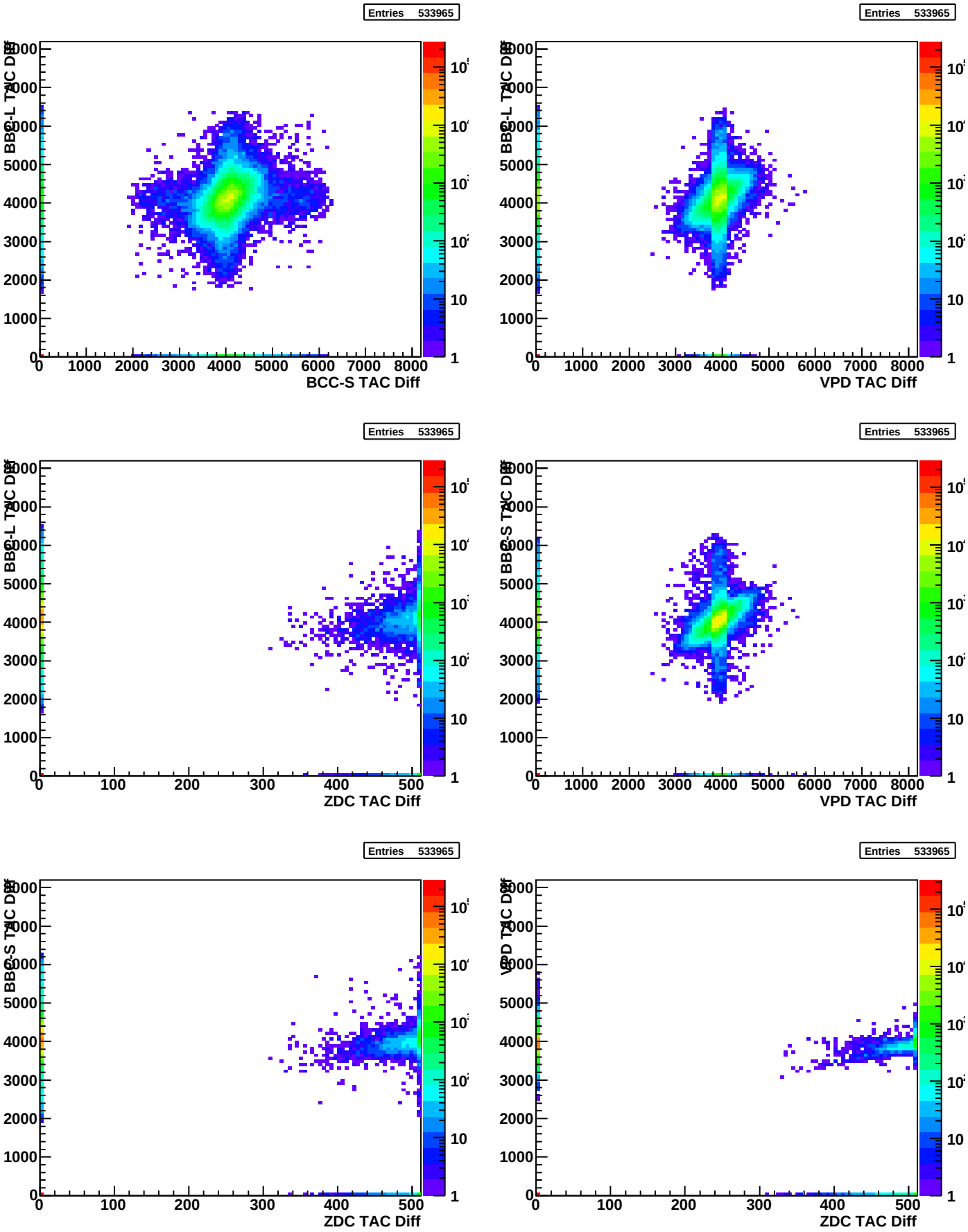

Entries 533965
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

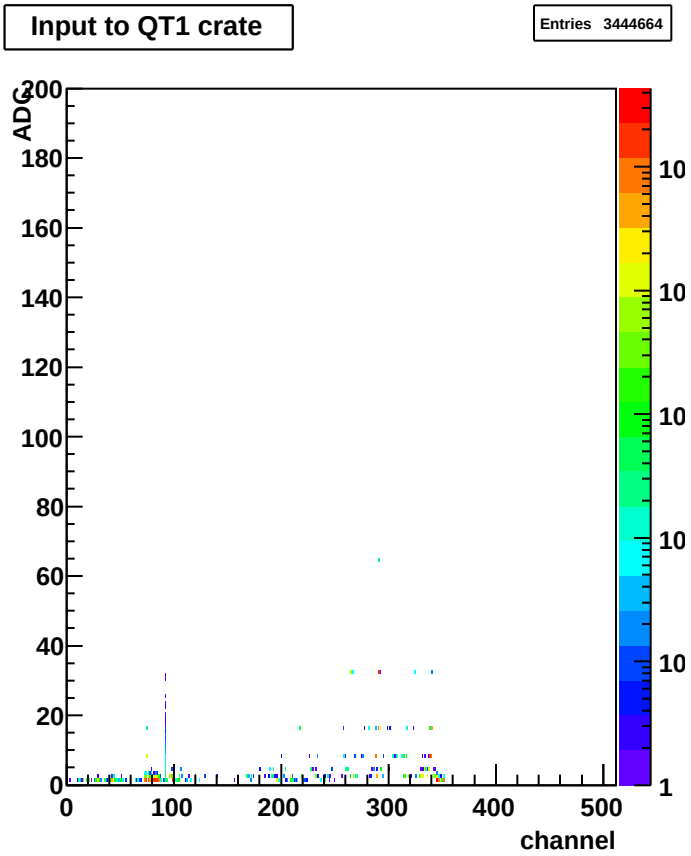

Entries 533965
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

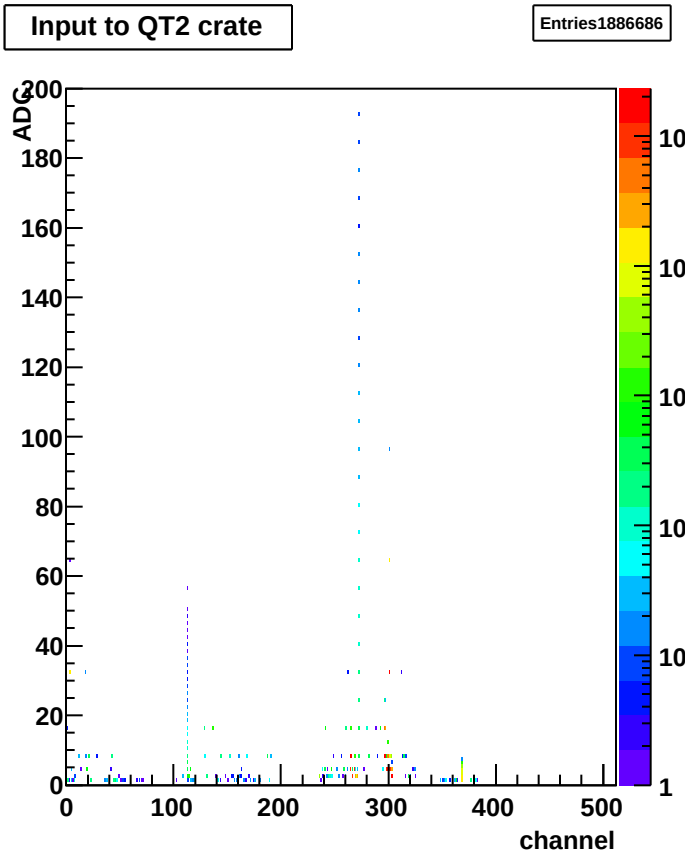

Entries 533965
BBC-L-East ADC Sum
BBC-L-West ADC Sum

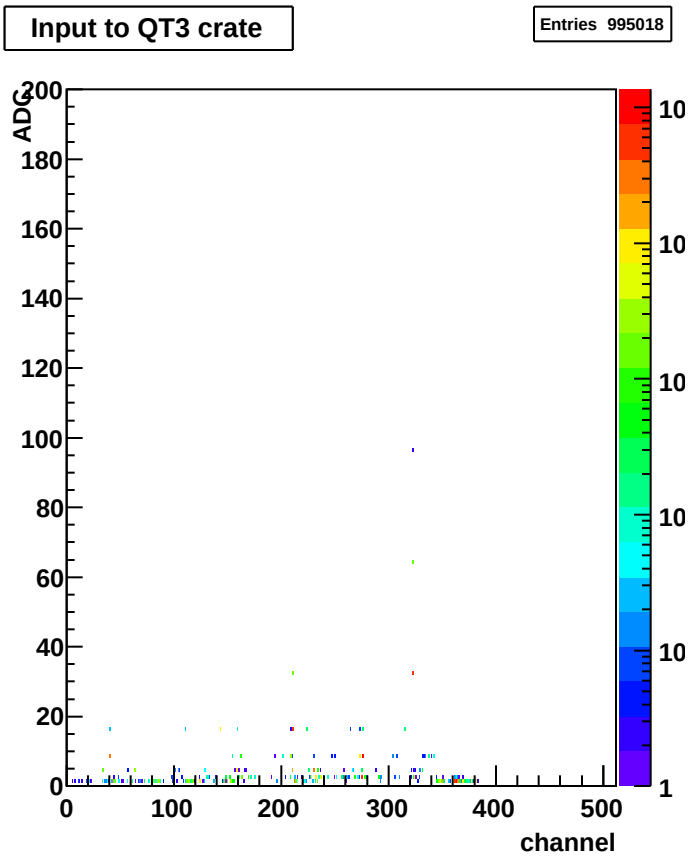

Entries 533965
BBC-S-East ADC Sum
BBC-S-West ADC Sum

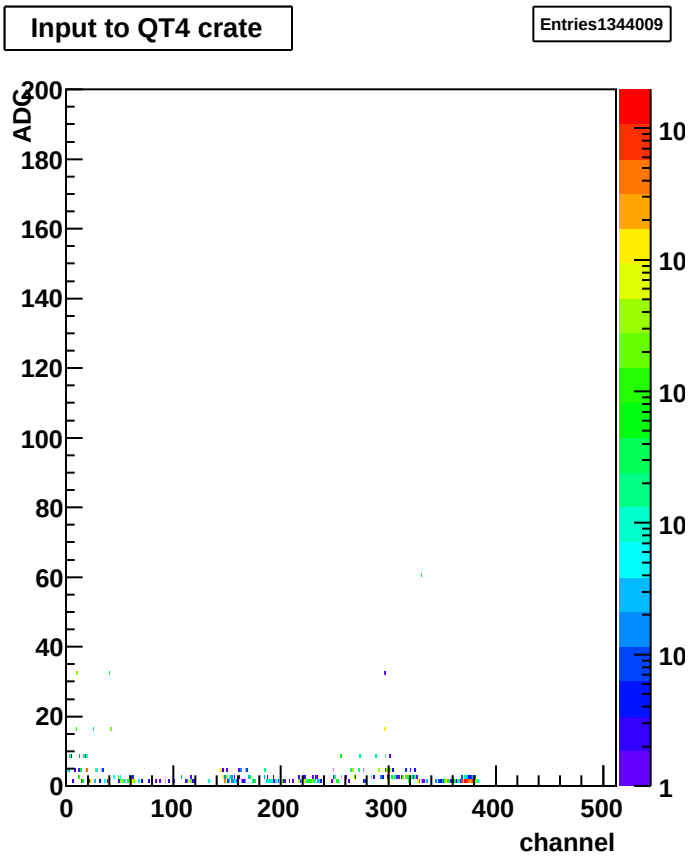

Entries 533965
ZDC East ADC Sum Att
ZDC West ADC Sum Att

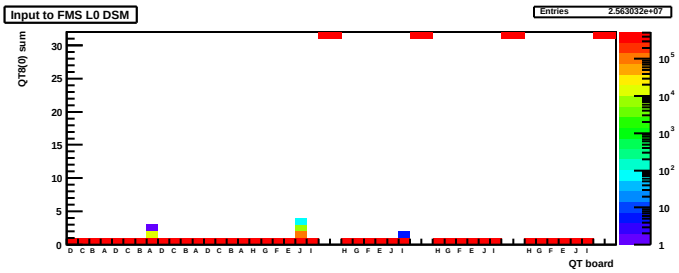

Entries 533965
BBC-L TAC Diff
BCC-S TAC Diff
Entries 533965
BBC-L TAC Diff
VPD TAC Diff
Entries 533965
BBC-L TAC Diff
ZDC TAC Diff
Entries 533965
BBC-S TAC Diff
VPD TAC Diff
Entries 533965
BBC-S TAC Diff
ZDC TAC Diff
Entries 533965
VPD TAC Diff
ZDC TAC Diff

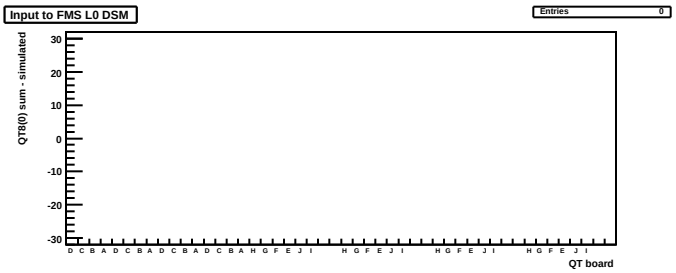

Input to QT1 crate
Entries 3444664
ADC
channel

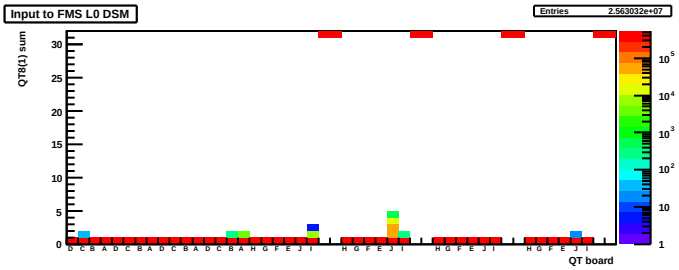

Input to QT2 crate
Entries1886686
ADC
channel

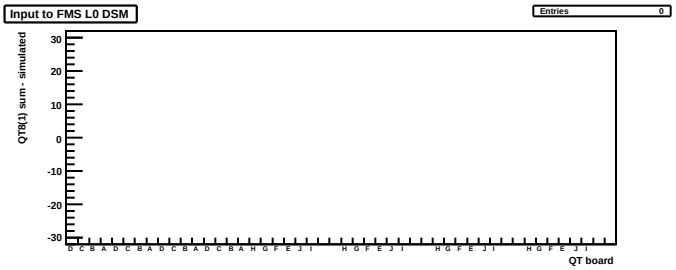

Input to QT3 crate
Entries 995018
ADC
channel

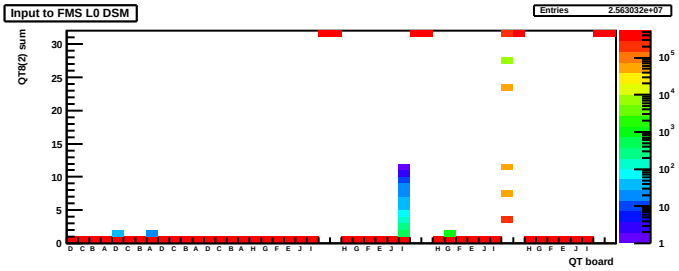

Input to QT4 crate
Entries1344009
ADC
channel

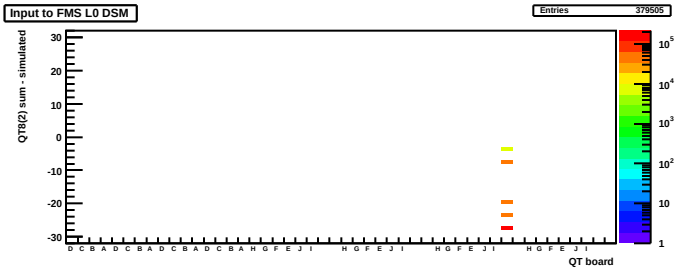

Input to FMS L0 DSM
Entries 2.563032e+07
QT8(0) sum
QT board

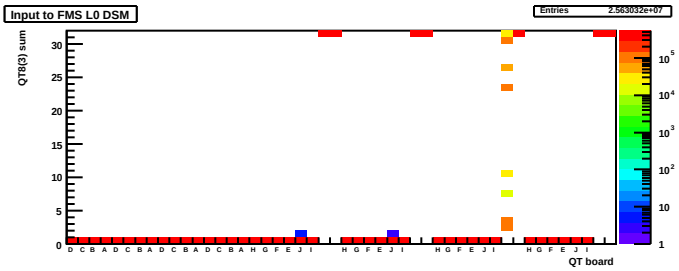

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

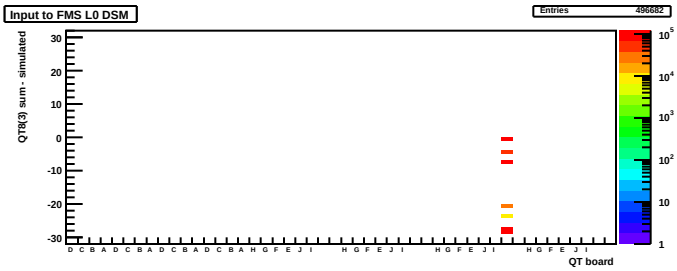

Input to FMS L0 DSM
Entries 2.563032e+07
QT8(1) sum
QT board

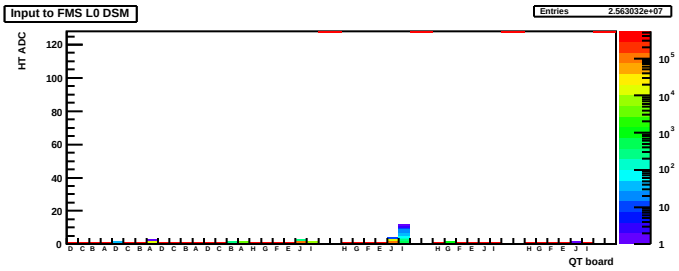

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

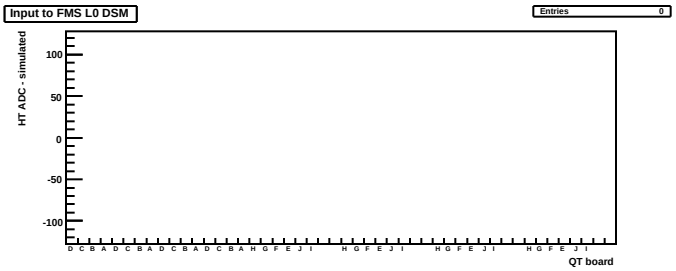

Input to FMS L0 DSM
Entries 2.563032e+07
QT8(2) sum
QT board

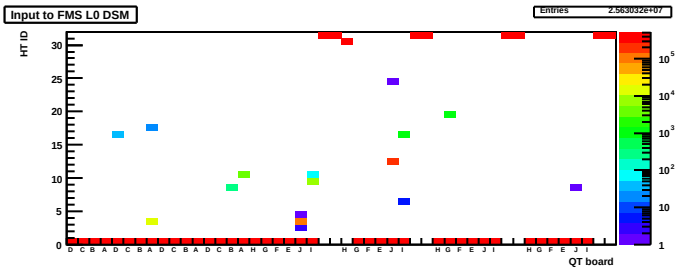

Input to FMS L0 DSM
Entries 379505
QT8(2) sum - simulated
QT board

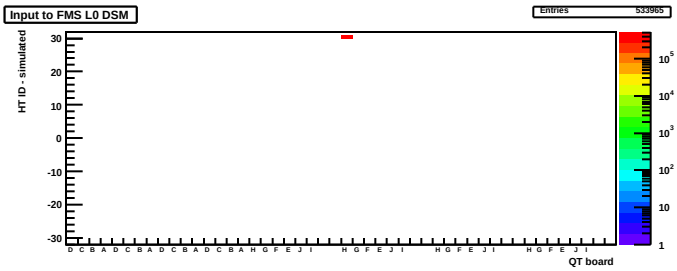

Input to FMS L0 DSM
Entries 2.563032e+07
QT8(3) sum
QT board

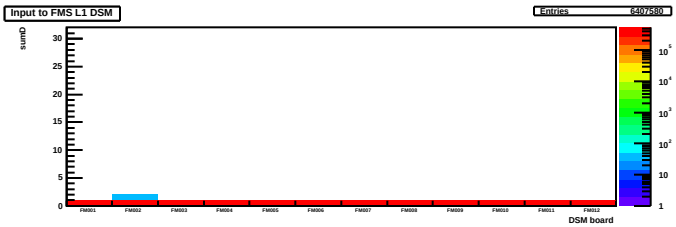

Input to FMS L0 DSM
Entries 496682
QT8(3) sum - simulated
QT board

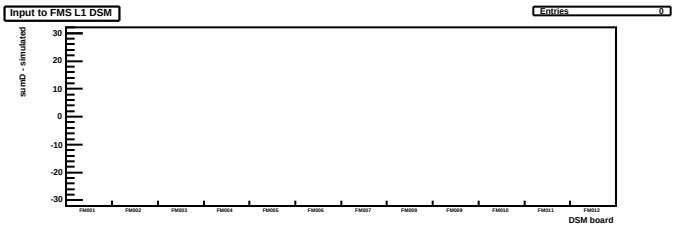

Input to FMS L0 DSM
Entries 2.563032e+07
HT ADC
QT board

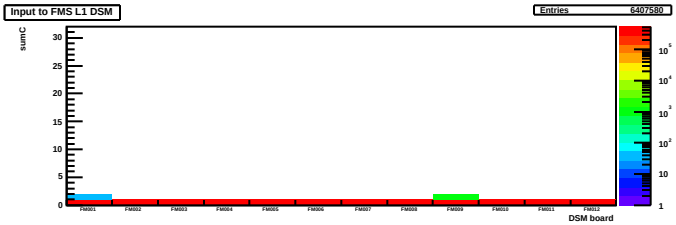

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

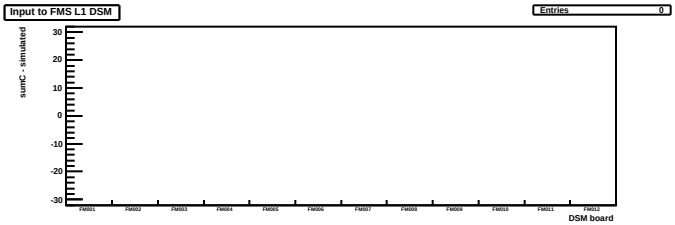

Input to FMS L0 DSM
Entries 2.563032e+07
HT ID
QT board

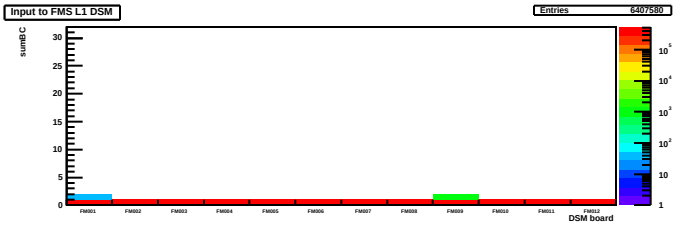

Input to FMS L0 DSM
Entries 533965
HT ID - simulated
QT board

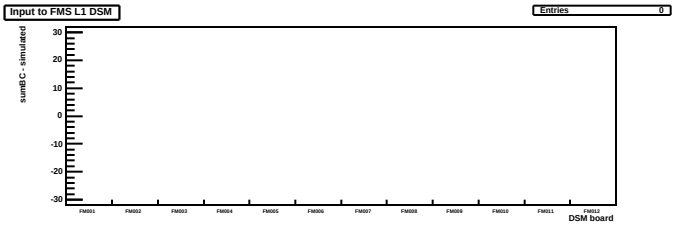

Input to FMS L1 DSM
Entries 6407580
sumD
DSM board

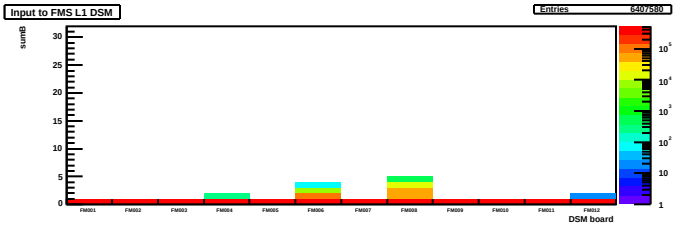

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

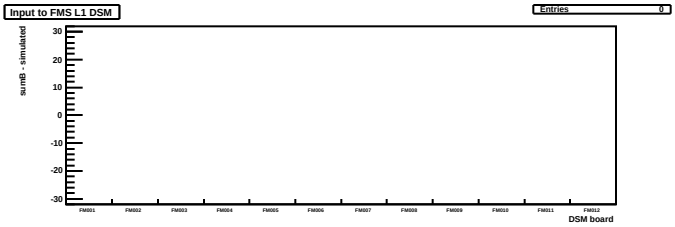

Input to FMS L1 DSM
Entries 6407580
sumC
DSM board

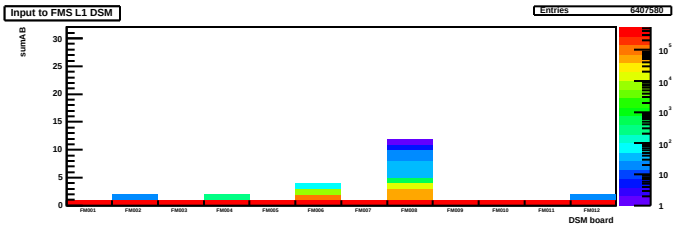

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

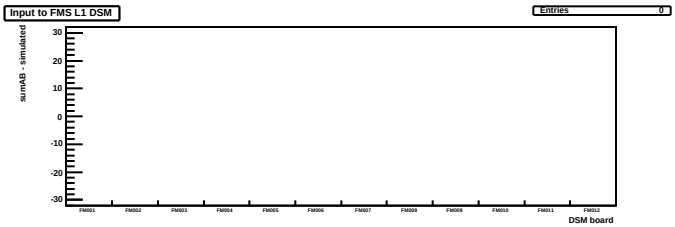

Input to FMS L1 DSM
Entries 6407580
sumBC
DSM board

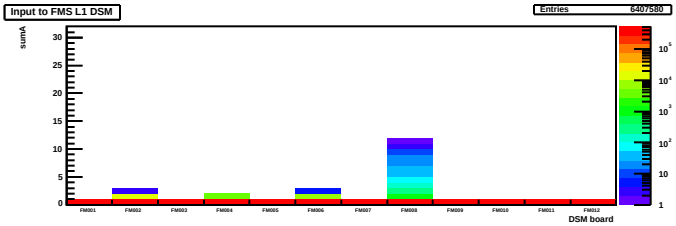

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

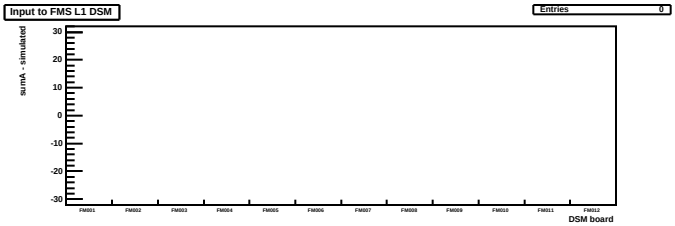

Input to FMS L1 DSM
Entries 6407580
sumB
DSM board

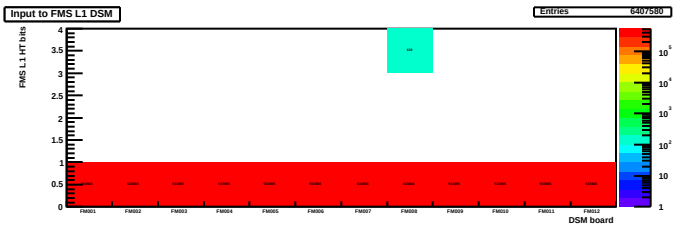

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

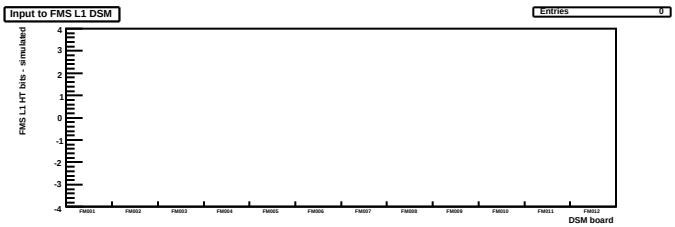

Input to FMS L1 DSM
Entries 6407580
sumAB
DSM board

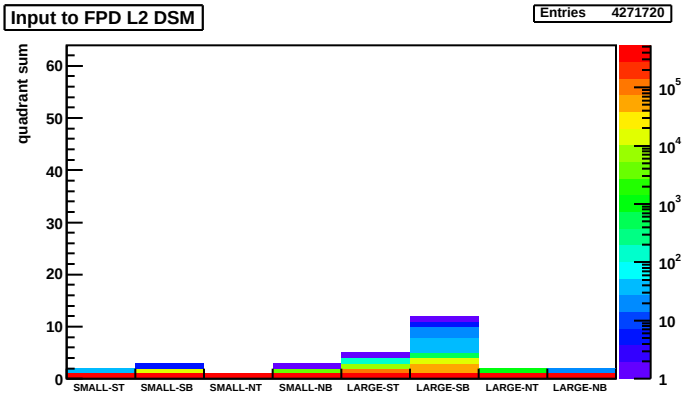

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

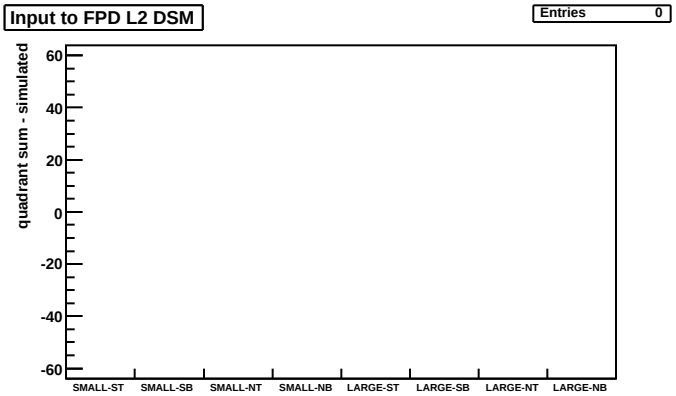

Input to FMS L1 DSM
Entries 6407580
sumA
DSM board

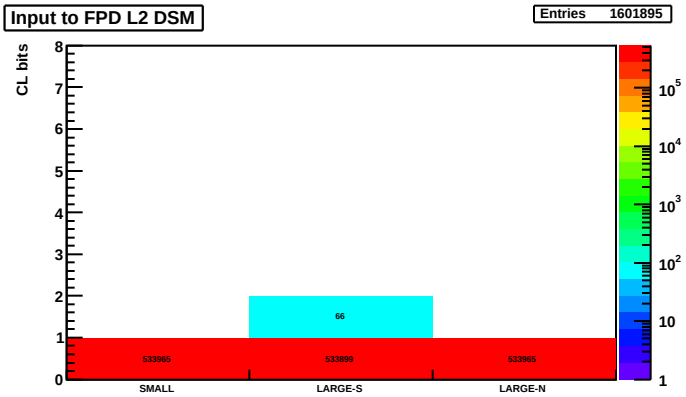

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

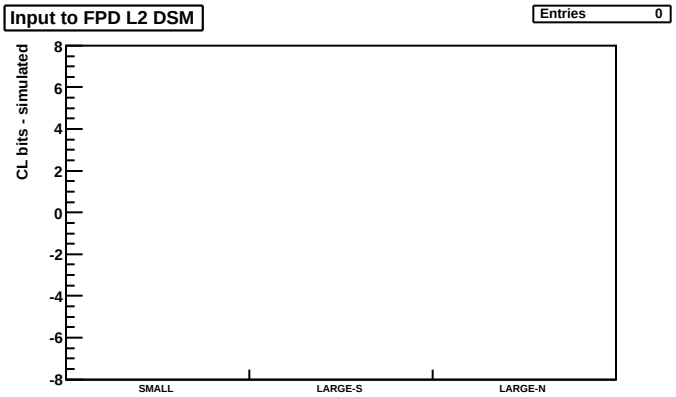

Input to FMS L1 DSM
Entries 6407580
FMS L1 HT bits
DSM board

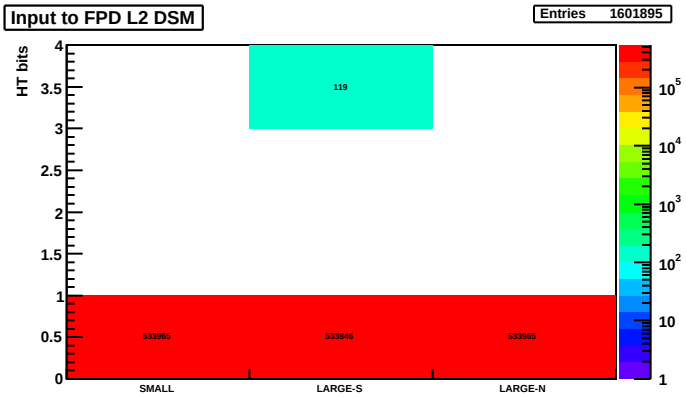

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

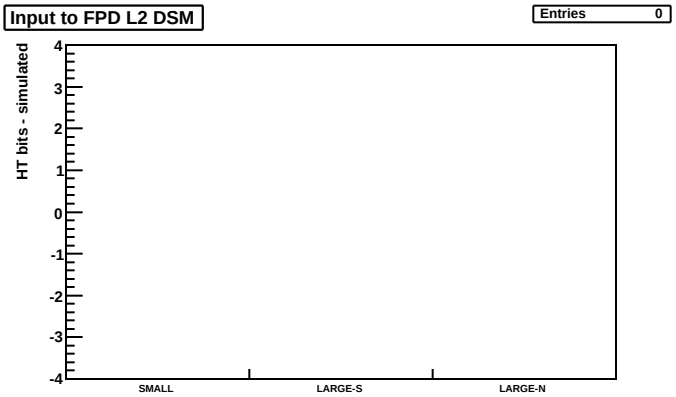

Input to FPD L2 DSM
Entries 4271720
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

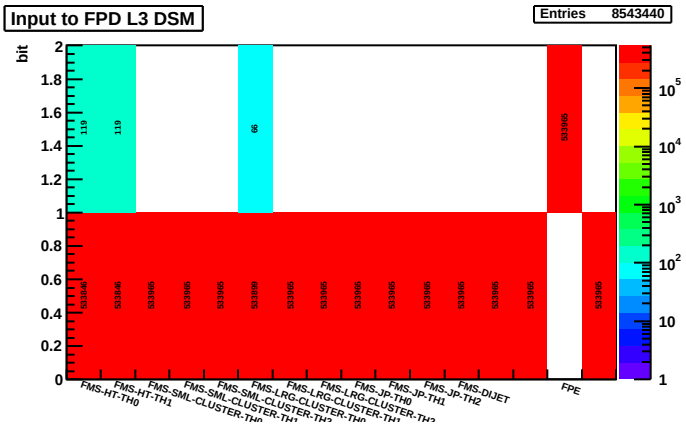

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

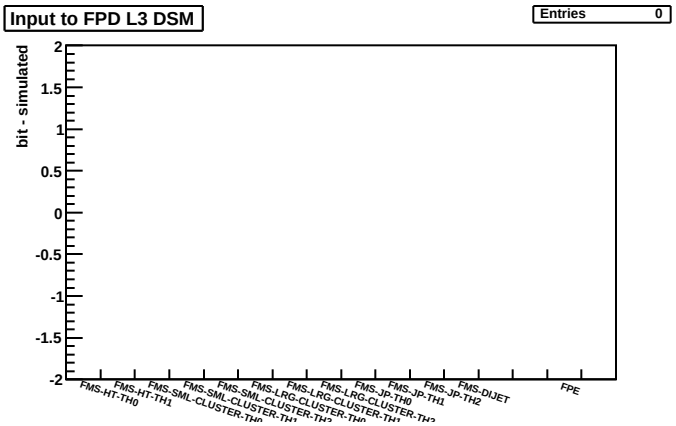

Input to FPD L2 DSM
Entries 1601895
CL bits
533965
66
533899
533965
SMALL
LARGE-S
LARGE-N

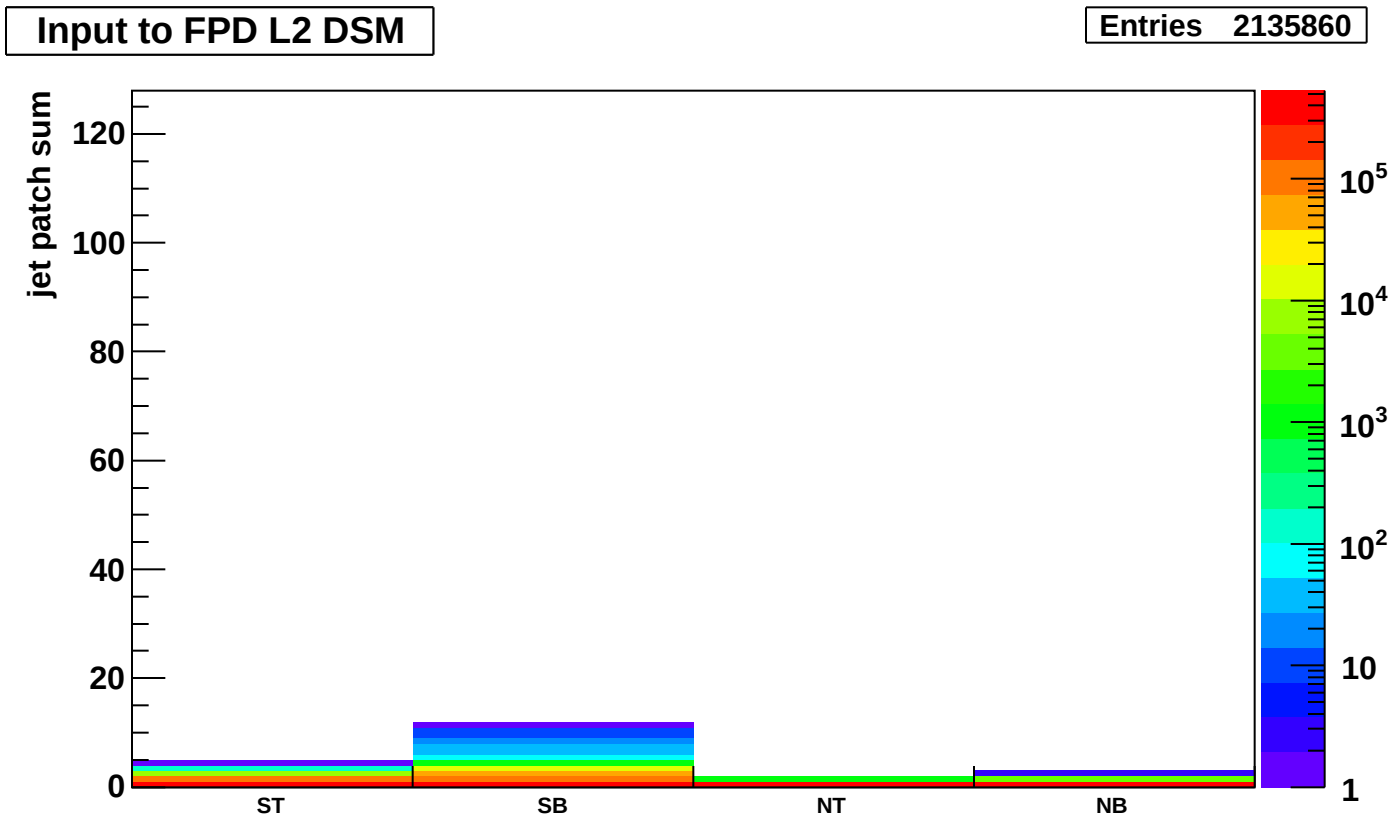

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

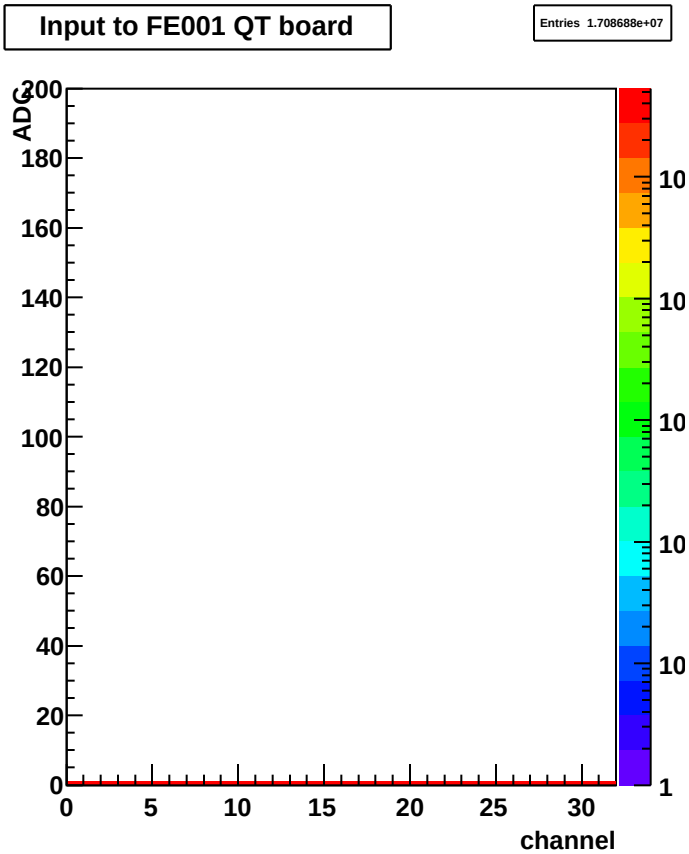

Input to FPD L2 DSM
Entries 1601895
HT bits
119
533965
533846
533965
SMALL
LARGE-S
LARGE-N

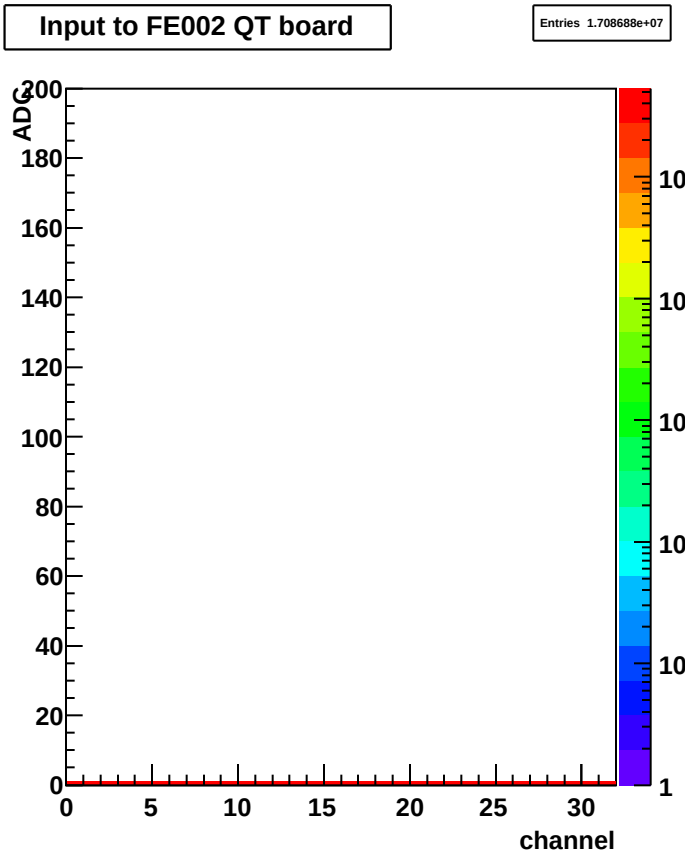

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

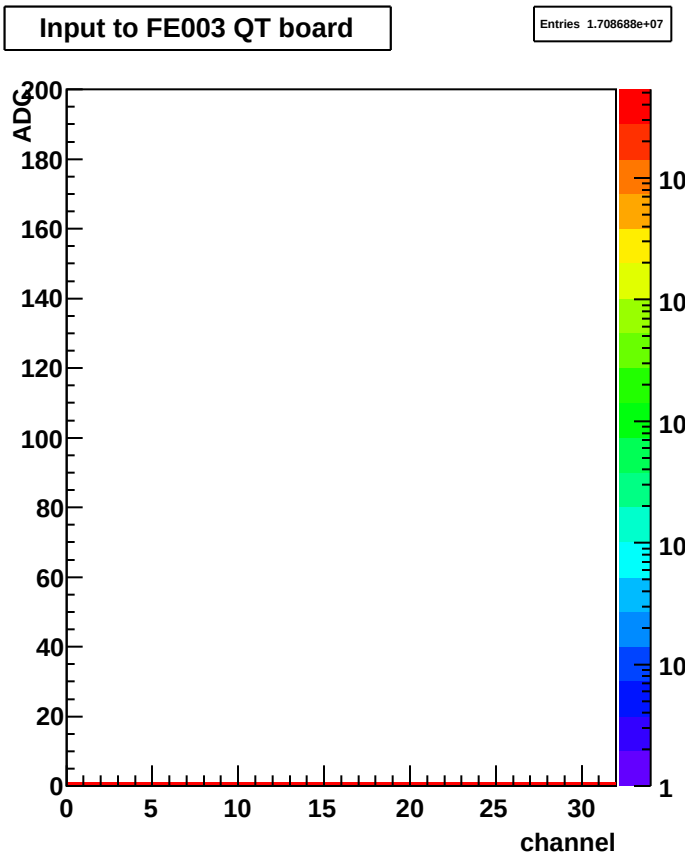

Input to FPD L3 DSM
Entries 8543440
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

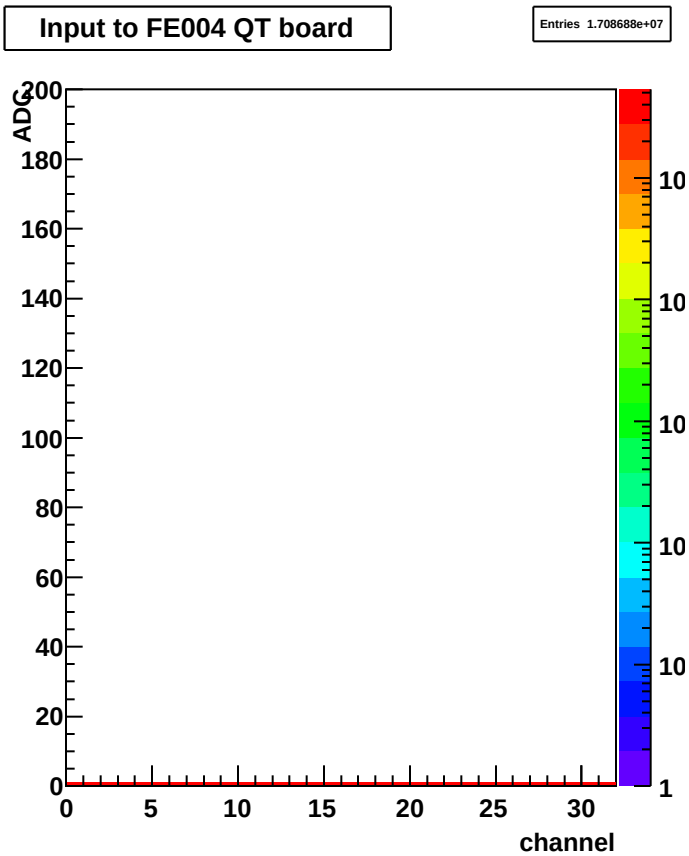

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

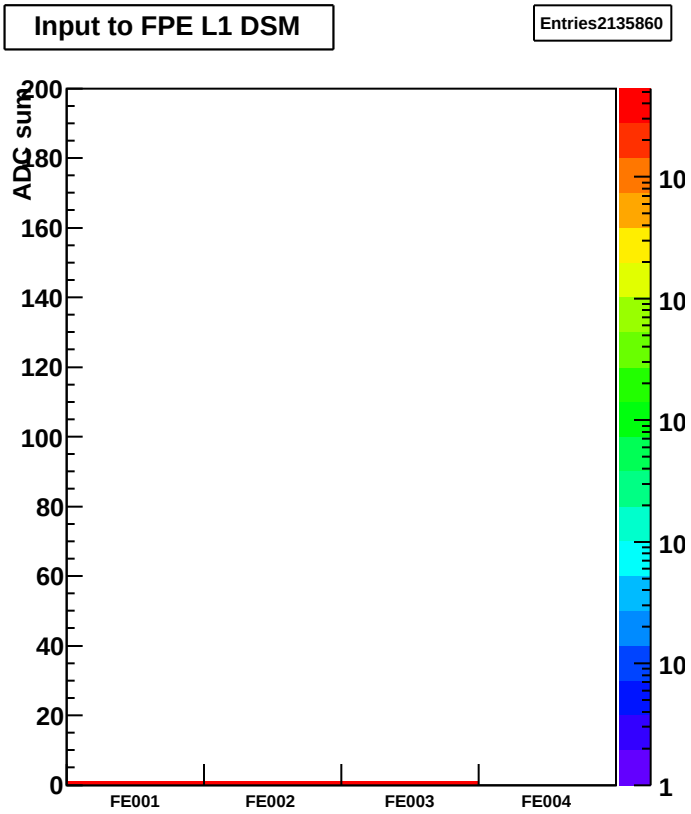

Input to FPD L2 DSM
Entries 2135860
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

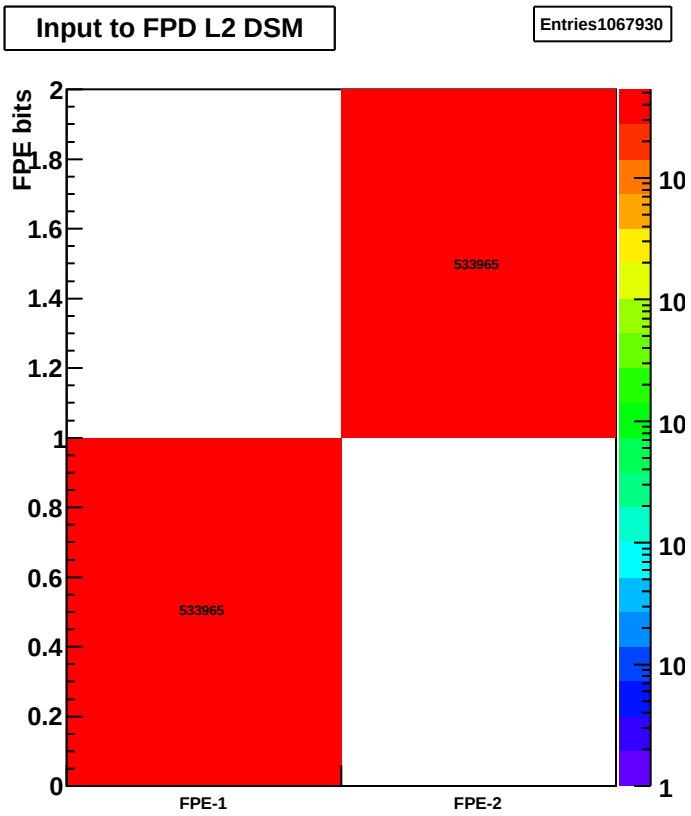

Input to FE001 QT board
Entries 1.708688e+07
ADC
channel

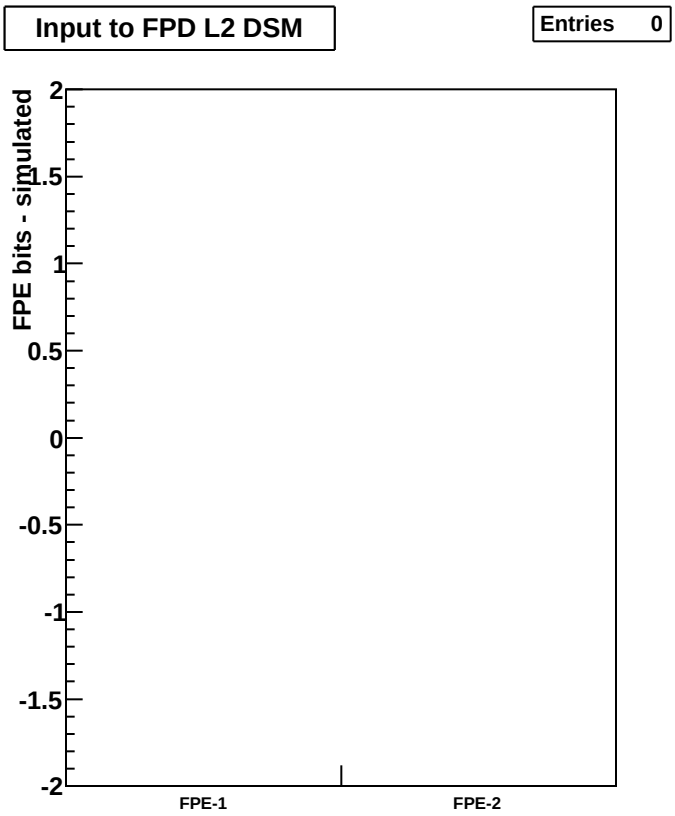

Input to FE002 QT board
Entries 1.708688e+07
ADC
channel


Input to FE003 QT board
Entries 1.708688e+07
ADC
channel


Input to FE004 QT board
Entries 1.708688e+07
ADC
channel


Input to FPE L1 DSM
Entries2135860
ADC sum
200
180
160
140
120
100
80
60
40
20
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries1067930
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
533965
533965
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2