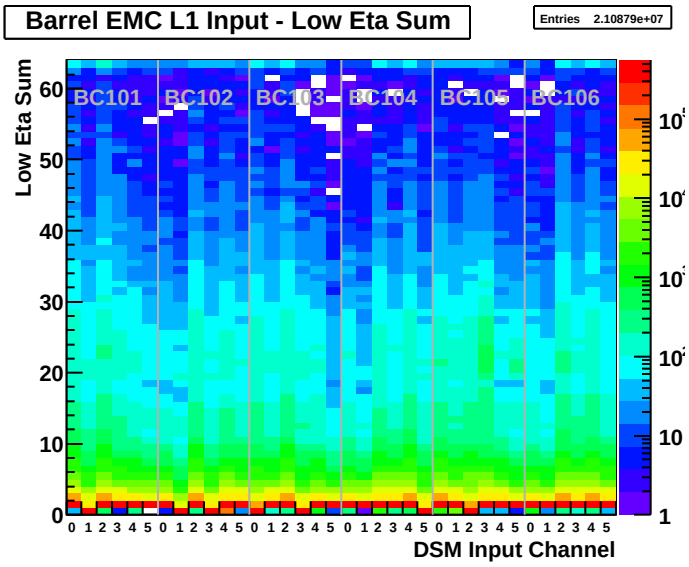
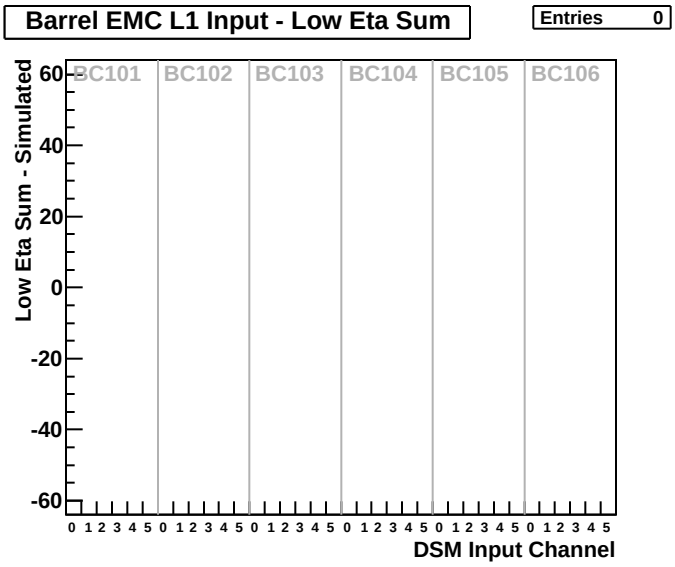
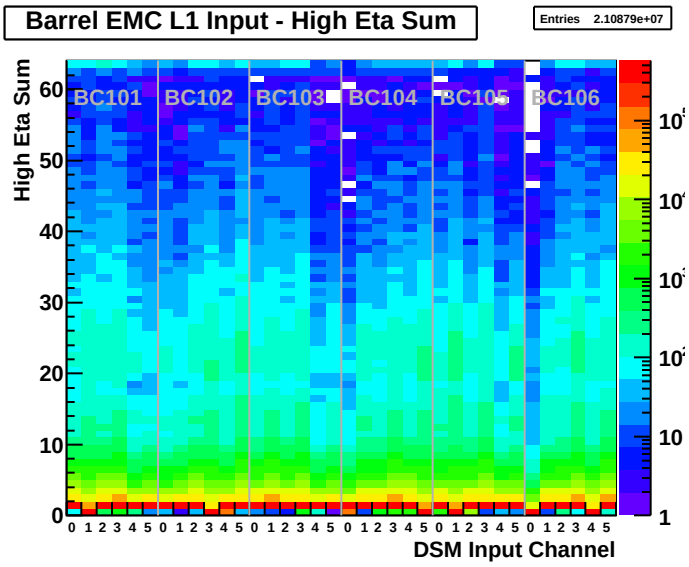
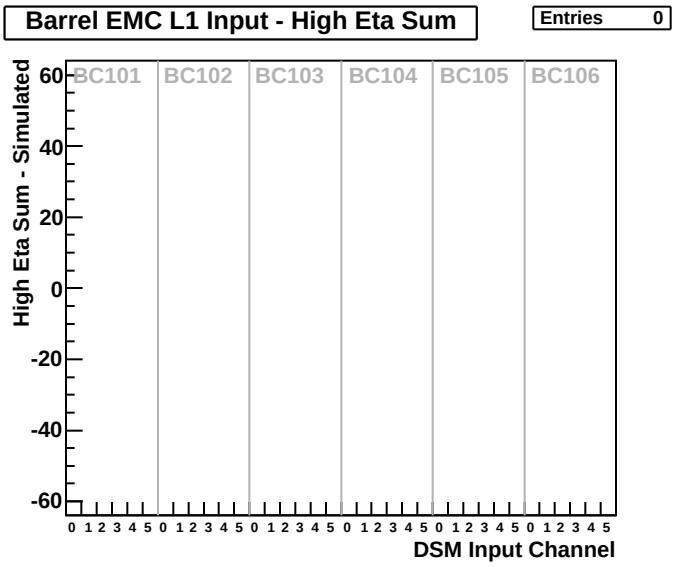
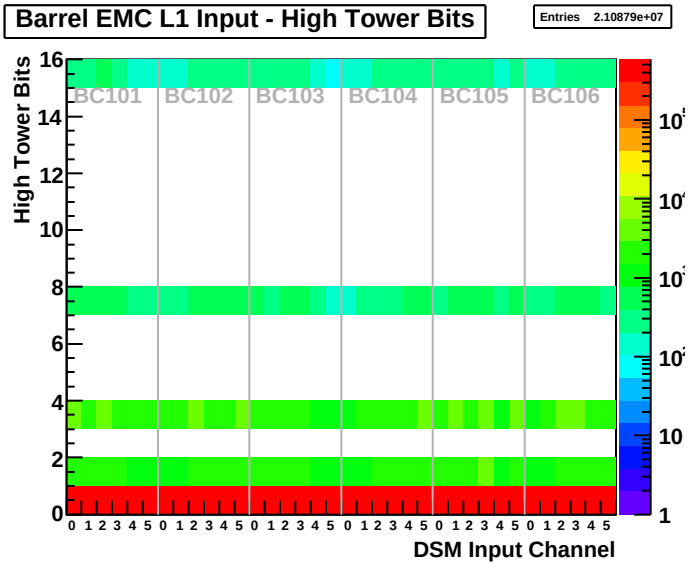
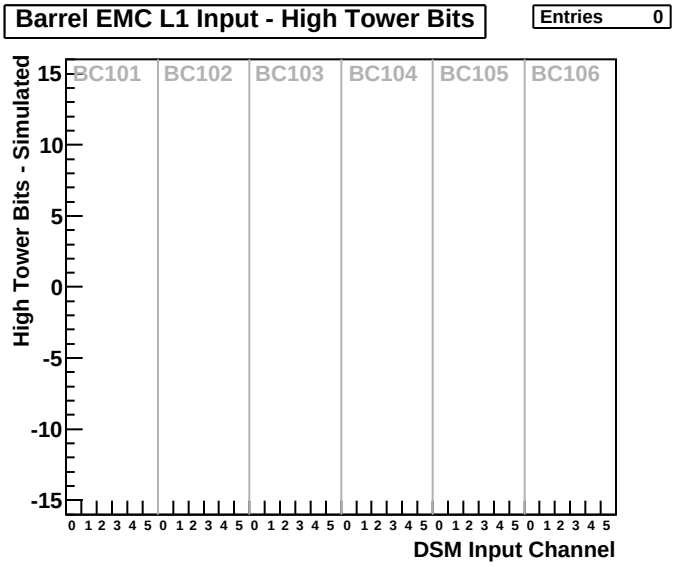

Barrel EMC L1 Input - Low Eta Sum
Entries 2.10879e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

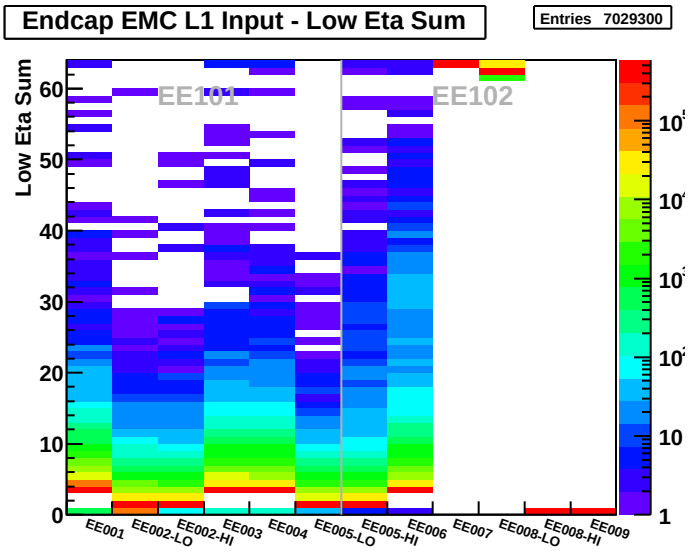

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

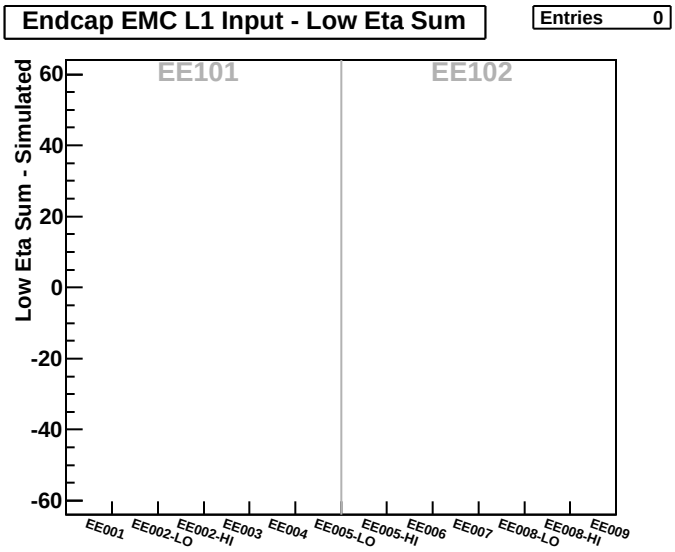

Barrel EMC L1 Input - High Eta Sum
Entries 2.10879e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

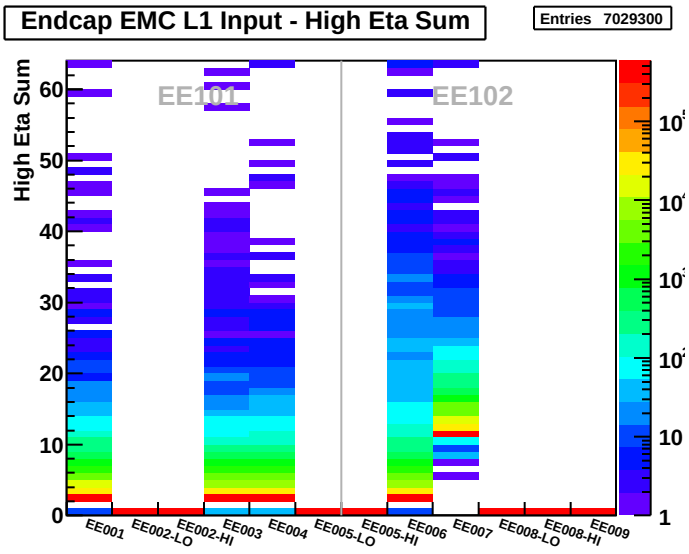

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

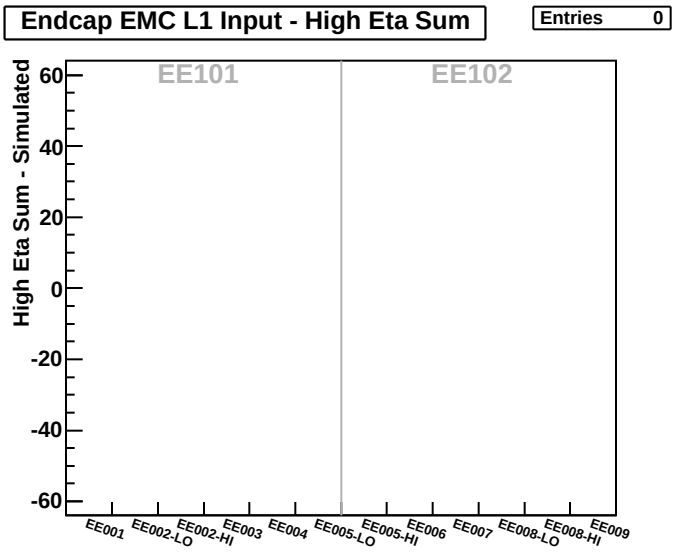

Barrel EMC L1 Input - High Tower Bits
Entries 2.10879e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

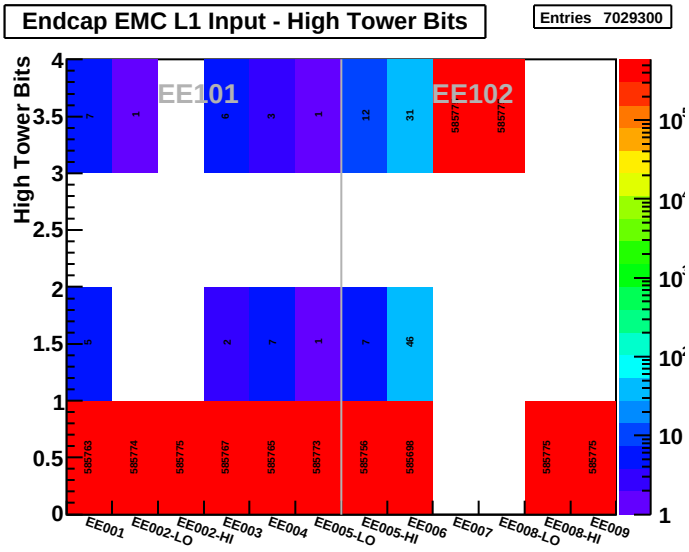

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

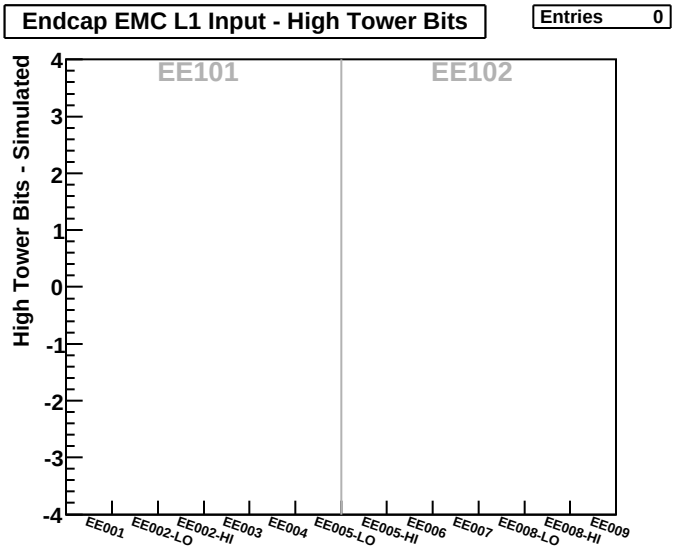

Endcap EMC L1 Input - Low Eta Sum
Entries 7029300
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

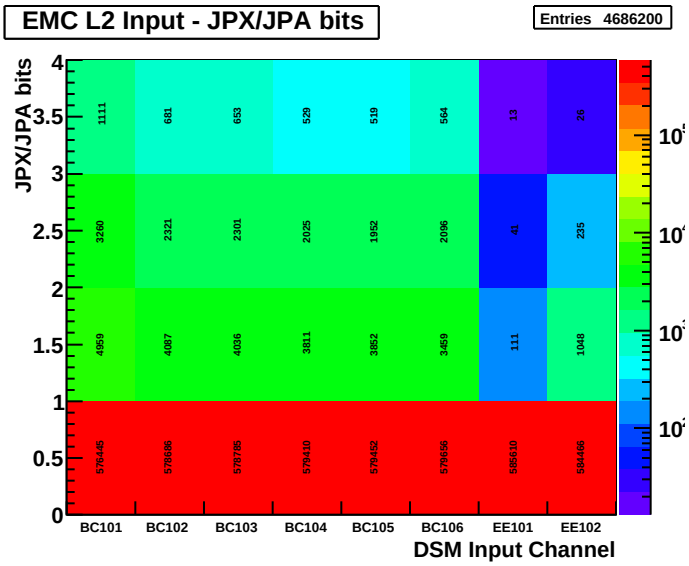

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

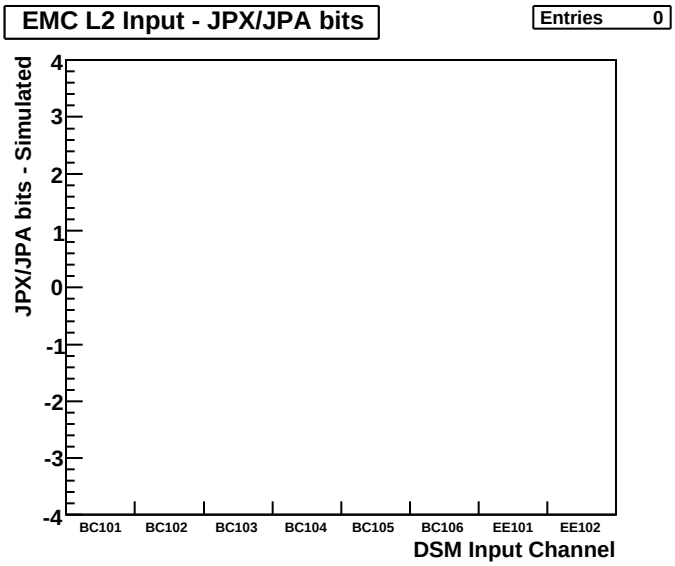

Endcap EMC L1 Input - High Eta Sum
Entries 7029300
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

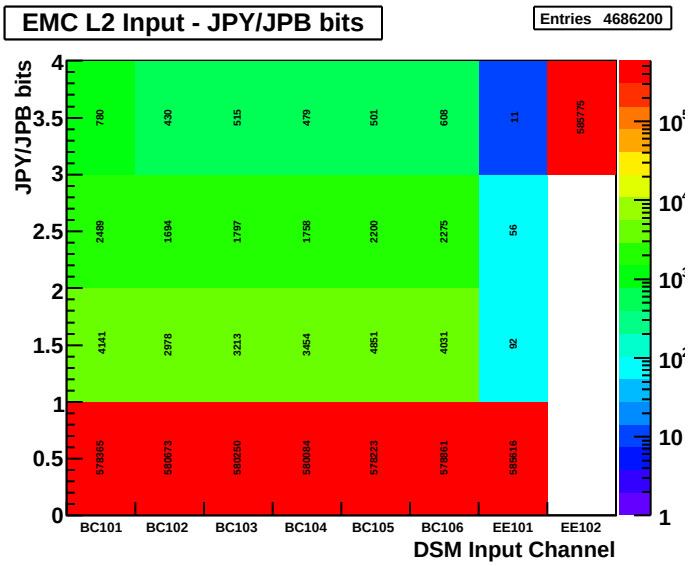

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

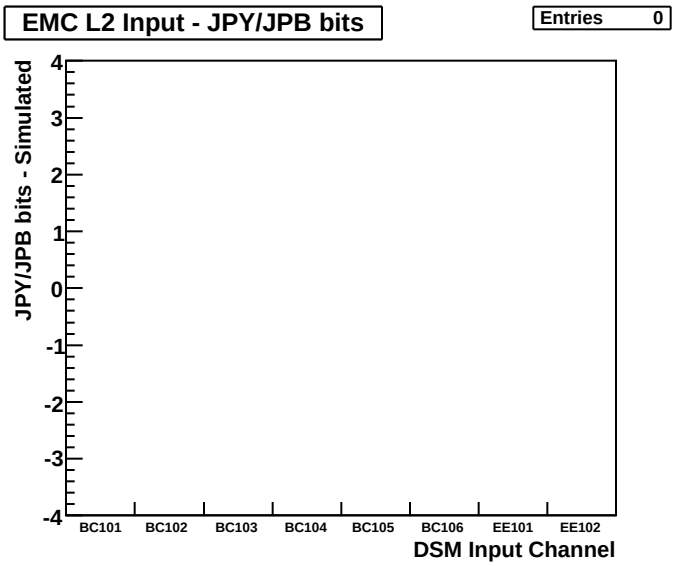

Endcap EMC L1 Input - High Tower Bits
Entries 7029300
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

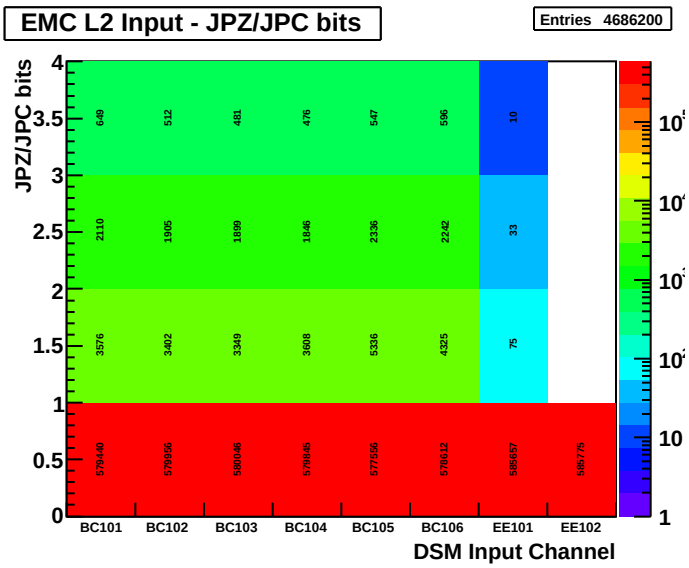

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

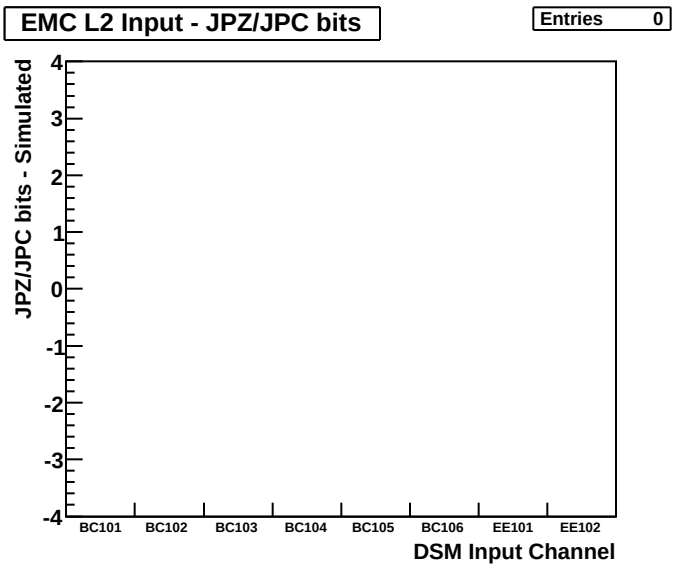

EMC L2 Input - JPX/JPA bits
Entries 4686200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1111 681 653 529 519 564 13 26
3260 2321 2301 2025 1952 2096 41 235
4969 4087 4036 3811 3852 3459 111 1048
576445 578686 578785 579410 579452 579656 585610 584466

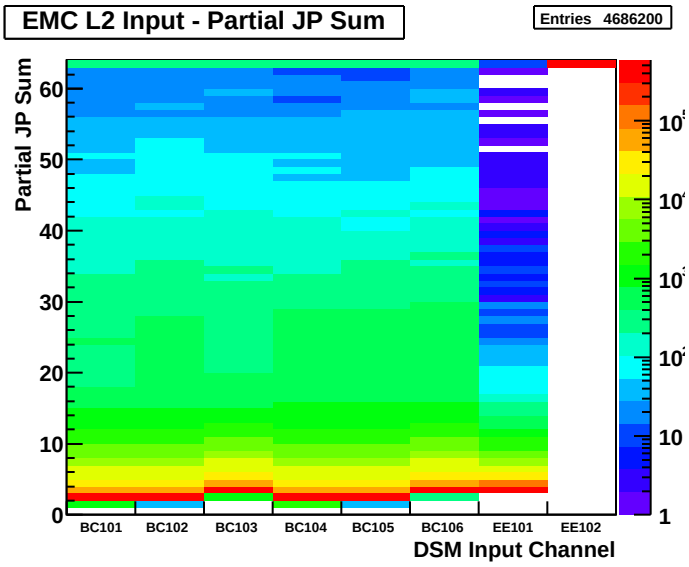

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

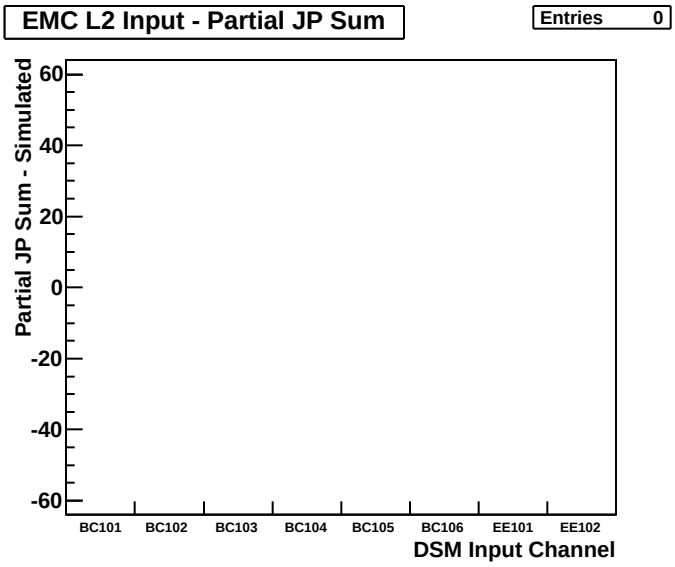

EMC L2 Input - JPY/JPB bits
Entries 4686200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
780 430 515 479 501 608 11 585775
2489 1694 1797 1758 2200 2275 56
4141 2978 3213 3454 4851 4031 92
578365 580673 580250 580084 578223 578881 585616

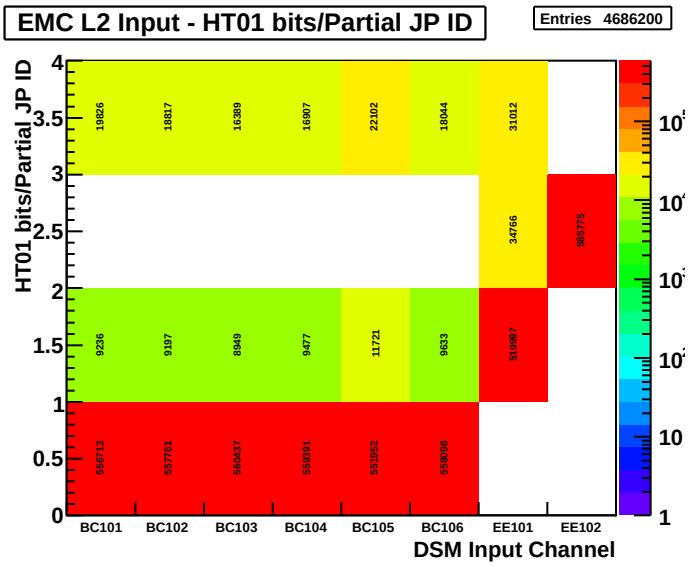

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

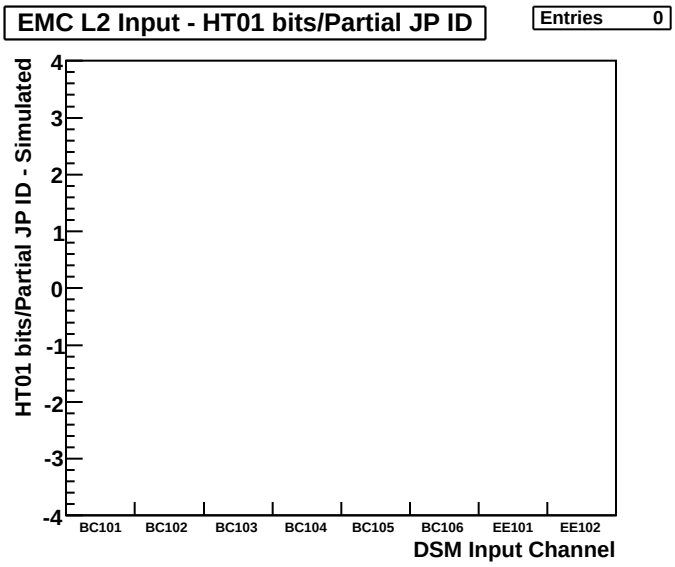

EMC L2 Input - JPZ/JPC bits
Entries 4686200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
649 512 481 476 547 596 10
2110 1905 1899 1846 2336 2242 33
3576 3402 3349 3608 5336 4325 75
579440 579356 580046 579845 577556 578612 585657 585775

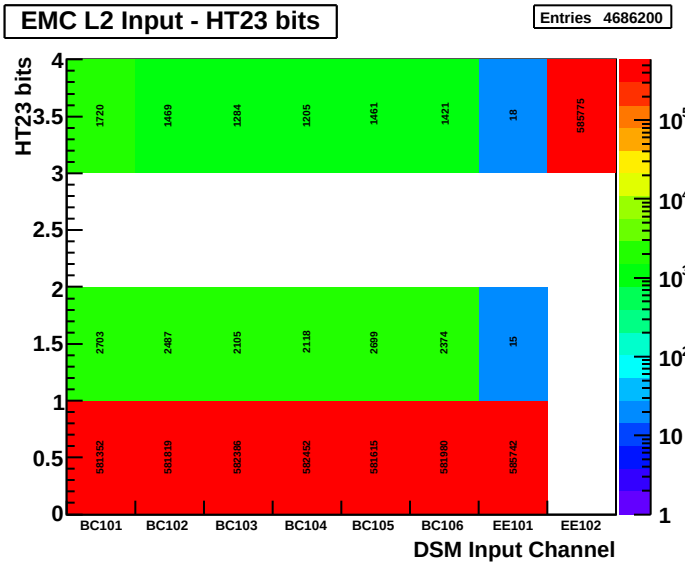

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

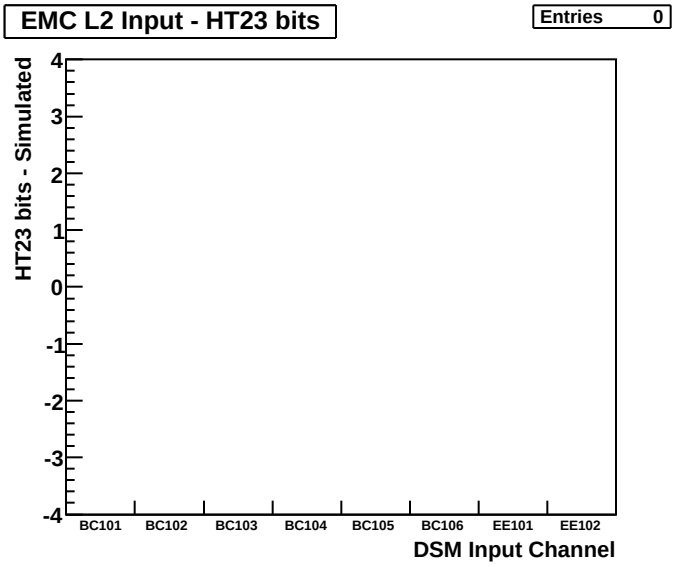

EMC L2 Input - Partial JP Sum
Entries 4686200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

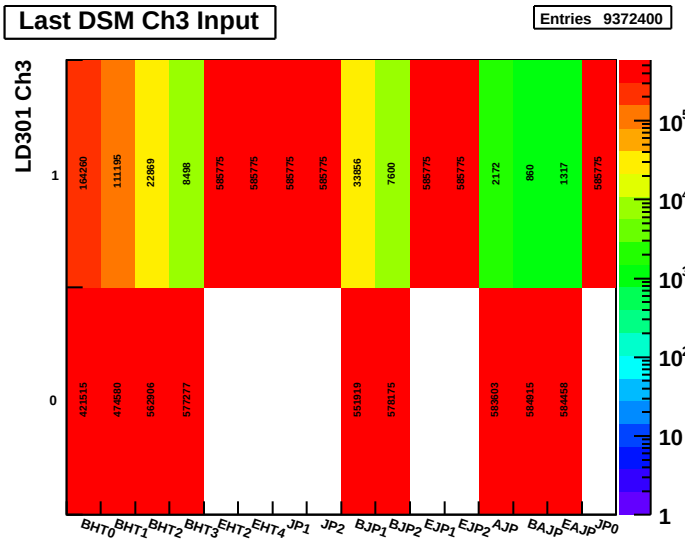

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

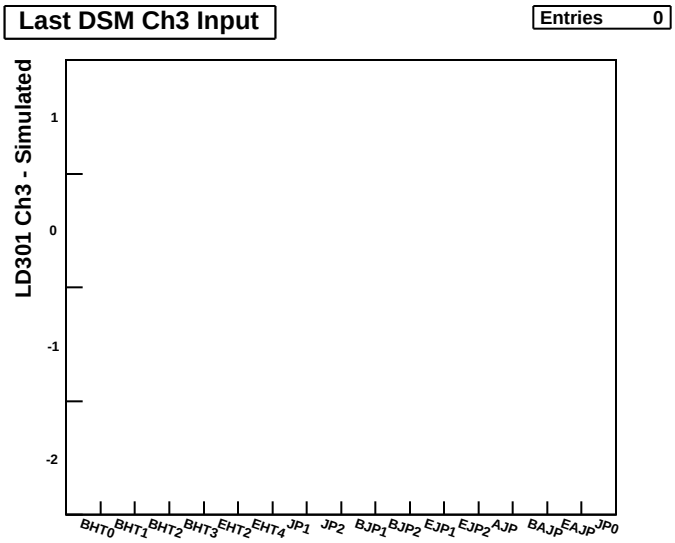

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4686200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

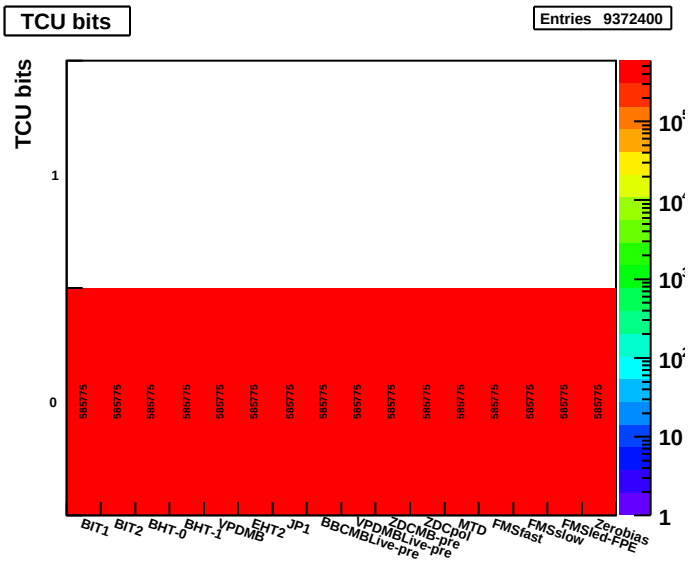

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

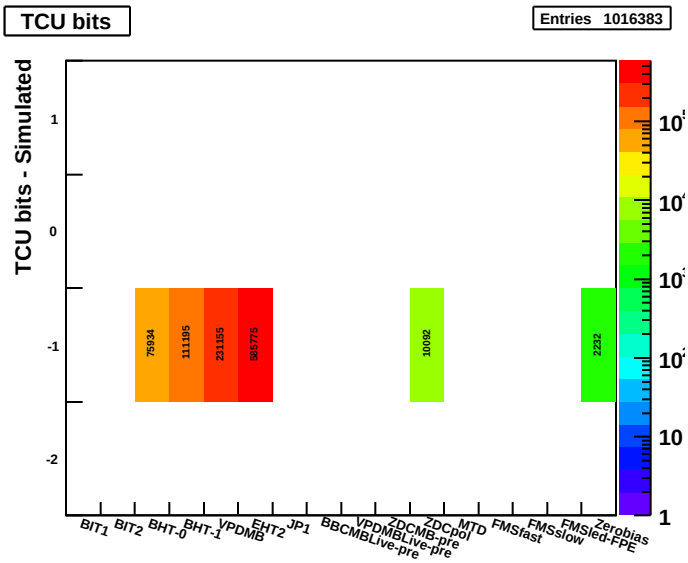

EMC L2 Input - HT23 bits
Entries 4686200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

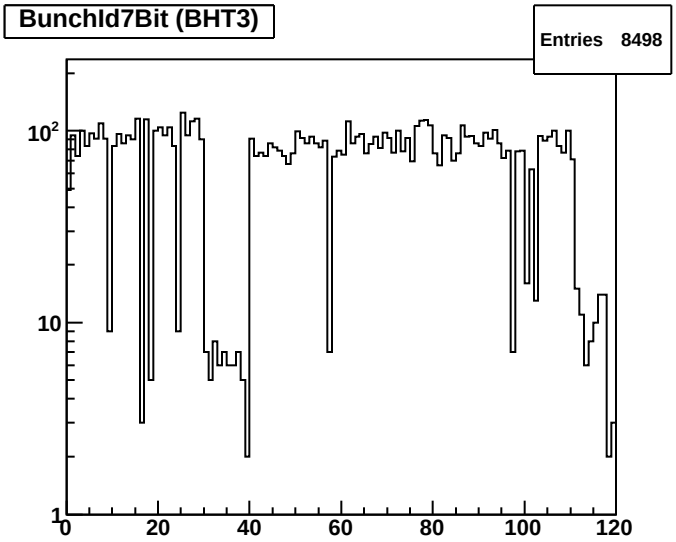

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

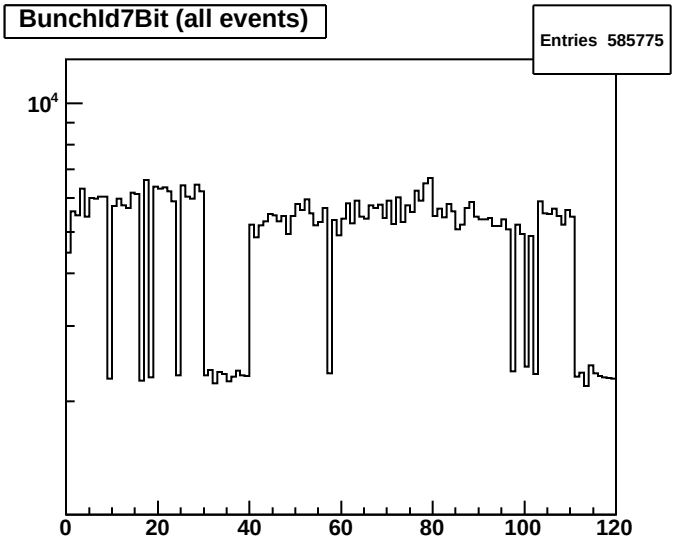

Last DSM Ch3 Input
Entries 9372400
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

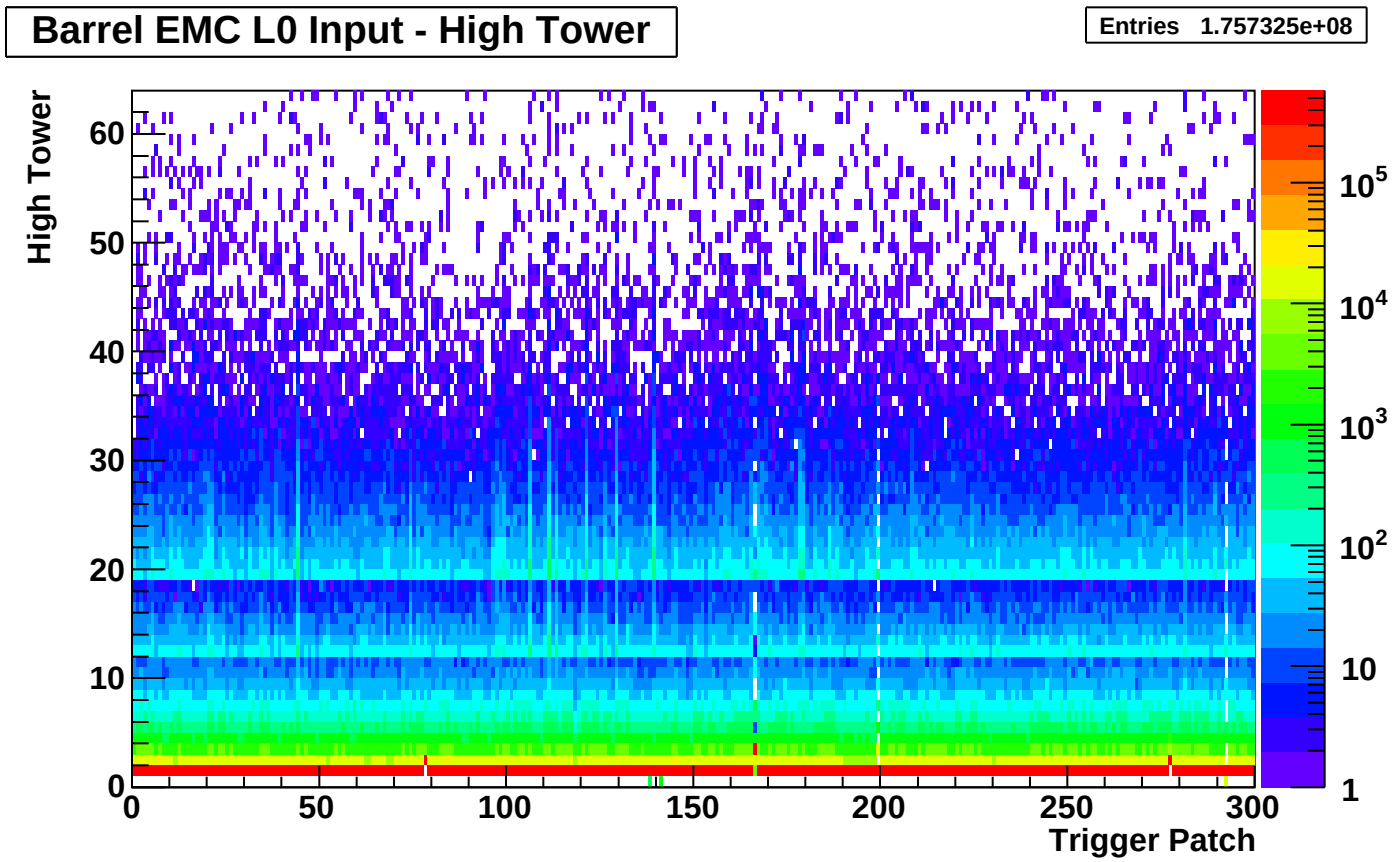

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

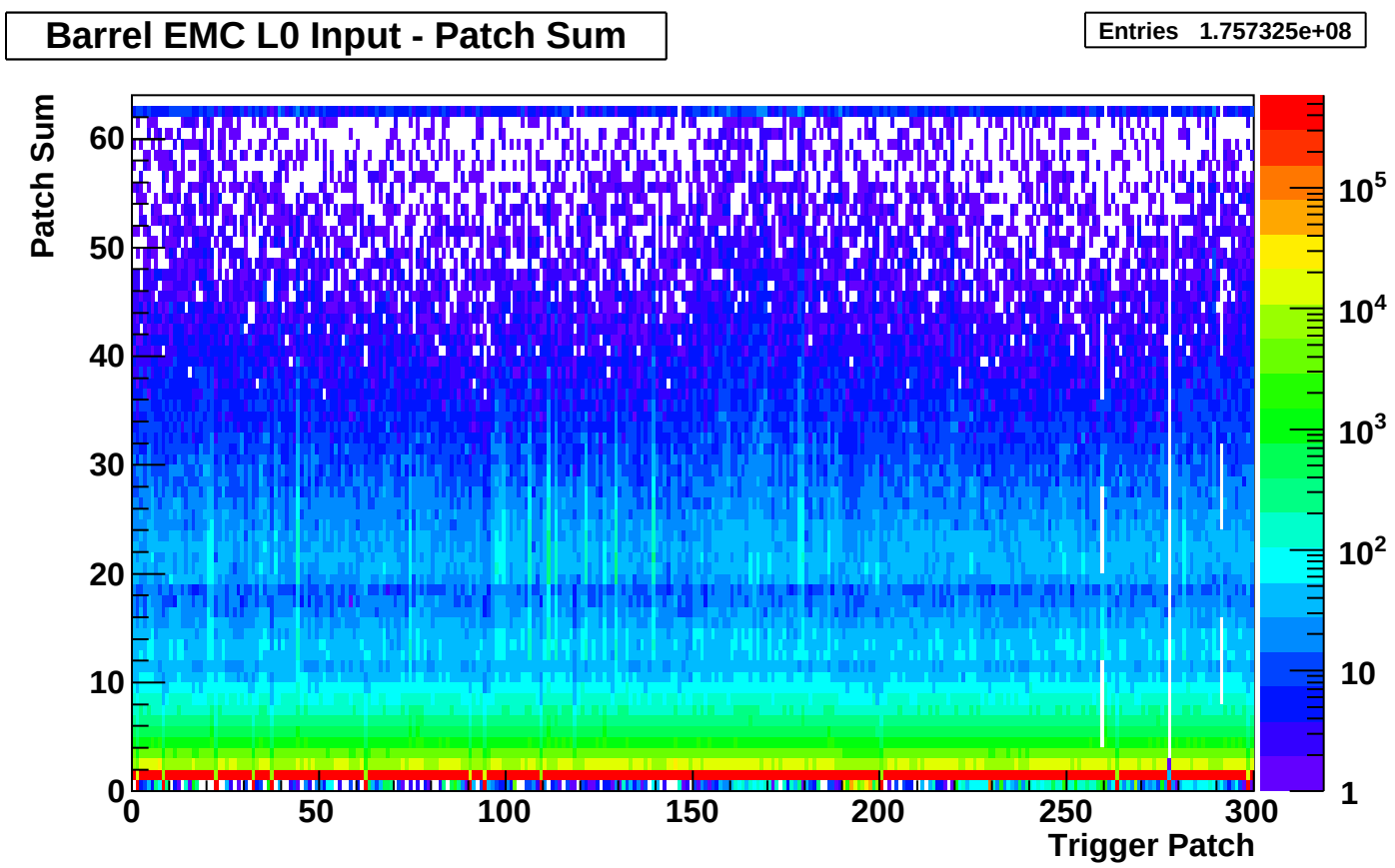

TCU bits
Entries 9372400
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

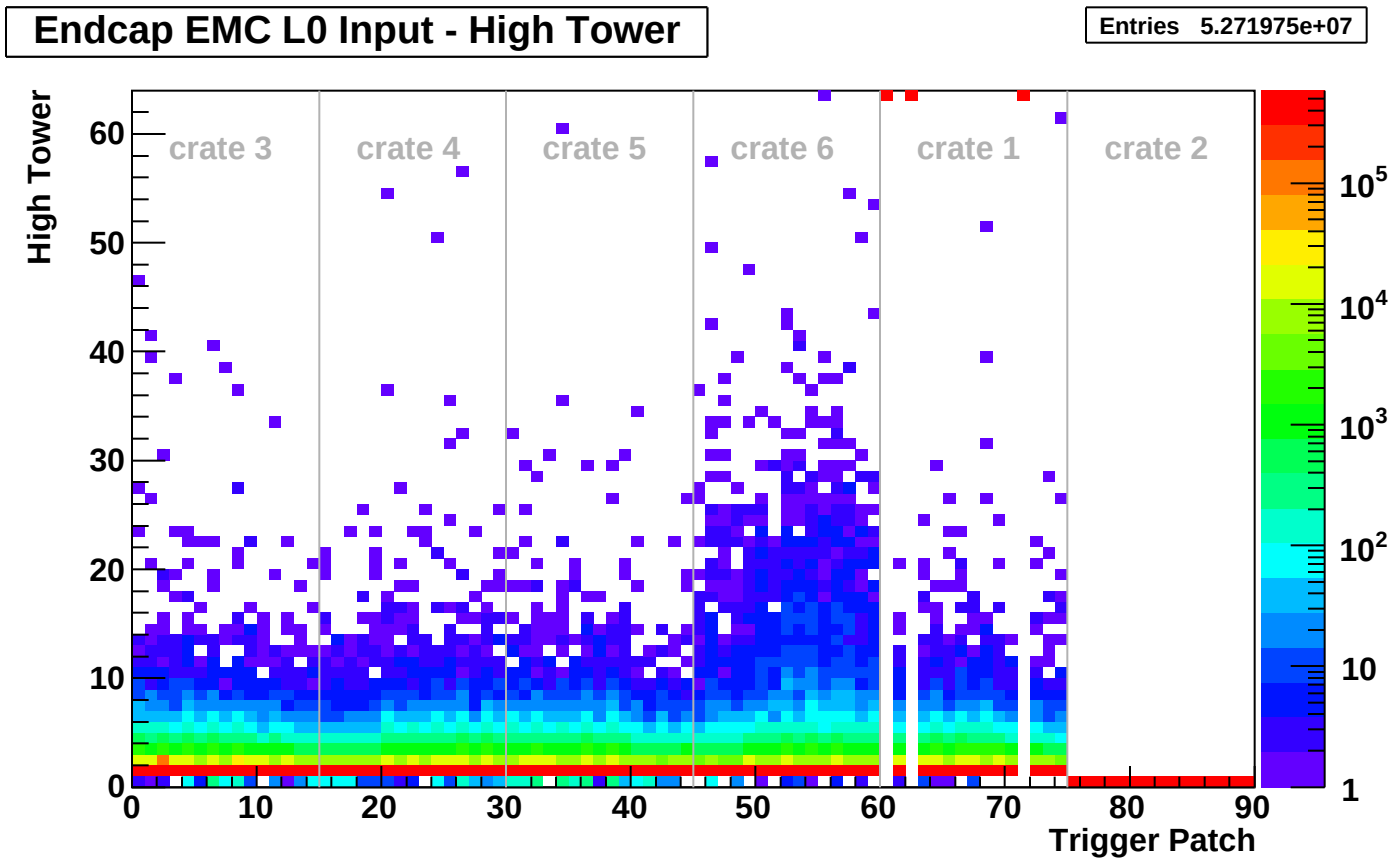

TCU bits
Entries 1016383
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

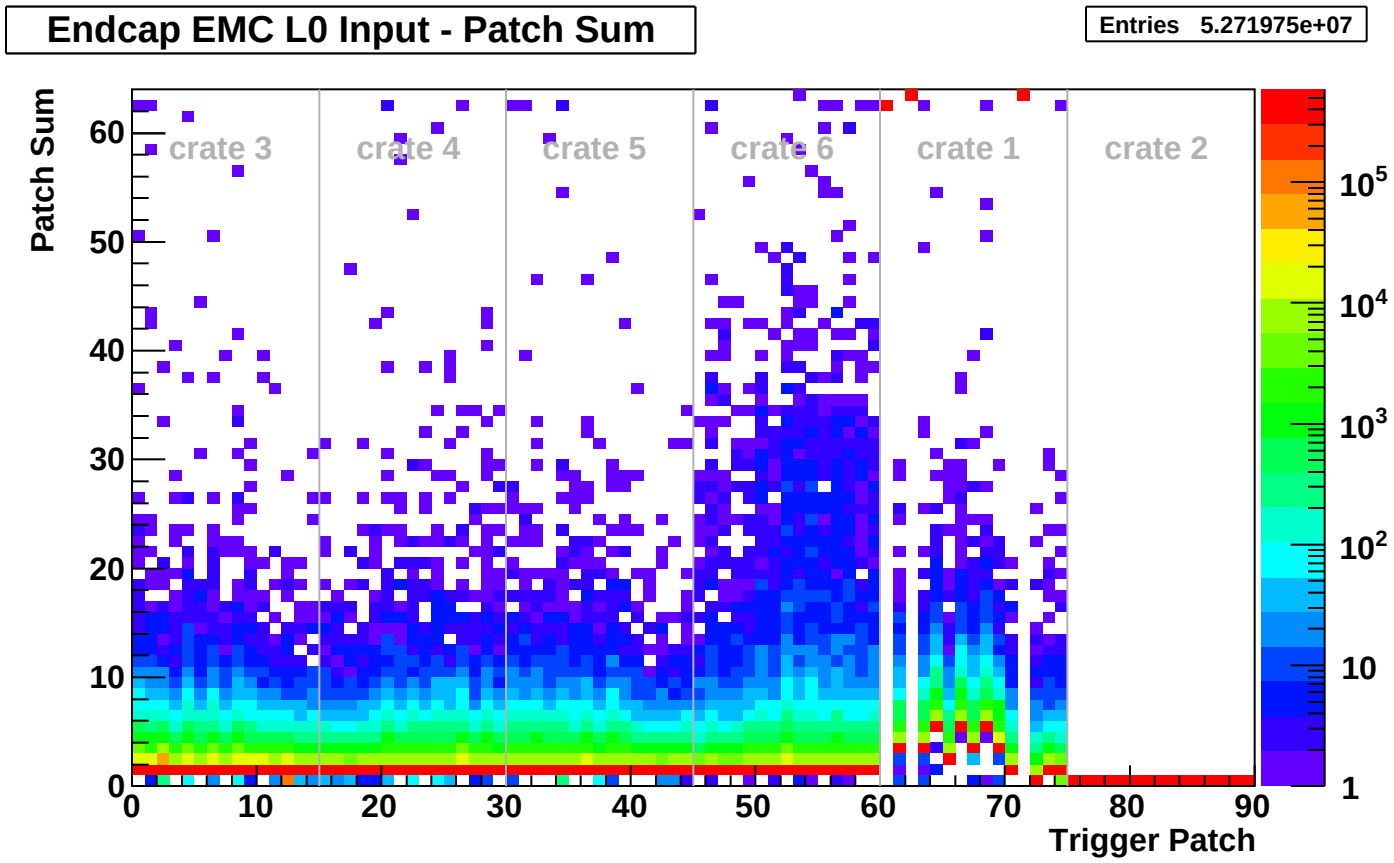

BunchId7Bit (BHT3)
Entries 8498

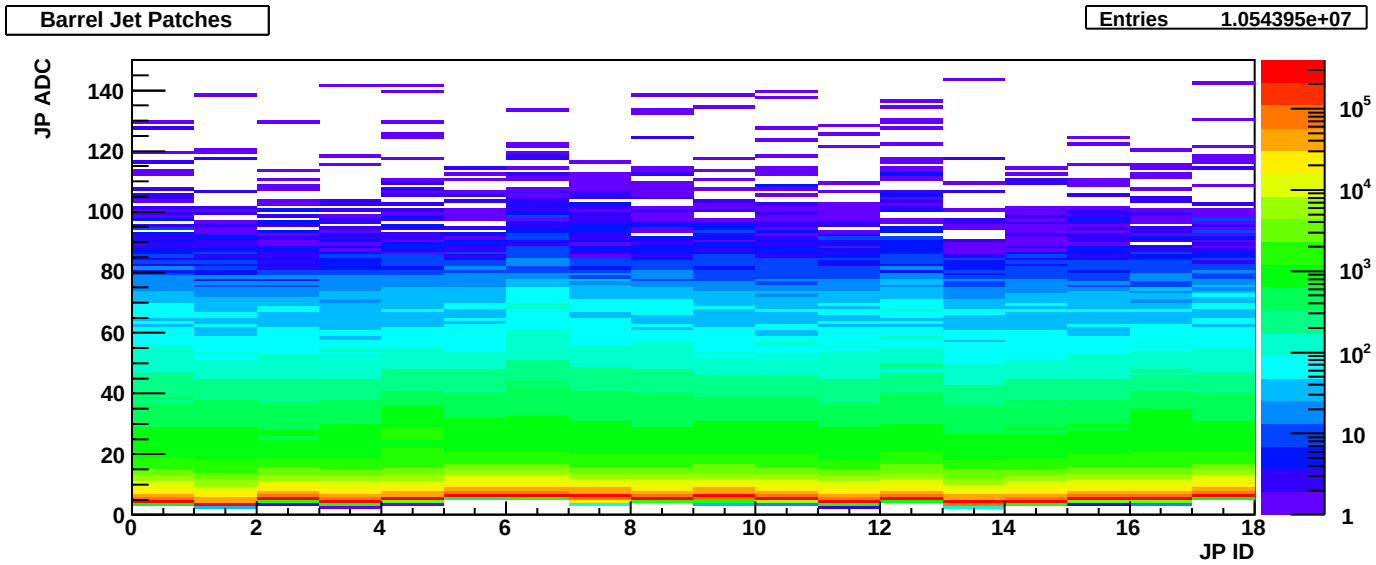

BunchId7Bit (all events)
Entries 585775

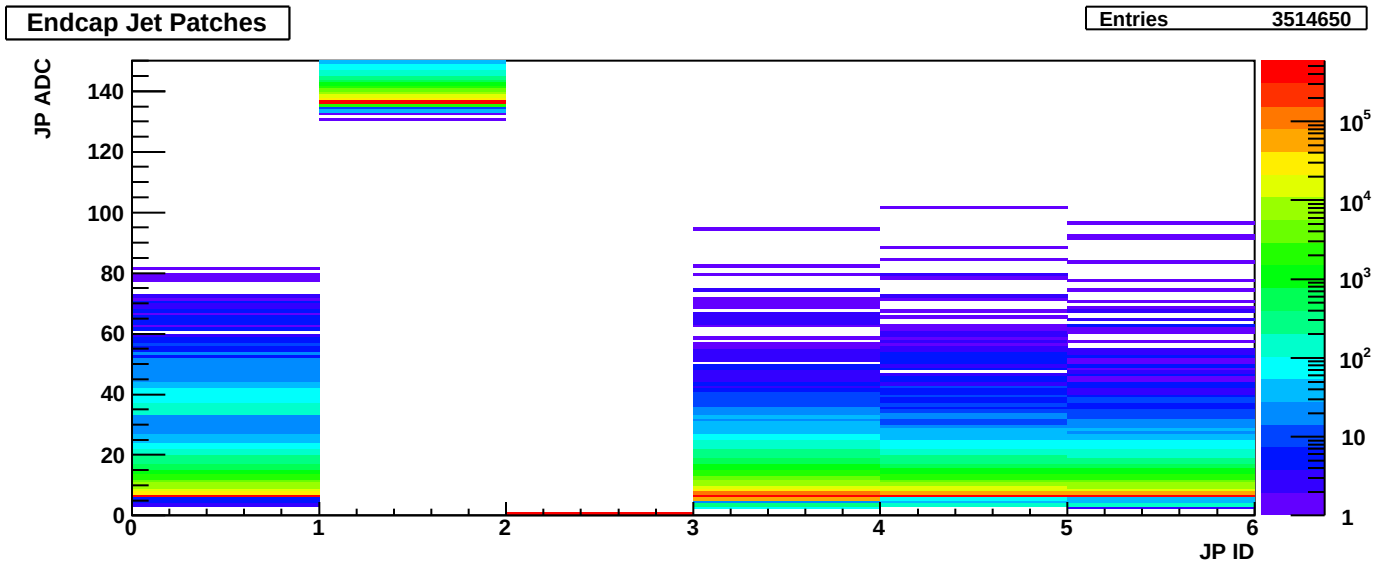

Barrel EMC L0 Input - High Tower
Entries 1.757325e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

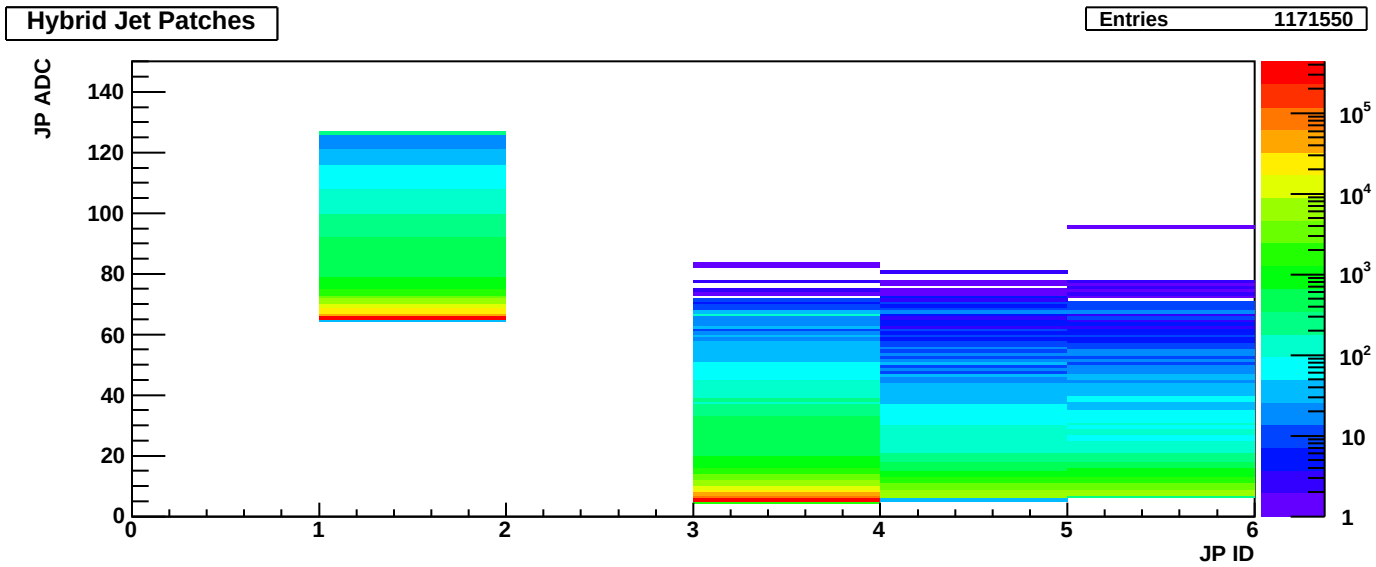

Barrel EMC L0 Input - Patch Sum
Entries 1.757325e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

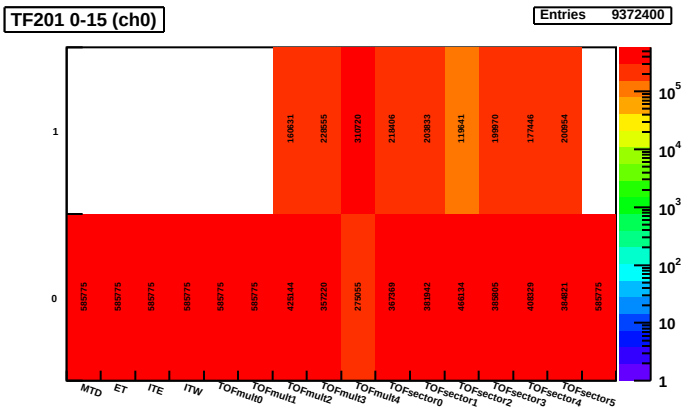

Endcap EMC L0 Input - High Tower
Entries 5.271975e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

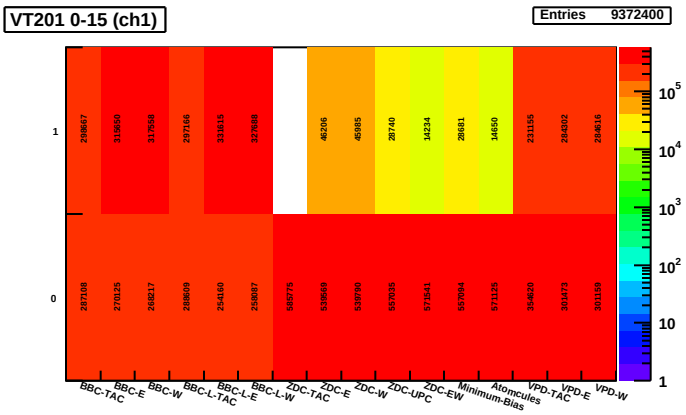

Endcap EMC L0 Input - Patch Sum
Entries 5.271975e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

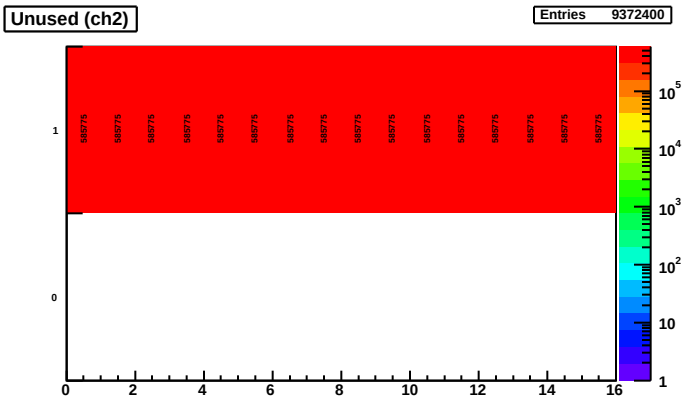

Barrel Jet Patches
Entries 1.054395e+07
JP ADC
JP ID

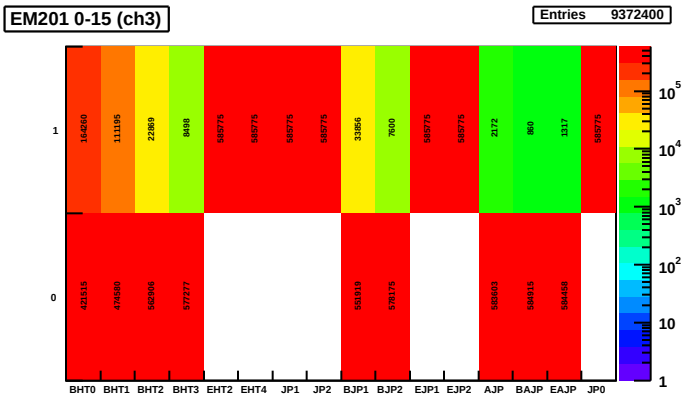

Endcap Jet Patches
Entries 3514650
JP ADC
JP ID

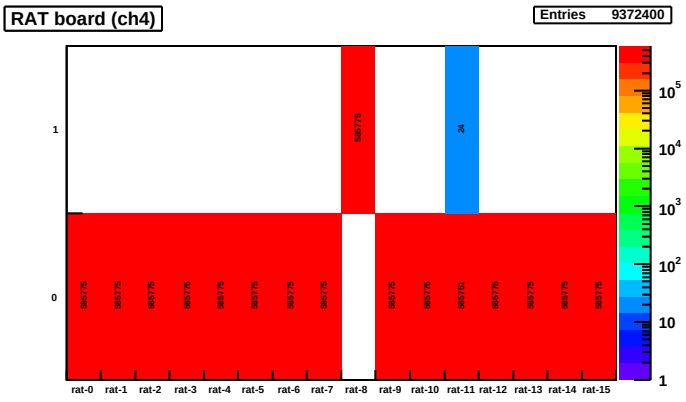

Hybrid Jet Patches
Entries 1171550
JP ADC
JP ID

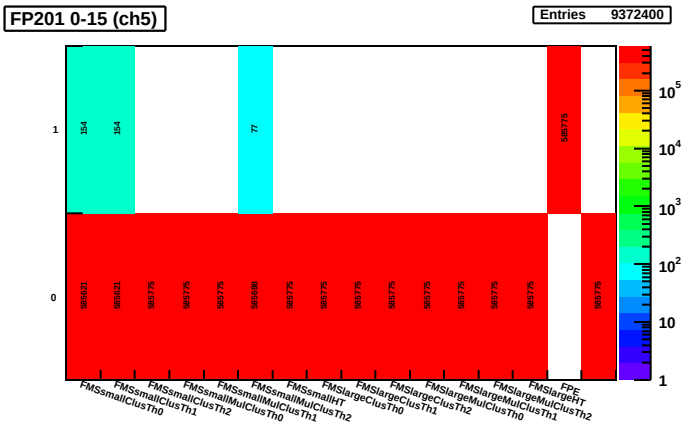

TF201 0-15 (ch0)
Entries 9372400
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

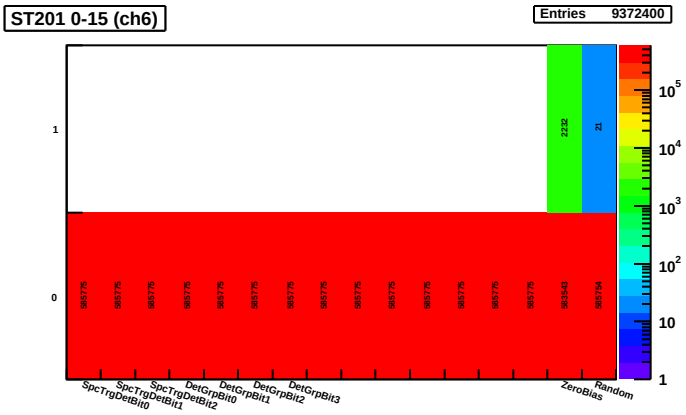

VT201 0-15 (ch1)
Entries 9372400
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

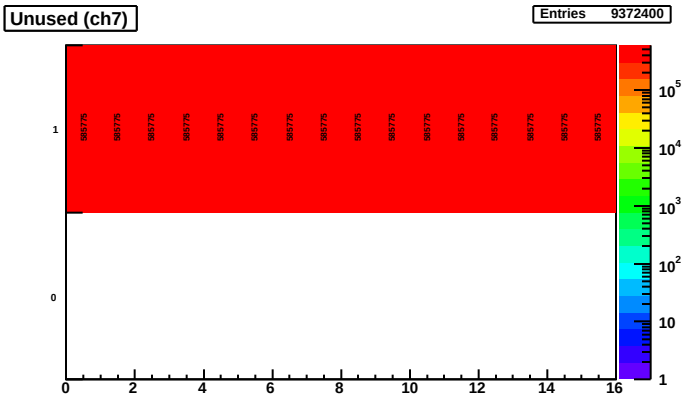

Unused (ch2)
Entries 9372400

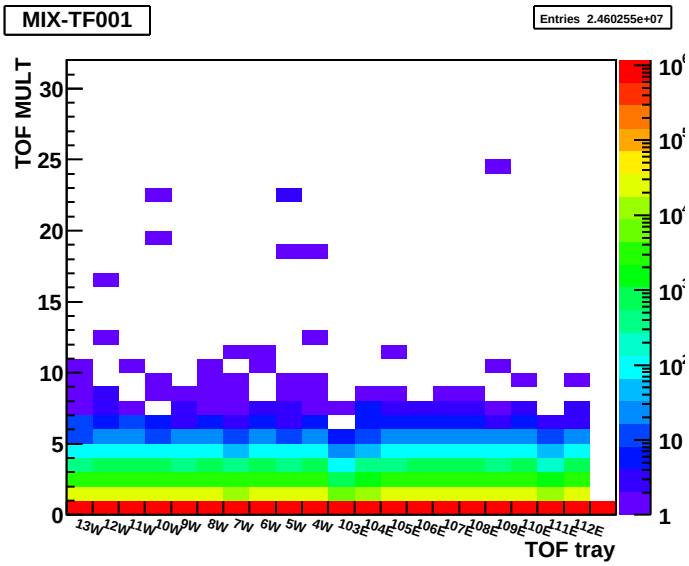

EM201 0-15 (ch3)
Entries 9372400
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

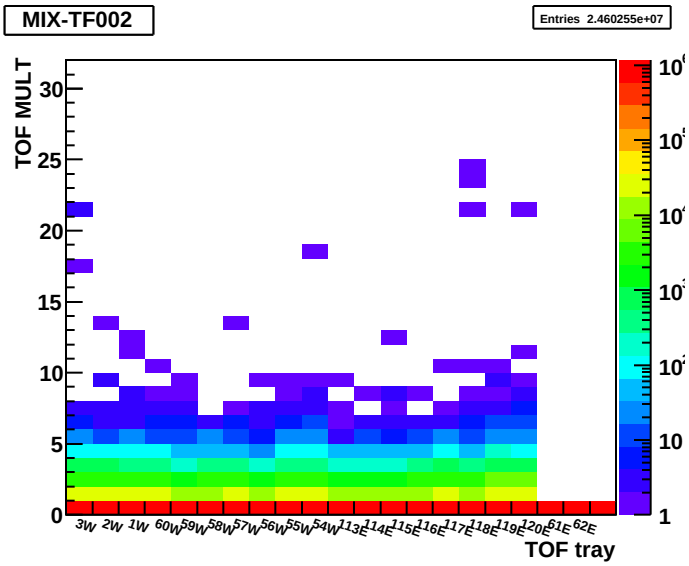

RAT board (ch4)
Entries 9372400
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

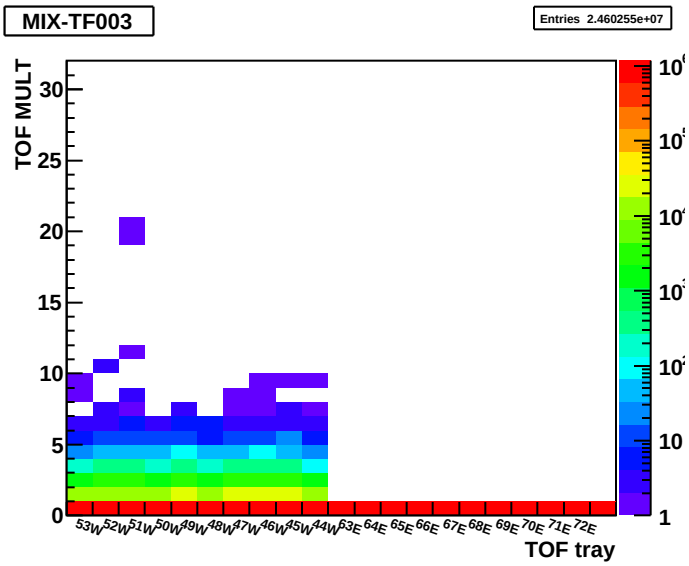

FP201 0-15 (ch5)
Entries 9372400
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

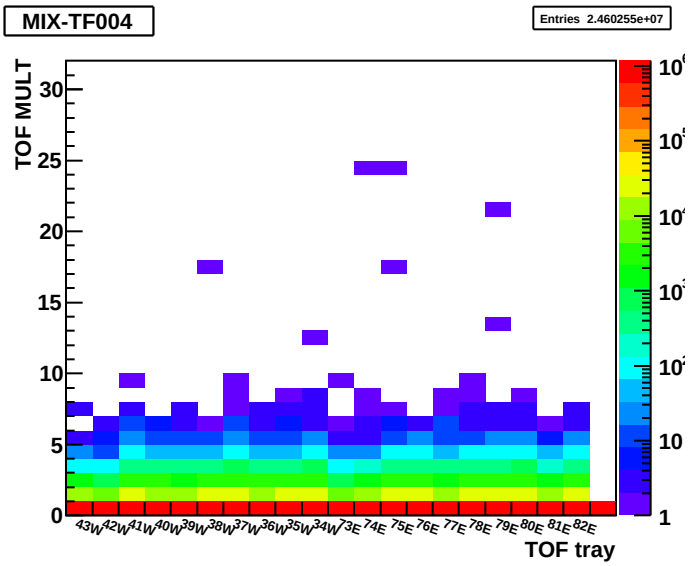

ST201 0-15 (ch6)
Entries 9372400
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

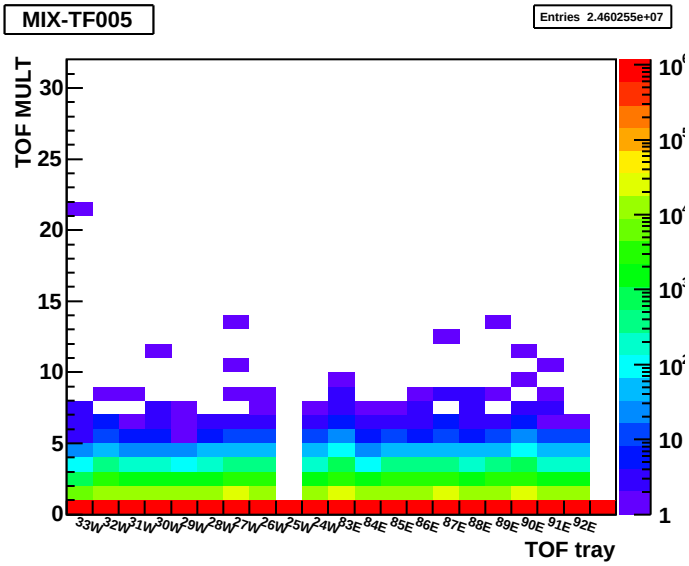

Unused (ch7)
Entries 9372400

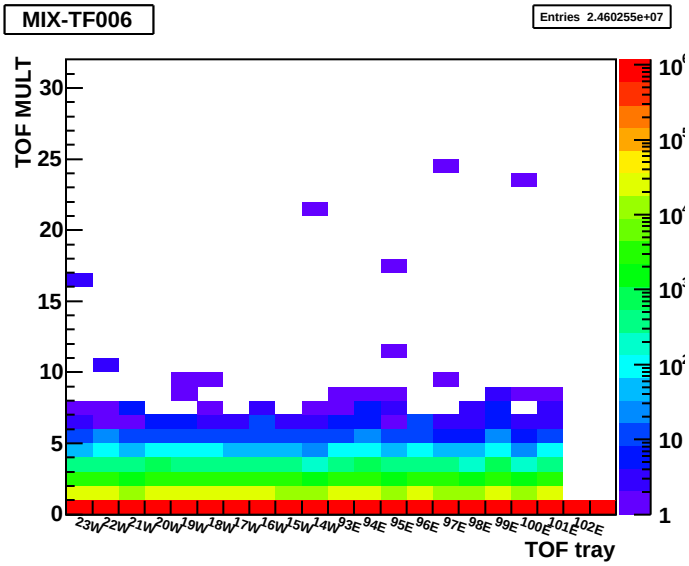

MIX-TF001
Entries 2.460255e+07
TOF MULT
TOF tray

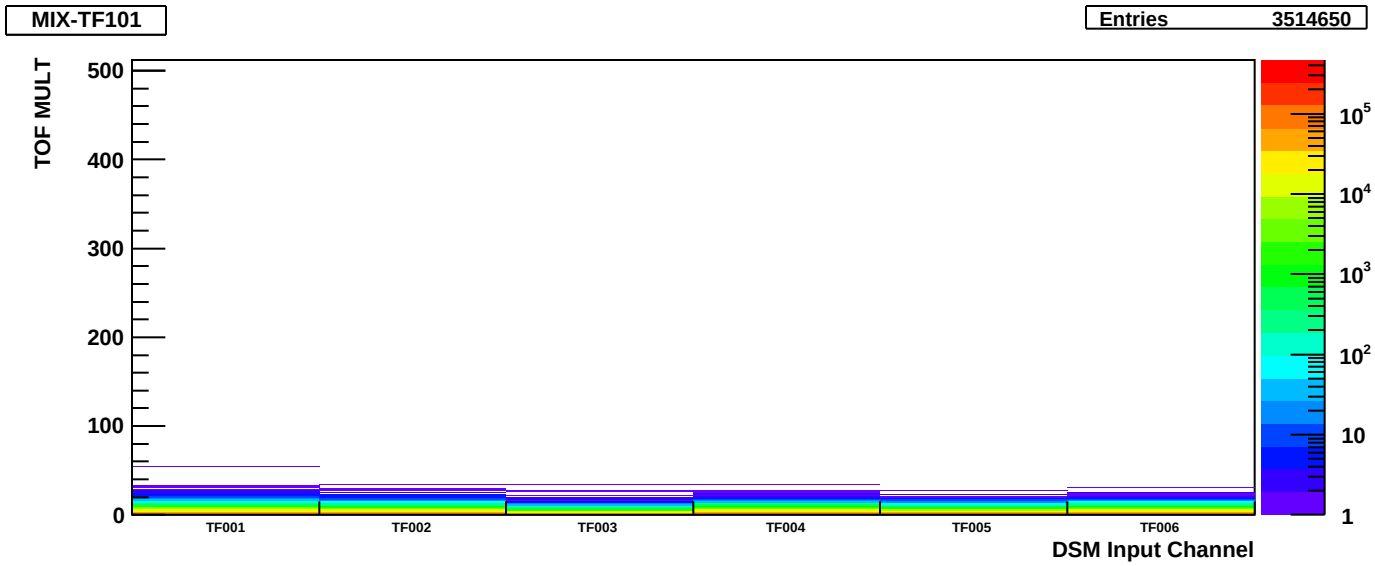

MIX-TF002
Entries 2.460255e+07
TOF MULT
TOF tray

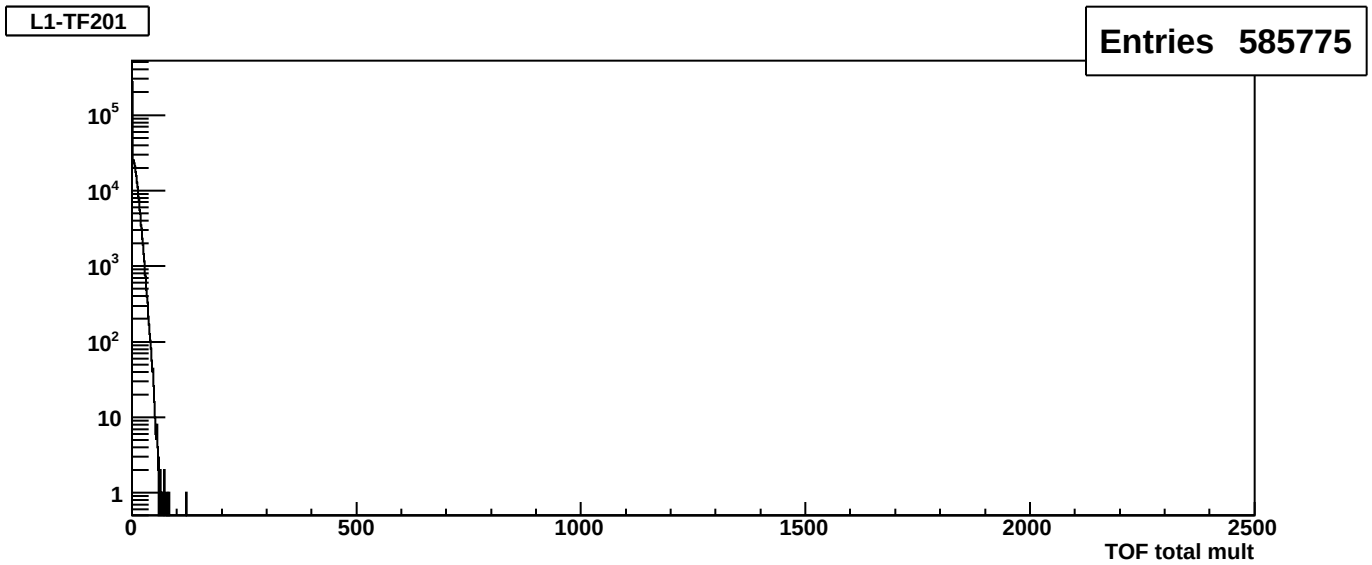

MIX-TF003
Entries 2.460255e+07
TOF MULT
TOF tray

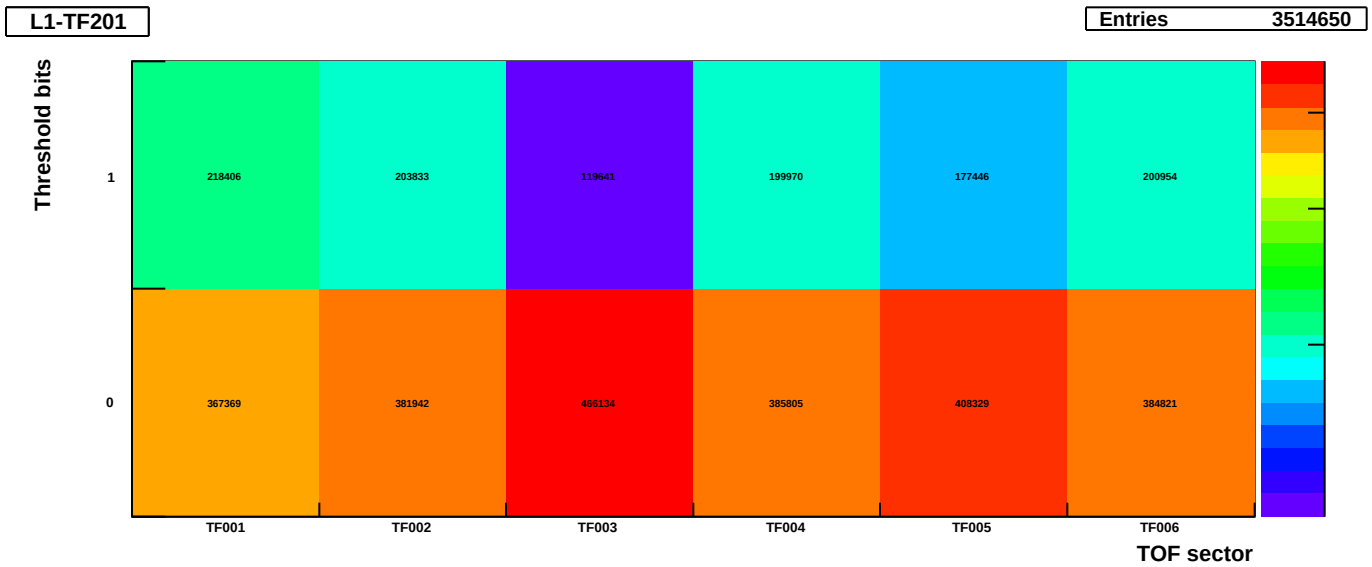

MIX-TF004
Entries 2.460255e+07
TOF MULT
TOF tray

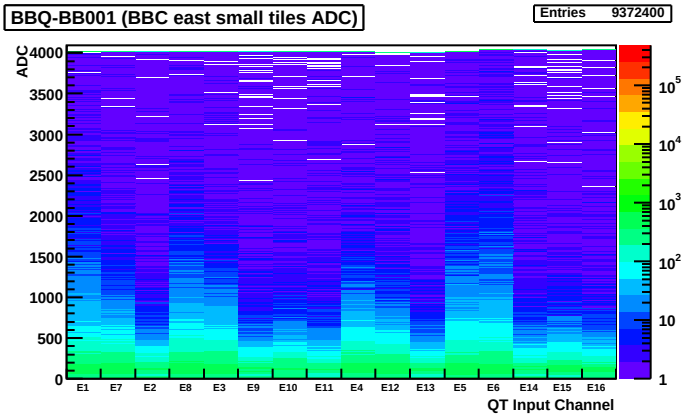

MIX-TF005
Entries 2.460255e+07
TOF MULT
TOF tray

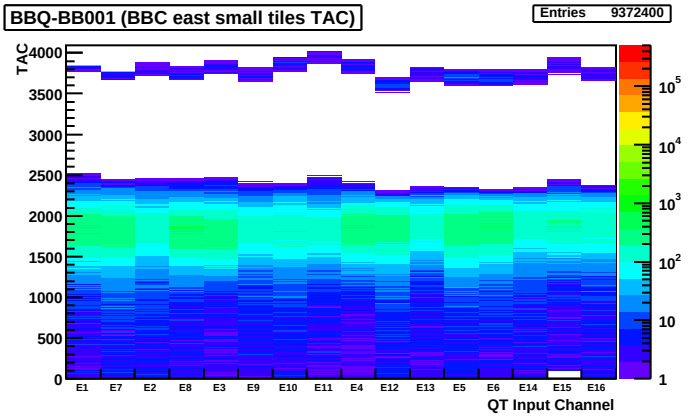

MIX-TF006
Entries 2.460255e+07
TOF MULT
TOF tray

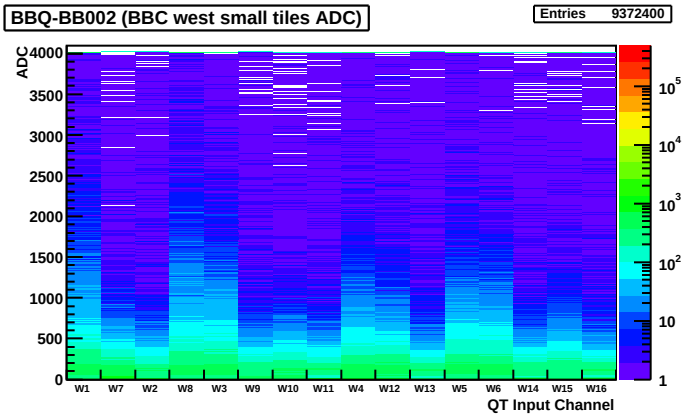

MIX-TF101
Entries 3514650
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

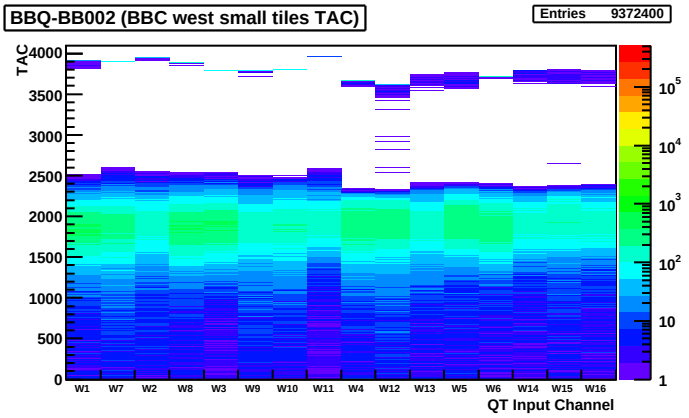

L1-TF201
Entries 585775
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

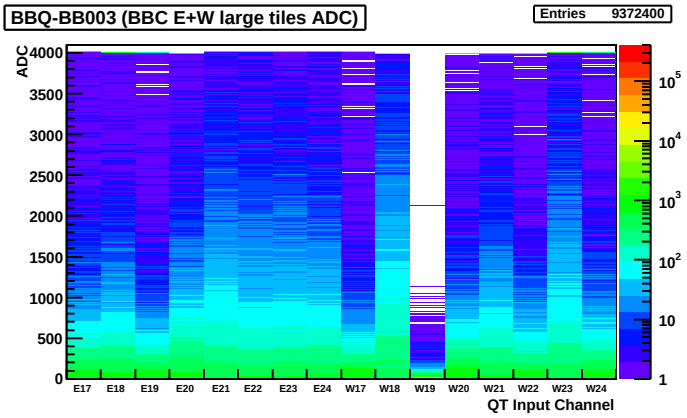

L1-TF201
Entries 3514650
Threshold bits
1
0
218406
203833
119641
199970
177446
200954
367369
381942
466134
385805
408329
384821
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

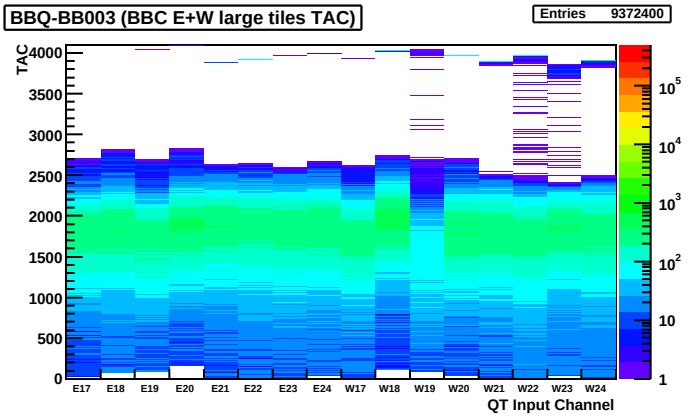

BBQ-BB001 (BBC east small tiles ADC)
Entries 9372400
ADC
QT Input Channel

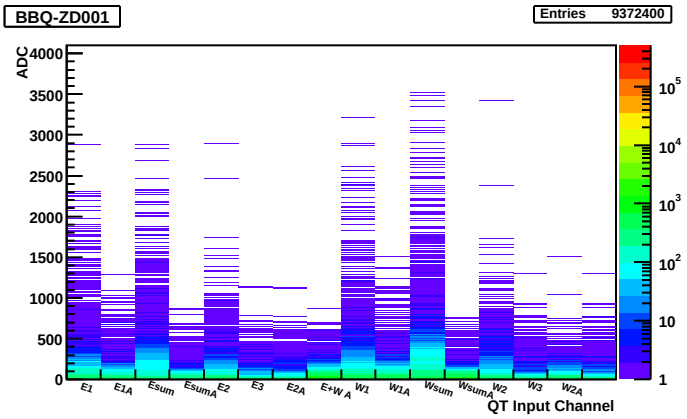

BBQ-BB001 (BBC east small tiles TAC)
Entries 9372400
TAC
QT Input Channel

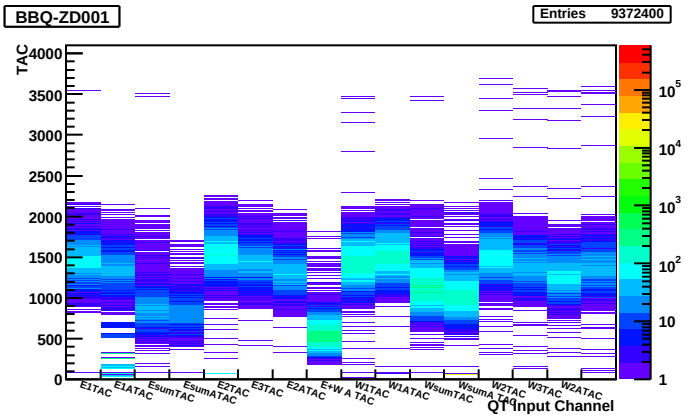

BBQ-BB002 (BBC west small tiles ADC)
Entries 9372400
ADC
QT Input Channel

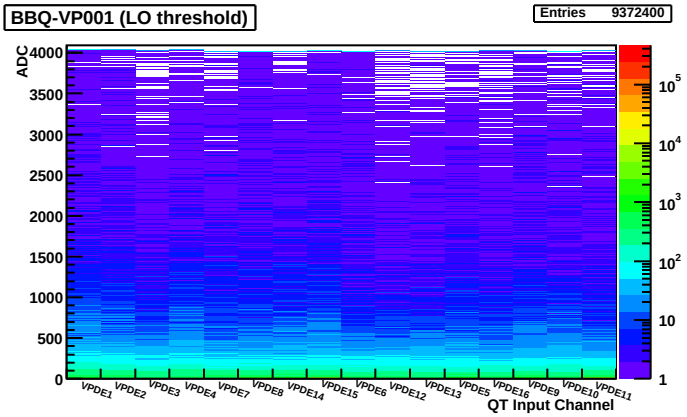

BBQ-BB002 (BBC west small tiles TAC)
Entries 9372400
TAC
QT Input Channel

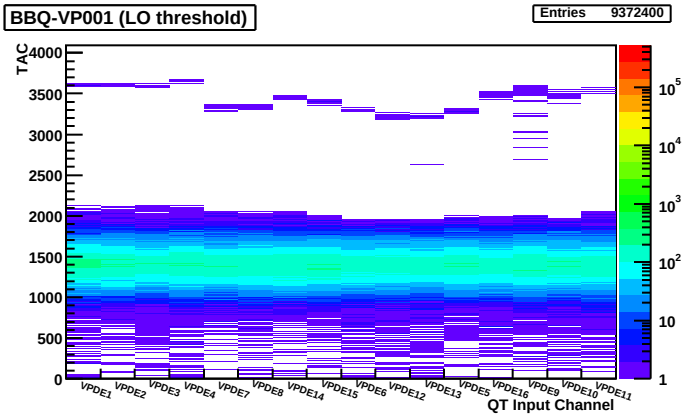

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9372400
ADC
QT Input Channel

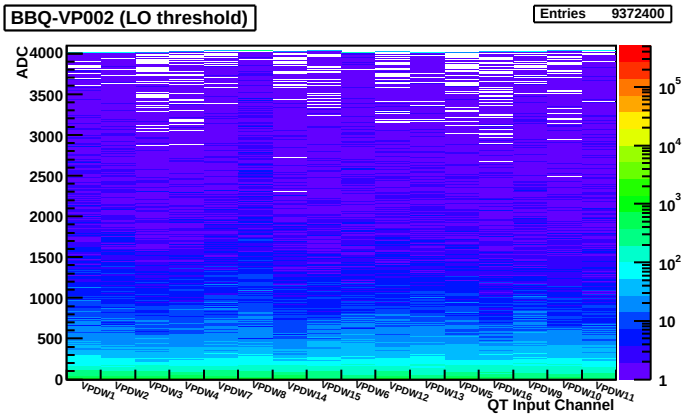

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9372400
TAC
QT Input Channel

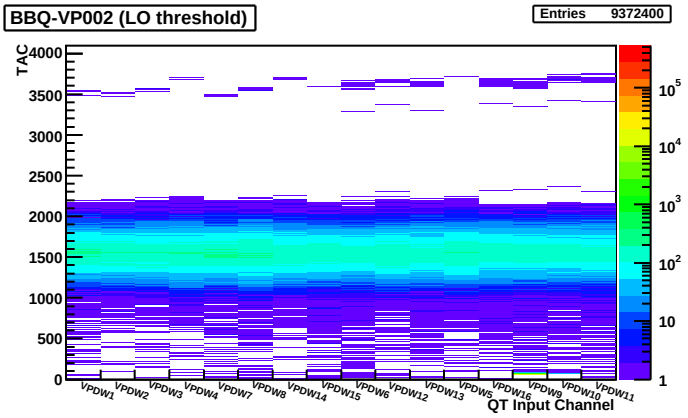

BBQ-ZD001
Entries 9372400
ADC
QT Input Channel

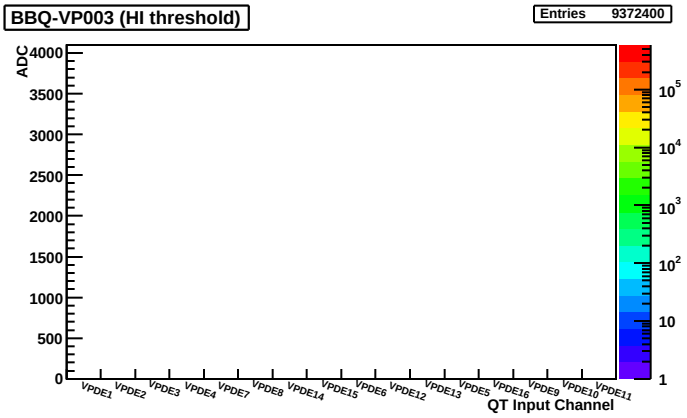

BBQ-ZD001
Entries 9372400
TAC
QT Input Channel

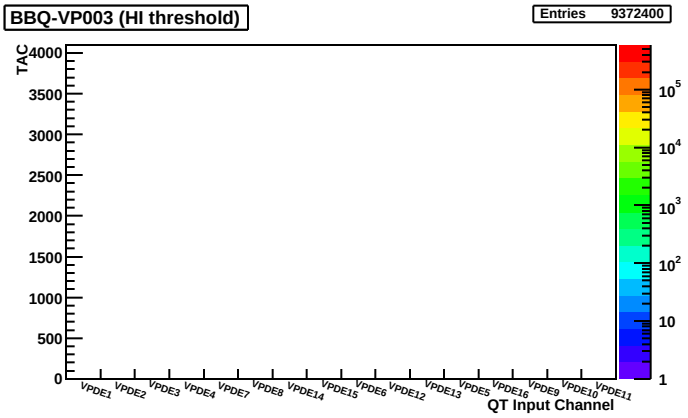

BBQ-VP001 (LO threshold)
Entries 9372400
ADC
QT Input Channel

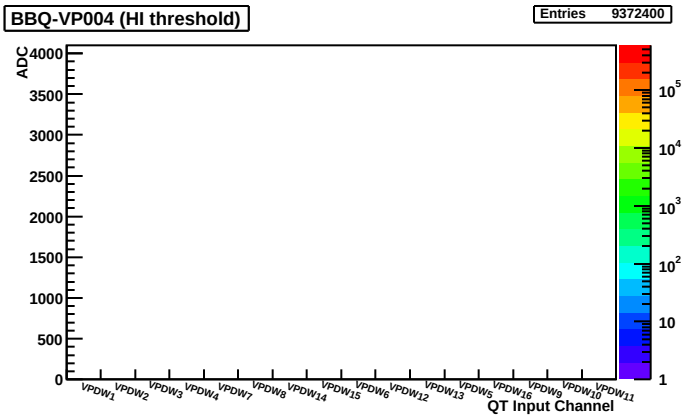

BBQ-VP001 (LO threshold)
Entries 9372400
TAC
QT Input Channel

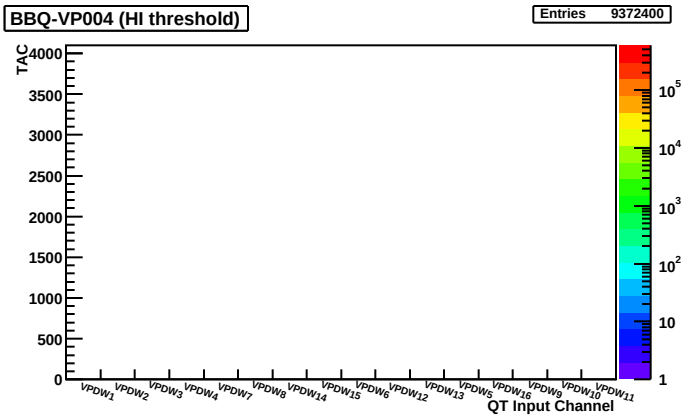

BBQ-VP002 (LO threshold)
Entries 9372400
ADC
QT Input Channel

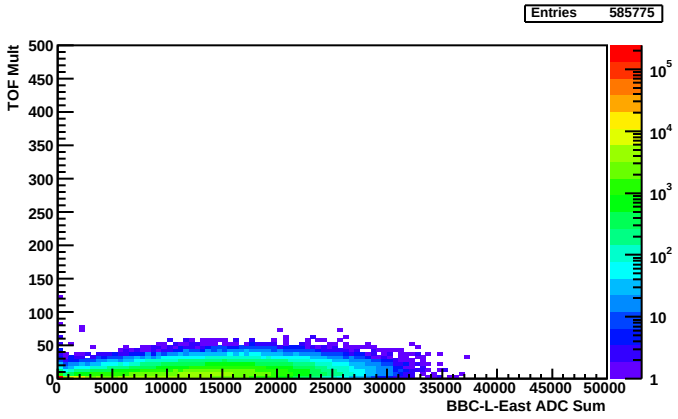

BBQ-VP002 (LO threshold)
Entries 9372400
TAC
QT Input Channel

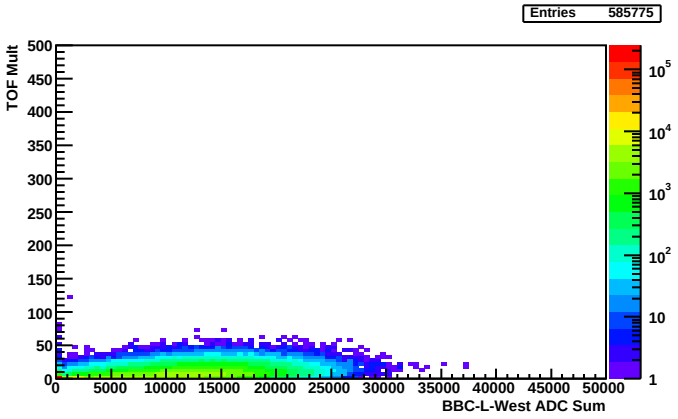

BBQ-VP003 (HI threshold)
Entries 9372400
ADC
QT Input Channel

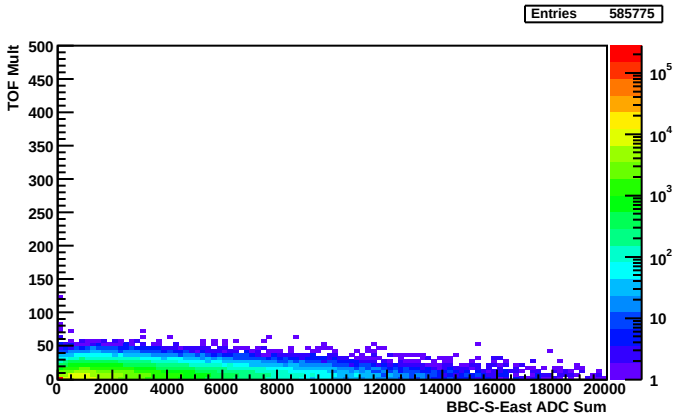

BBQ-VP003 (HI threshold)
Entries 9372400
TAC
QT Input Channel

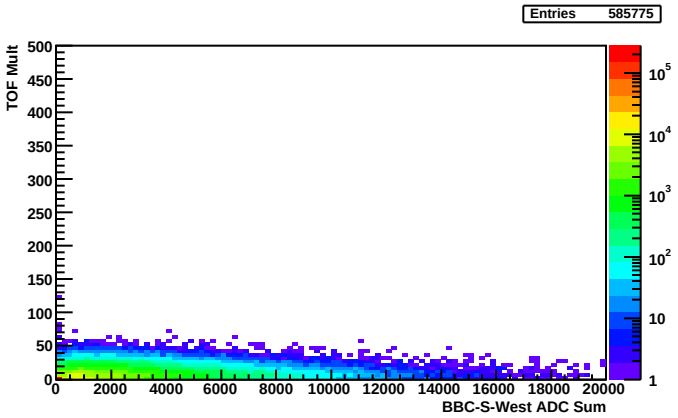

BBQ-VP004 (HI threshold)
Entries 9372400
ADC
QT Input Channel

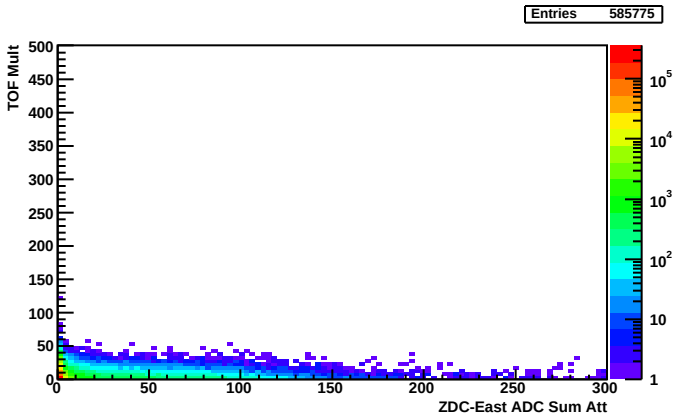

BBQ-VP004 (HI threshold)
Entries 9372400
TAC
QT Input Channel

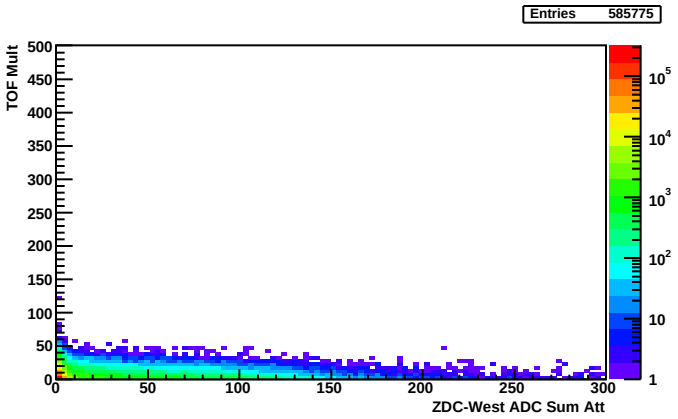

Entries 585775
TOF Mult
BBC-L-East ADC Sum

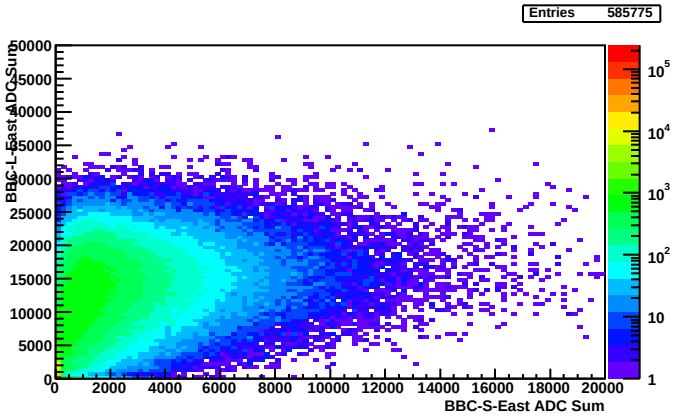

Entries 585775
TOF Mult
BBC-L-West ADC Sum

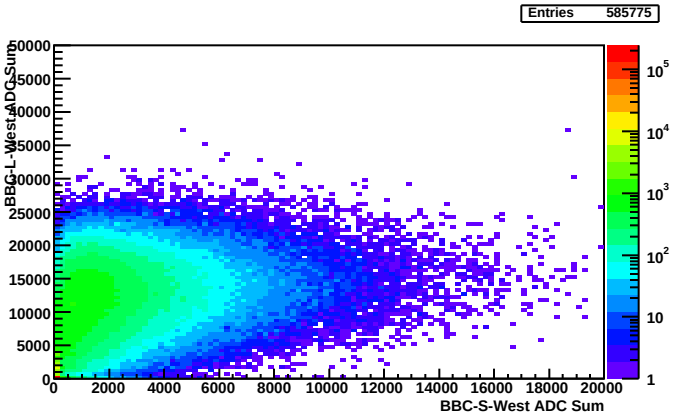

Entries 585775
TOF Mult
BBC-S-East ADC Sum

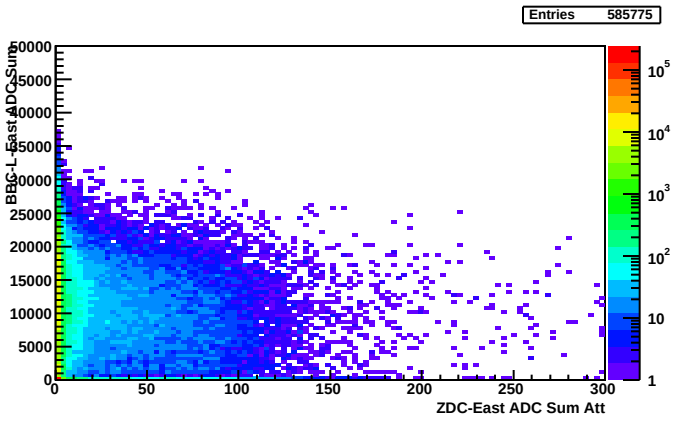

Entries 585775
TOF Mult
BBC-S-West ADC Sum

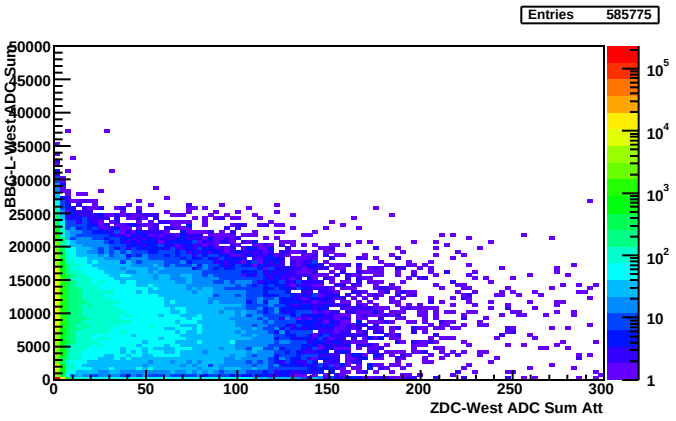

Entries 585775
TOF Mult
ZDC-East ADC Sum Att

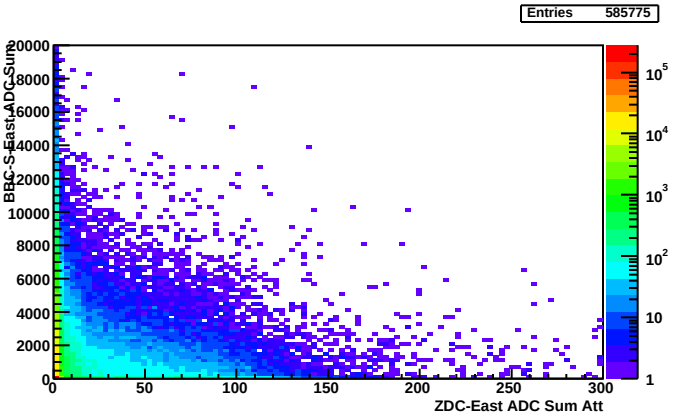

Entries 585775
TOF Mult
ZDC-West ADC Sum Att

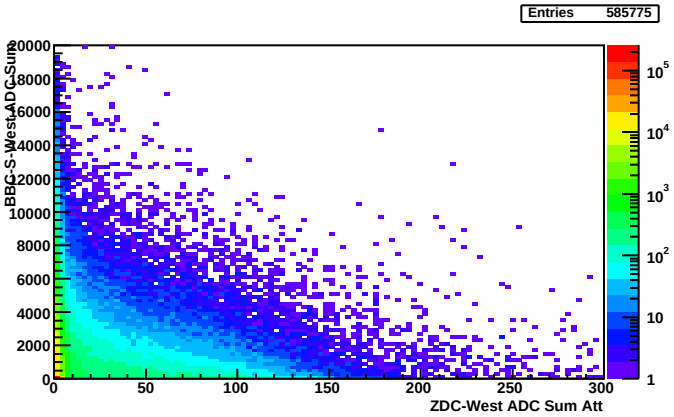

Entries 585775
BBC-L-East ADC Sum
BBC-S-East ADC Sum

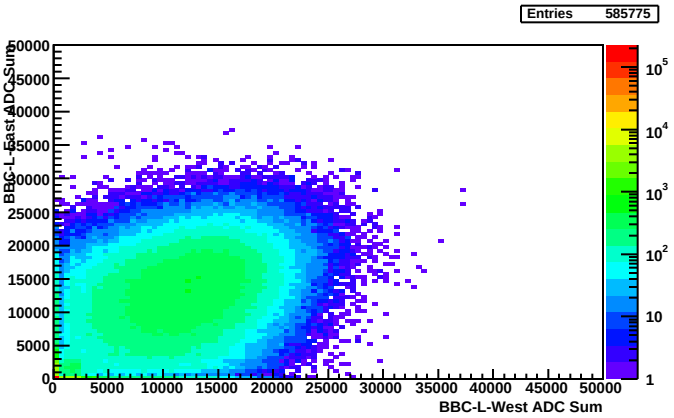

Entries 585775
BBC-L-West ADC Sum
BBC-S-West ADC Sum

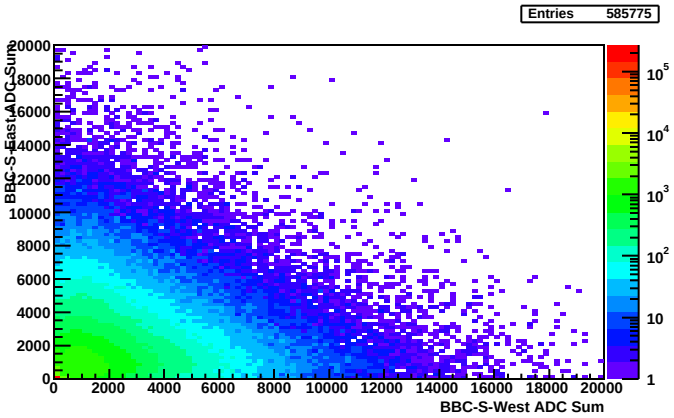

Entries 585775
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

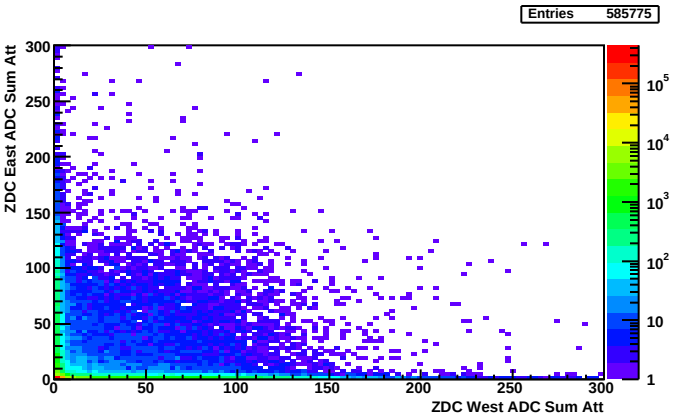

Entries 585775
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

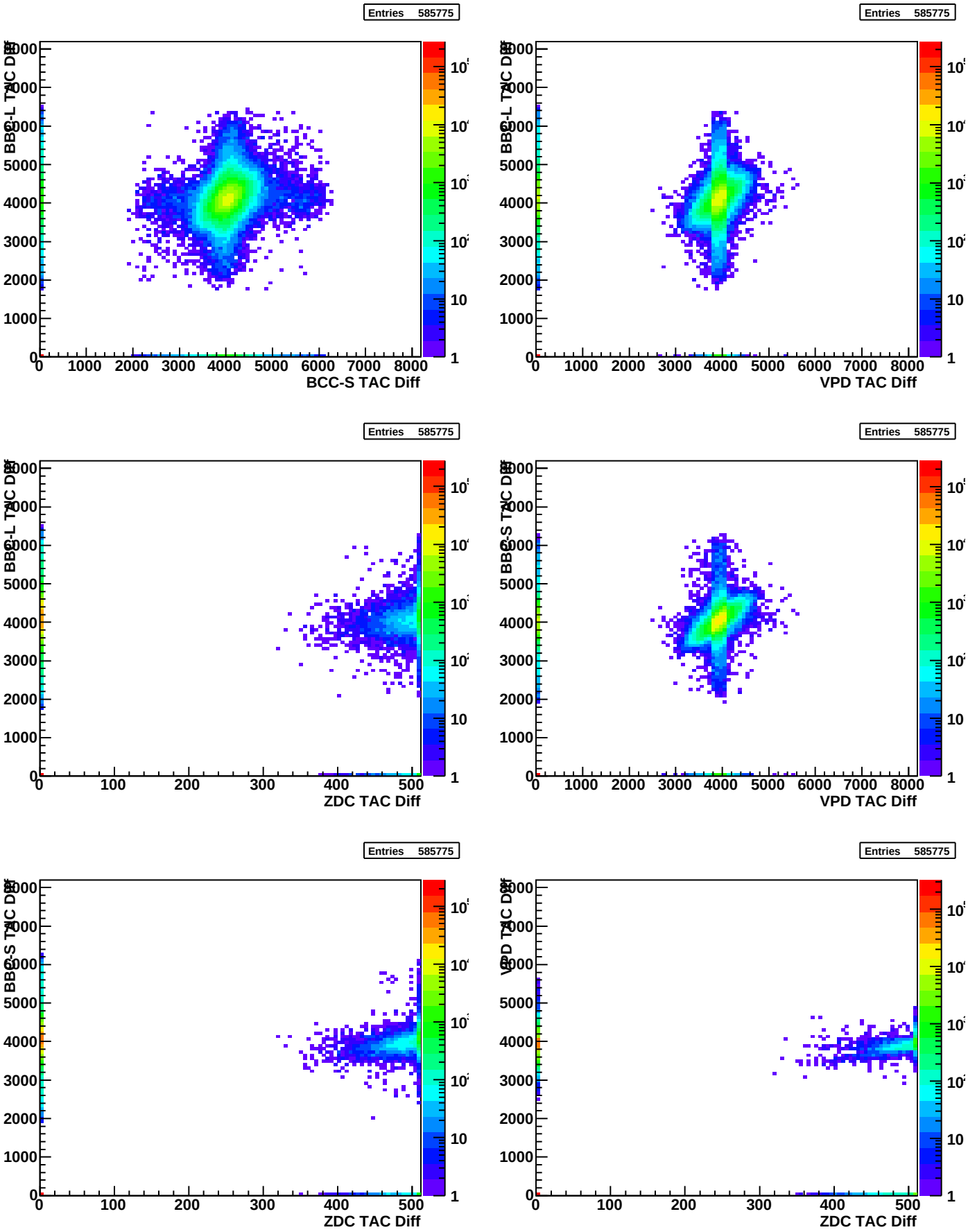

Entries 585775
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

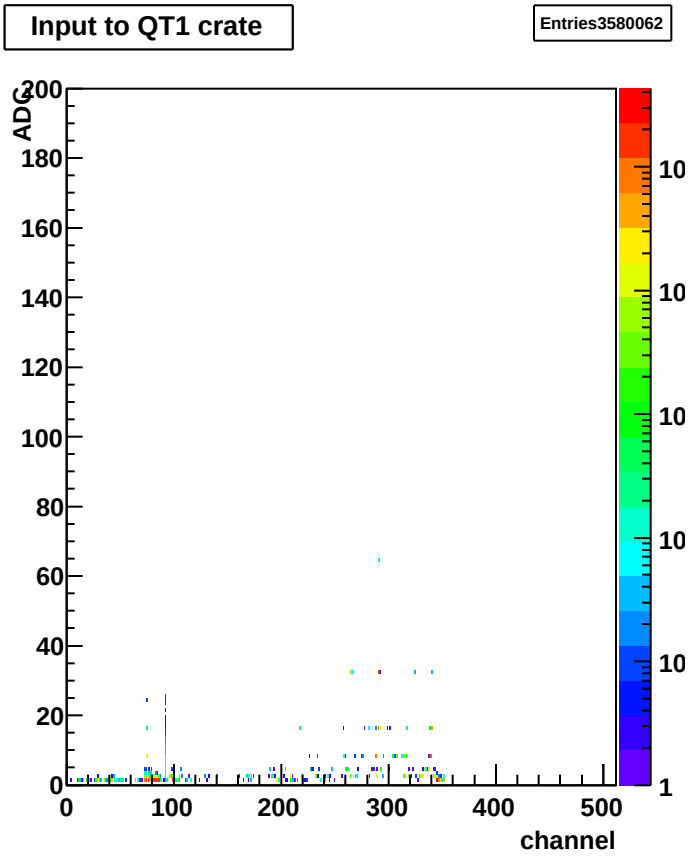

Entries 585775
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

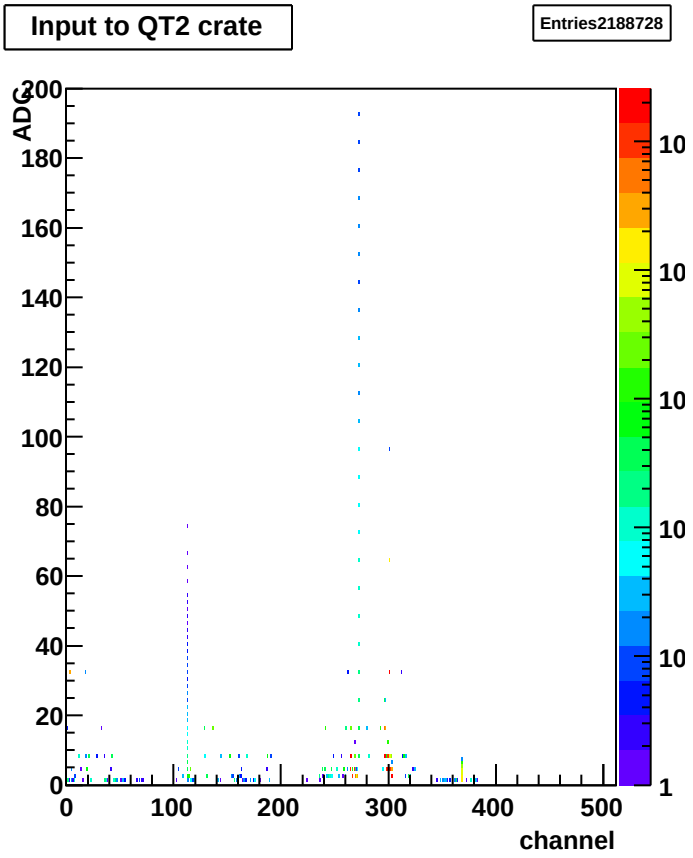

Entries 585775
BBC-L-East ADC Sum
BBC-L-West ADC Sum

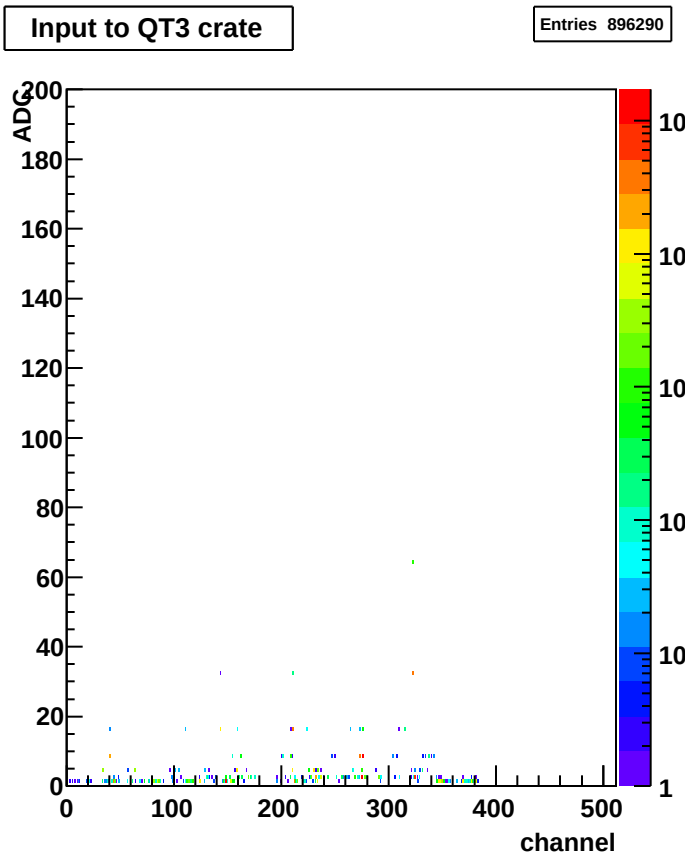

Entries 585775
BBC-S-East ADC Sum
BBC-S-West ADC Sum

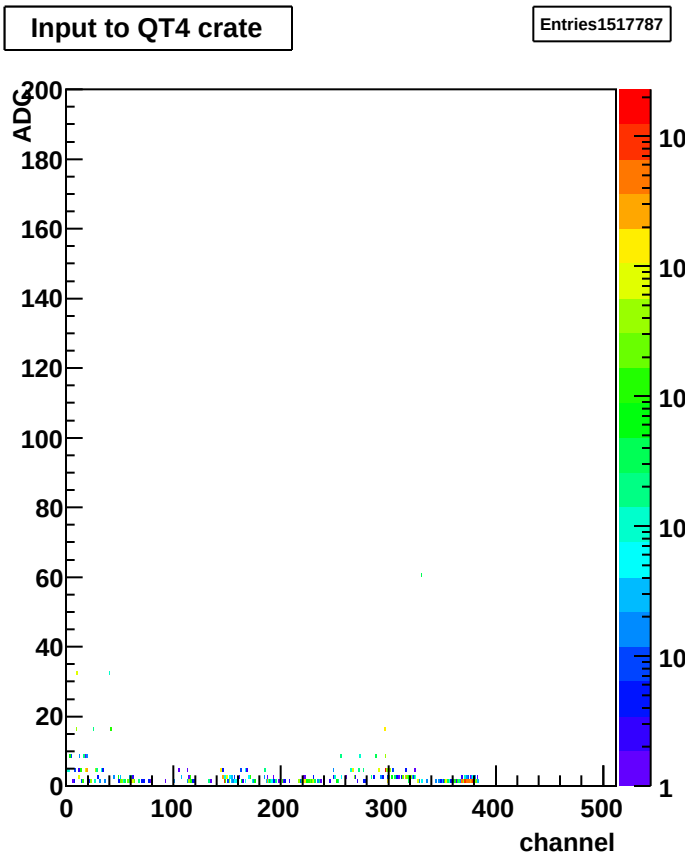

Entries 585775
ZDC East ADC Sum Att
ZDC West ADC Sum Att

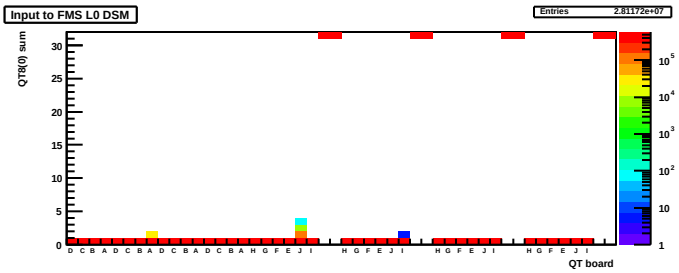

Entries 585775
BBC-L TAC Diff
BCC-S TAC Diff
Entries 585775
BBC-L TAC Diff
VPD TAC Diff
Entries 585775
BBC-L TAC Diff
ZDC TAC Diff
Entries 585775
BBC-S TAC Diff
VPD TAC Diff
Entries 585775
BBC-S TAC Diff
ZDC TAC Diff
Entries 585775
VPD TAC Diff
ZDC TAC Diff

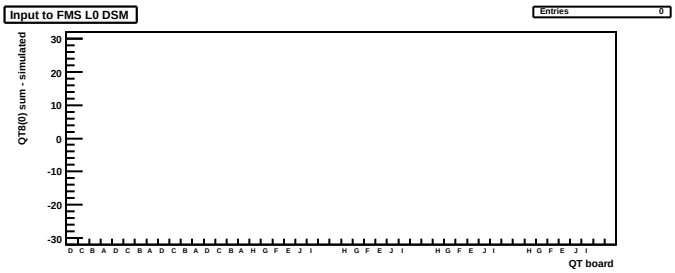

Input to QT1 crate
Entries3580062
ADC
channel

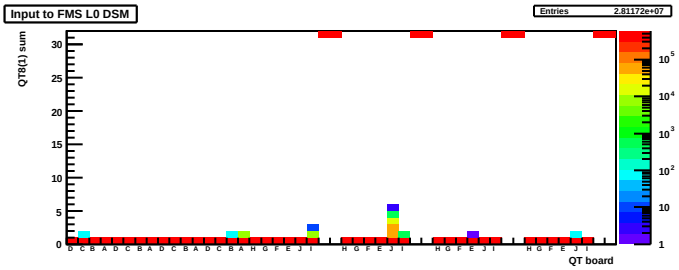

Input to QT2 crate
Entries2188728
ADC
channel

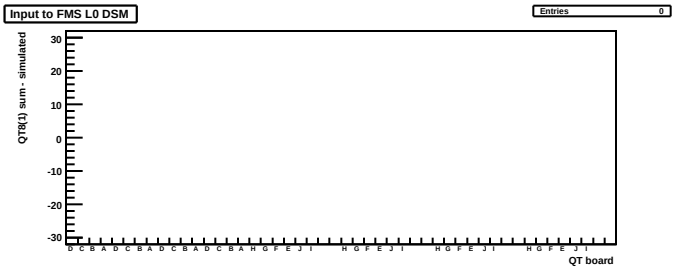

Input to QT3 crate
Entries 896290
ADC
channel

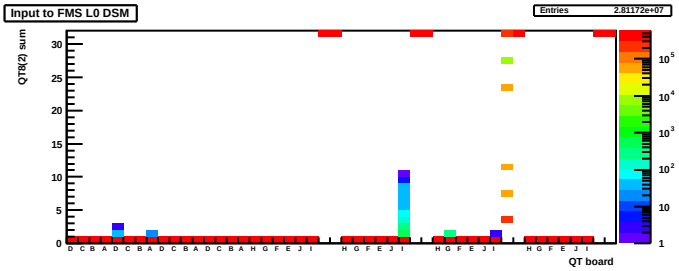

Input to QT4 crate
Entries1517787
ADC
channel

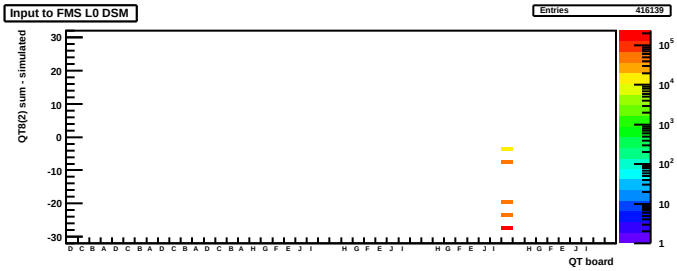

Input to FMS L0 DSM
Entries 2.81172e+07
QT8(0) sum
QT board

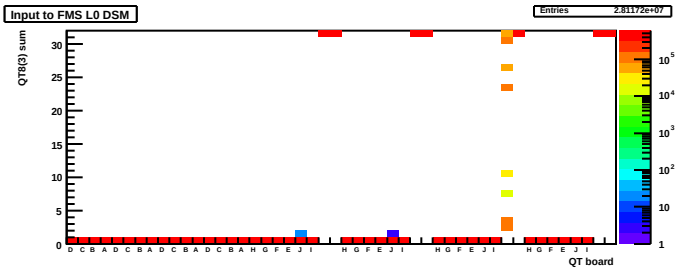

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

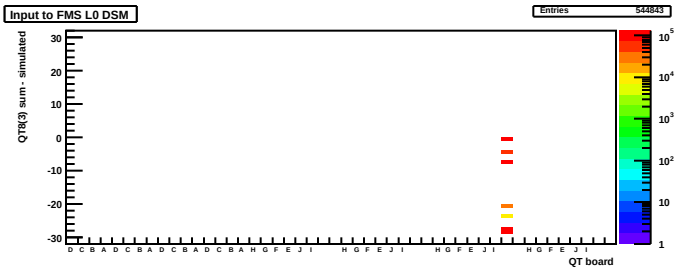

Input to FMS L0 DSM
Entries 2.81172e+07
QT8(1) sum
QT board

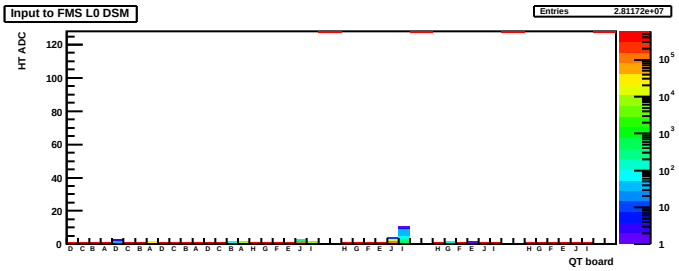

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

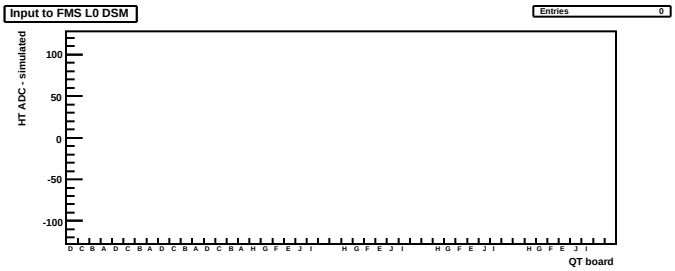

Input to FMS L0 DSM
Entries 2.81172e+07
QT8(2) sum
QT board

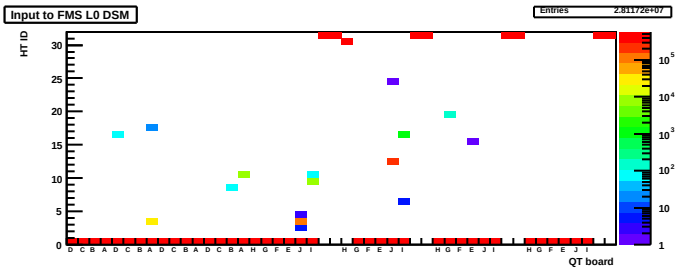

Input to FMS L0 DSM
Entries 416139
QT8(2) sum - simulated
QT board

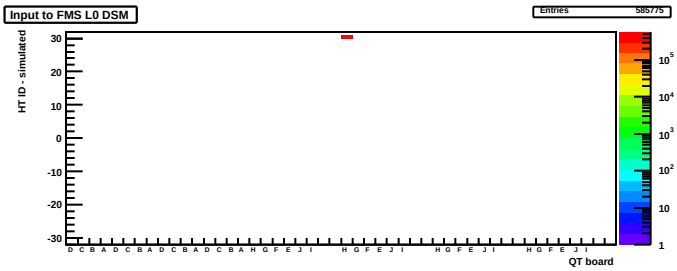

Input to FMS L0 DSM
Entries 2.81172e+07
QT8(3) sum
QT board

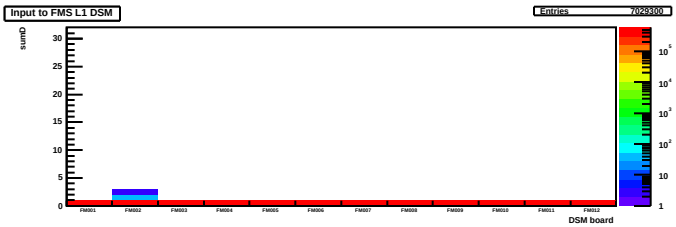

Input to FMS L0 DSM
Entries 544843
QT8(3) sum - simulated
QT board

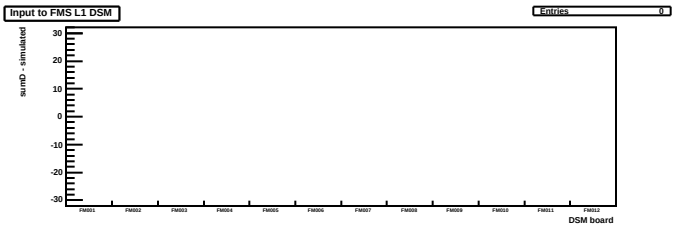

Input to FMS L0 DSM
Entries 2.81172e+07
HT ADC
QT board

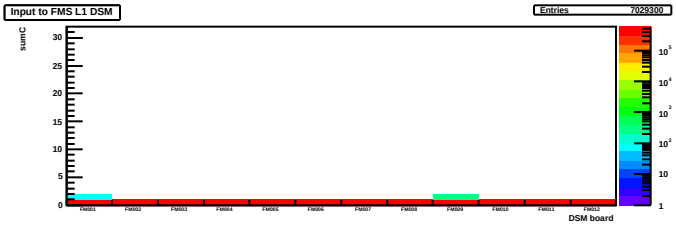

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

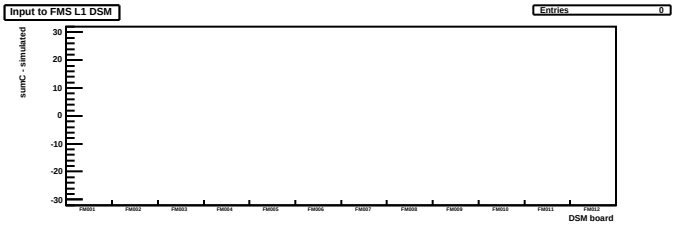

Input to FMS L0 DSM
Entries 2.81172e+07
HT ID
QT board

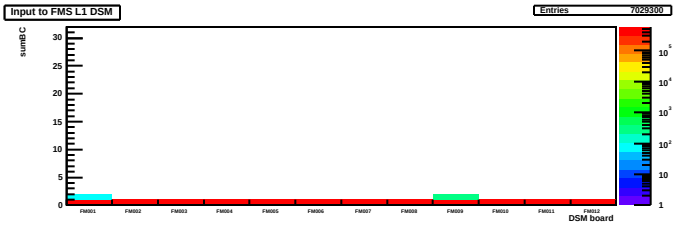

Input to FMS L0 DSM
Entries 585775
HT ID - simulated
QT board

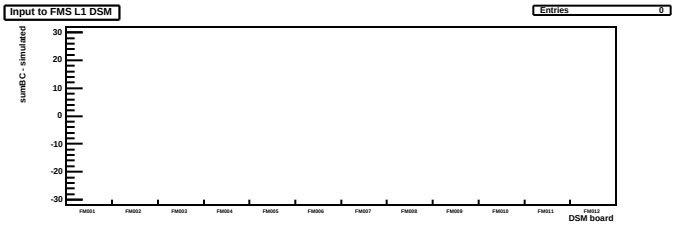

Input to FMS L1 DSM
Entries 7029300
sumD
DSM board

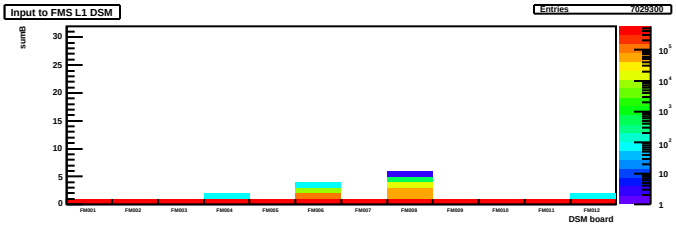

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

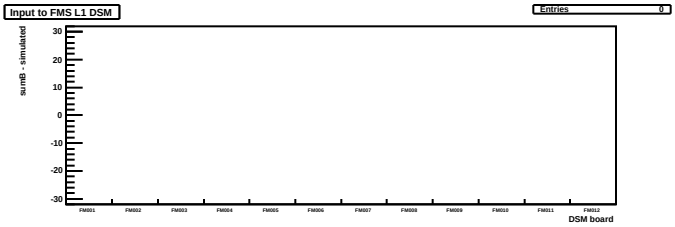

Input to FMS L1 DSM
Entries 7029300
sumC
DSM board

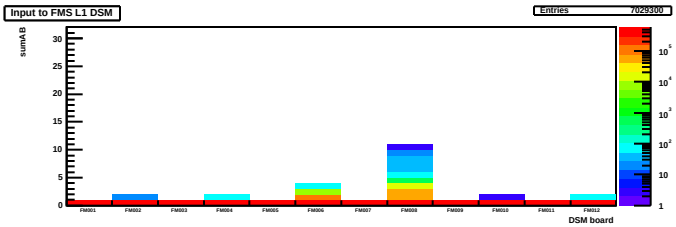

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

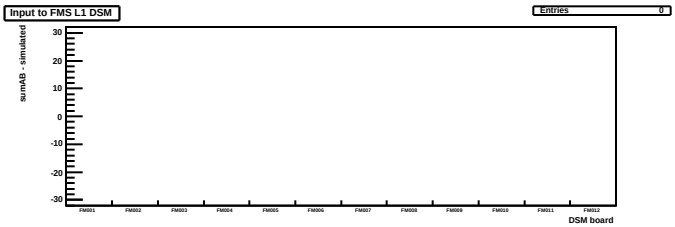

Input to FMS L1 DSM
Entries 7029300
sumBC
DSM board

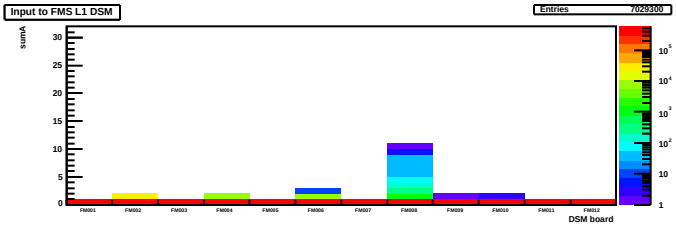

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

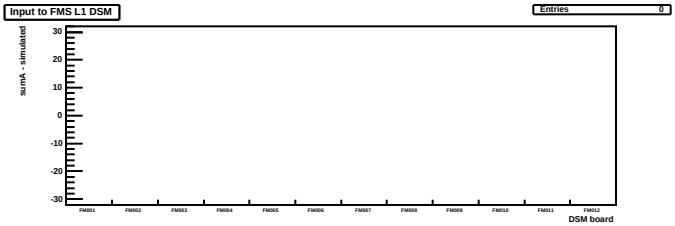

Input to FMS L1 DSM
Entries 7029300
sumB
DSM board

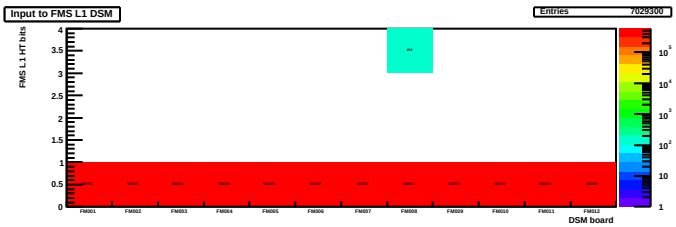

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

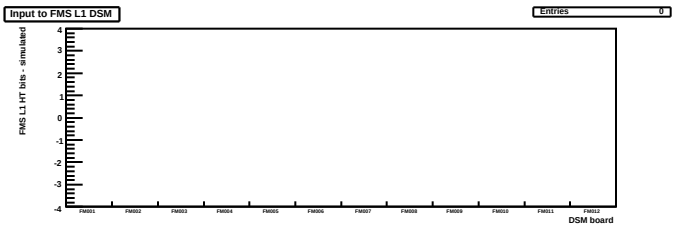

Input to FMS L1 DSM
Entries 7029300
sumAB
DSM board

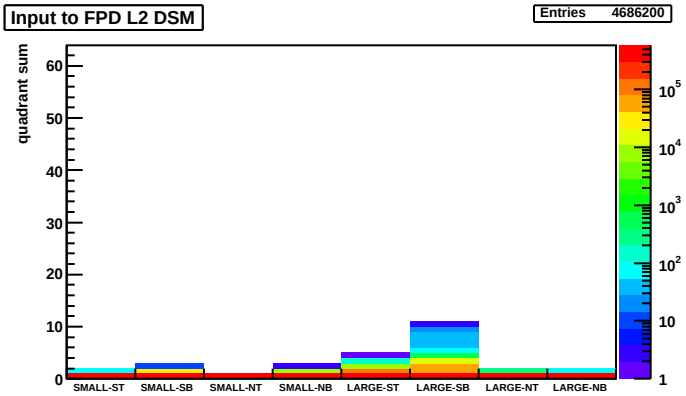

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

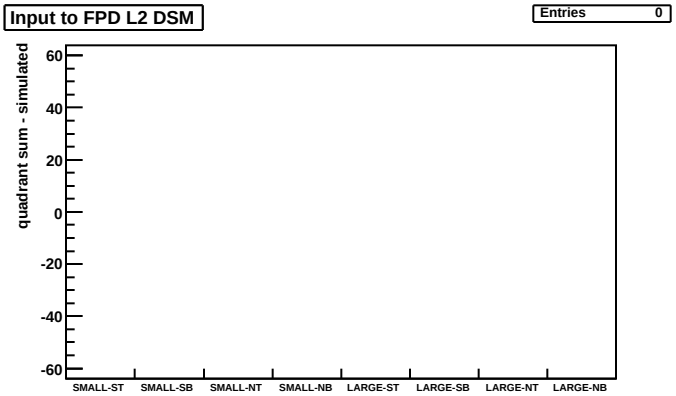

Input to FMS L1 DSM
Entries 7029300
sumA
DSM board

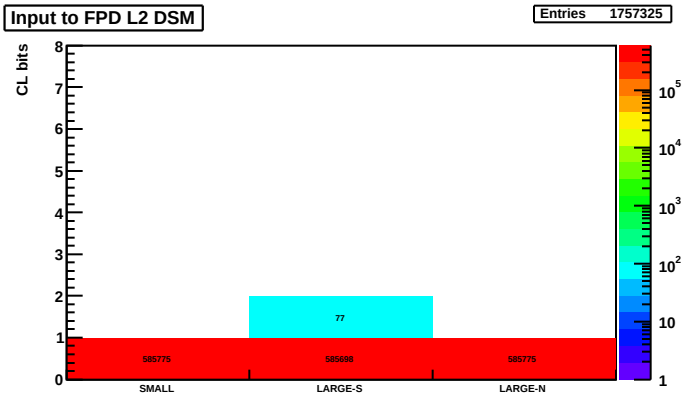

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

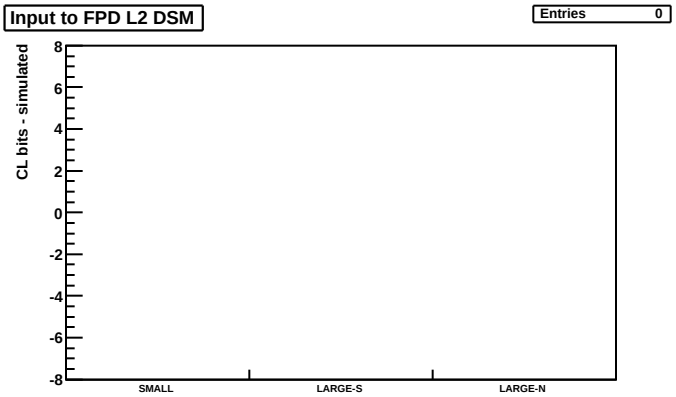

Input to FMS L1 DSM
Entries 7029300
FMS L1 HT bits
DSM board

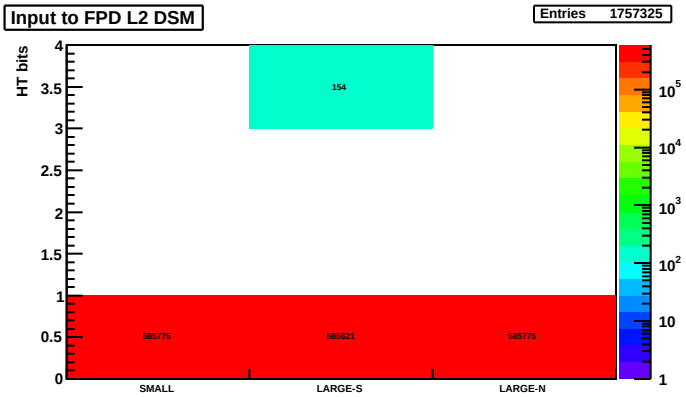

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

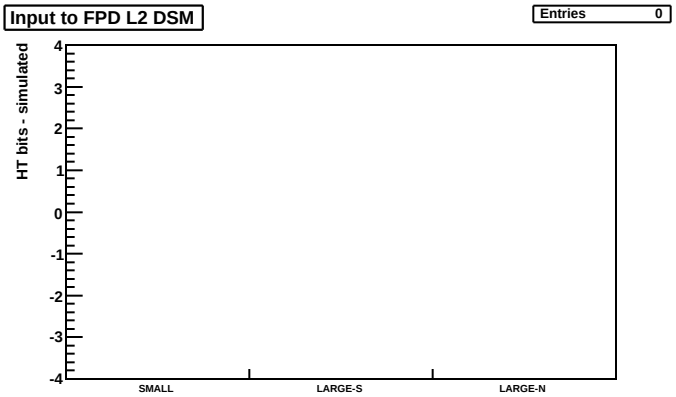

Input to FPD L2 DSM
Entries 4686200
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

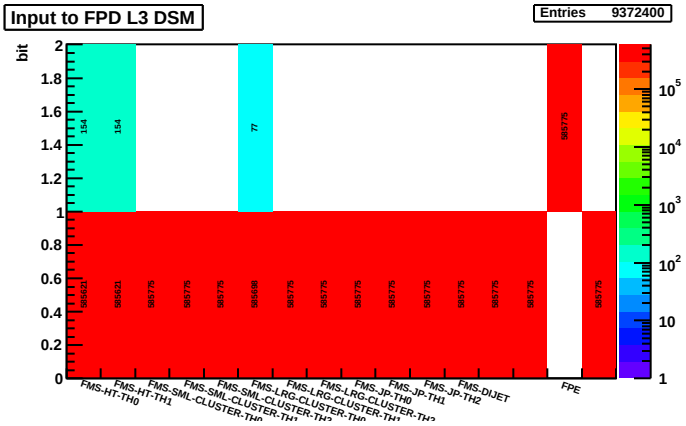

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

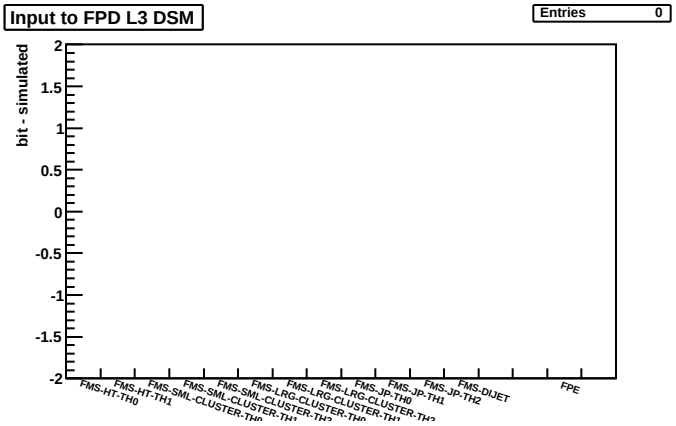

Input to FPD L2 DSM
Entries 1757325
CL bits
77
585775
585698
585775
SMALL
LARGE-S
LARGE-N

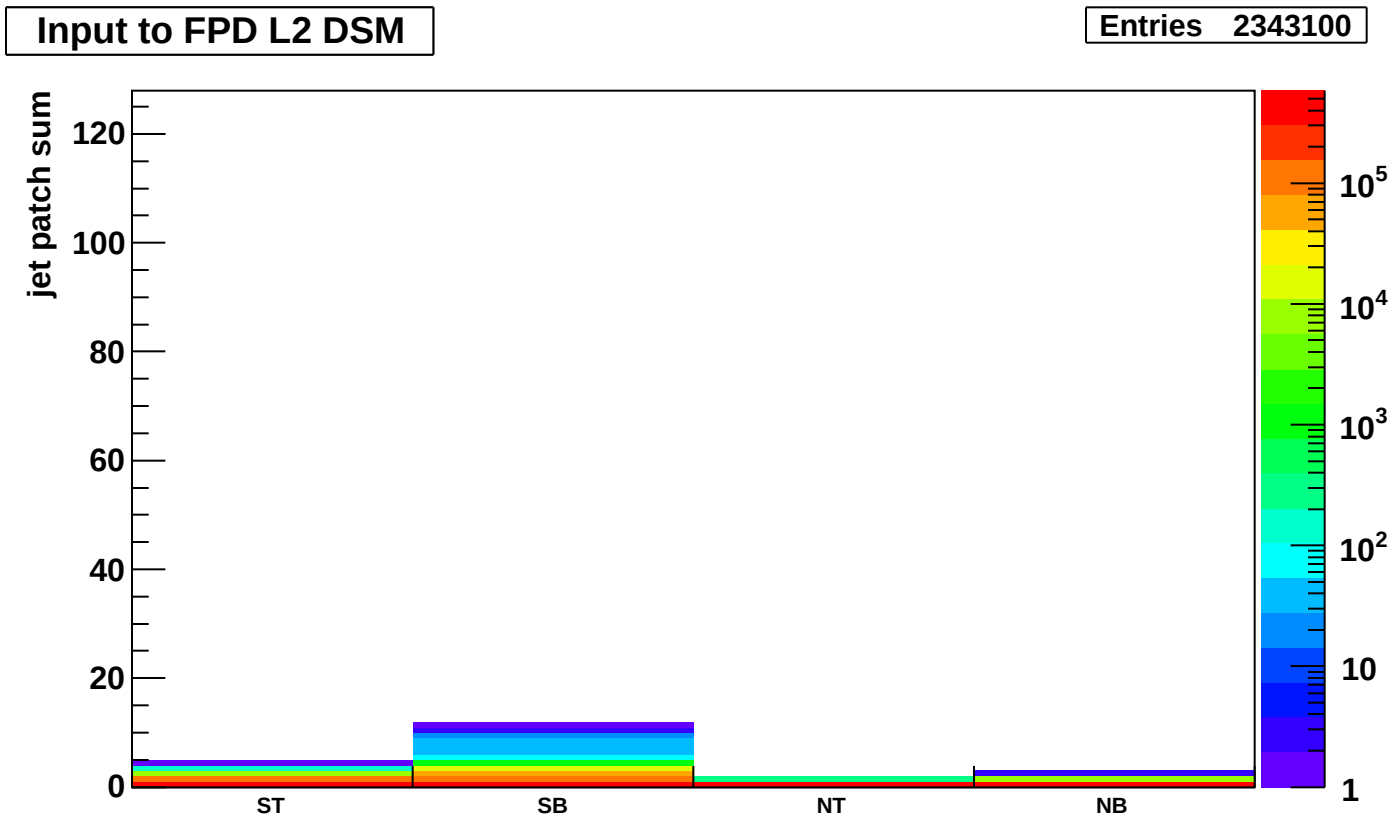

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

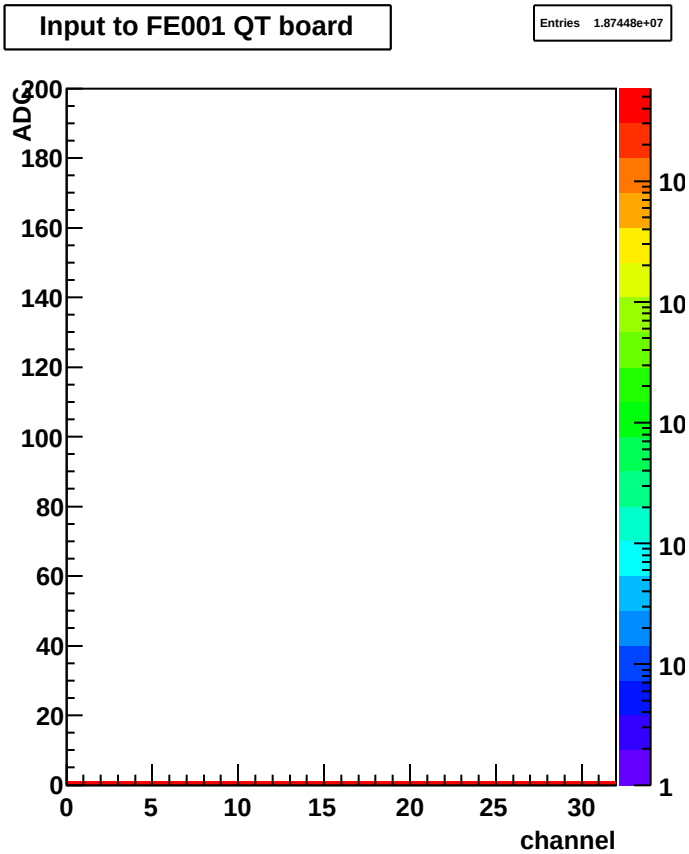

Input to FPD L2 DSM
Entries 1757325
HT bits
154
585775
585621
585775
SMALL
LARGE-S
LARGE-N

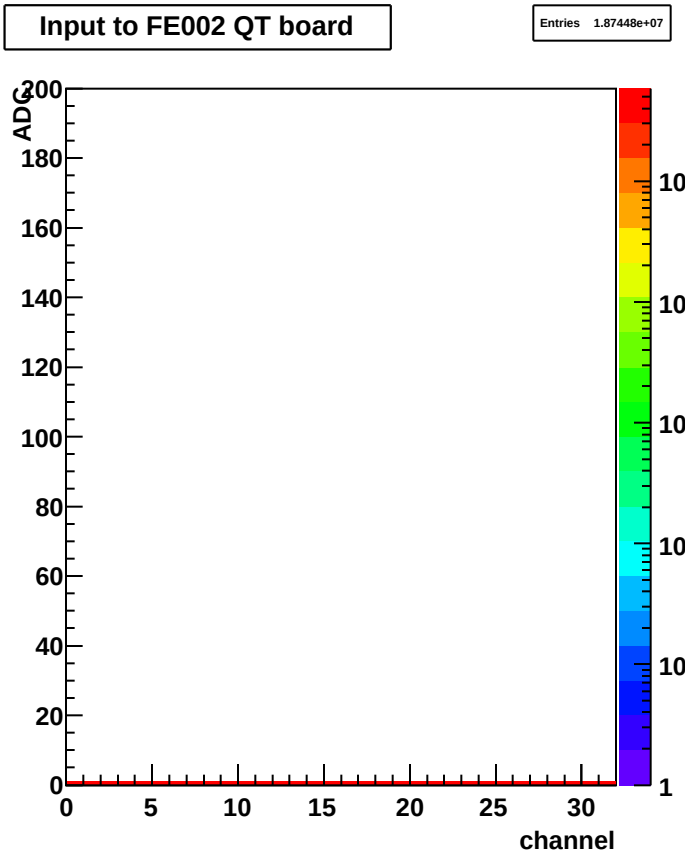

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

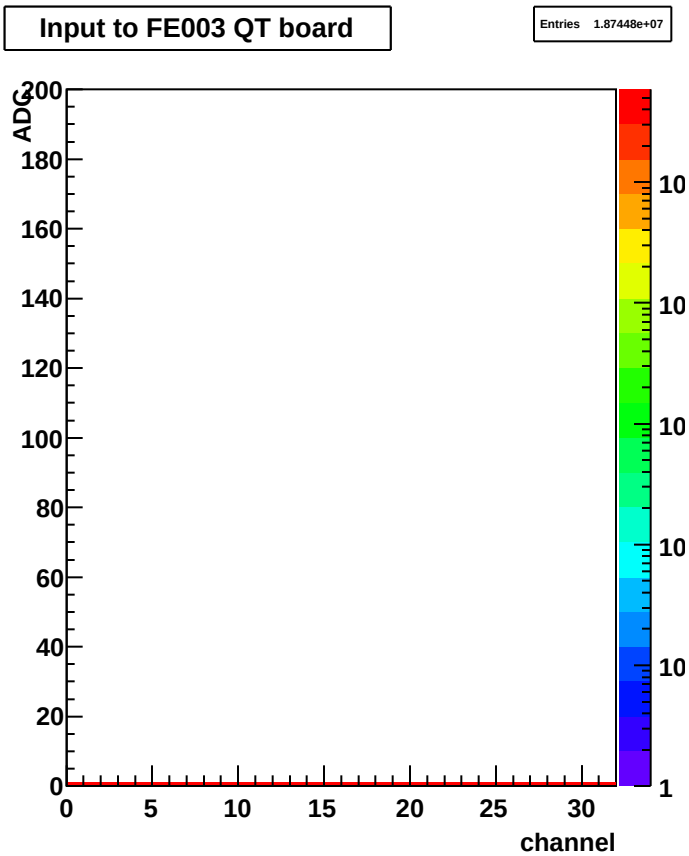

Input to FPD L3 DSM
Entries 9372400
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

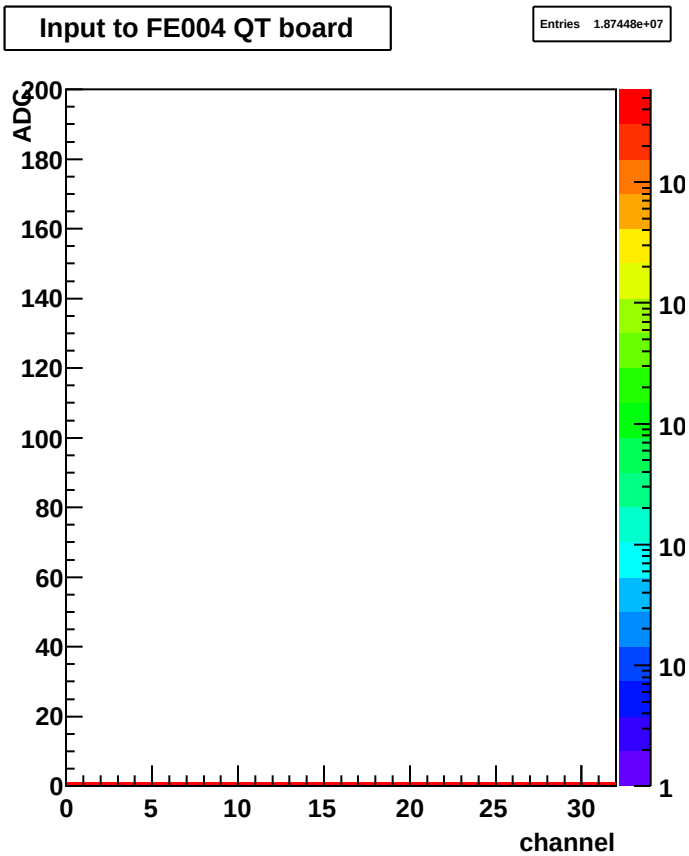

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

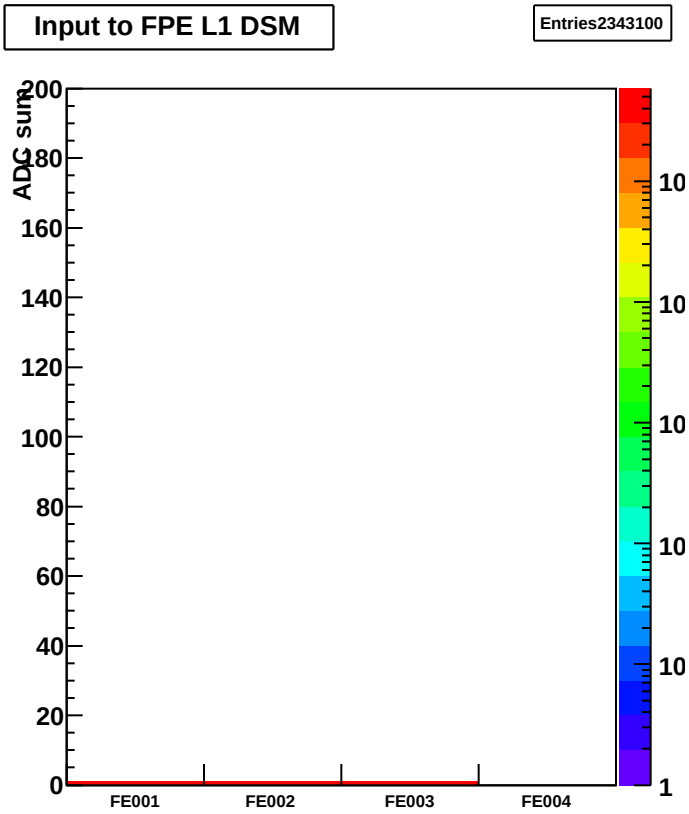

Input to FPD L2 DSM
Entries 2343100
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

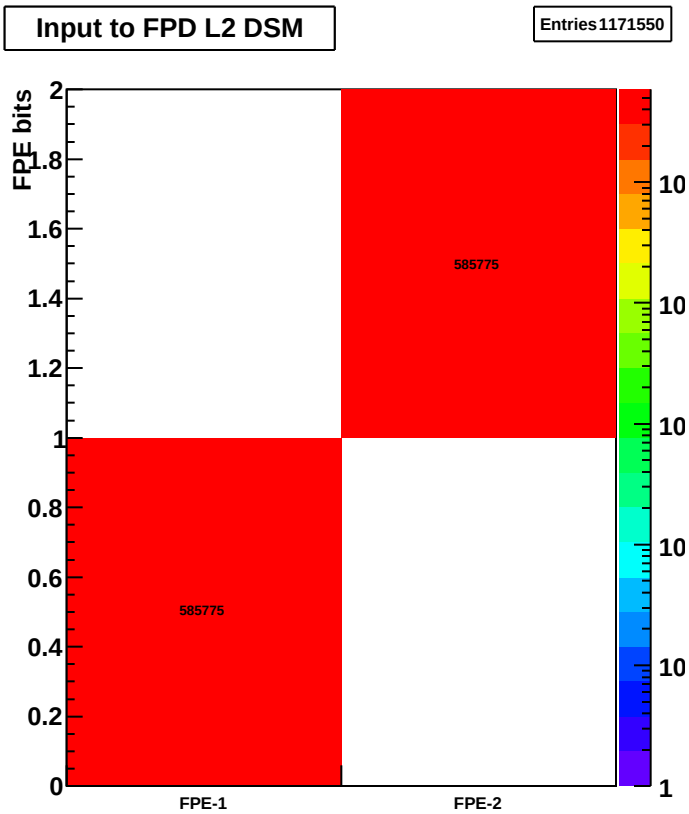

Input to FE001 QT board
Entries 1.87448e+07
ADC
channel

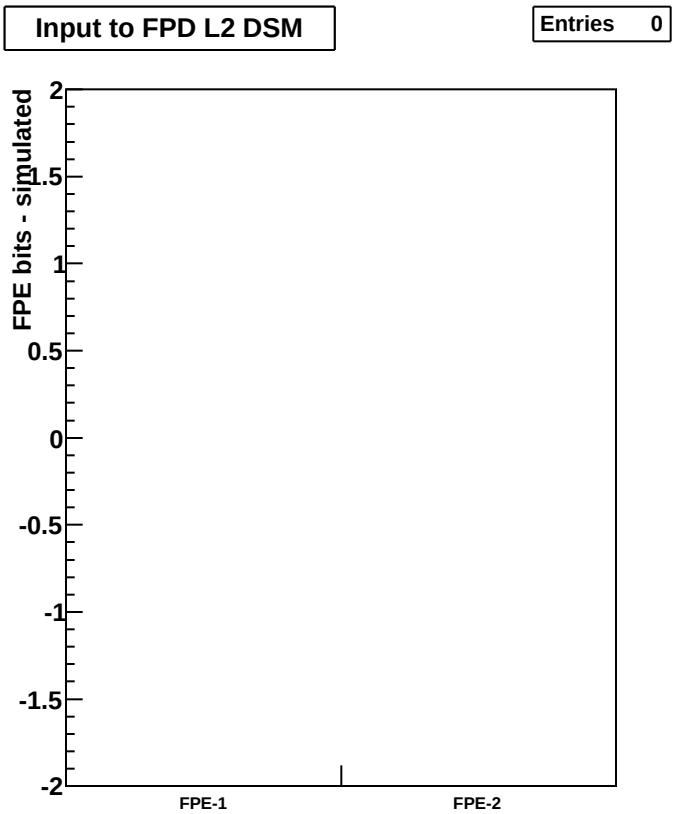

Input to FE002 QT board
Entries 1.87448e+07
ADC
channel


Input to FE003 QT board
Entries 1.87448e+07
ADC
channel


Input to FE004 QT board
Entries 1.87448e+07
ADC
channel


Input to FPE L1 DSM
Entries2343100
ADC sum
200
180
160
140
120
100
80
60
40
20
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries1171550
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
585775
585775
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2