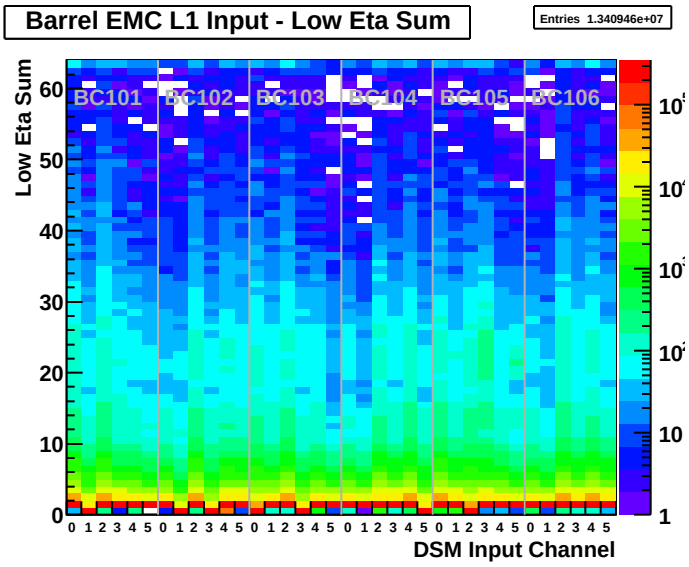
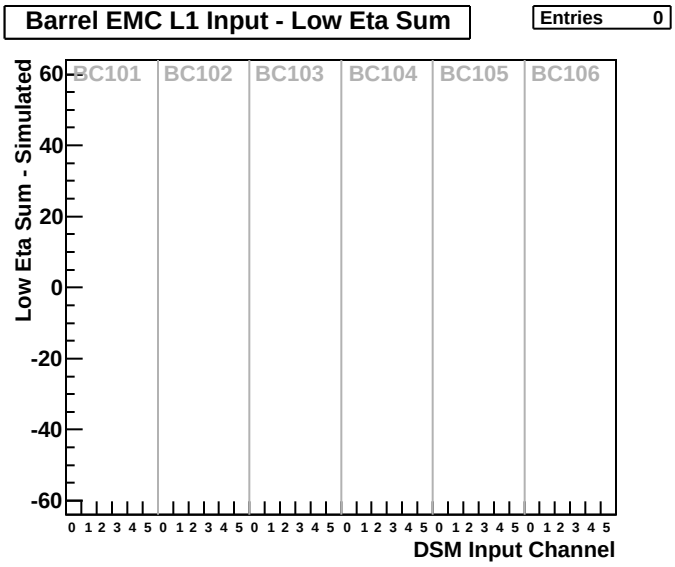
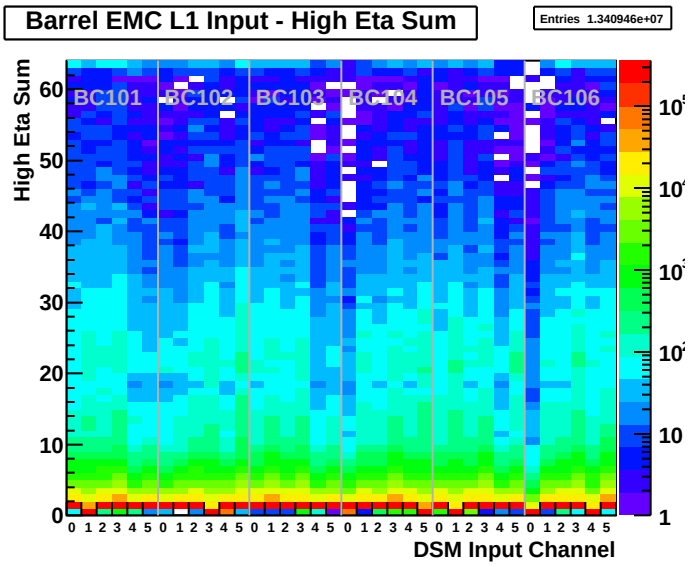
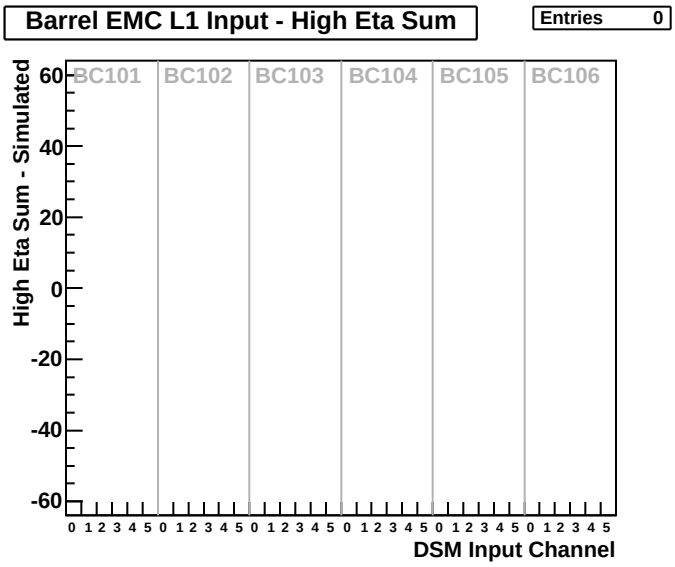
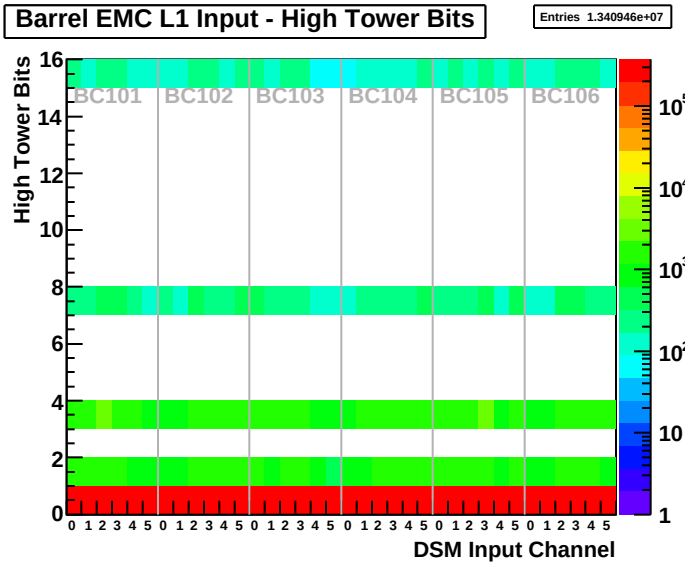
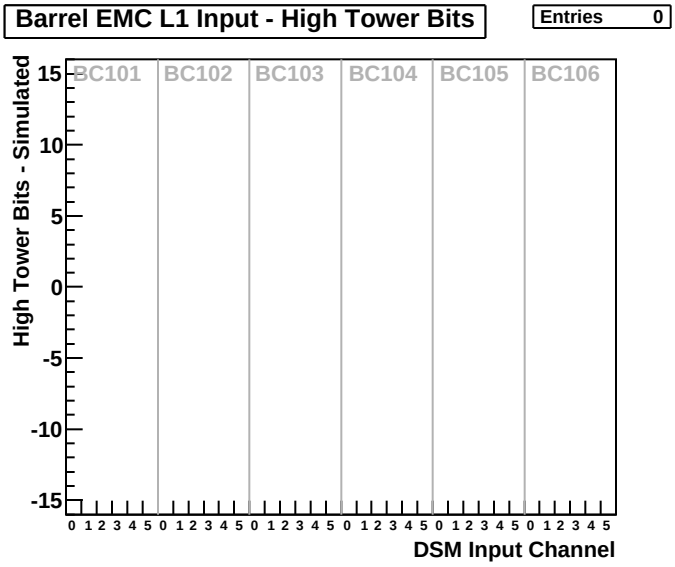

Barrel EMC L1 Input - Low Eta Sum
Entries 1.340946e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

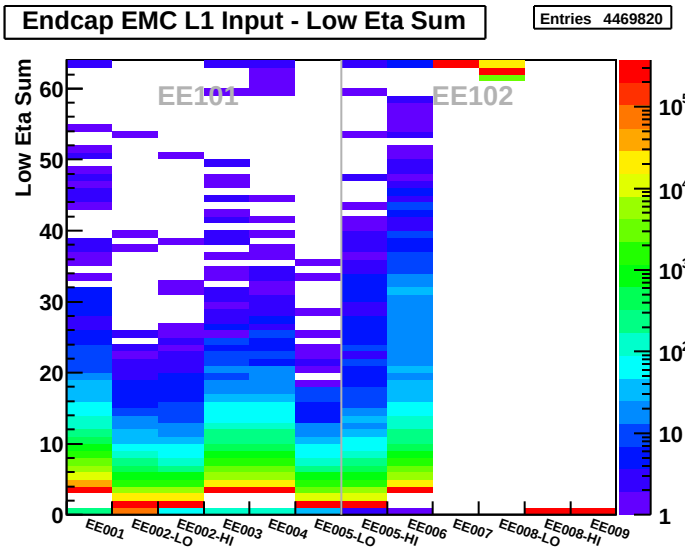

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

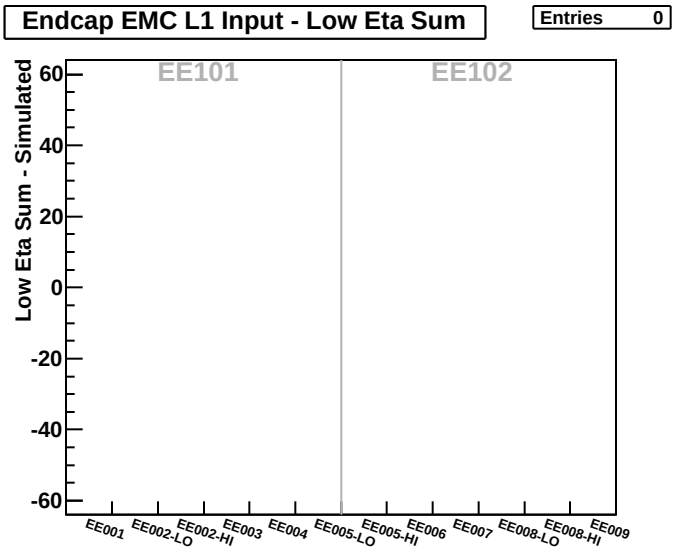

Barrel EMC L1 Input - High Eta Sum
Entries 1.340946e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

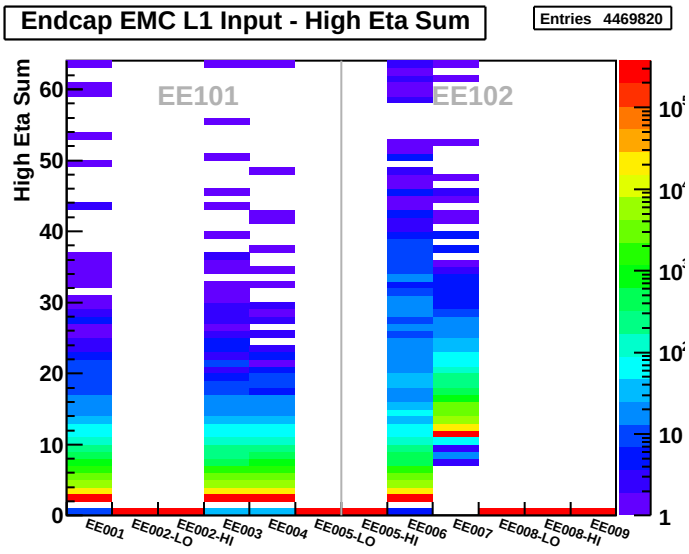

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

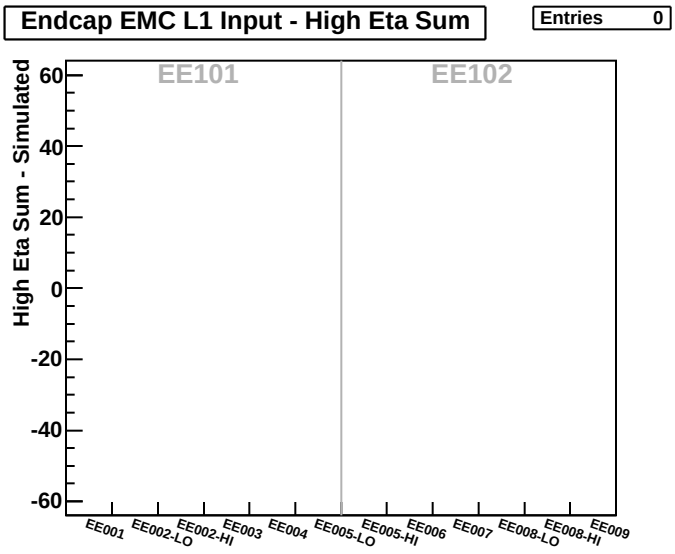

Barrel EMC L1 Input - High Tower Bits
Entries 1.340946e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

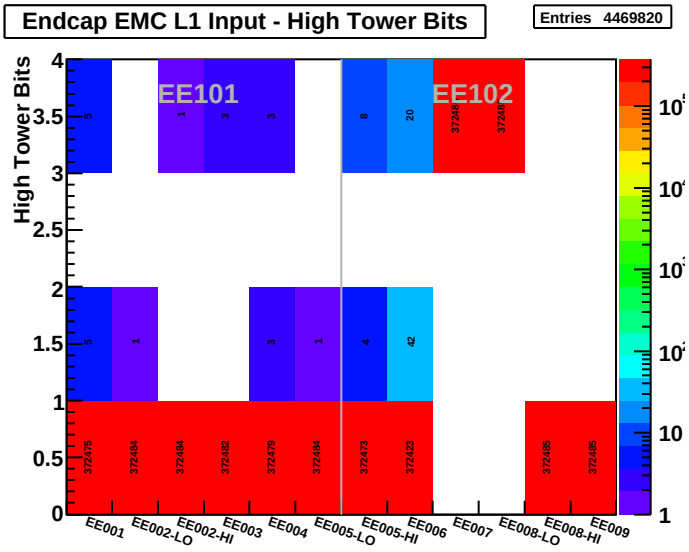

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

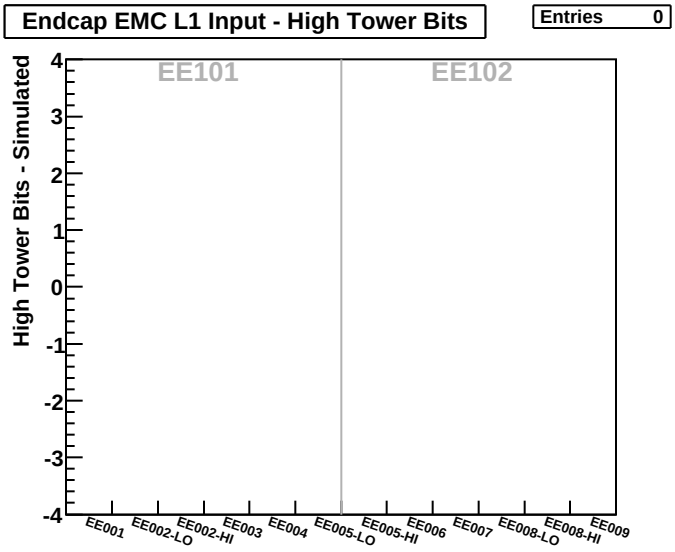

Endcap EMC L1 Input - Low Eta Sum
Entries 4469820
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

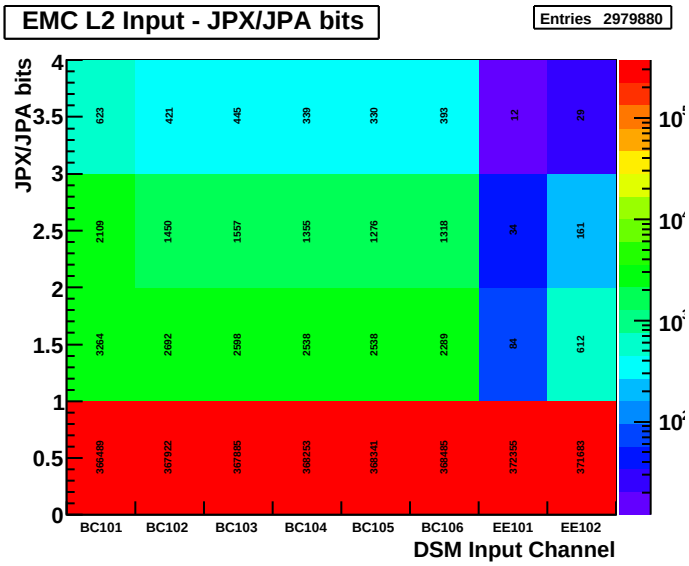

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

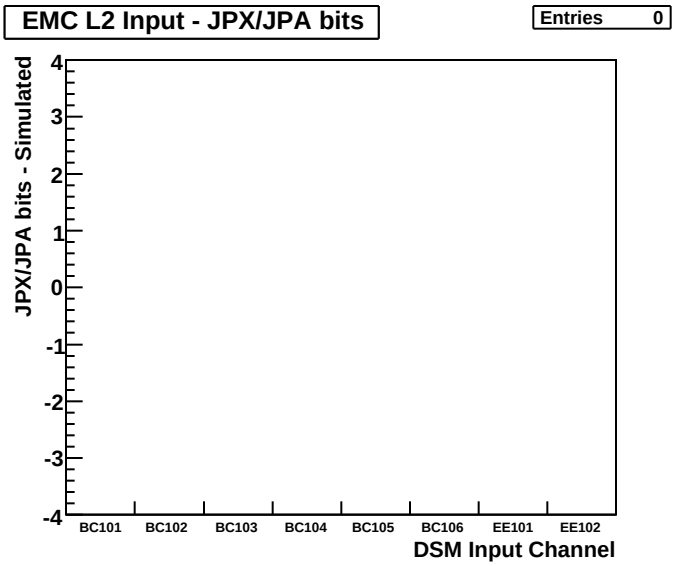

Endcap EMC L1 Input - High Eta Sum
Entries 4469820
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

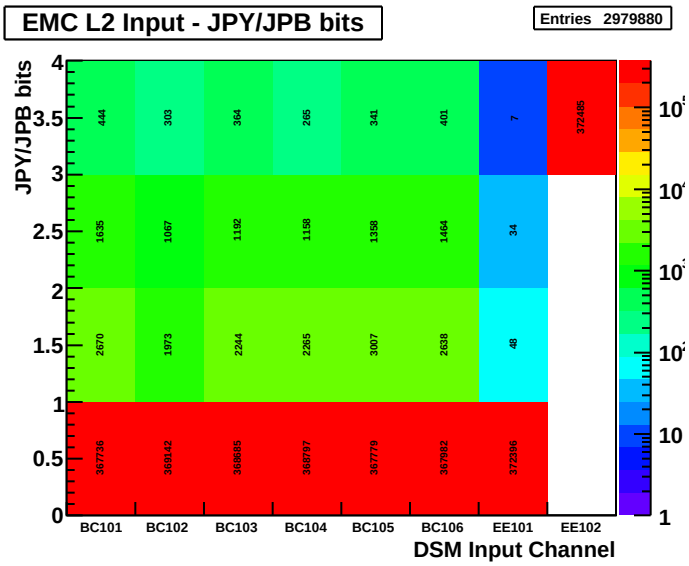

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

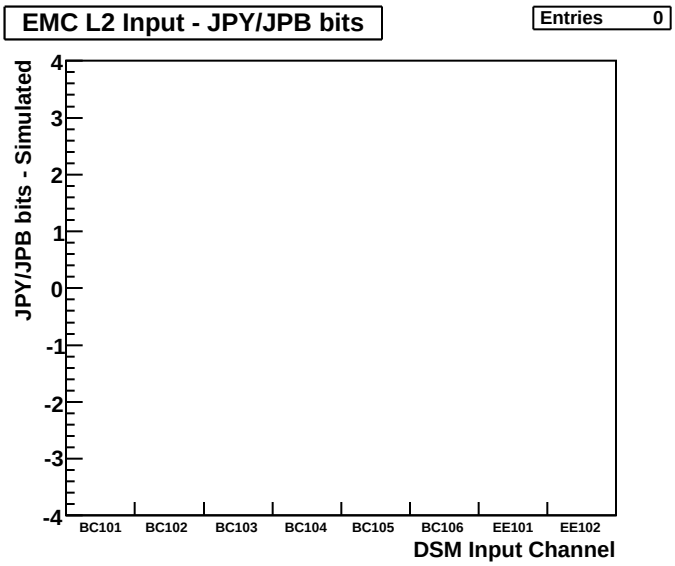

Endcap EMC L1 Input - High Tower Bits
Entries 4469820
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

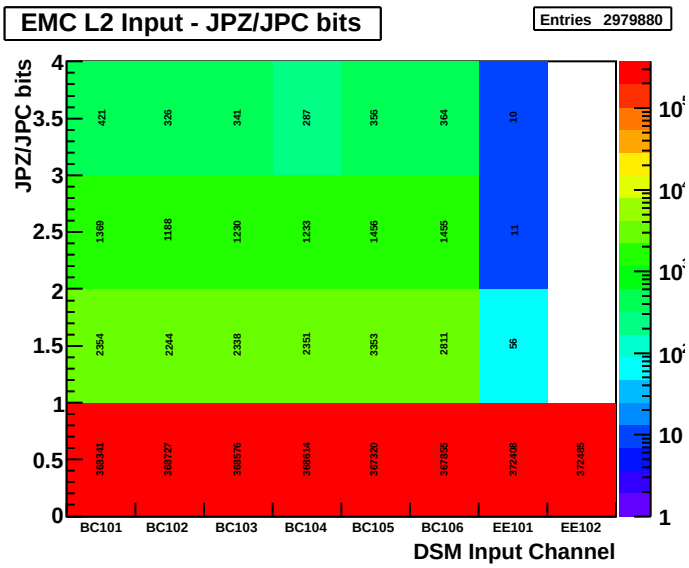

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

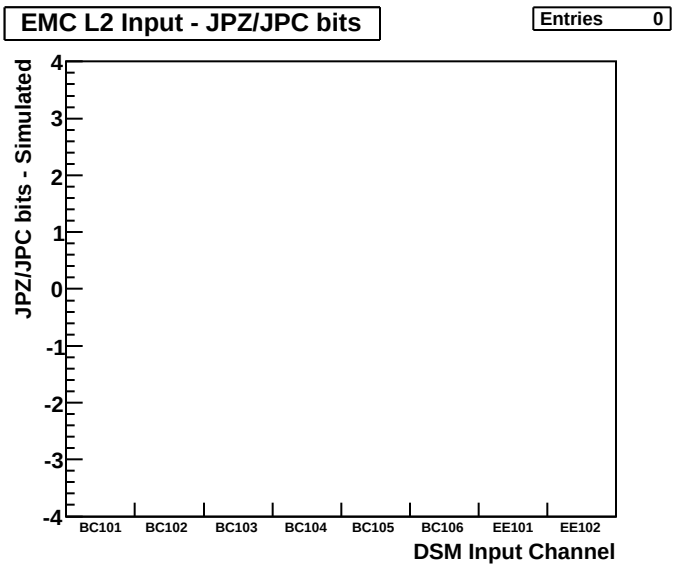

EMC L2 Input - JPX/JPA bits
Entries 2979880
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
623 421 445 339 330 393 12 29
2109 1450 1557 1355 1276 1318 34 161
3264 2892 2598 2538 2538 2289 84 612
366469 367922 367885 368253 368341 368495 372355 371663

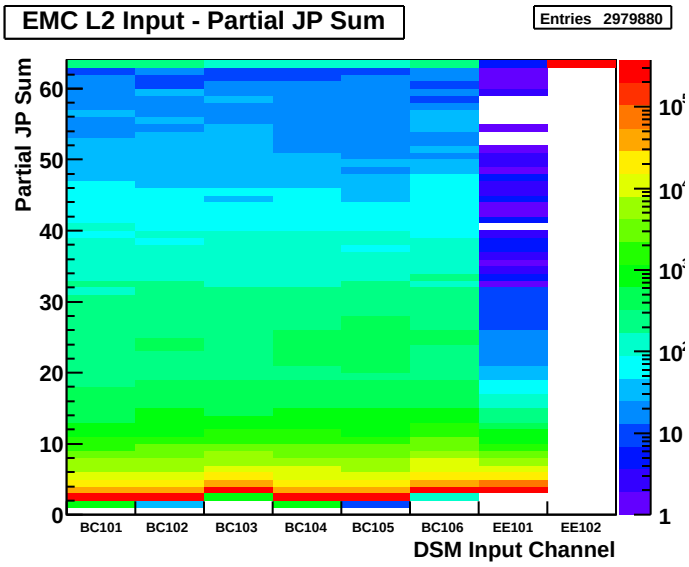

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

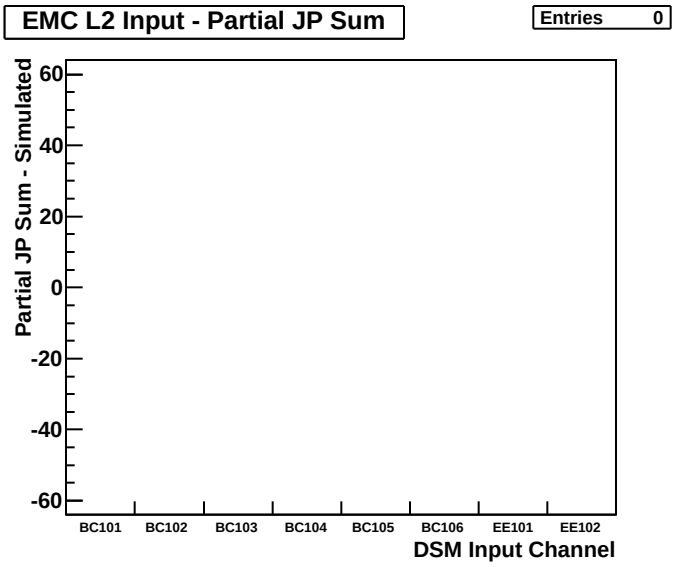

EMC L2 Input - JPY/JPB bits
Entries 2979880
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
444 303 364 265 341 401 7 372485
1635 1067 1192 1158 1358 1464 34
2670 1973 2244 2265 3007 2538 48
367736 368142 368685 368797 367779 367982 372396

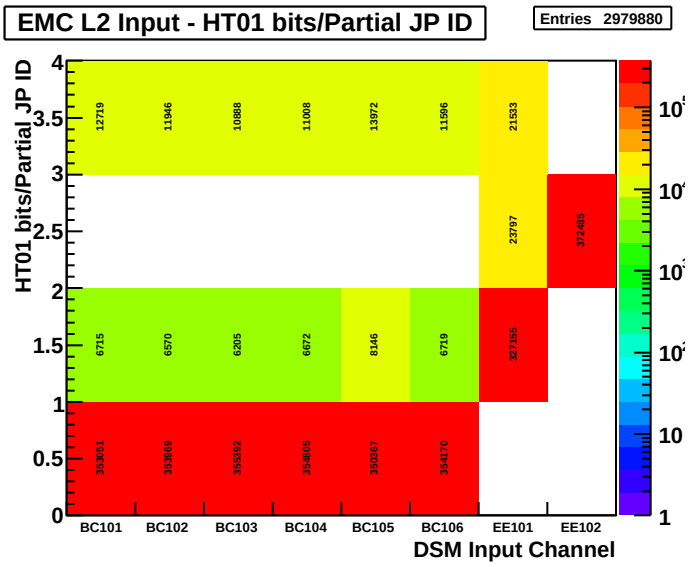

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

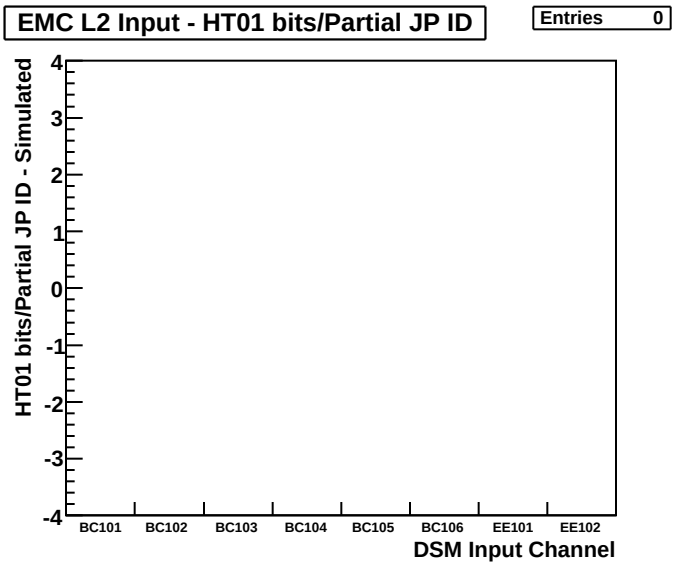

EMC L2 Input - JPZ/JPC bits
Entries 2979880
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
421 326 341 267 356 364 10
1369 1188 1230 1233 1456 1455 11
2354 2244 2338 2351 3353 2811 56
368341 368727 368576 368614 367320 367855 372408 372485

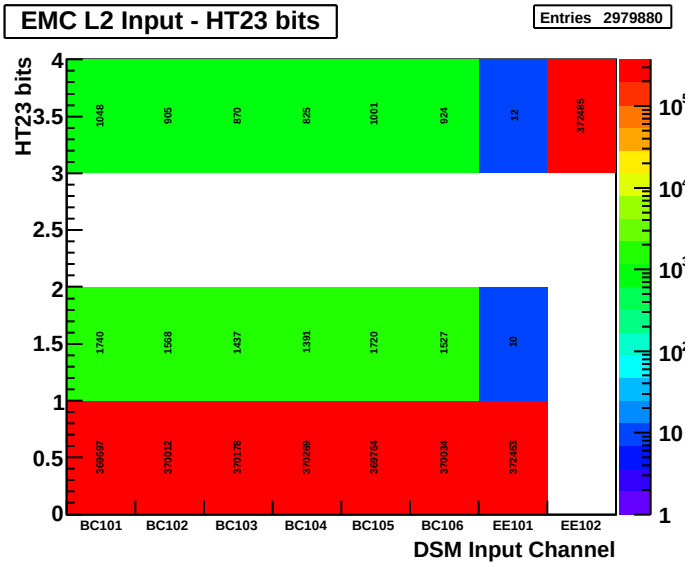

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

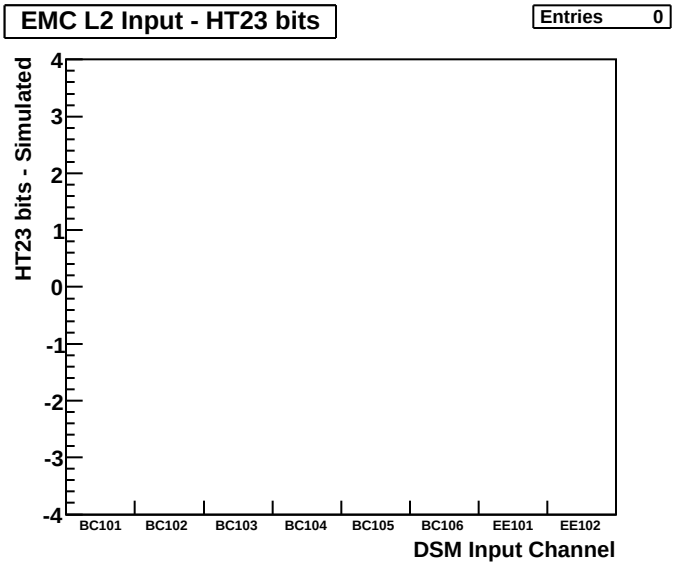

EMC L2 Input - Partial JP Sum
Entries 2979880
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

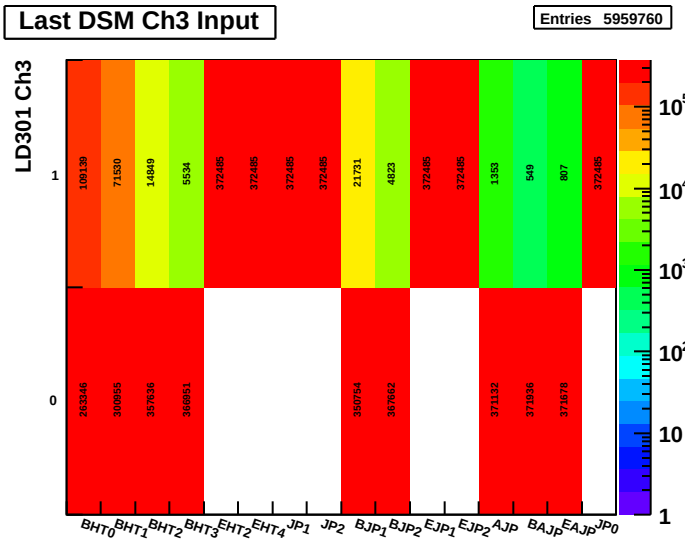

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

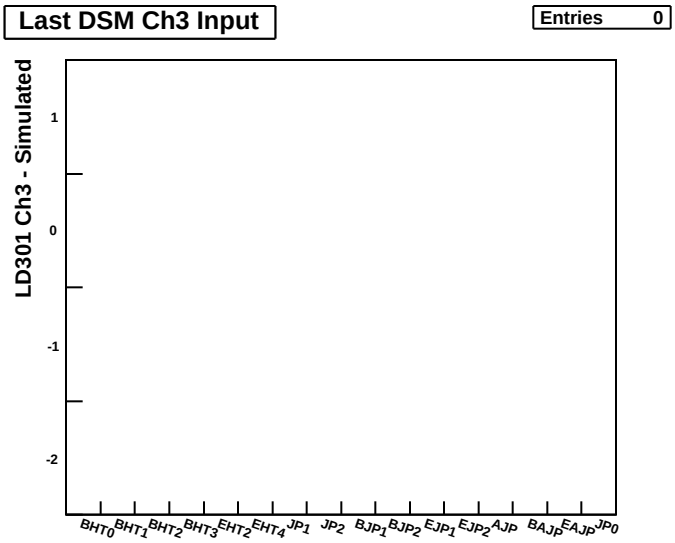

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2979880
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

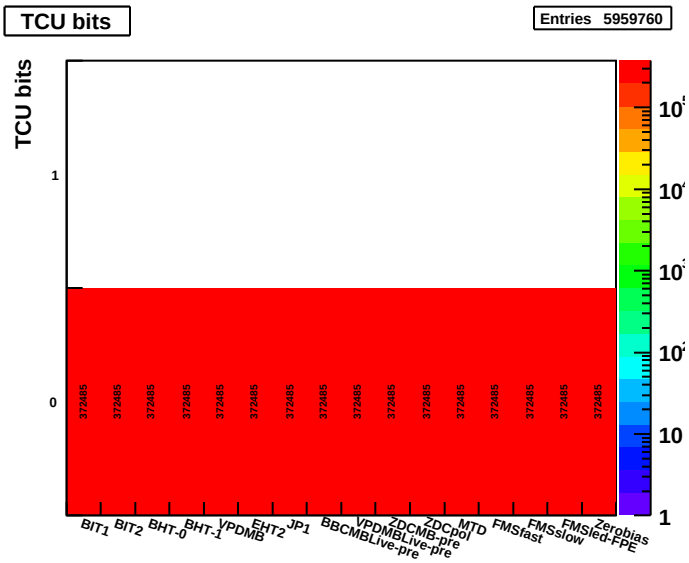

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

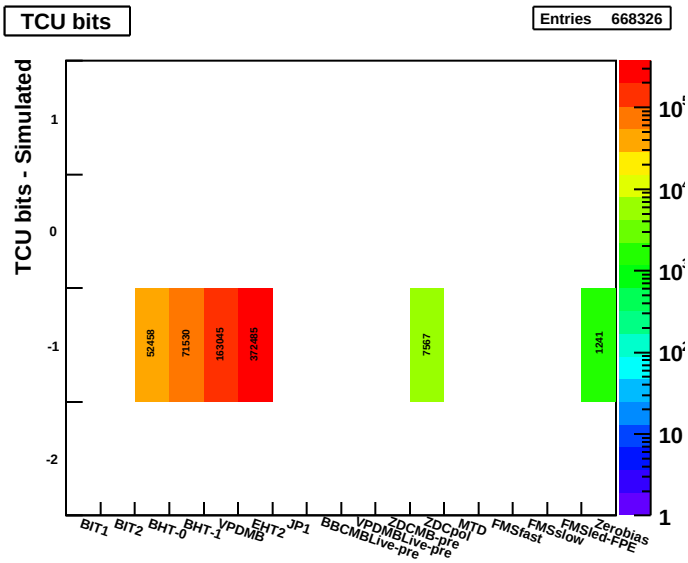

EMC L2 Input - HT23 bits
Entries 2979880
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

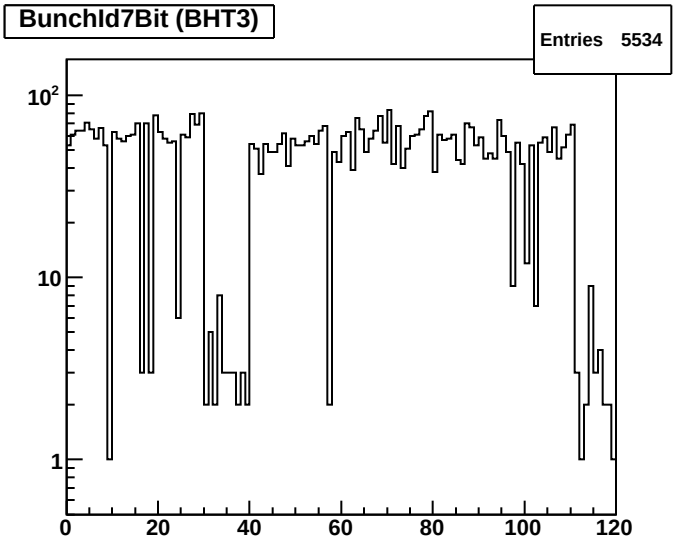

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

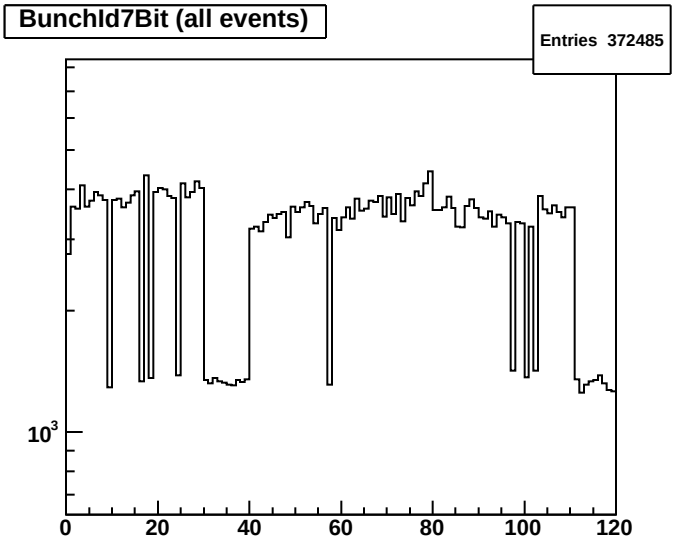

Last DSM Ch3 Input
Entries 5959760
LD301 Ch3
1
0
109139
71530
14849
5534
372485
372485
372485
372485
21731
4823
372485
372485
1353
549
807
372485
263346
300955
357636
366951
350754
367662
371132
371936
371678
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0
1

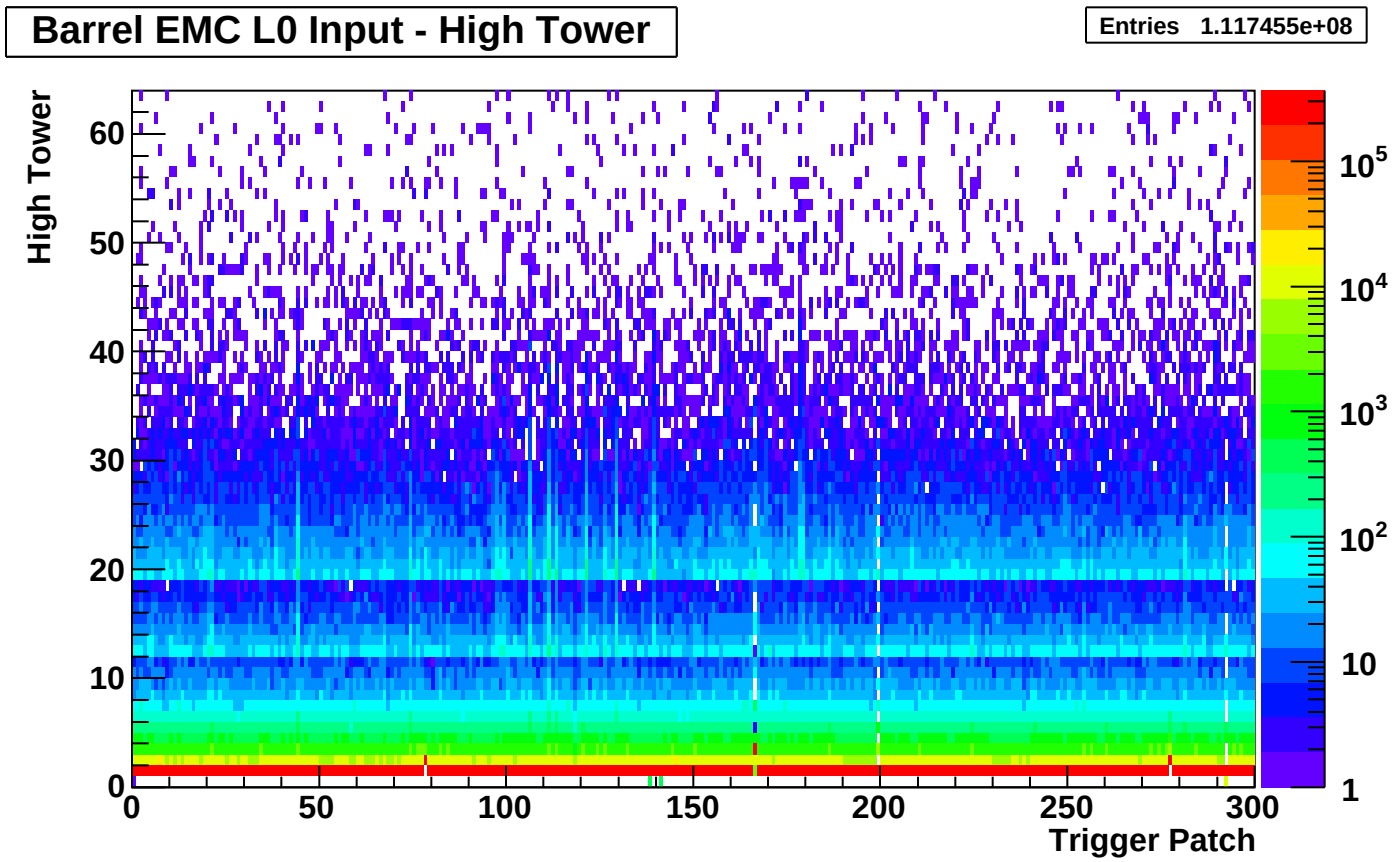

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

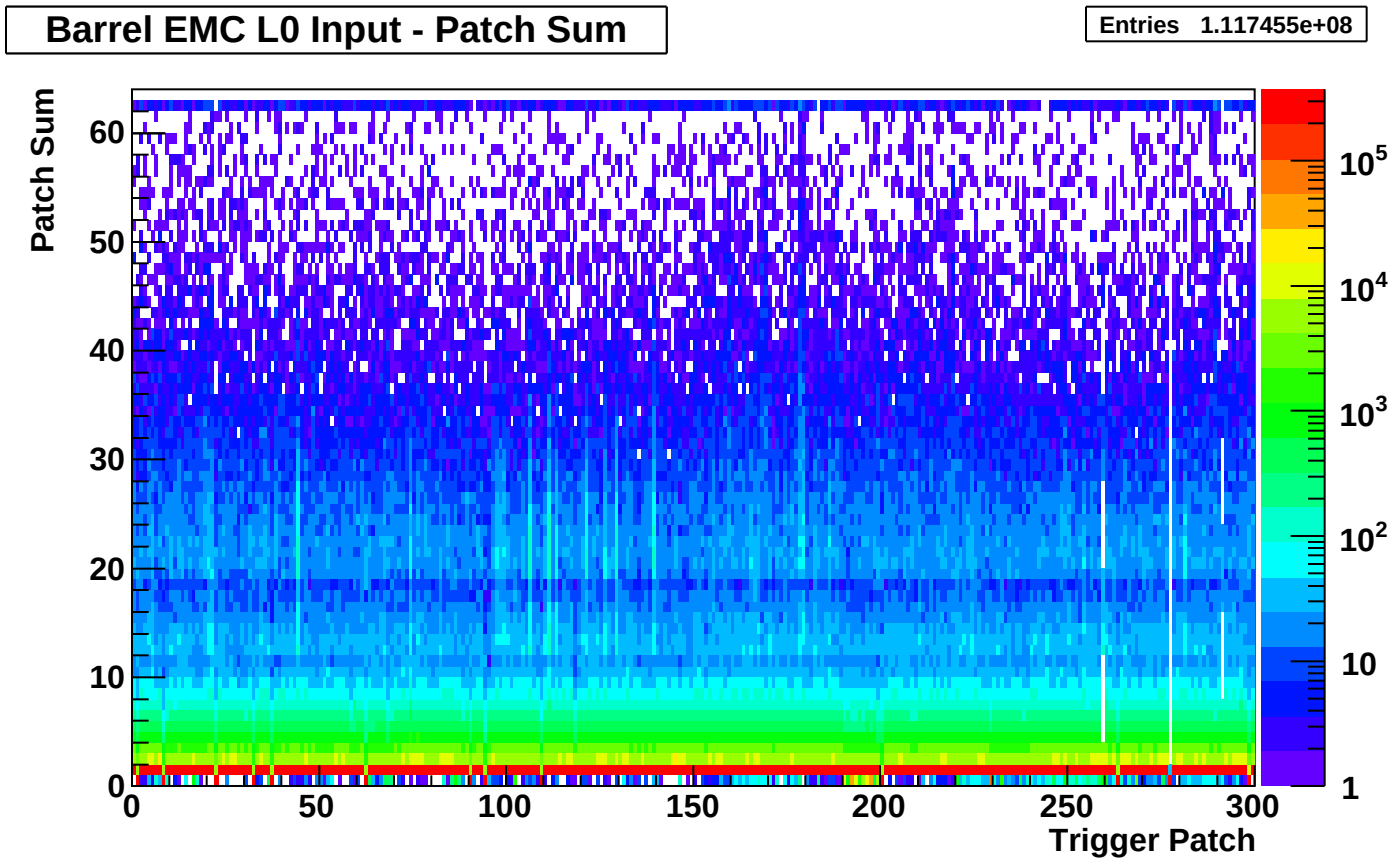

TCU bits
Entries 5959760
TCU bits
1
0
372485
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

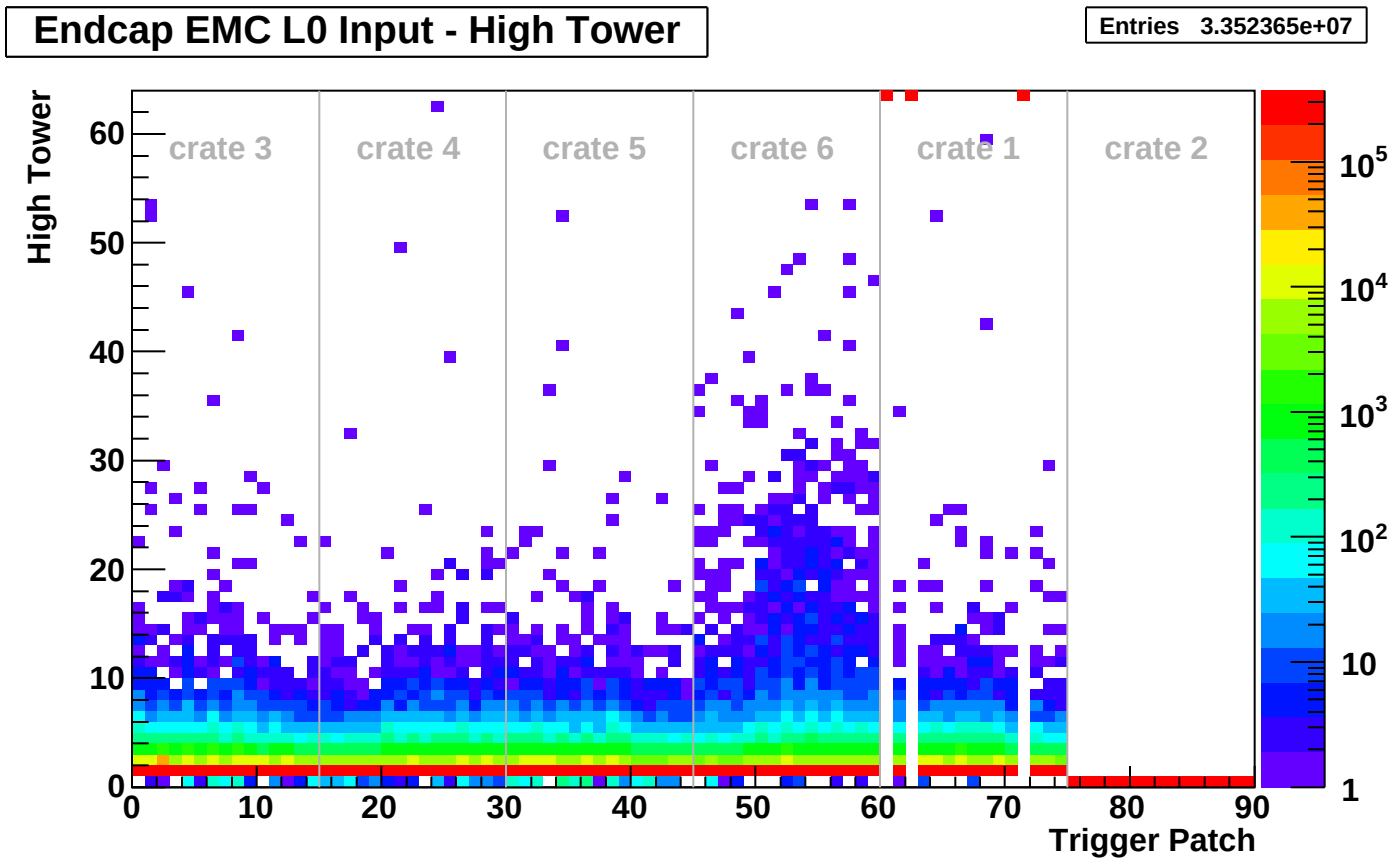

TCU bits
Entries 668326
TCU bits - Simulated
1
0
-1
-2
52458
71530
163045
372485
7567
1241
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

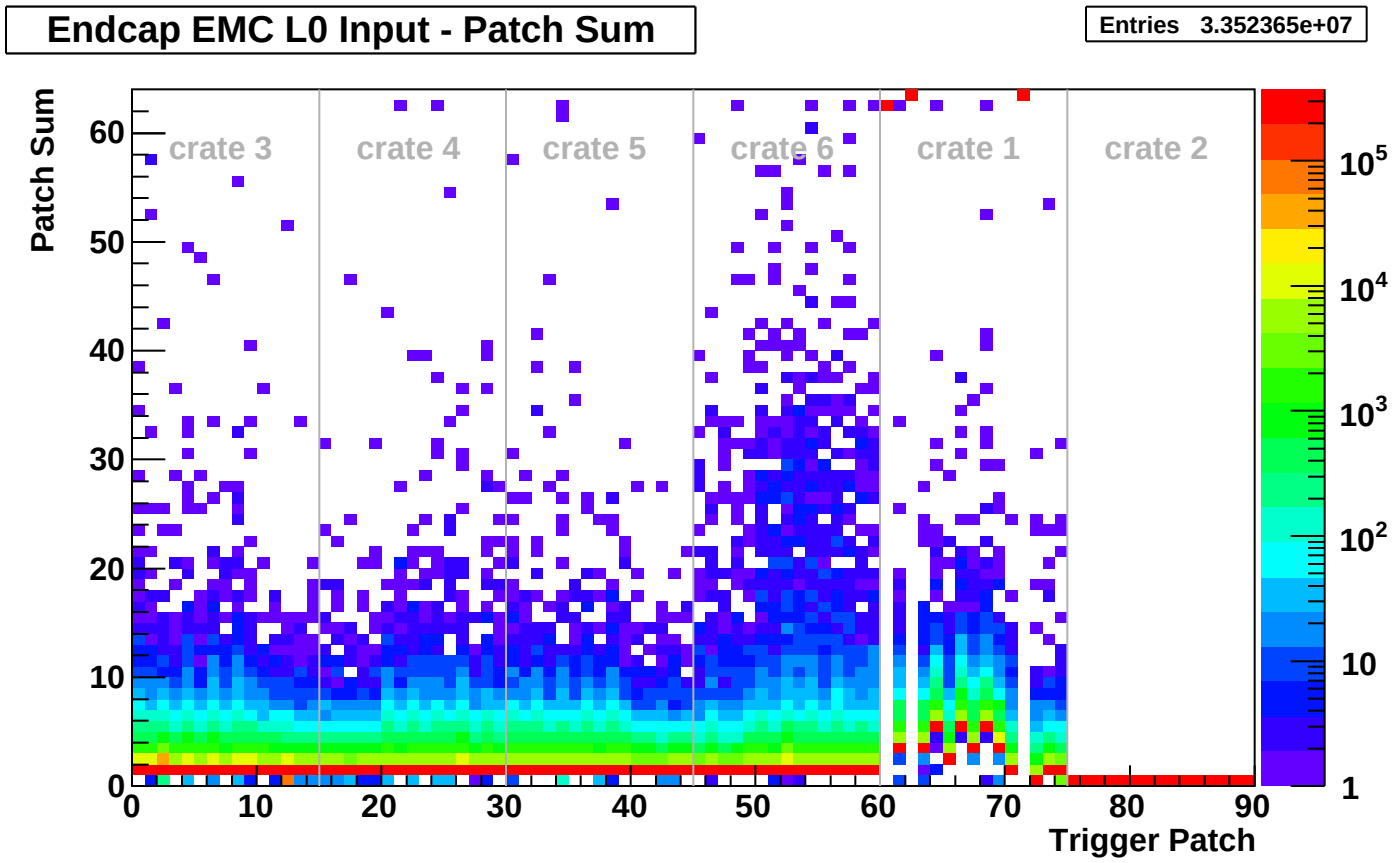

BunchId7Bit (BHT3)
Entries 5534
10^2
10
1
0
20
40
60
80
100
120

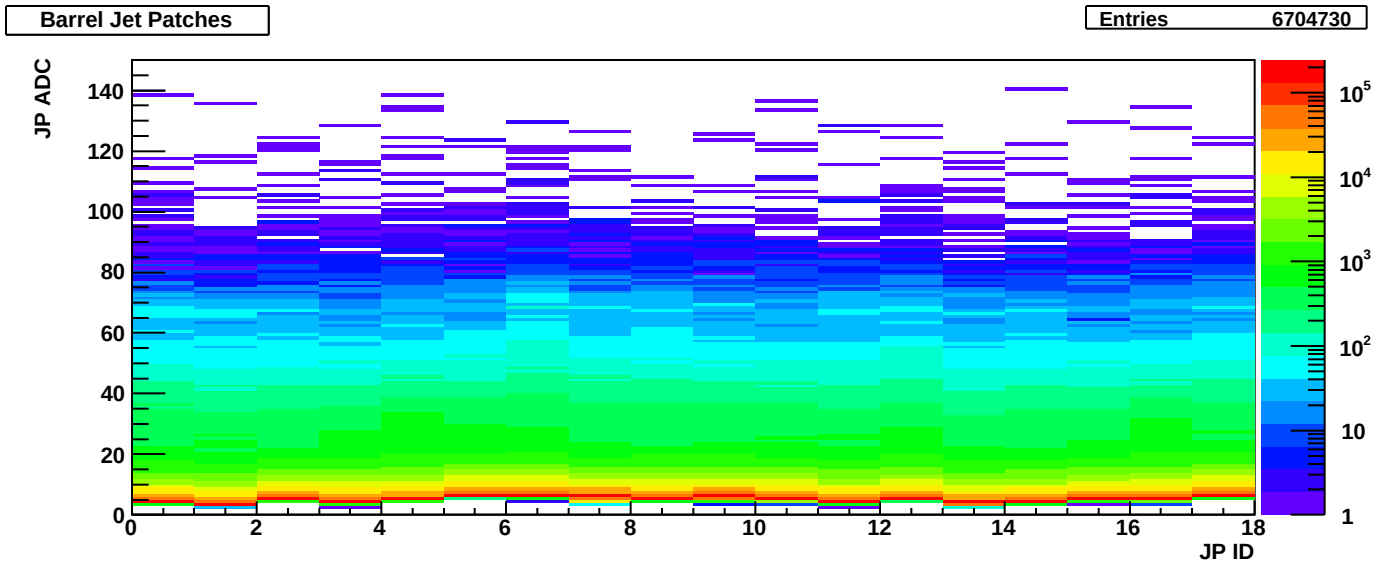

BunchId7Bit (all events)
Entries 372485
10^3
0
20
40
60
80
100
120

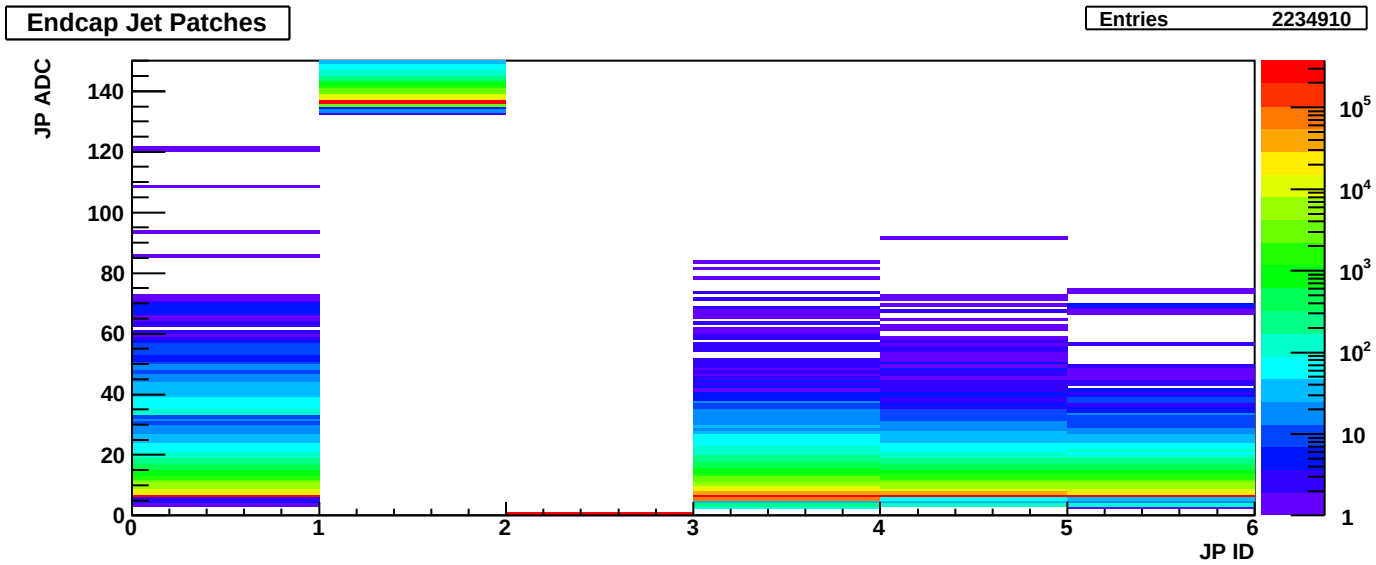

Barrel EMC L0 Input - High Tower
Entries 1.117455e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

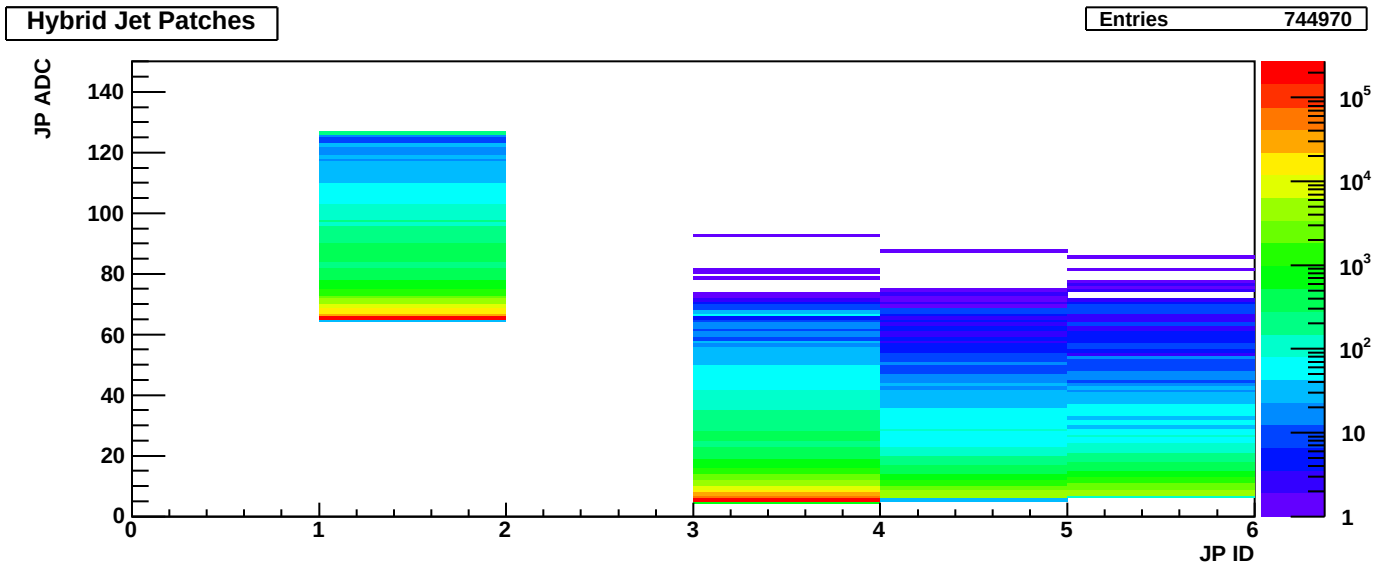

Barrel EMC L0 Input - Patch Sum
Entries 1.117455e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

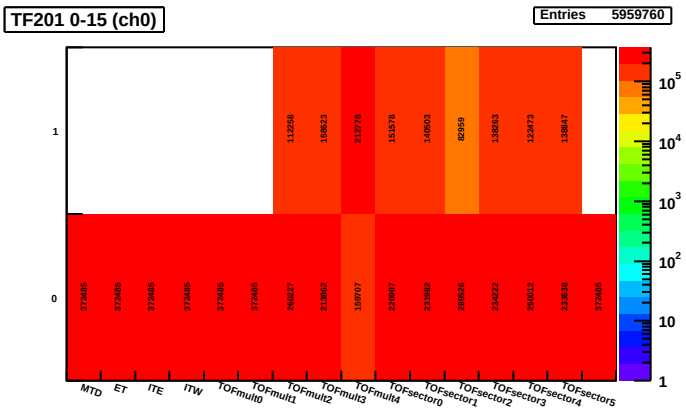

Endcap EMC L0 Input - High Tower
Entries 3.352365e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

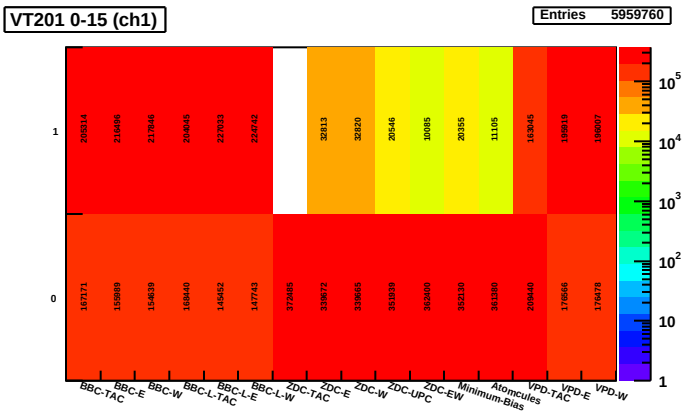

Endcap EMC L0 Input - Patch Sum
Entries 3.352365e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

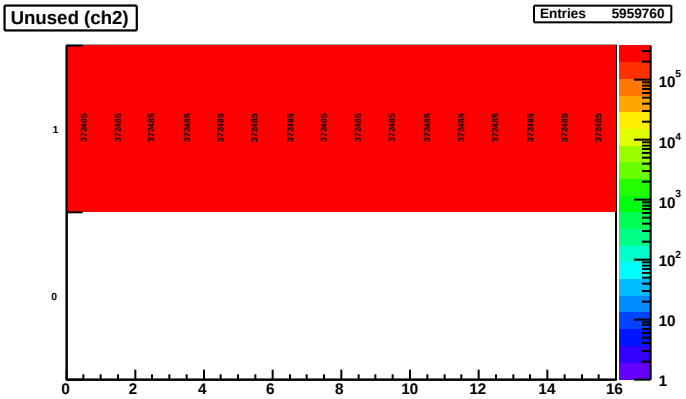

Barrel Jet Patches
Entries 6704730
JP ADC
JP ID

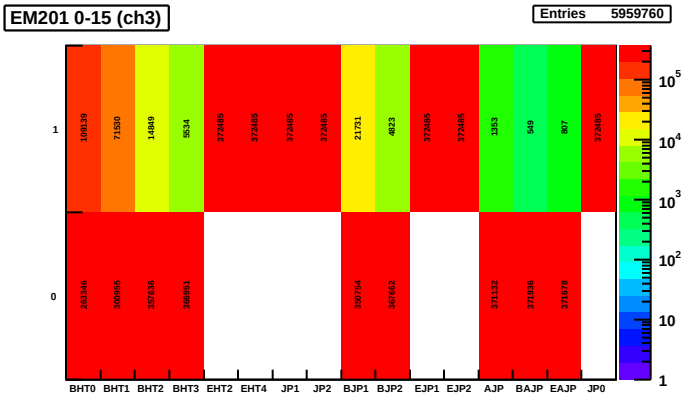

Endcap Jet Patches
Entries 2234910
JP ADC
JP ID

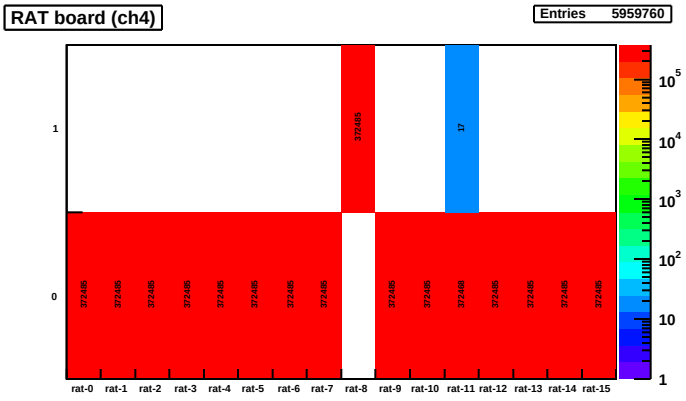

Hybrid Jet Patches
Entries 744970
JP ADC
JP ID

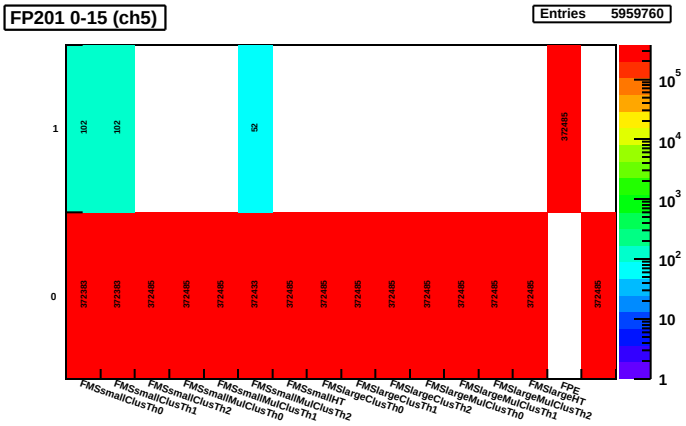

TF201 0-15 (ch0)
Entries 5959760
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

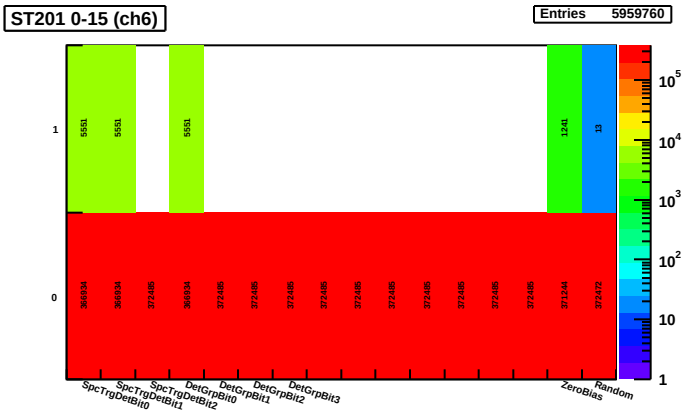

VT201 0-15 (ch1)
Entries 5959760
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

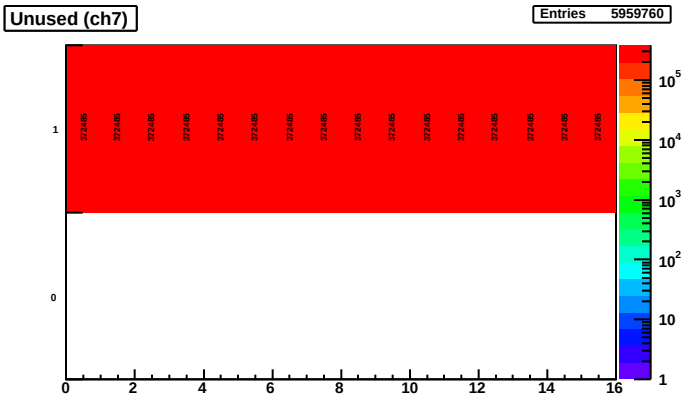

Unused (ch2)
Entries 5959760

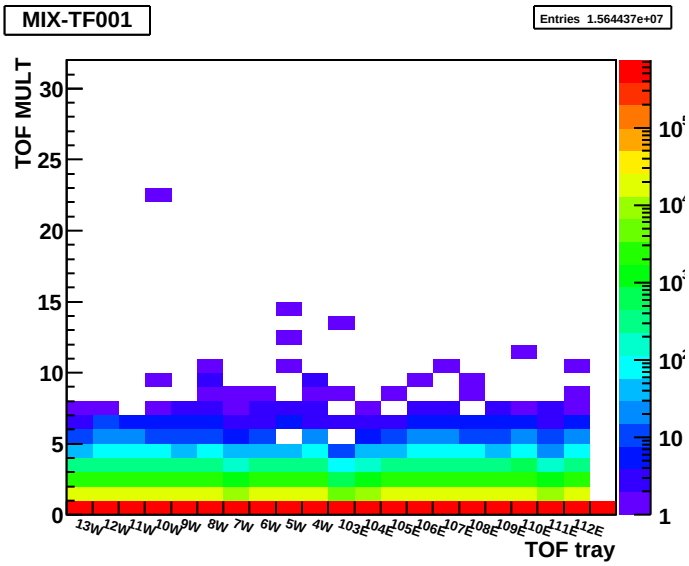

EM201 0-15 (ch3)
Entries 5959760
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

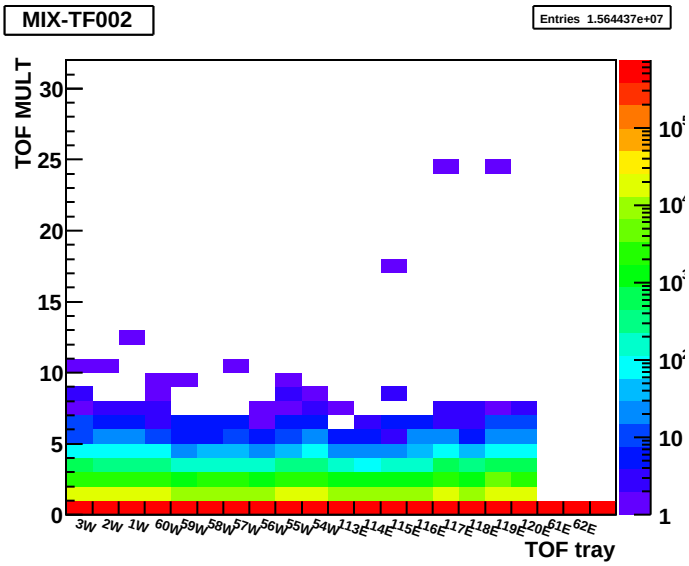

RAT board (ch4)
Entries 5959760
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

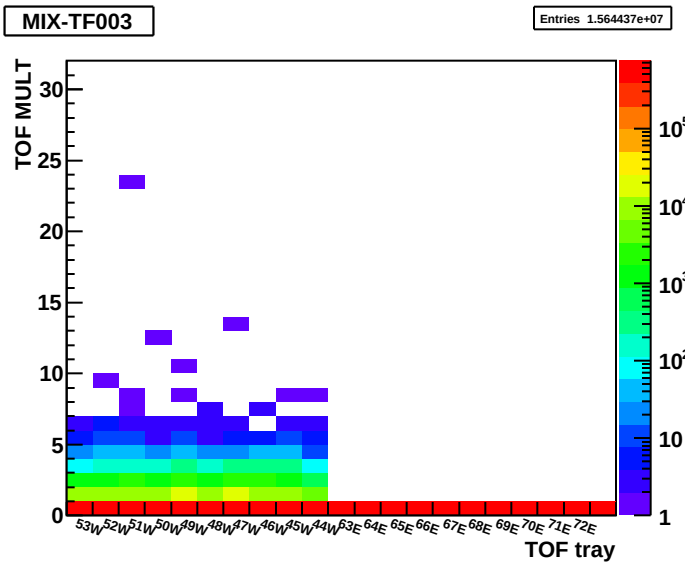

FP201 0-15 (ch5)
Entries 5959760
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

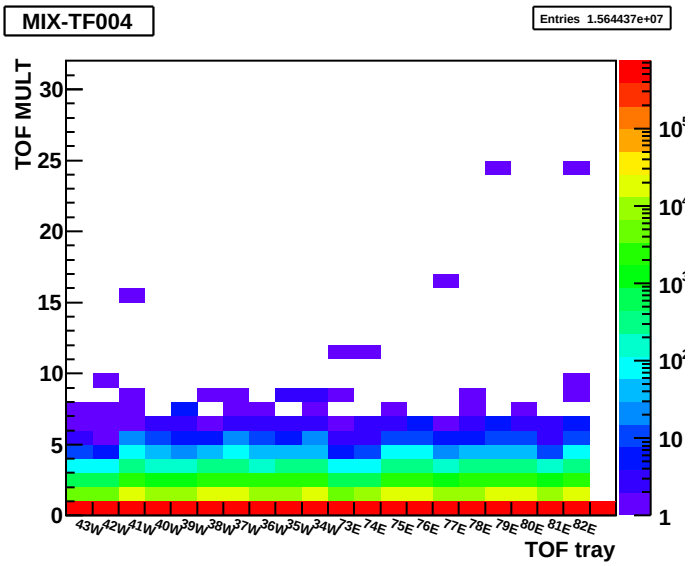

ST201 0-15 (ch6)
Entries 5959760
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

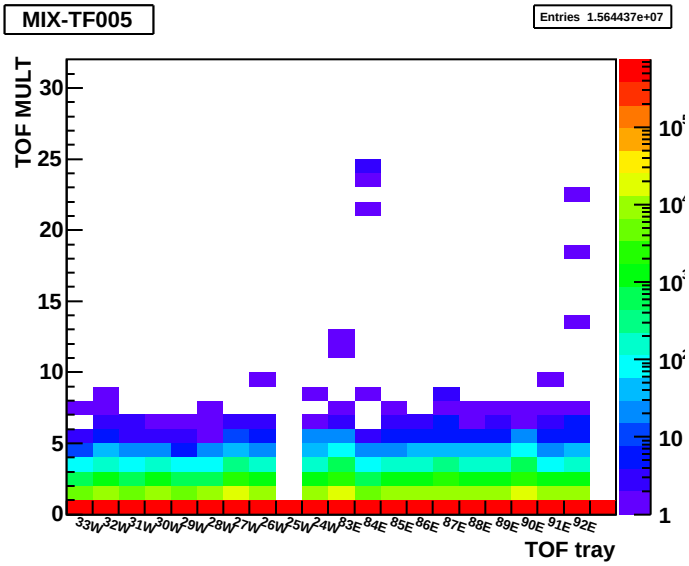

Unused (ch7)
Entries 5959760

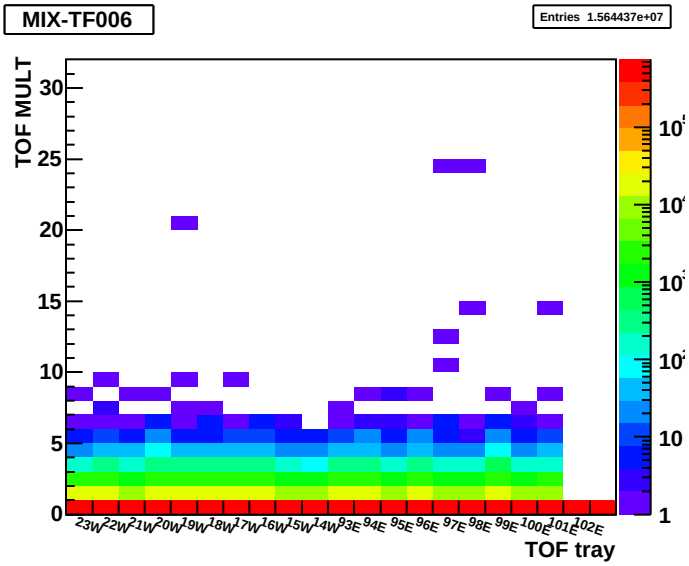

MIX-TF001
Entries 1.564437e+07
TOF MULT
TOF tray

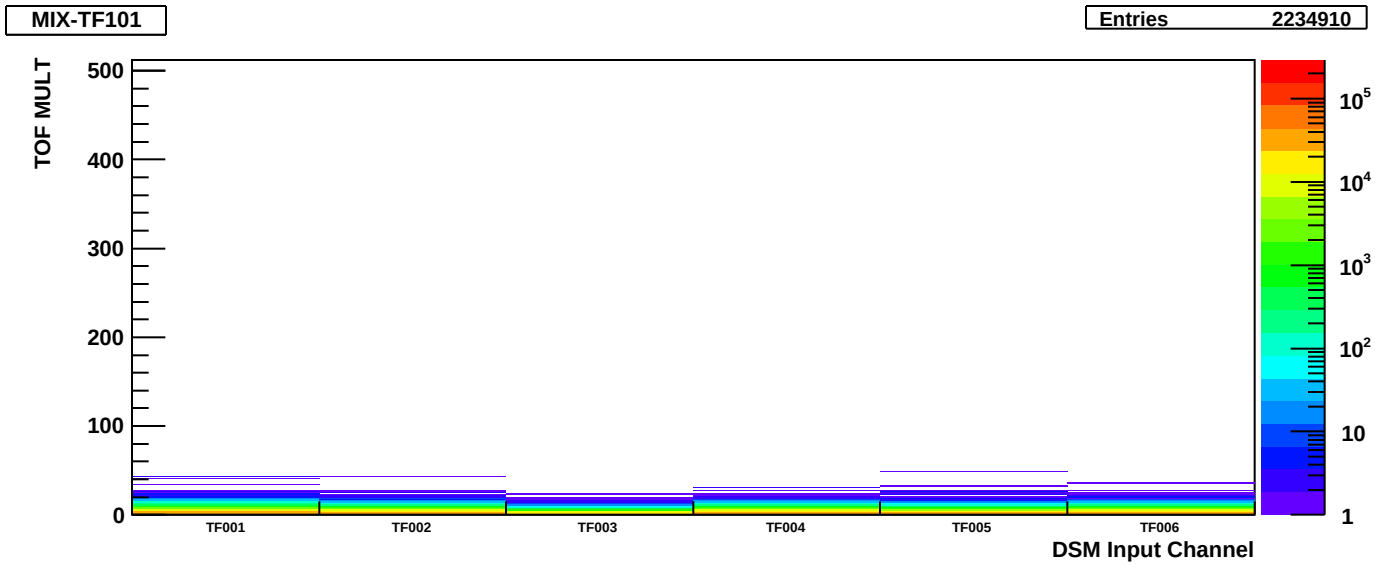

MIX-TF002
Entries 1.564437e+07
TOF MULT
TOF tray

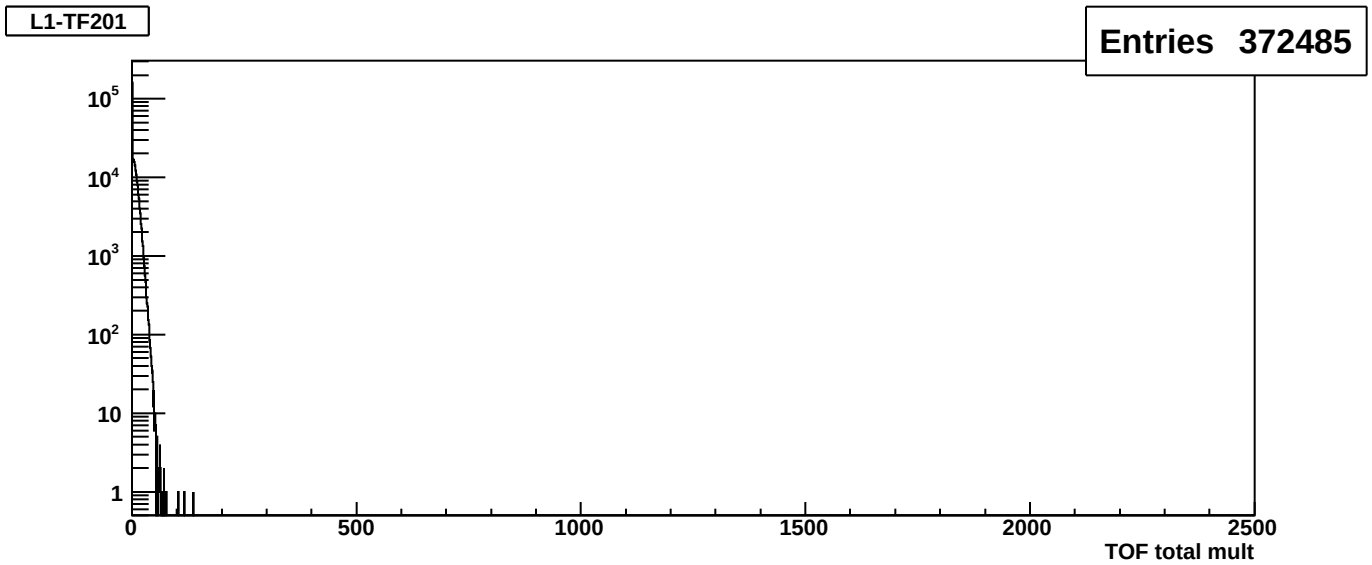

MIX-TF003
Entries 1.564437e+07
TOF MULT
TOF tray

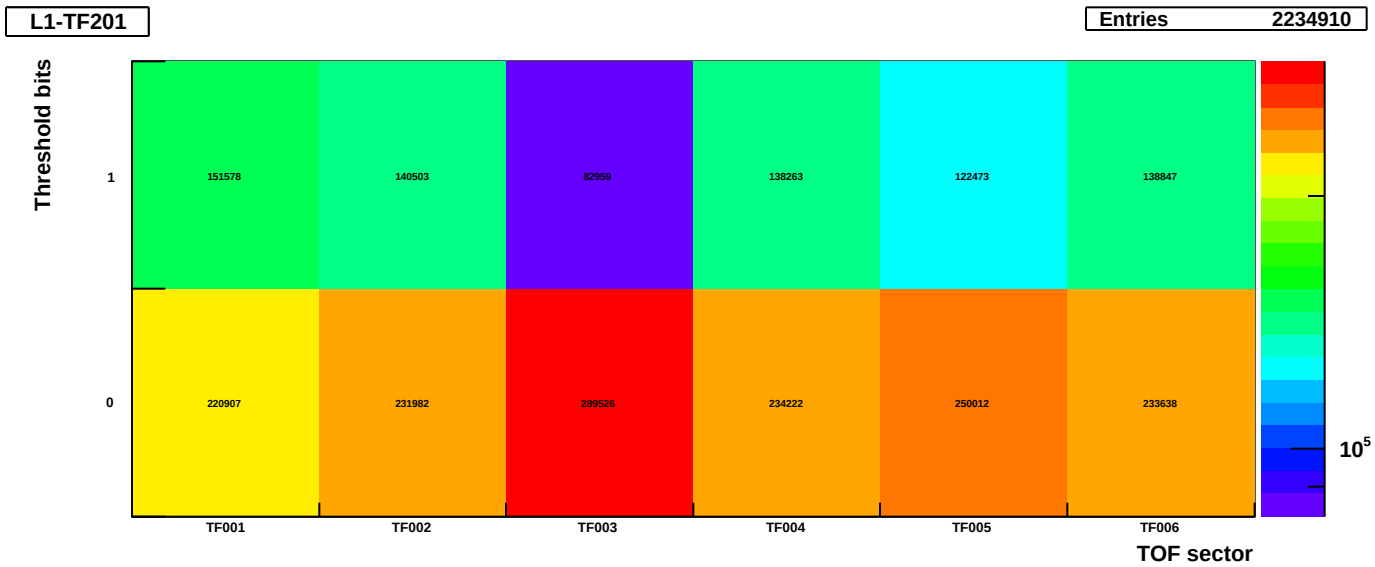

MIX-TF004
Entries 1.564437e+07
TOF MULT
TOF tray

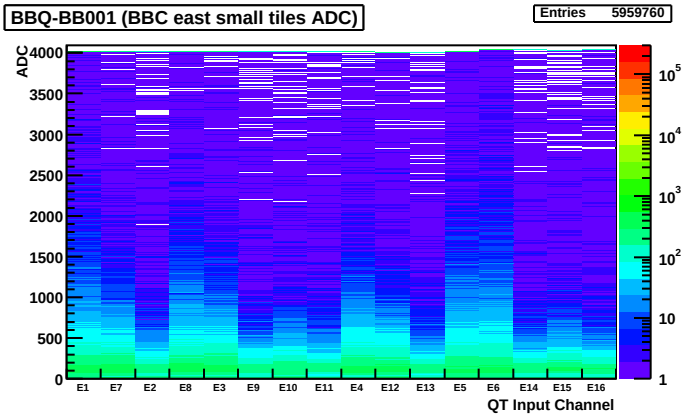

MIX-TF005
Entries 1.564437e+07
TOF MULT
TOF tray

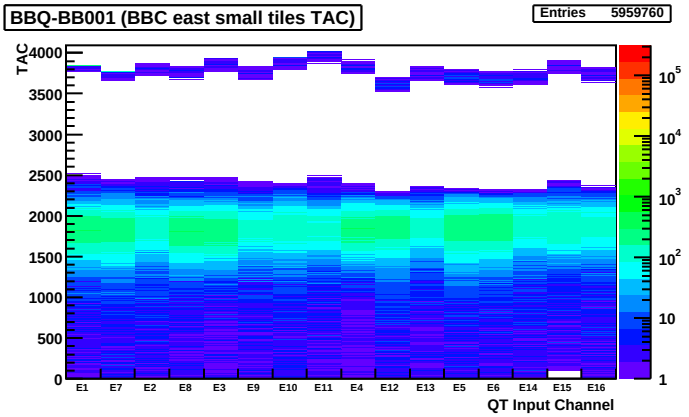

MIX-TF006
Entries 1.564437e+07
TOF MULT
TOF tray

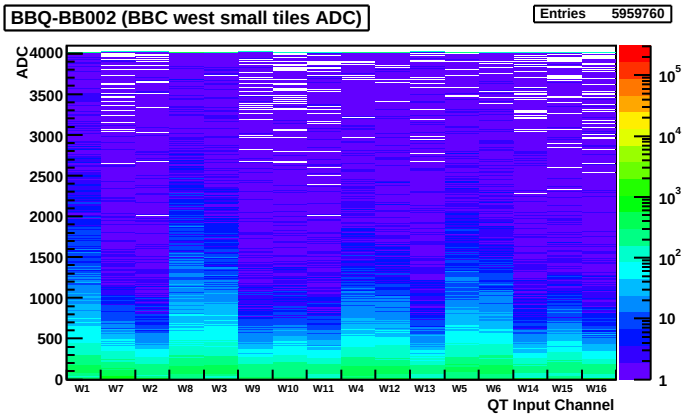

MIX-TF101
Entries 2234910
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

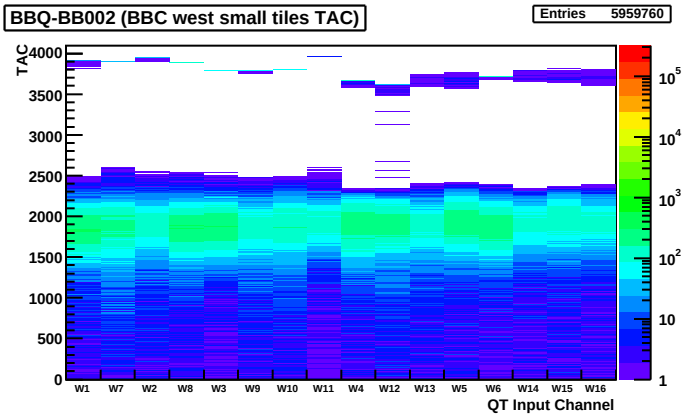

L1-TF201
Entries 372485
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

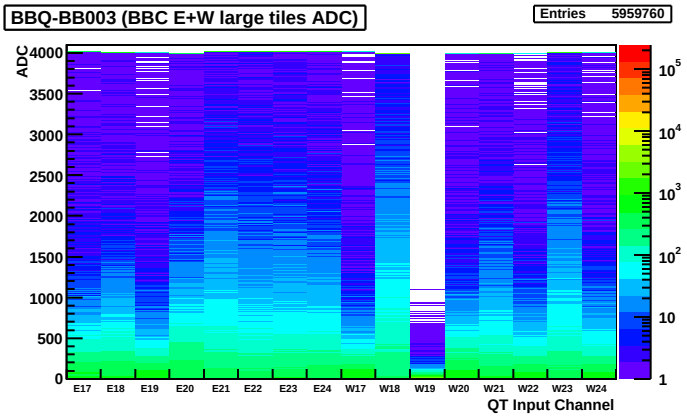

L1-TF201
Entries 2234910
Threshold bits
1
0
151578
140503
82959
138263
122473
138847
220907
231982
289526
234222
250012
233638
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

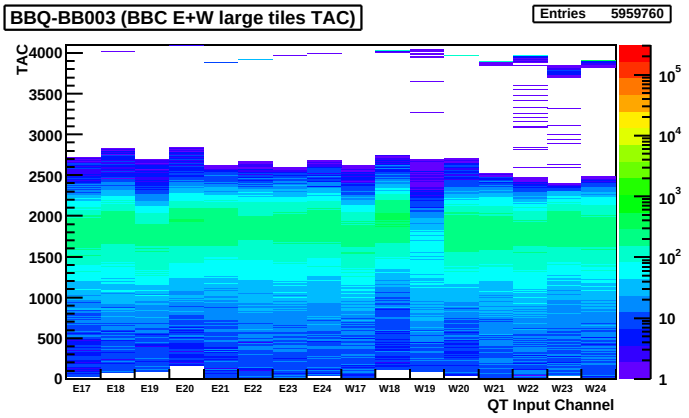

BBQ-BB001 (BBC east small tiles ADC)
Entries 5959760
ADC
QT Input Channel

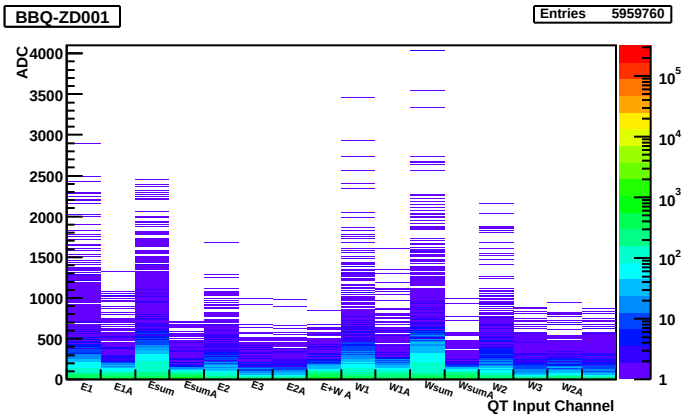

BBQ-BB001 (BBC east small tiles TAC)
Entries 5959760
TAC
QT Input Channel

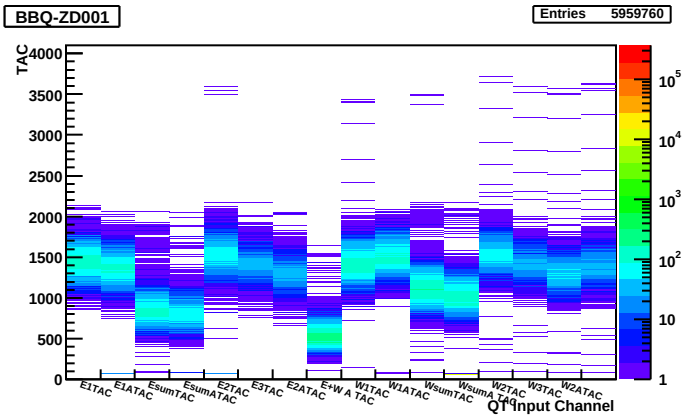

BBQ-BB002 (BBC west small tiles ADC)
Entries 5959760
ADC
QT Input Channel

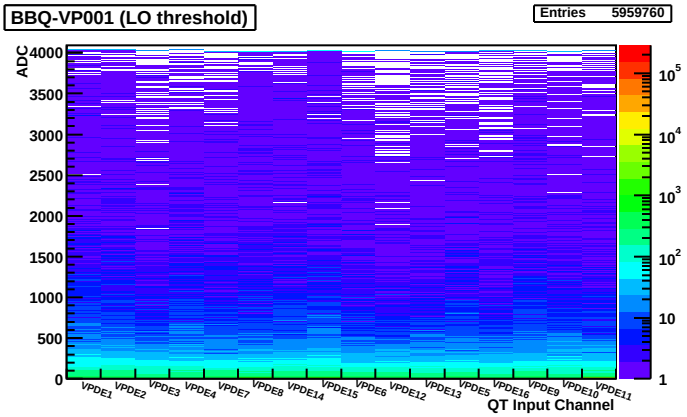

BBQ-BB002 (BBC west small tiles TAC)
Entries 5959760
TAC
QT Input Channel

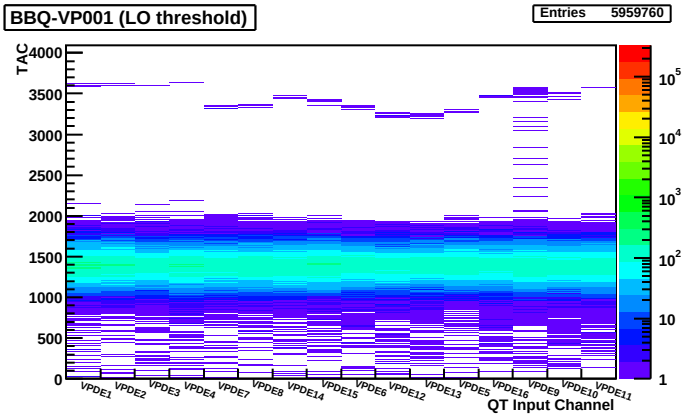

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5959760
ADC
QT Input Channel

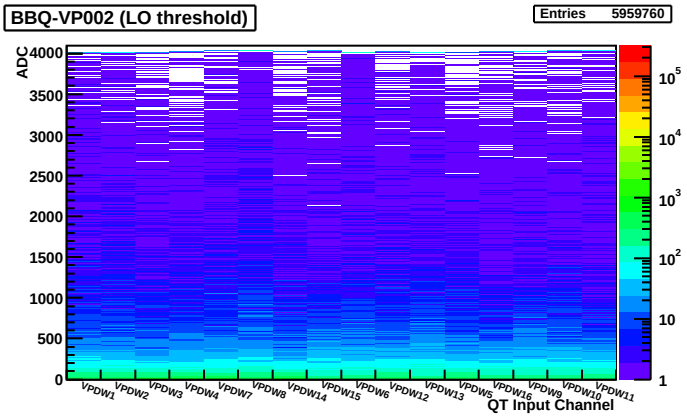

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5959760
TAC
QT Input Channel

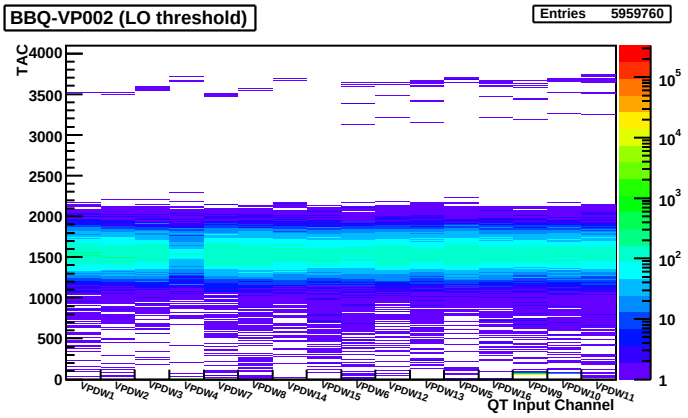

BBQ-ZD001
Entries 5959760
ADC
QT Input Channel

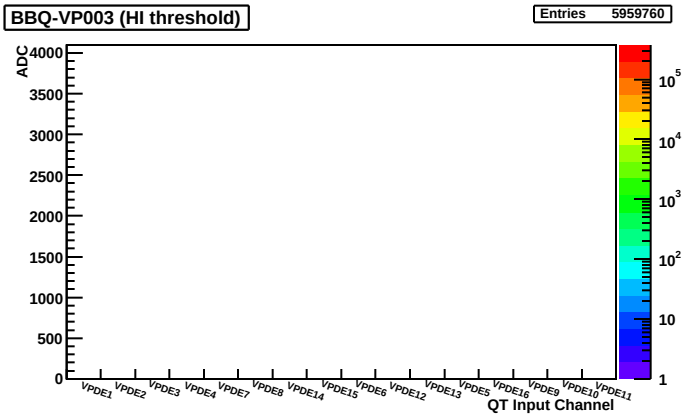

BBQ-ZD001
Entries 5959760
TAC
QT Input Channel

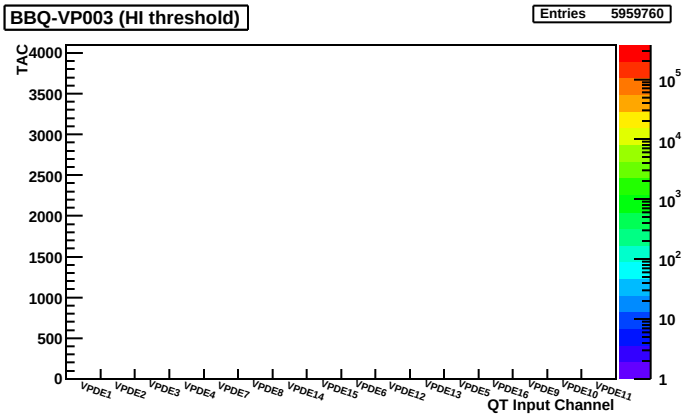

BBQ-VP001 (LO threshold)
Entries 5959760
ADC
QT Input Channel

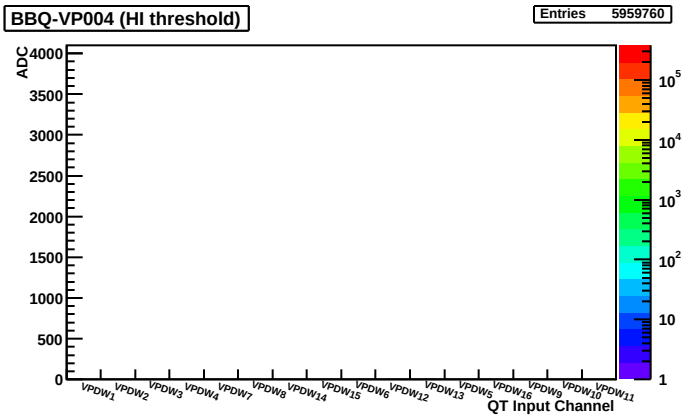

BBQ-VP001 (LO threshold)
Entries 5959760
TAC
QT Input Channel

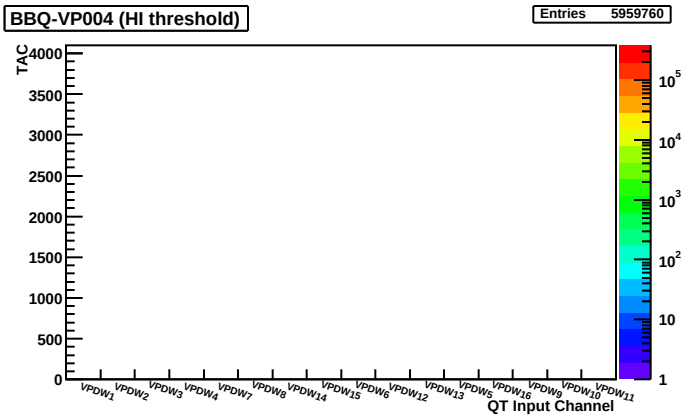

BBQ-VP002 (LO threshold)
Entries 5959760
ADC
QT Input Channel

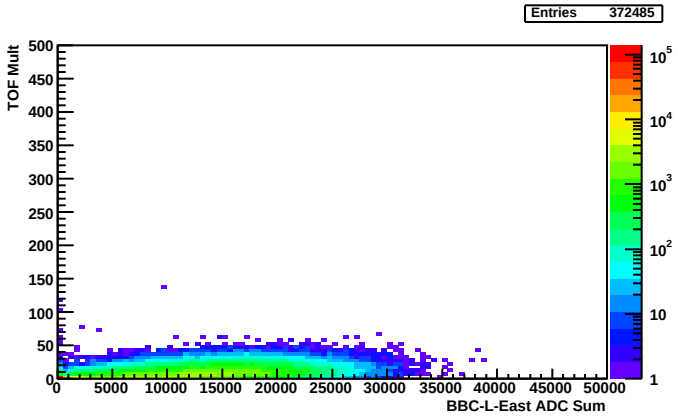

BBQ-VP002 (LO threshold)
Entries 5959760
TAC
QT Input Channel

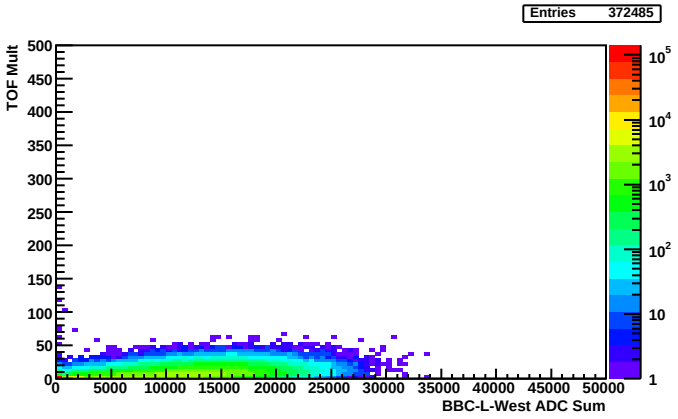

BBQ-VP003 (HI threshold)
Entries 5959760
ADC
QT Input Channel

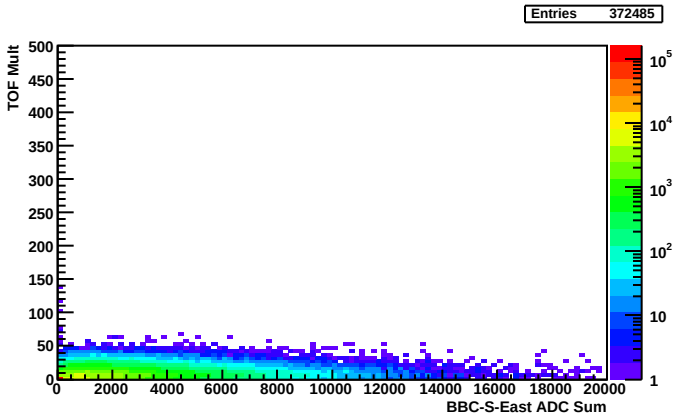

BBQ-VP003 (HI threshold)
Entries 5959760
TAC
QT Input Channel

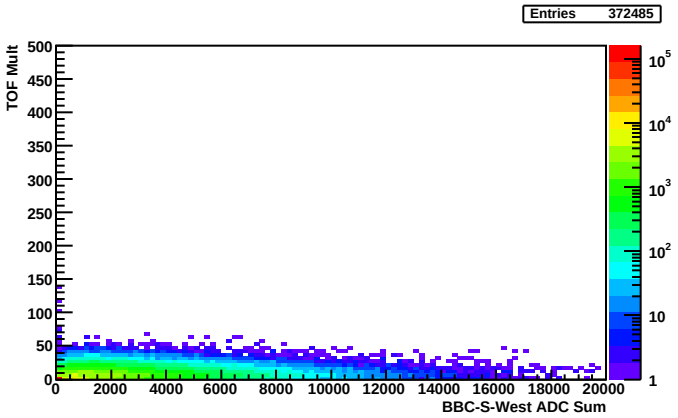

BBQ-VP004 (HI threshold)
Entries 5959760
ADC
QT Input Channel

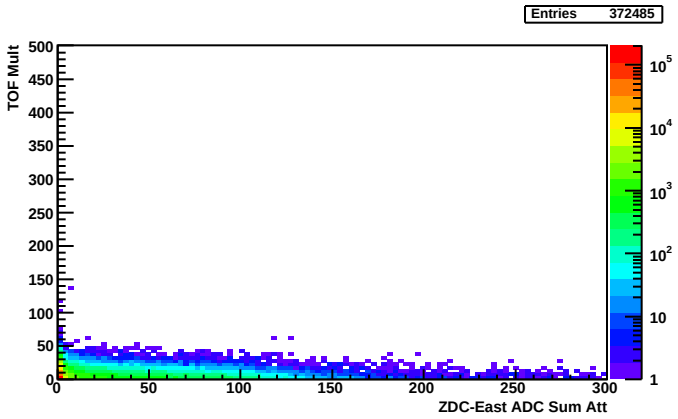

BBQ-VP004 (HI threshold)
Entries 5959760
TAC
QT Input Channel

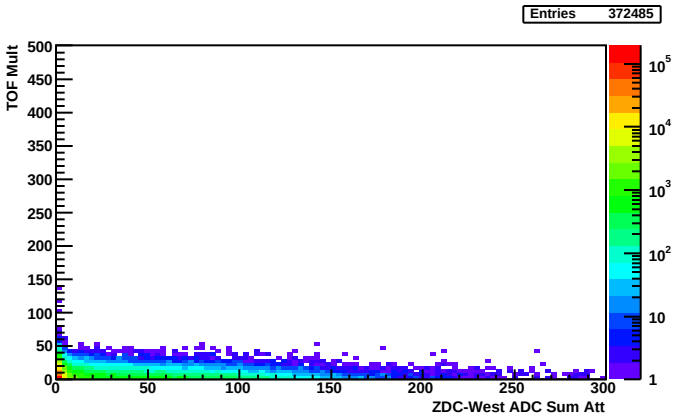

Entries 372485
TOF Mult
BBC-L-East ADC Sum

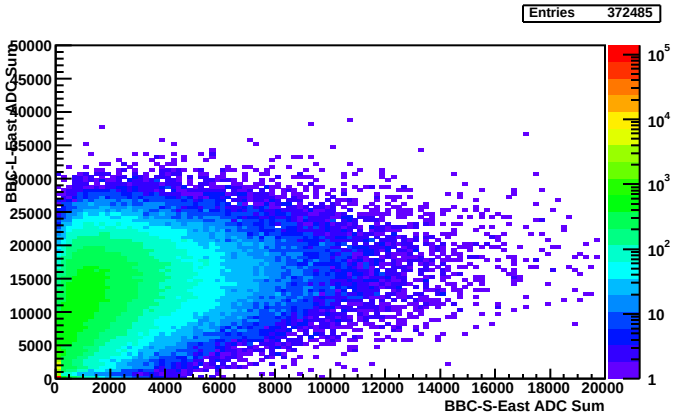

Entries 372485
TOF Mult
BBC-L-West ADC Sum

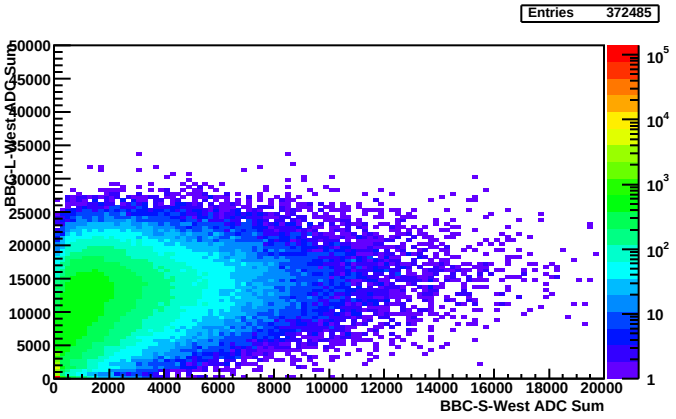

Entries 372485
TOF Mult
BBC-S-East ADC Sum

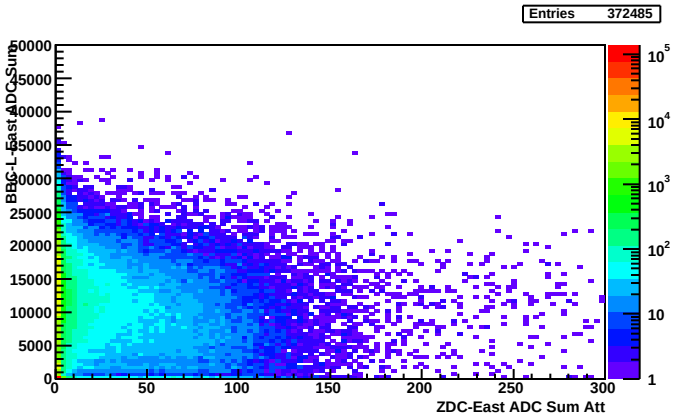

Entries 372485
TOF Mult
BBC-S-West ADC Sum

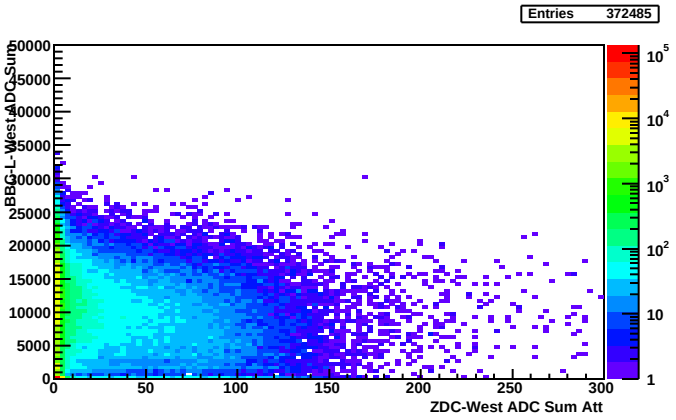

Entries 372485
TOF Mult
ZDC-East ADC Sum Att

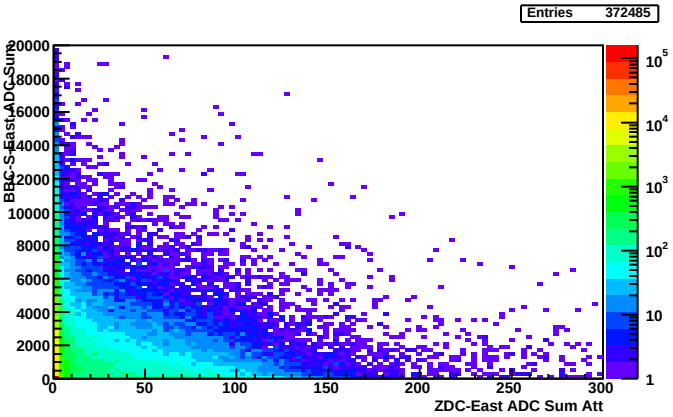

Entries 372485
TOF Mult
ZDC-West ADC Sum Att

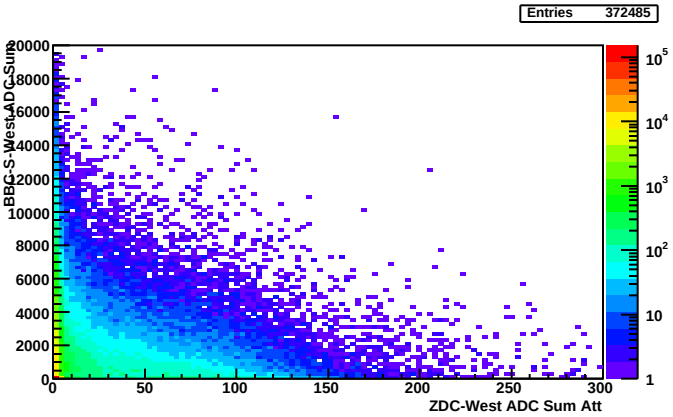

Entries 372485
BBC-L-East ADC Sum
BBC-S-East ADC Sum

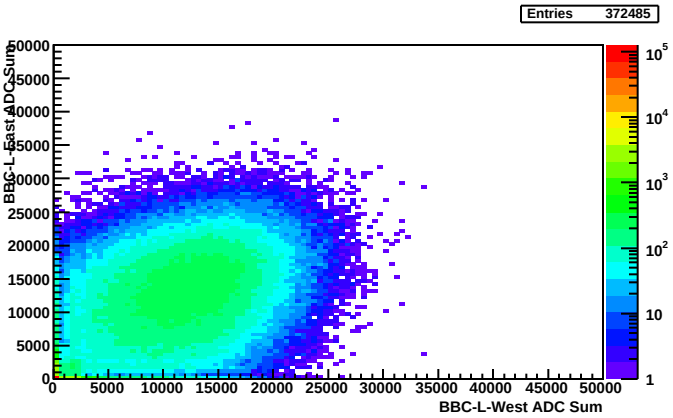

Entries 372485
BBC-L-West ADC Sum
BBC-S-West ADC Sum

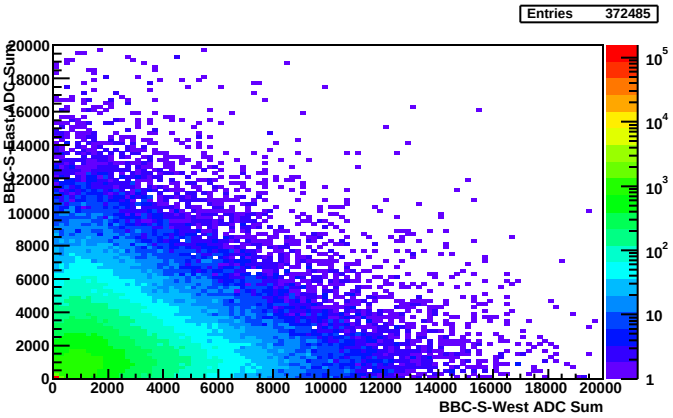

Entries 372485
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

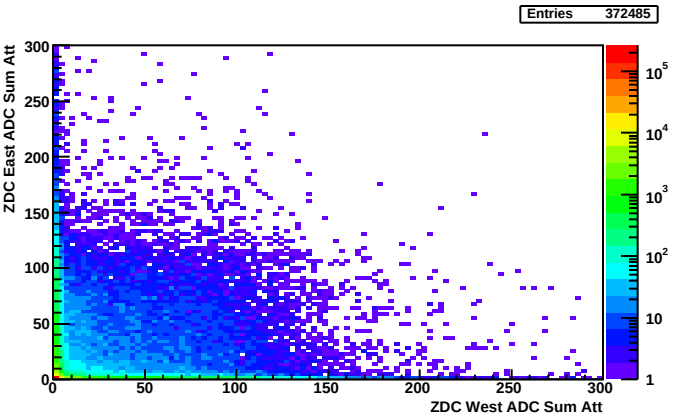

Entries 372485
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

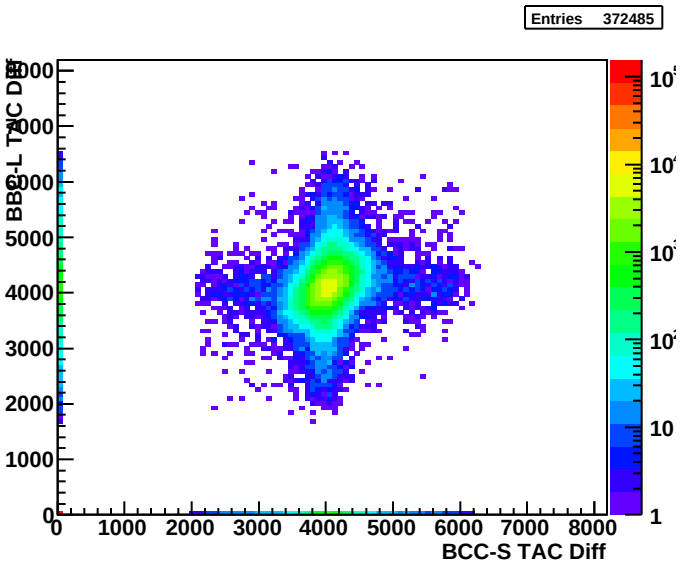

Entries 372485
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

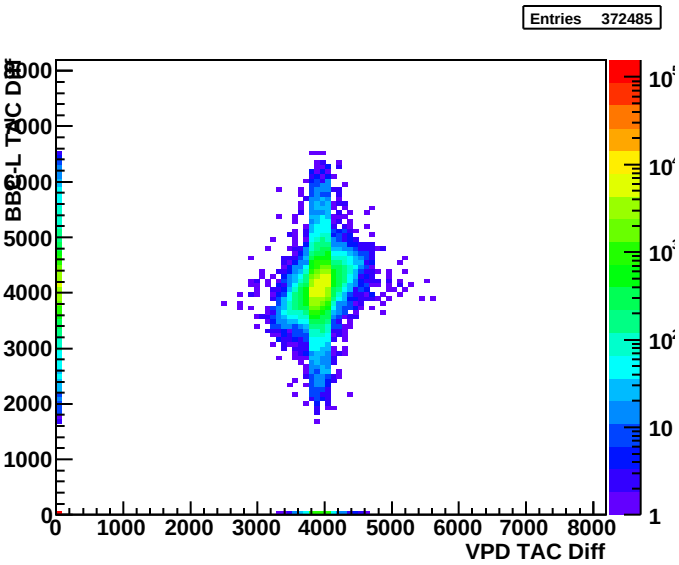

Entries 372485
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

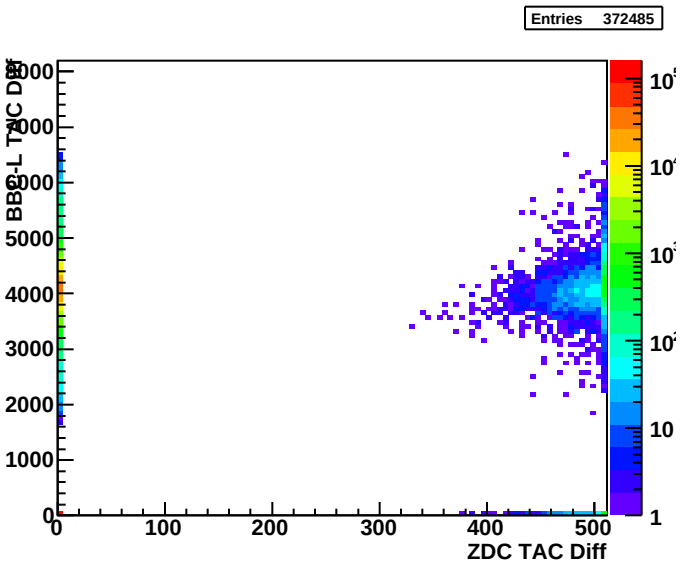

Entries 372485
BBC-L-East ADC Sum
BBC-L-West ADC Sum

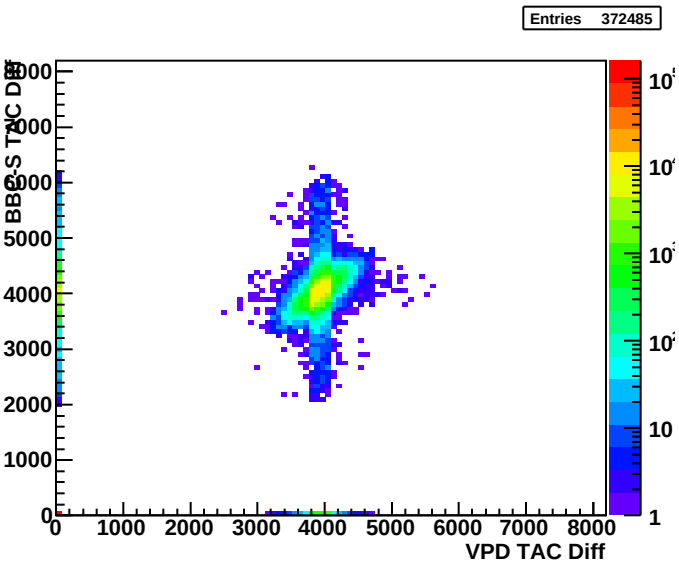

Entries 372485
BBC-S-East ADC Sum
BBC-S-West ADC Sum

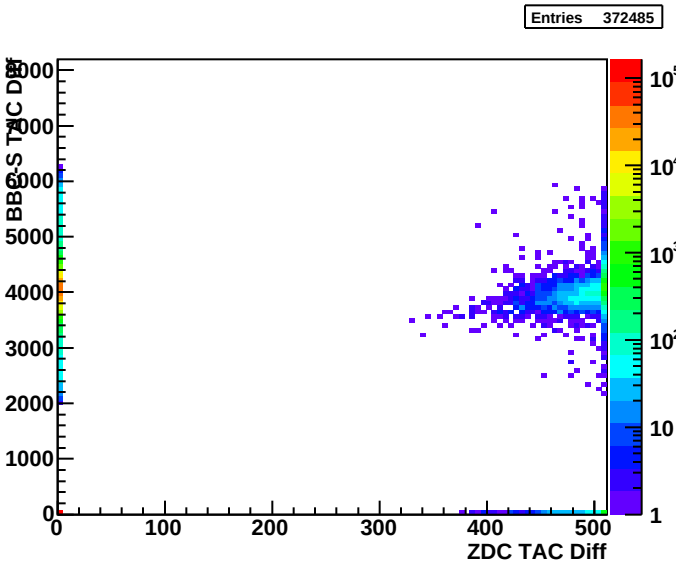

Entries 372485
ZDC East ADC Sum Att
ZDC West ADC Sum Att

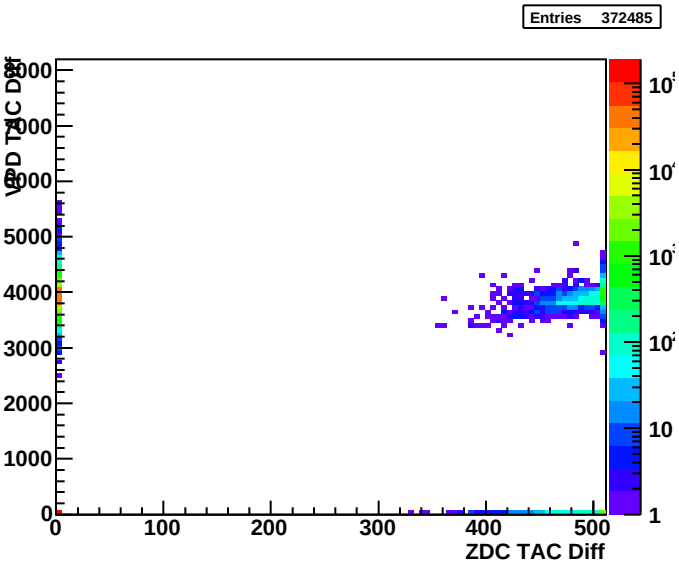

Entries 372485
BBC-L TAC Diff
BCC-S TAC Diff

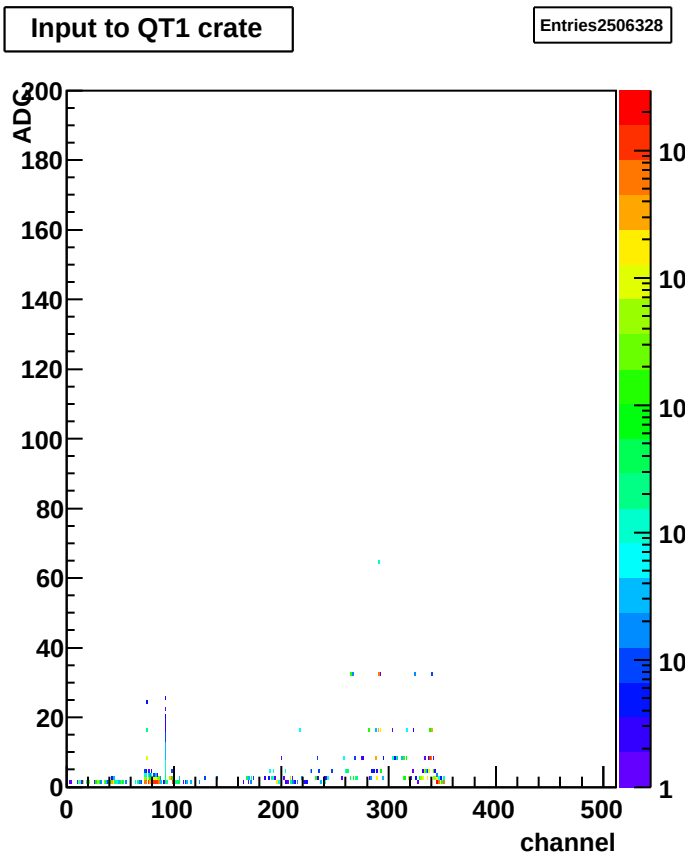

Entries 372485
BBC-L TAC Diff
VPD TAC Diff

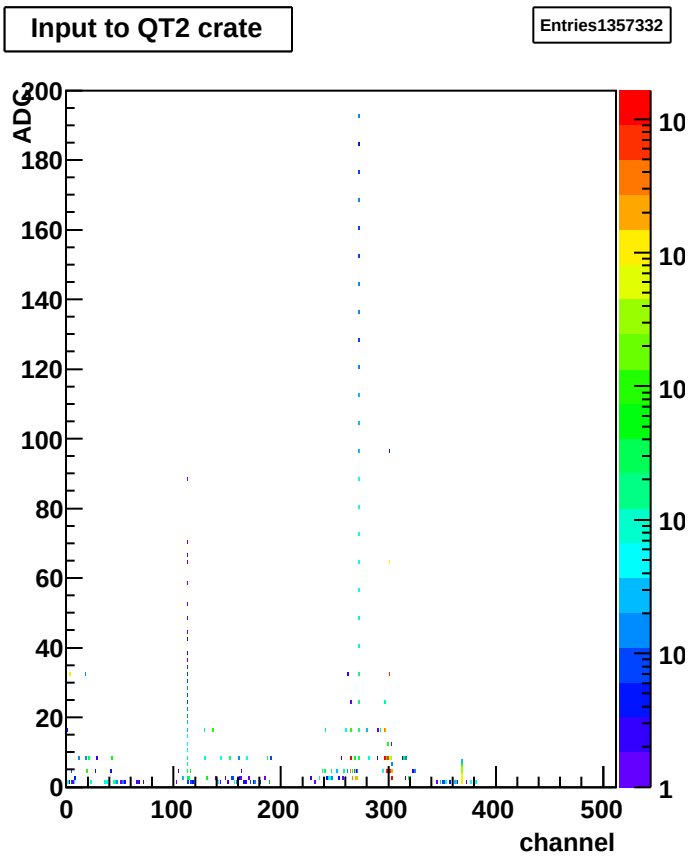

Entries 372485
BBC-L TAC Diff
ZDC TAC Diff

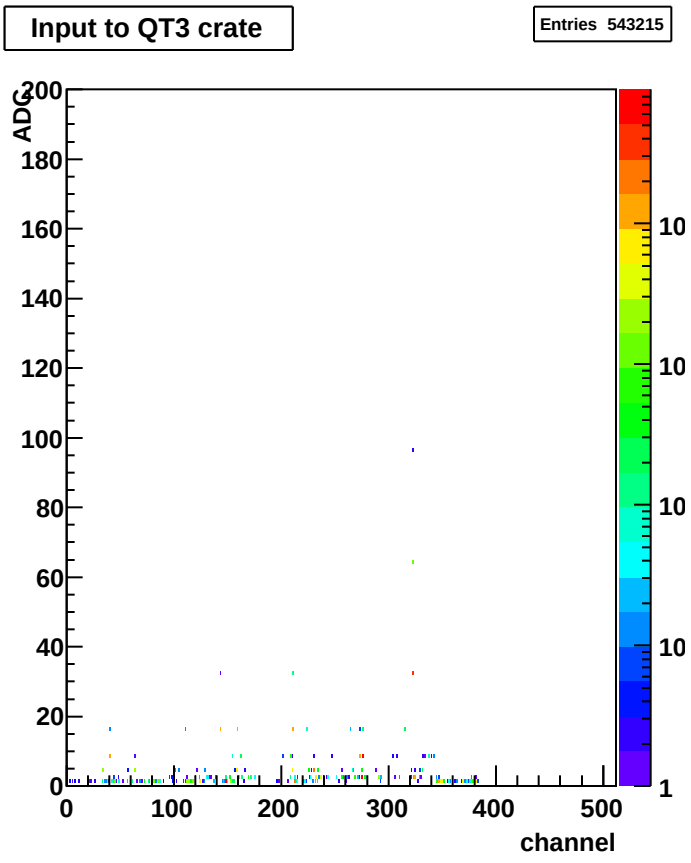

Entries 372485
BBC-S TAC Diff
VPD TAC Diff

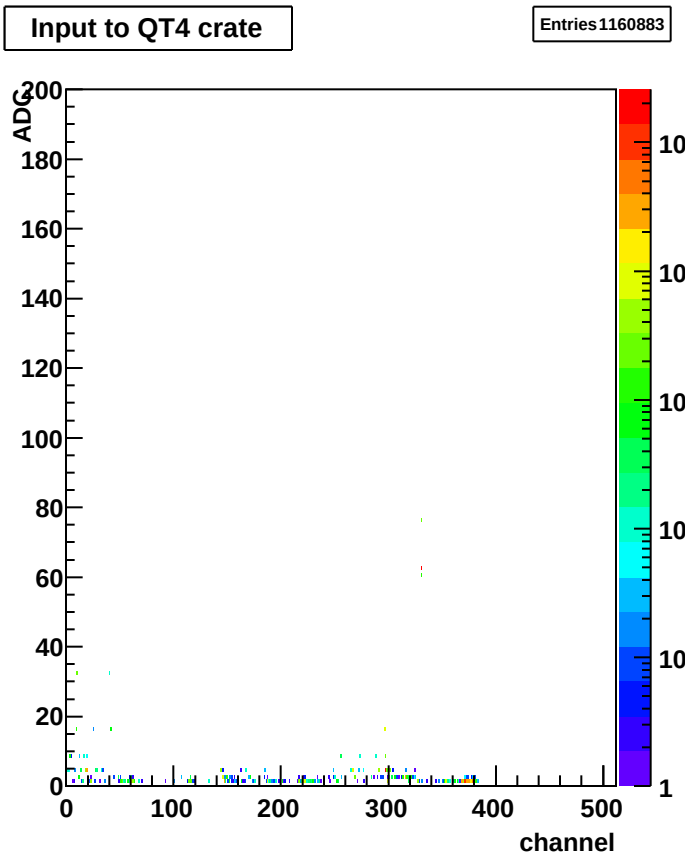

Entries 372485
BBC-S TAC Diff
ZDC TAC Diff

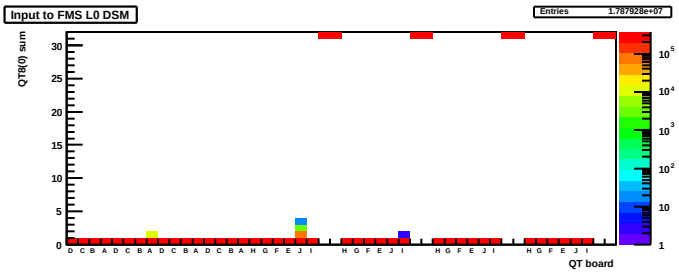

Entries 372485
VPD TAC Diff
ZDC TAC Diff

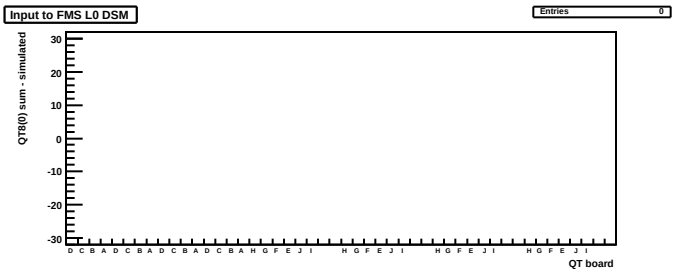

Input to QT1 crate
Entries2506328
ADC
channel

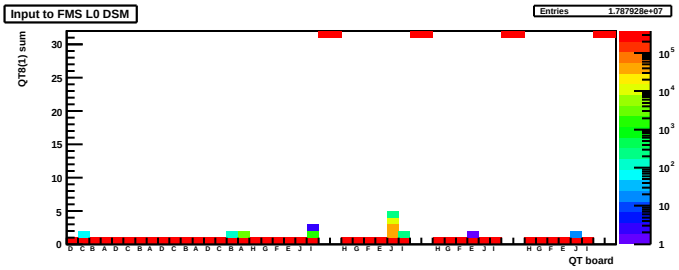

Input to QT2 crate
Entries1357332
ADC
channel

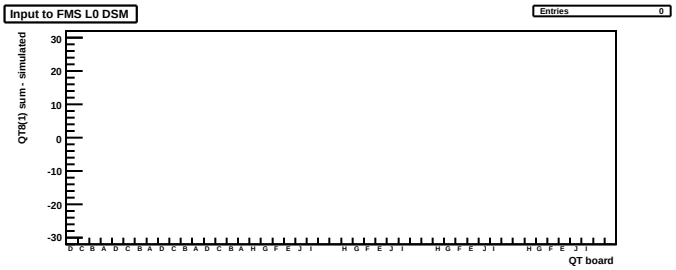

Input to QT3 crate
Entries 543215
ADC
channel

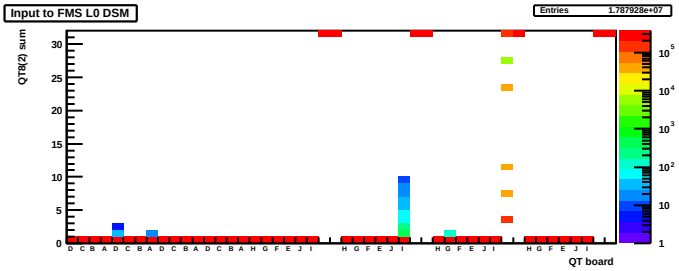

Input to QT4 crate
Entries1160883
ADC
channel

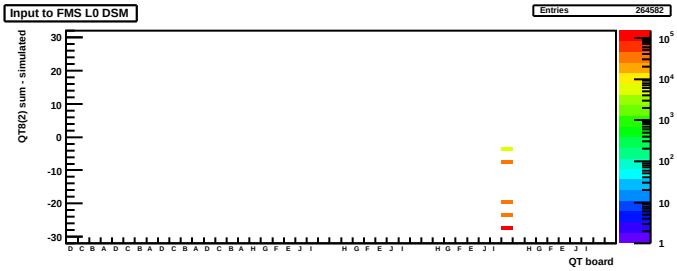

Input to FMS L0 DSM
Entries 1.787928e+07
QT8(0) sum
QT board

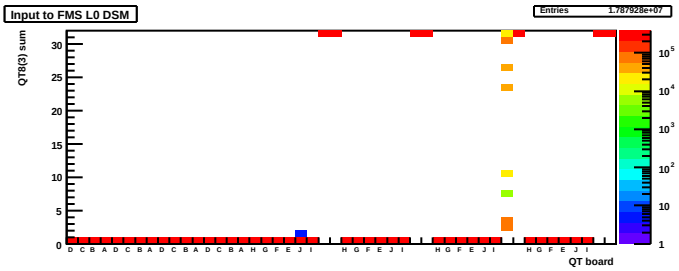

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

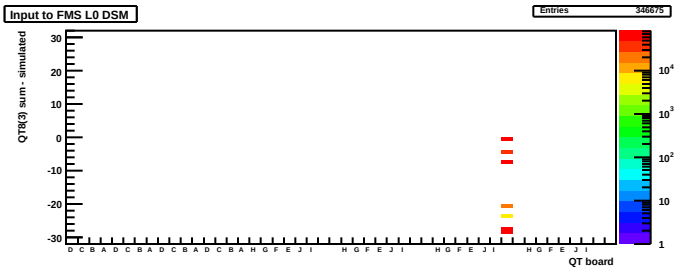

Input to FMS L0 DSM
Entries 1.787928e+07
QT8(1) sum
QT board

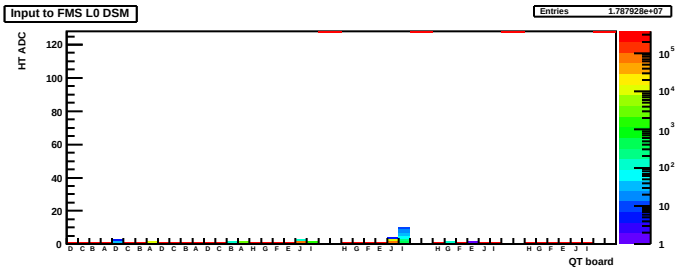

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

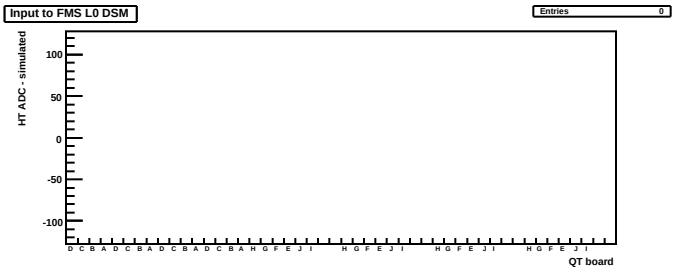

Input to FMS L0 DSM
Entries 1.787928e+07
QT8(2) sum
QT board

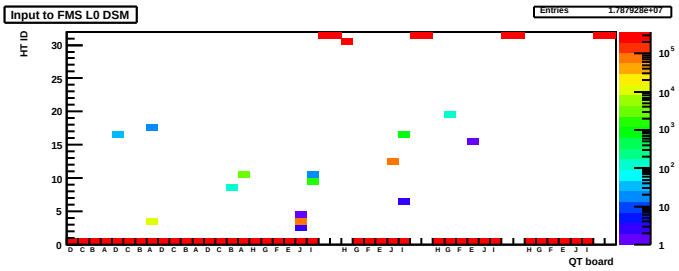

Input to FMS L0 DSM
Entries 264582
QT8(2) sum - simulated
QT board

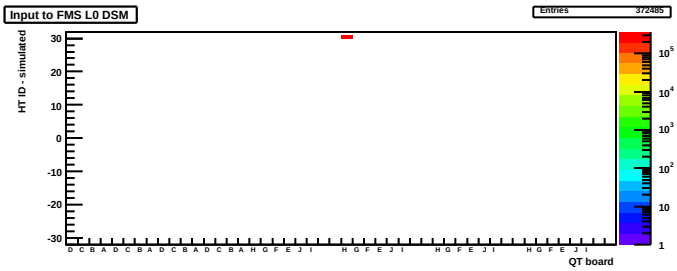

Input to FMS L0 DSM
Entries 1.787928e+07
QT8(3) sum
QT board

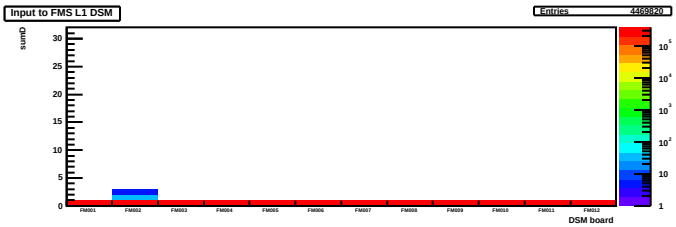

Input to FMS L0 DSM
Entries 346675
QT8(3) sum - simulated
QT board

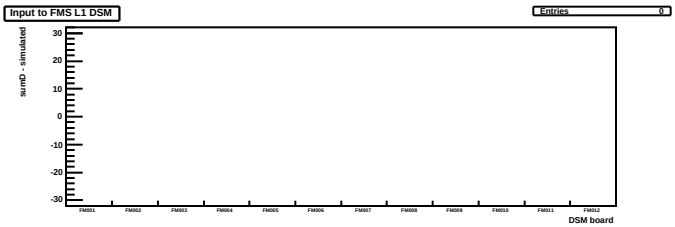

Input to FMS L0 DSM
Entries 1.787928e+07
HT ADC
QT board

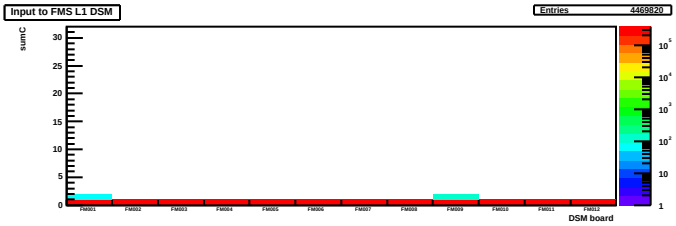

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

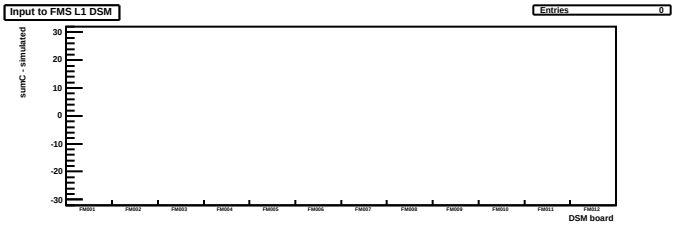

Input to FMS L0 DSM
Entries 1.787928e+07
HT ID
QT board

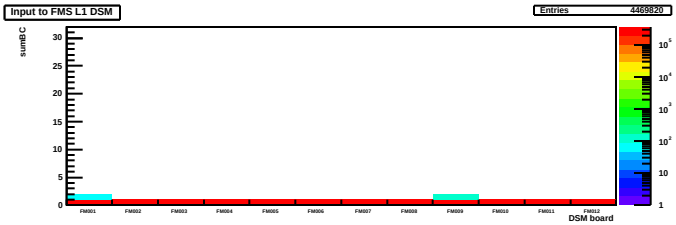

Input to FMS L0 DSM
Entries 372485
HT ID - simulated
QT board

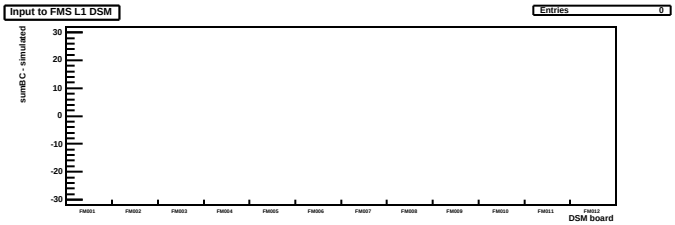

Input to FMS L1 DSM
Entries 4469820
sumD
DSM board

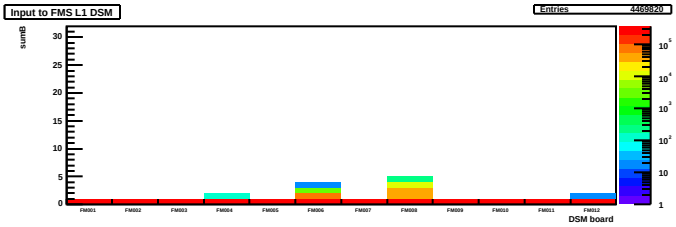

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

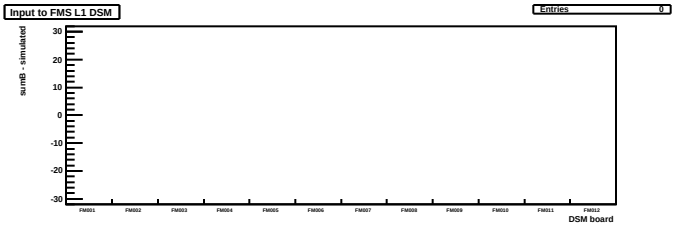

Input to FMS L1 DSM
Entries 4469820
sumC
DSM board

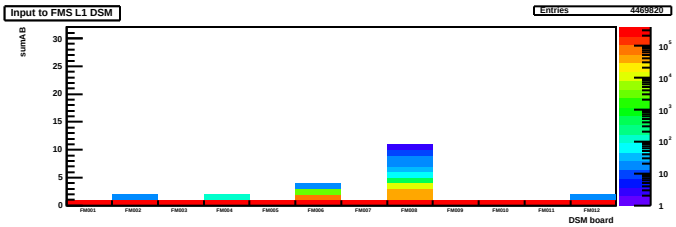

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

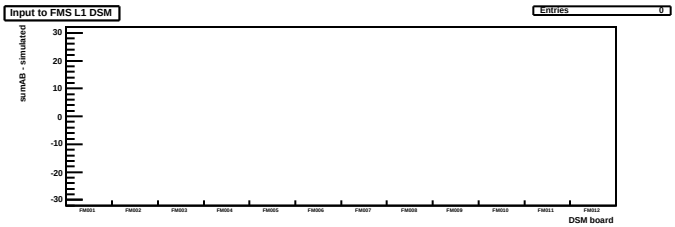

Input to FMS L1 DSM
Entries 4469820
sumBC
DSM board

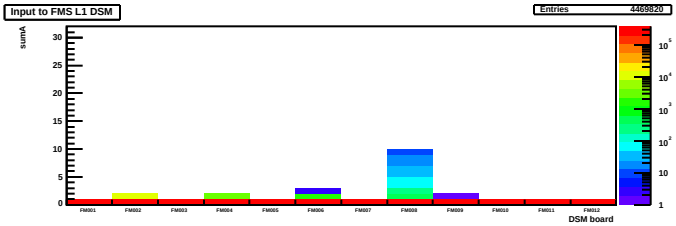

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

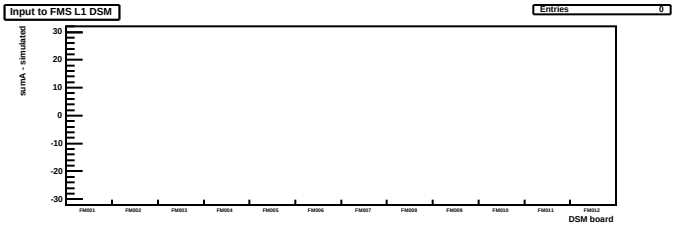

Input to FMS L1 DSM
Entries 4469820
sumB
DSM board

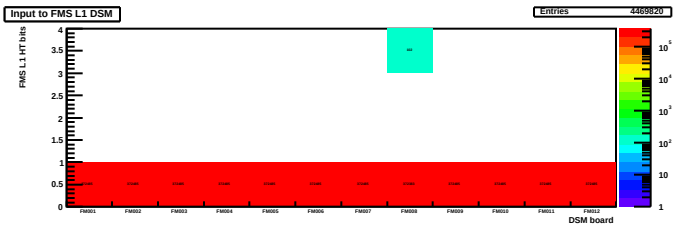

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

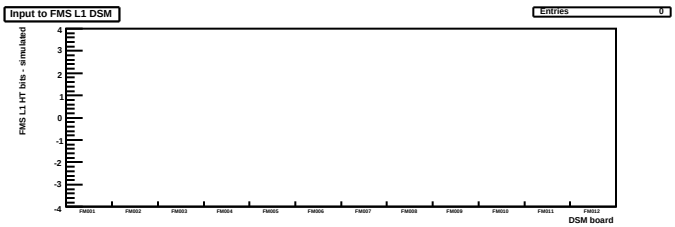

Input to FMS L1 DSM
Entries 4469820
sumAB
DSM board

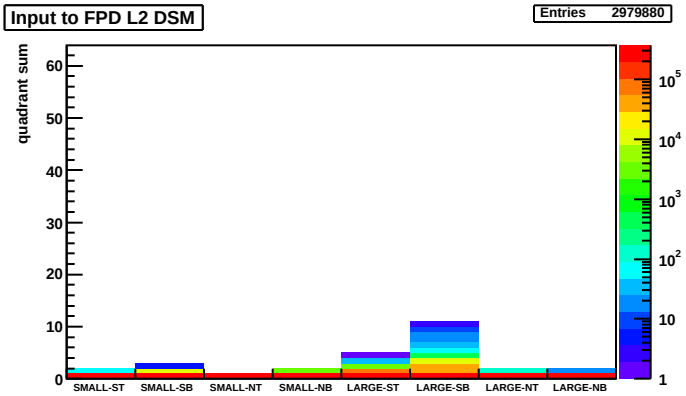

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

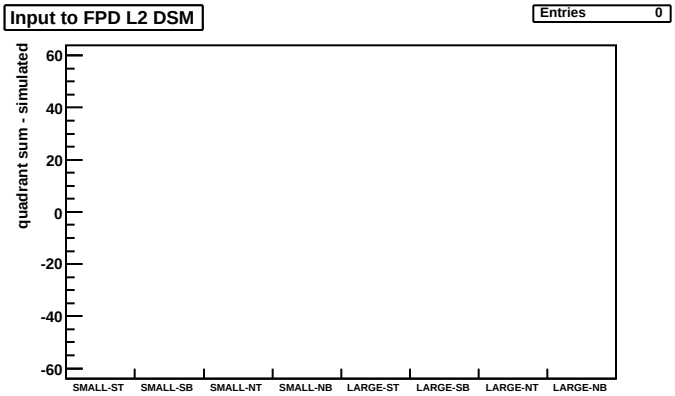

Input to FMS L1 DSM
Entries 4469820
sumA
DSM board

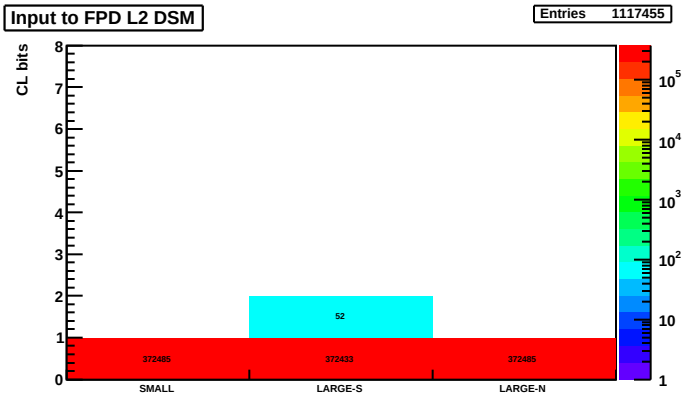

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

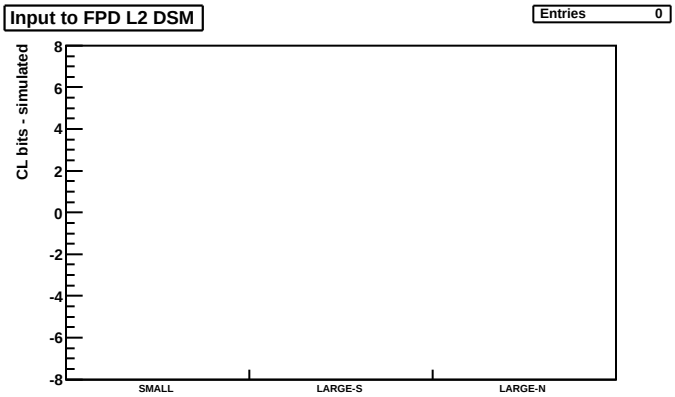

Input to FMS L1 DSM
Entries 4469820
FMS L1 HT bits
DSM board

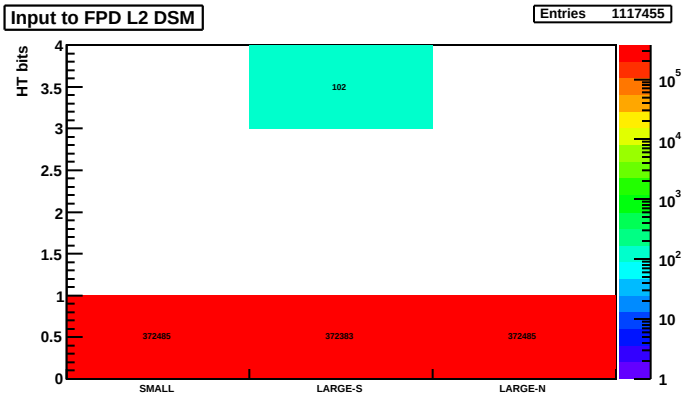

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

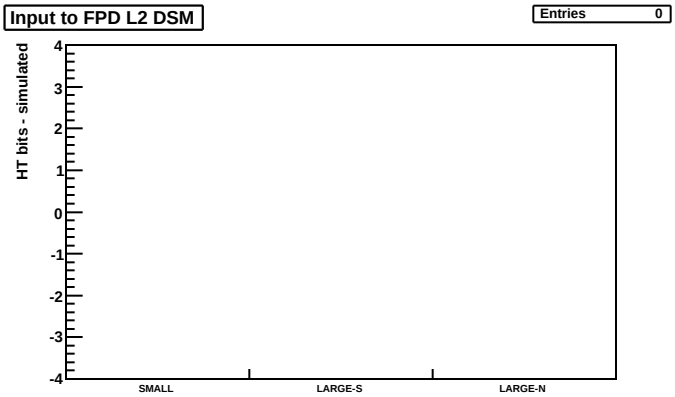

Input to FPD L2 DSM
Entries 2979880
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

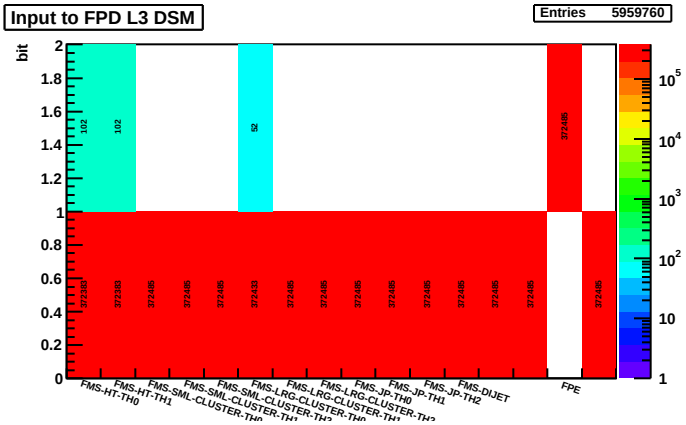

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

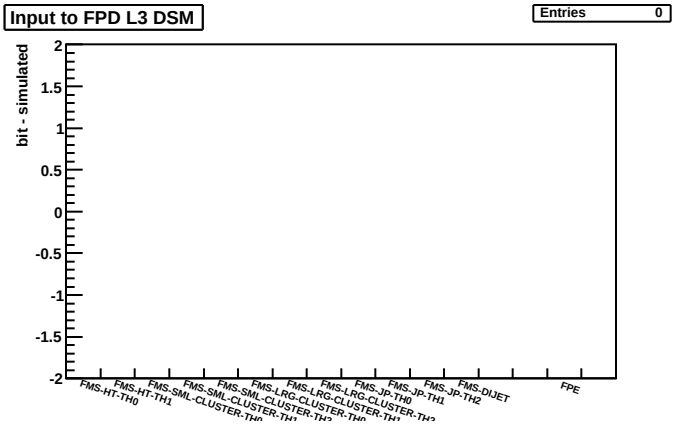

Input to FPD L2 DSM
Entries 1117455
CL bits
372485
52
372433
372485
SMALL
LARGE-S
LARGE-N

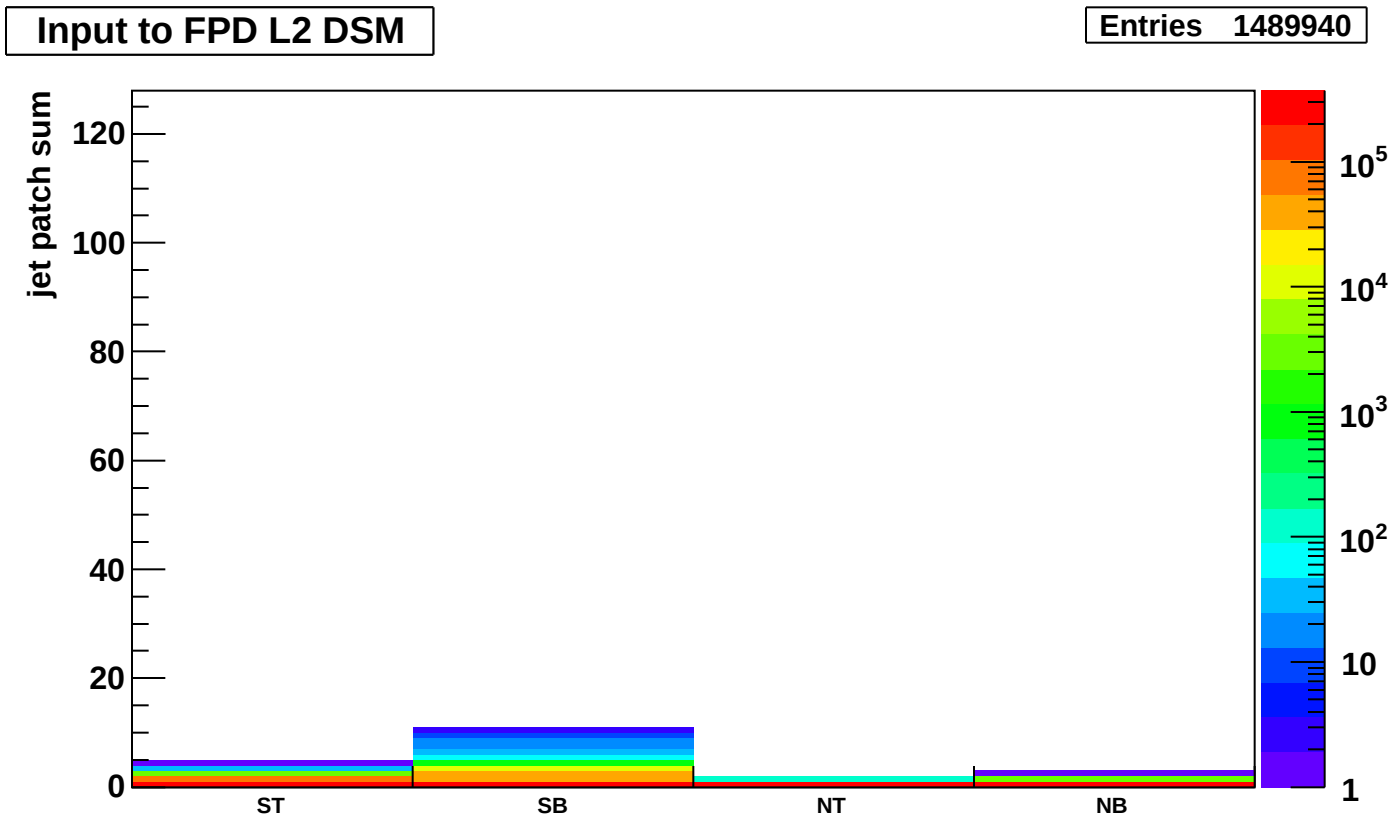

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

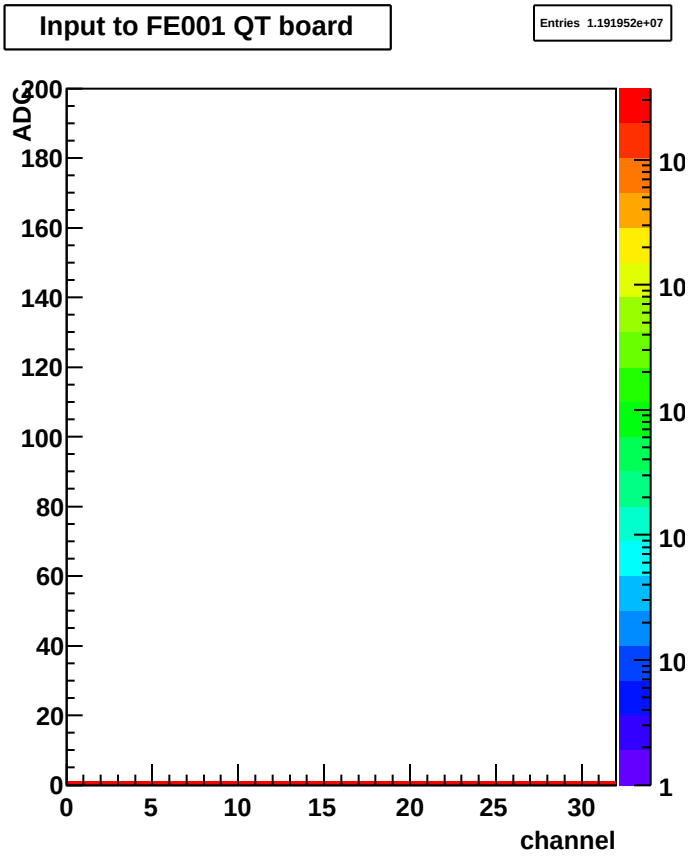

Input to FPD L2 DSM
Entries 1117455
HT bits
102
372485
372383
372485
SMALL
LARGE-S
LARGE-N

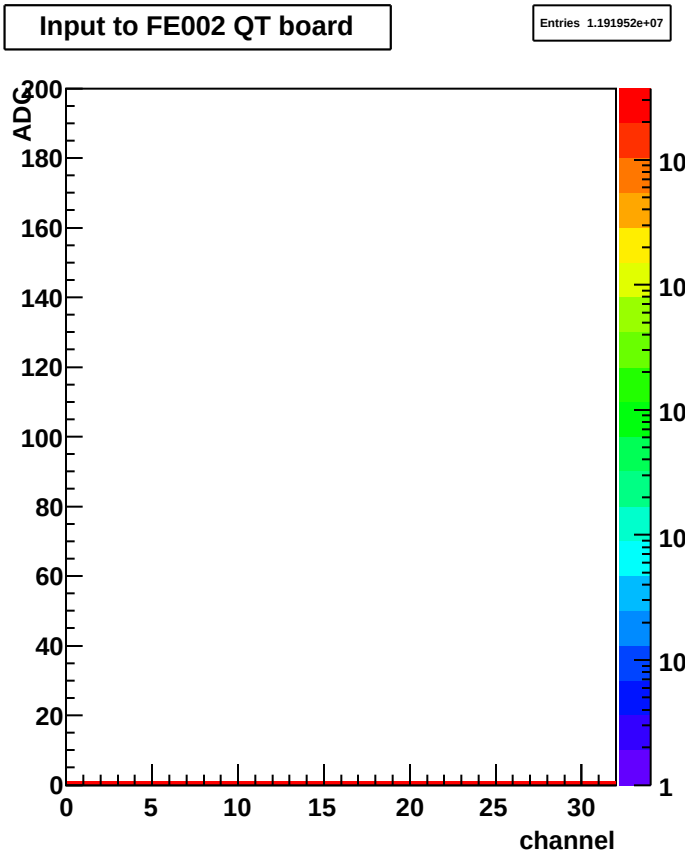

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

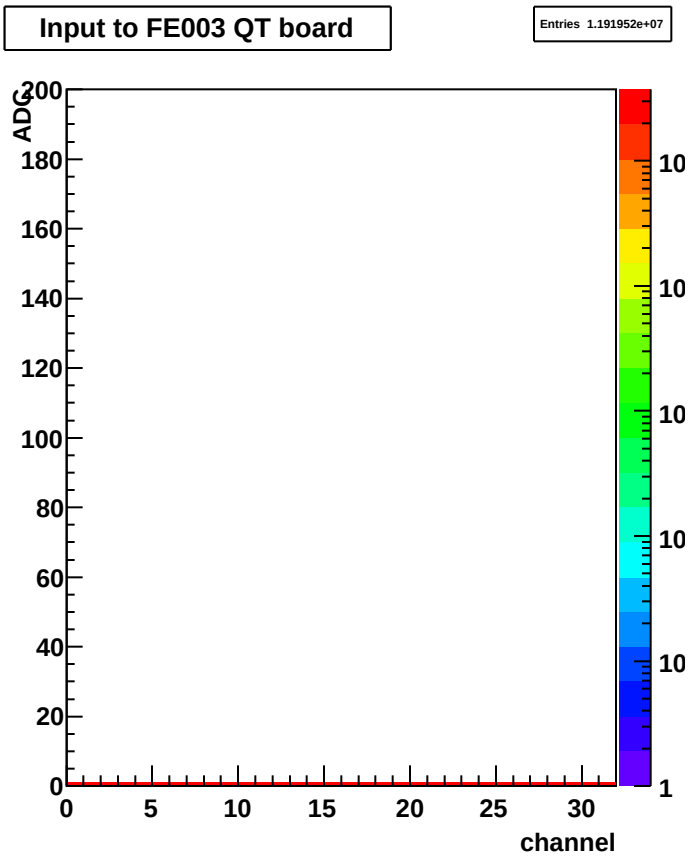

Input to FPD L3 DSM
Entries 5959760
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

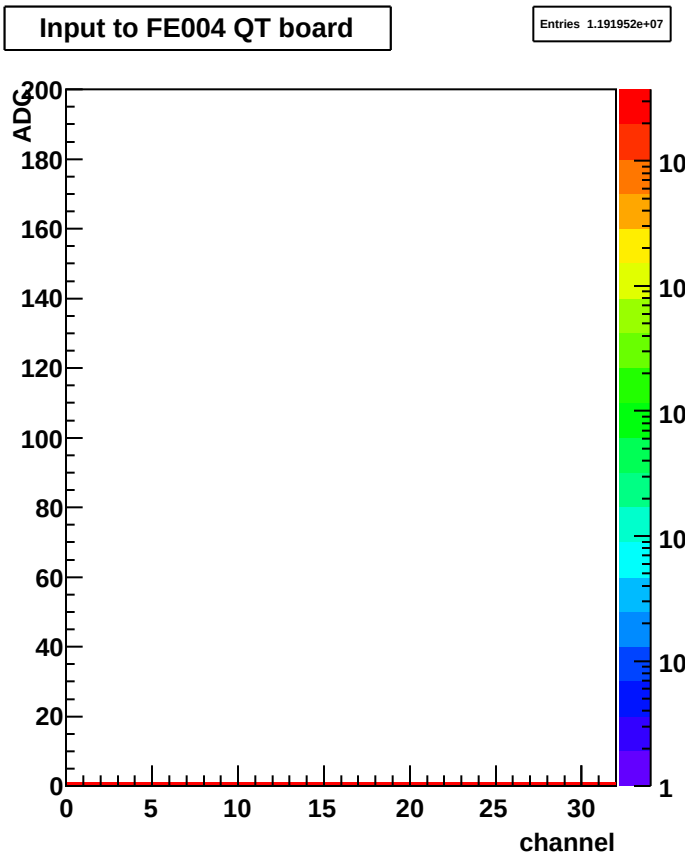

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

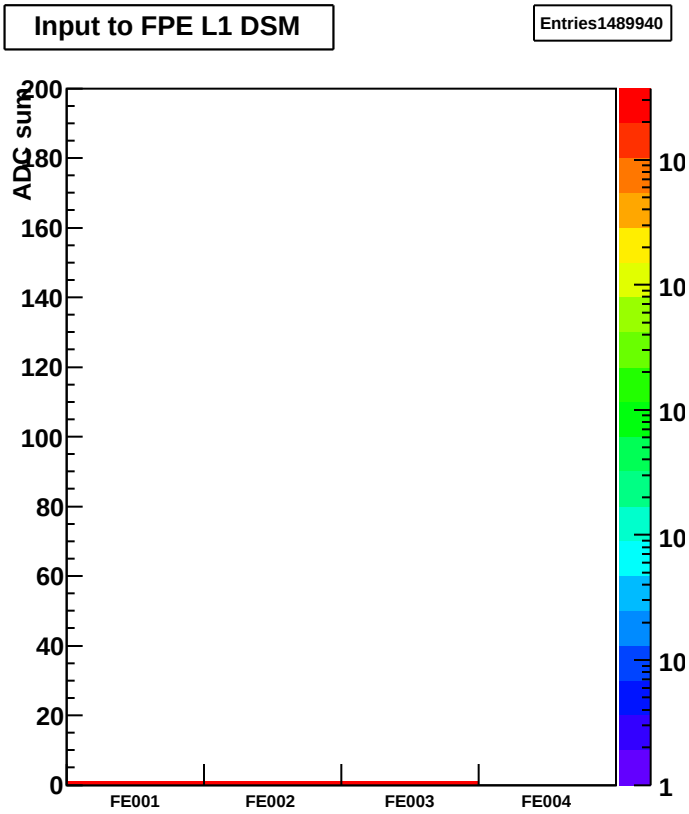

Input to FPD L2 DSM
Entries 1489940
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

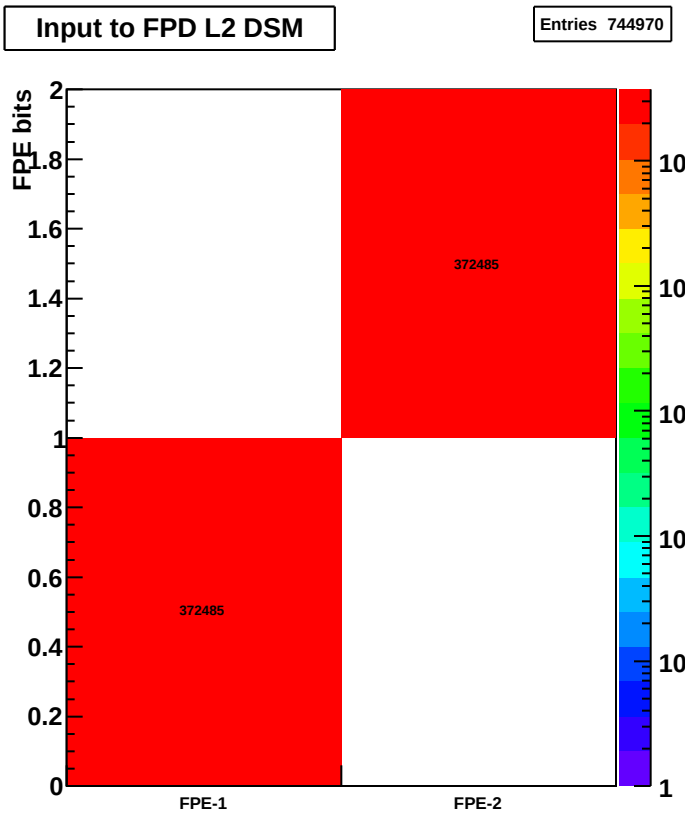

Input to FE001 QT board
Entries 1.191952e+07
ADC
channel

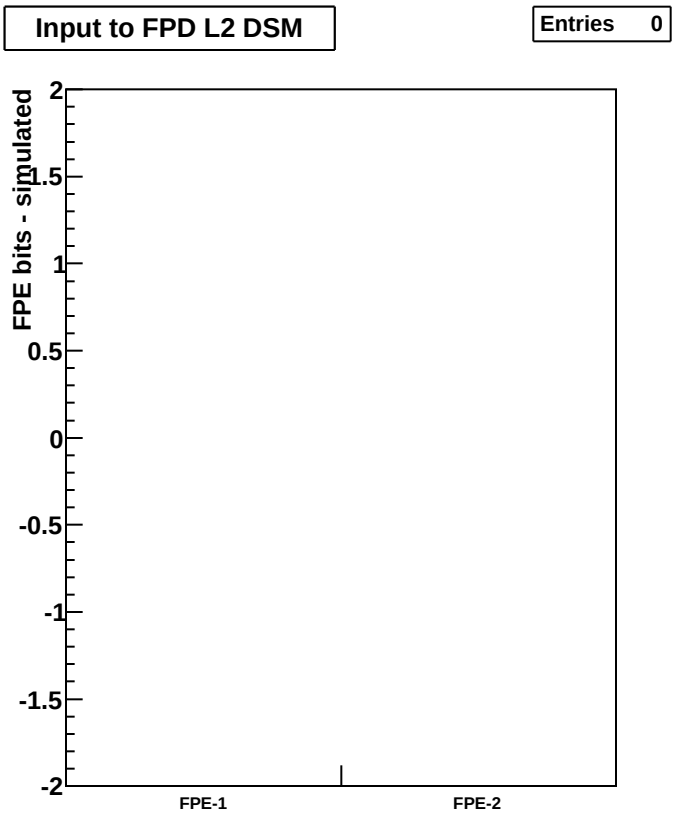

Input to FE002 QT board
Entries 1.191952e+07
ADC
channel


Input to FE003 QT board
Entries 1.191952e+07
ADC
channel


Input to FE004 QT board
Entries 1.191952e+07
ADC
channel


Input to FPE L1 DSM
Entries1489940
ADC sum
200
180
160
140
120
100
80
60
40
20
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 744970
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
372485
372485
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2