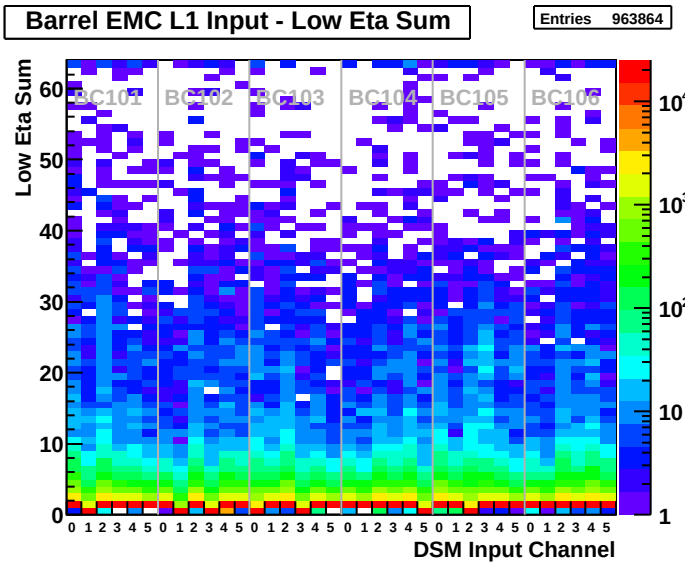
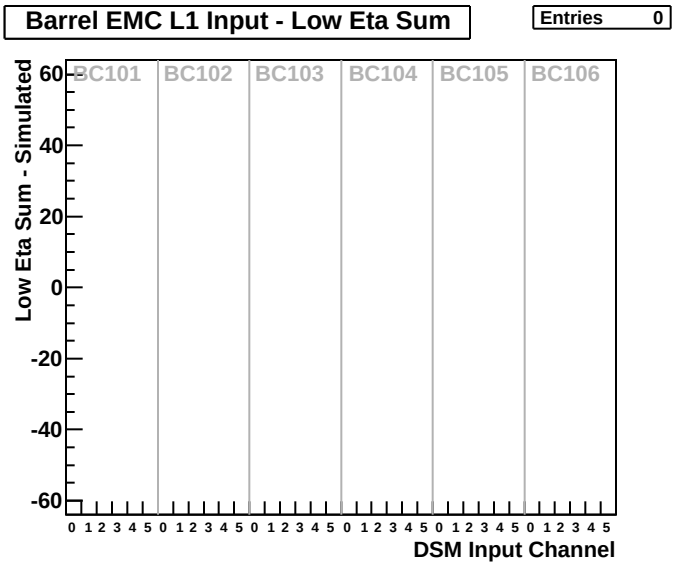
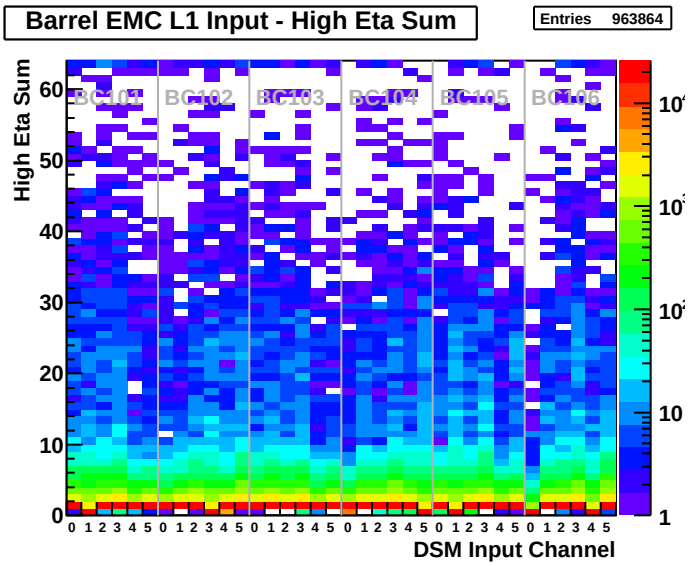
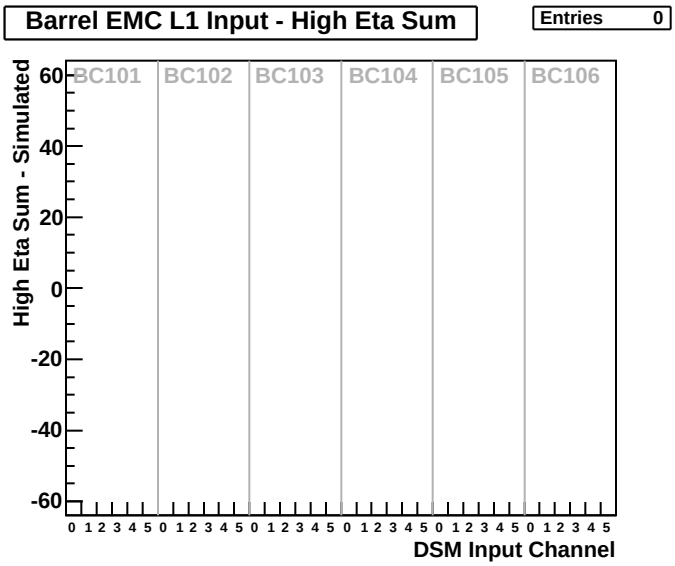
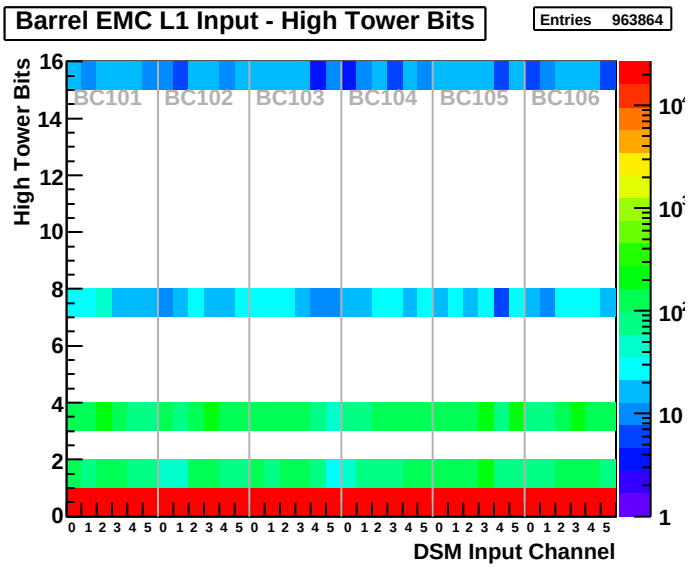
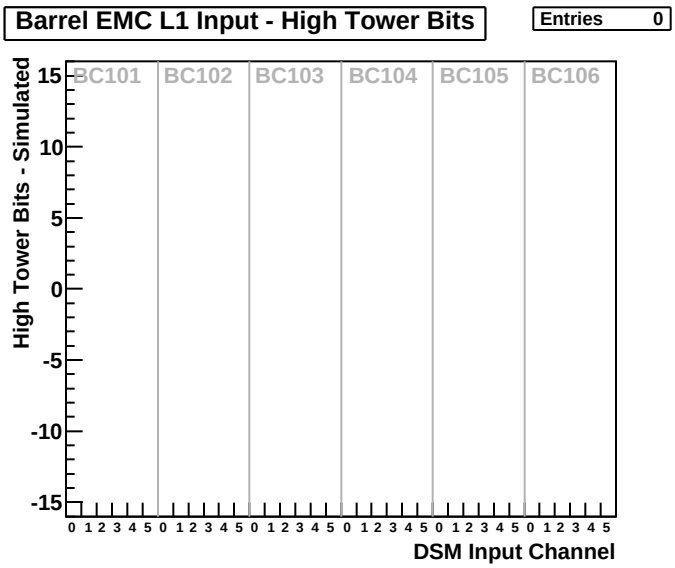

Barrel EMC L1 Input - Low Eta Sum
Entries 963864
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

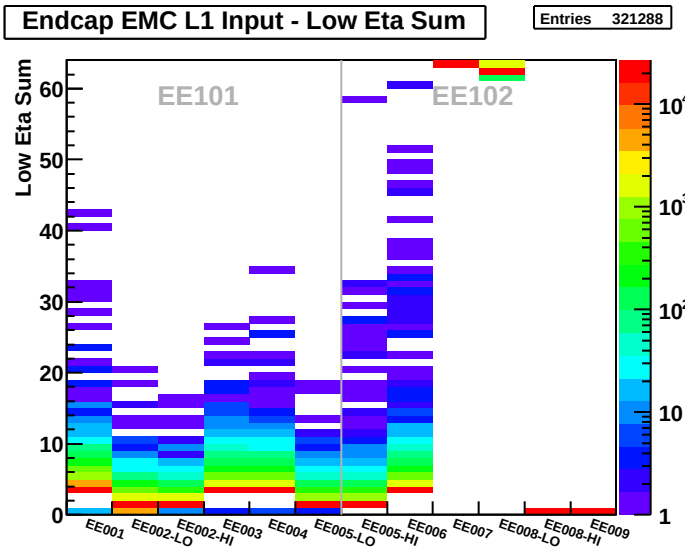

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

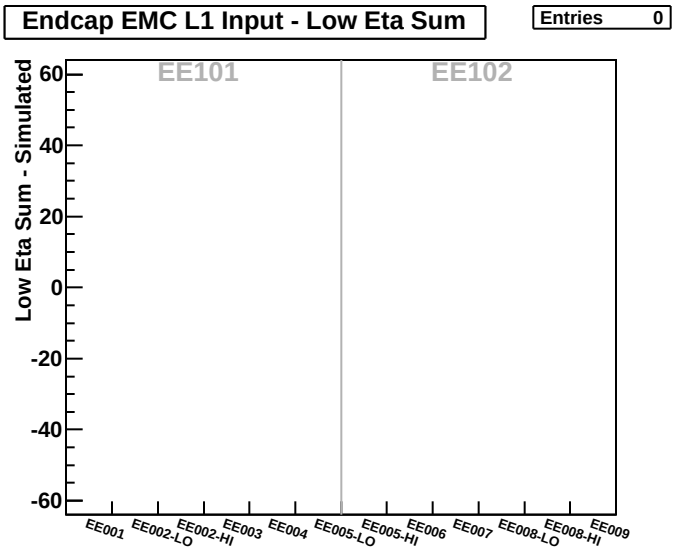

Barrel EMC L1 Input - High Eta Sum
Entries 963864
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

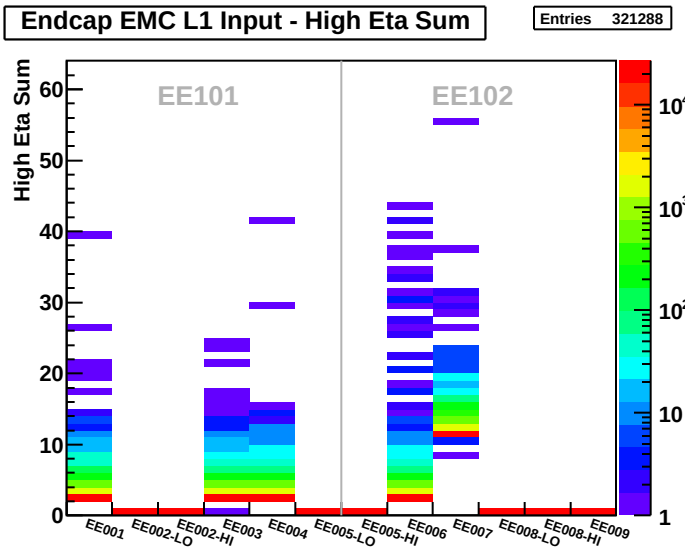

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

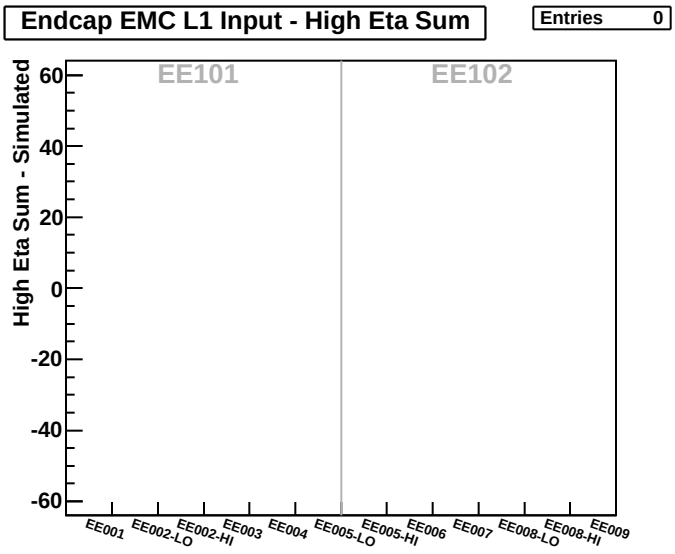

Barrel EMC L1 Input - High Tower Bits
Entries 963864
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

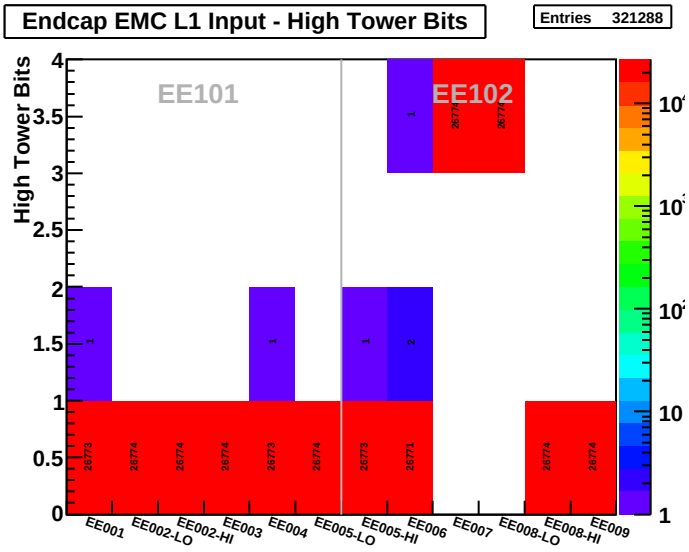

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

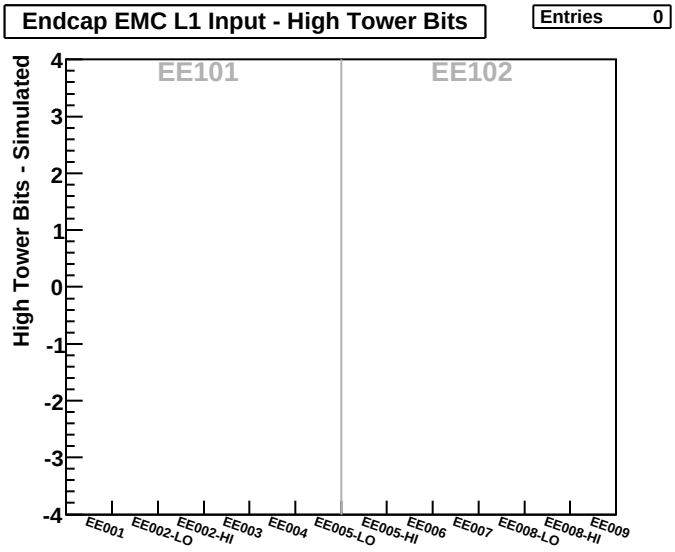

Endcap EMC L1 Input - Low Eta Sum
Entries 321288
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

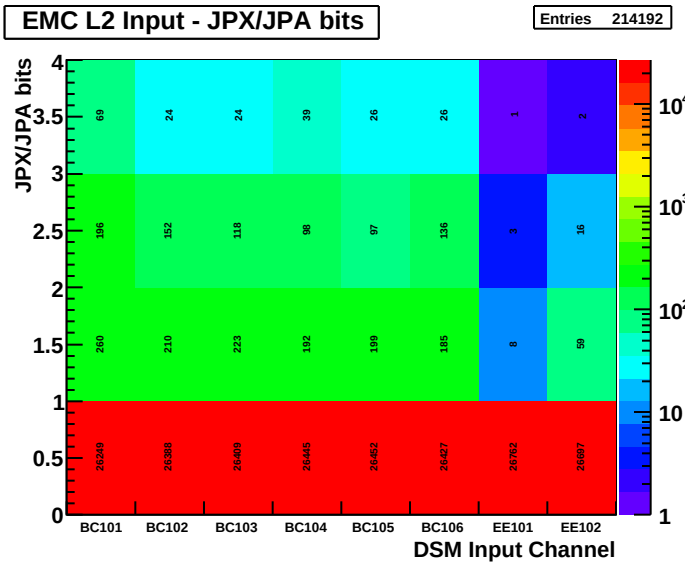

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

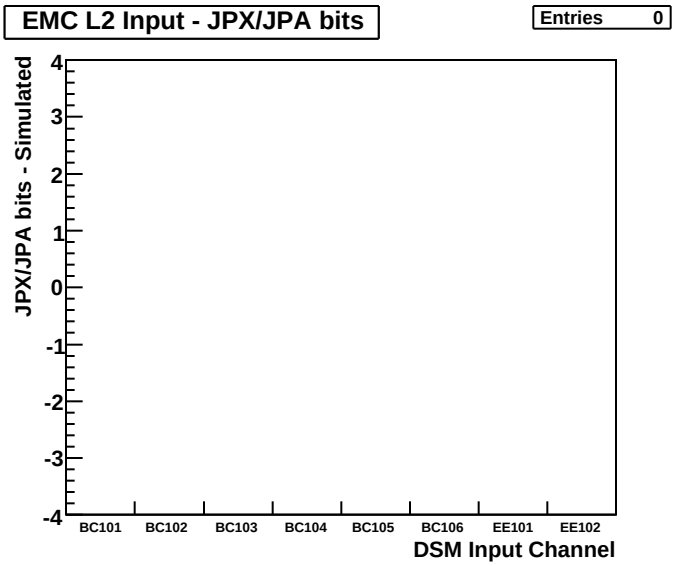

Endcap EMC L1 Input - High Eta Sum
Entries 321288
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

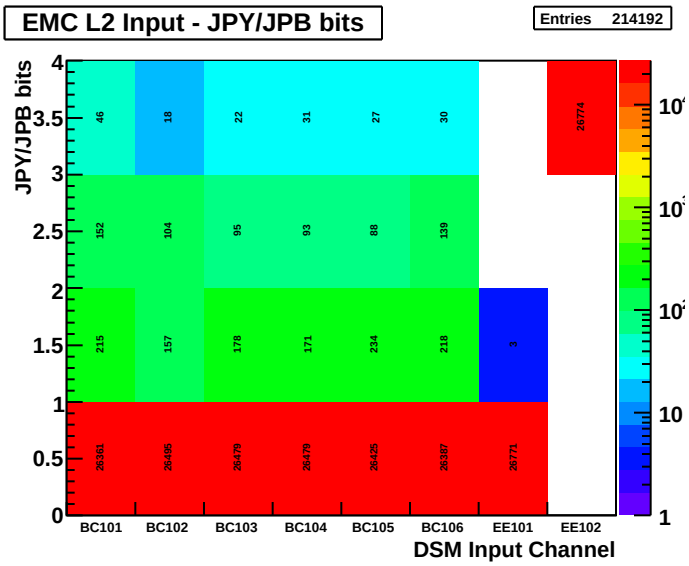

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

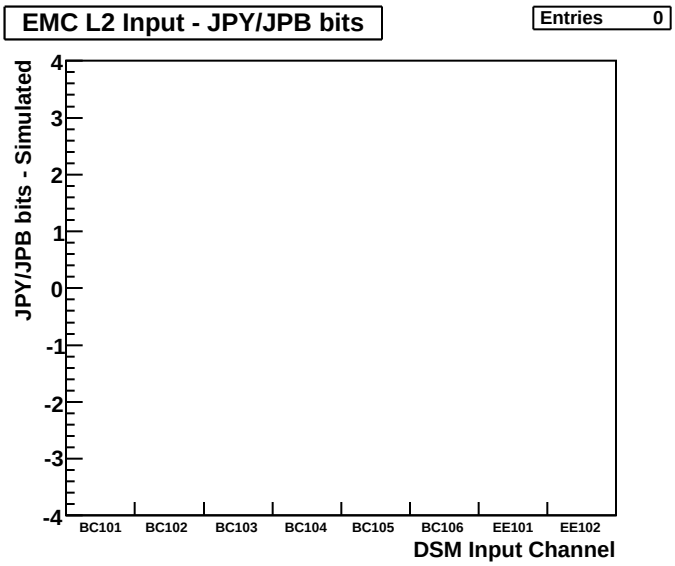

Endcap EMC L1 Input - High Tower Bits
Entries 321288
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

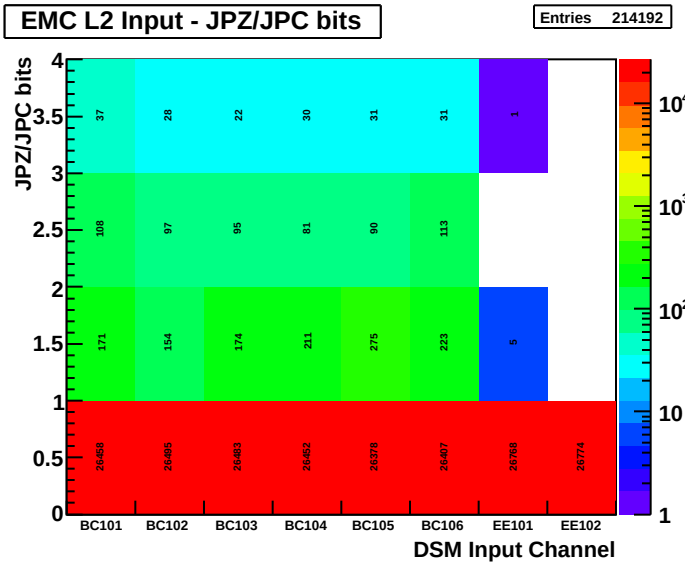

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

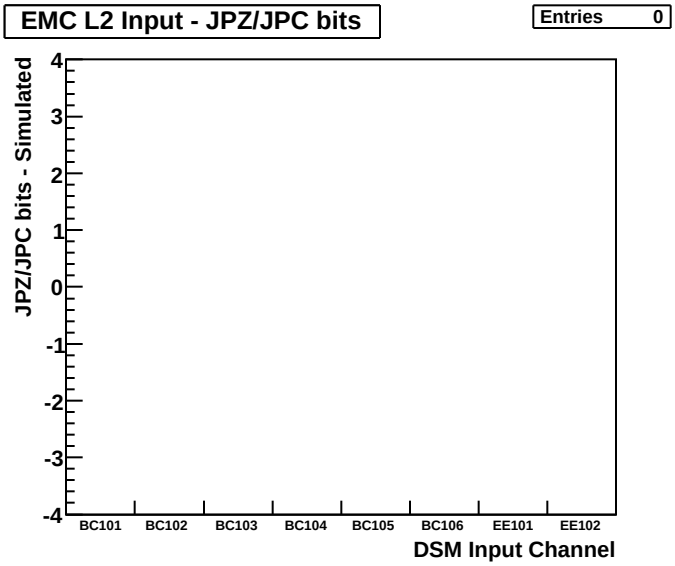

EMC L2 Input - JPX/JPA bits
Entries 214192
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
69 24 24 39 26 26 1 2
196 152 118 98 97 136 3 16
260 210 223 192 199 185 8 59
26249 26338 26409 26445 26452 26427 26762 26697

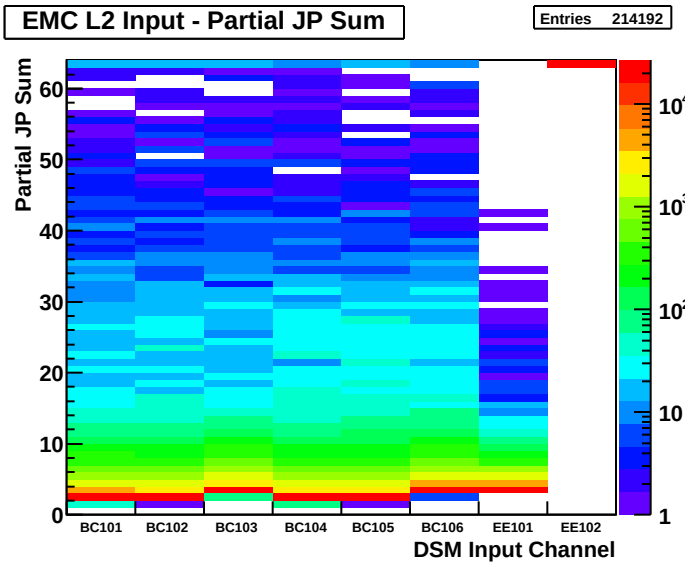

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

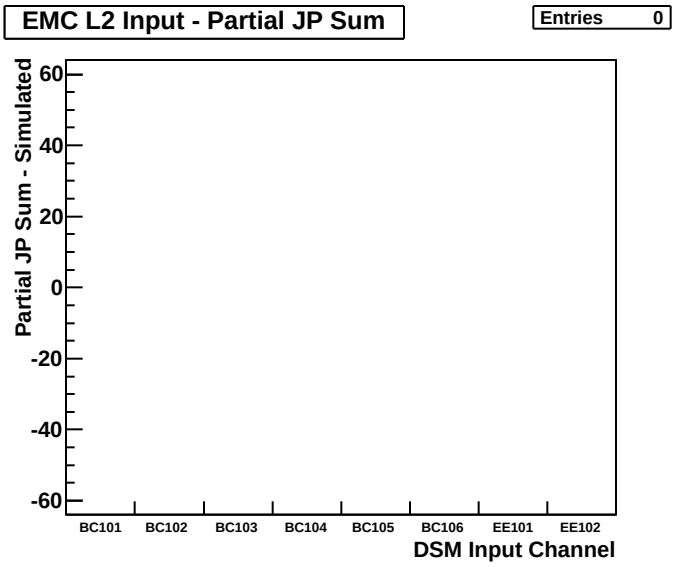

EMC L2 Input - JPY/JPB bits
Entries 214192
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
46 18 22 31 27 30 26774
152 104 95 93 88 139
215 157 178 171 234 218 3
26361 26495 26479 26479 26425 26387 26771

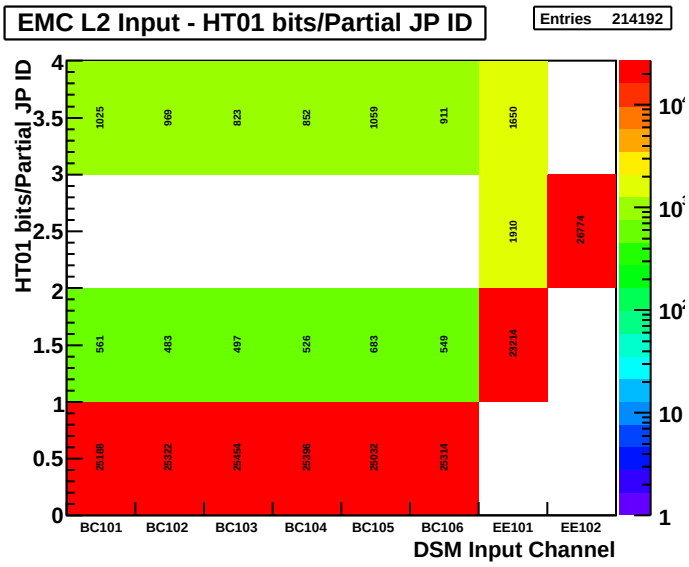

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

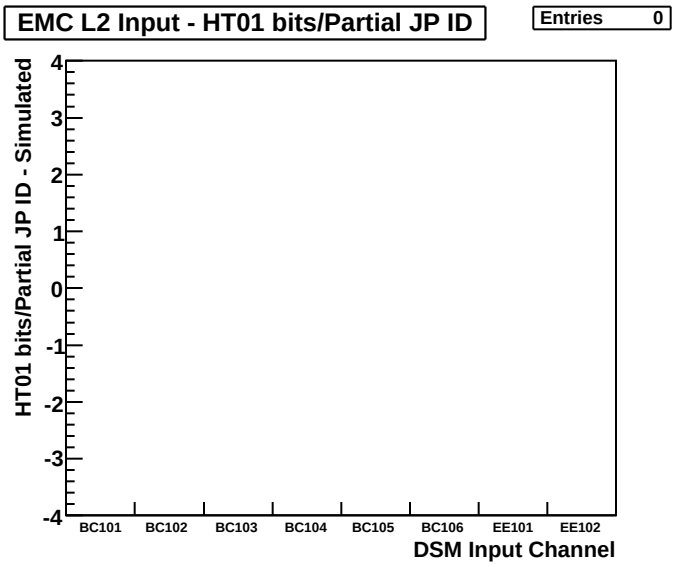

EMC L2 Input - JPZ/JPC bits
Entries 214192
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
37 28 22 30 31 31 1
108 97 95 81 90 113
171 154 174 211 275 223 5
26458 26495 26483 26452 26378 26407 26768 26774

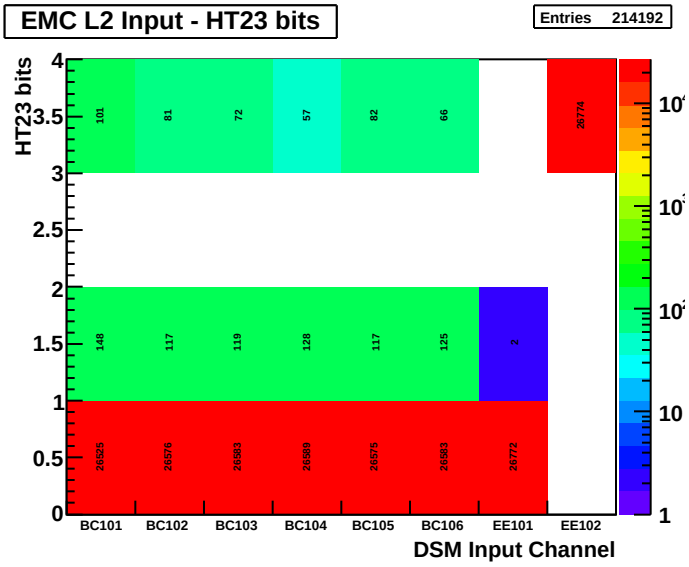

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

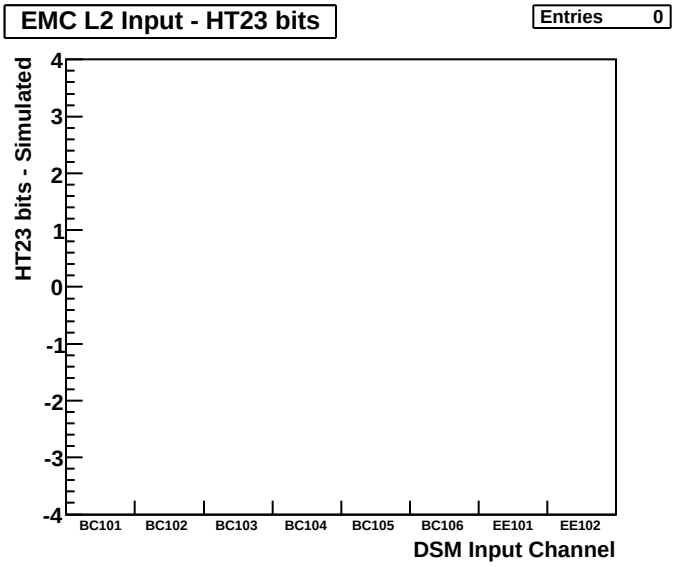

EMC L2 Input - Partial JP Sum
Entries 214192
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

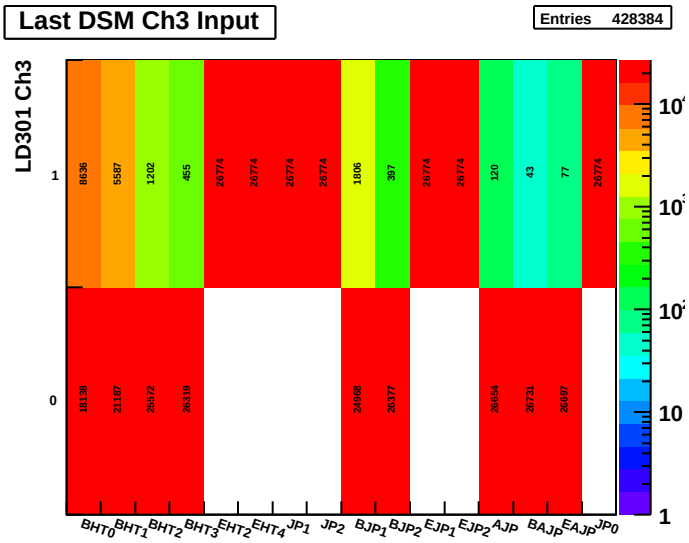

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

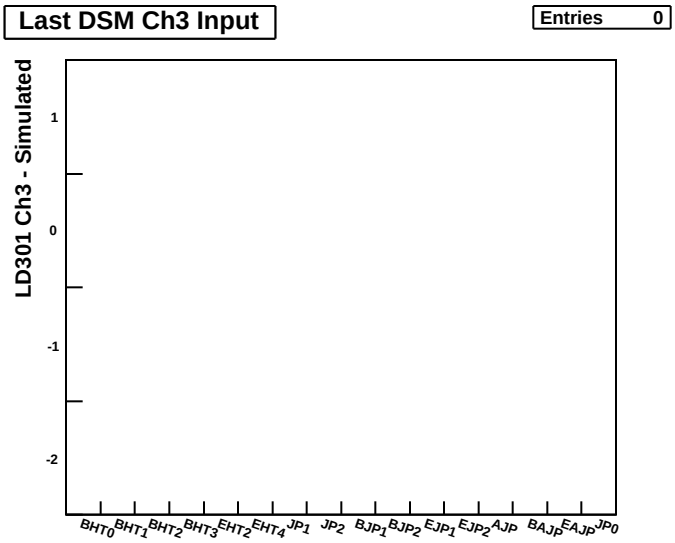

EMC L2 Input - HT01 bits/Partial JP ID
Entries 214192
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

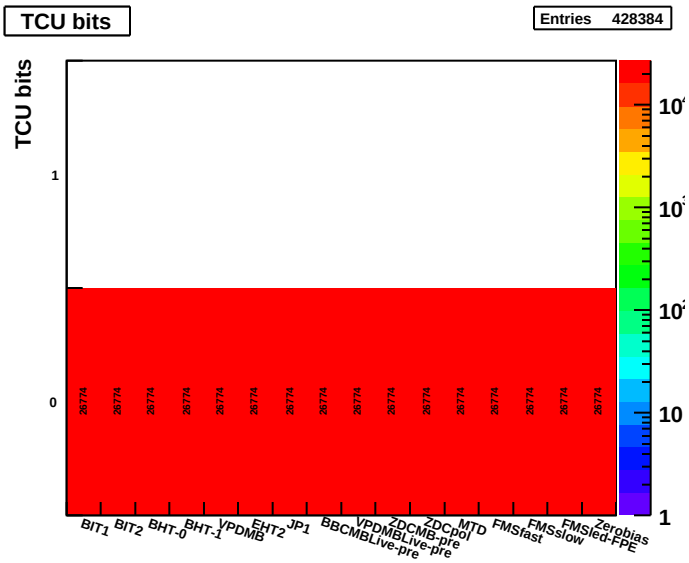

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

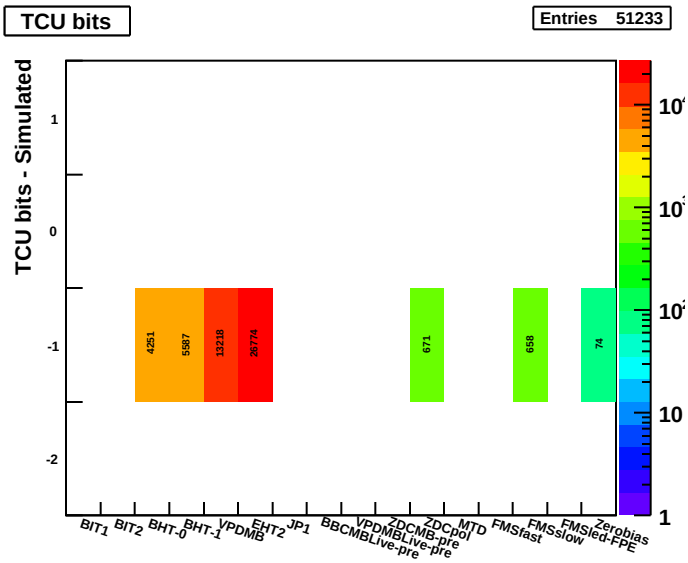

EMC L2 Input - HT23 bits
Entries 214192
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

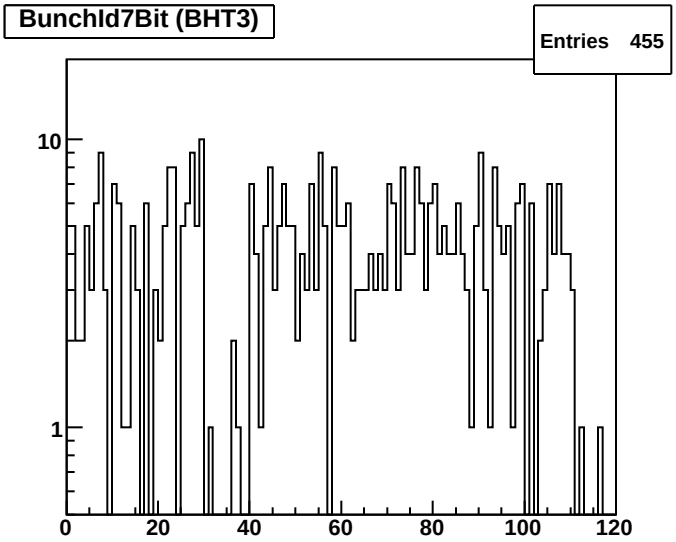

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

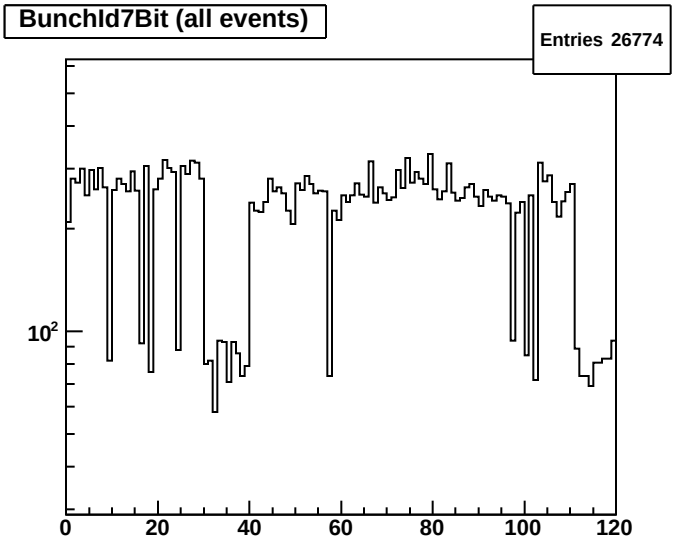

Last DSM Ch3 Input
Entries 428384
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

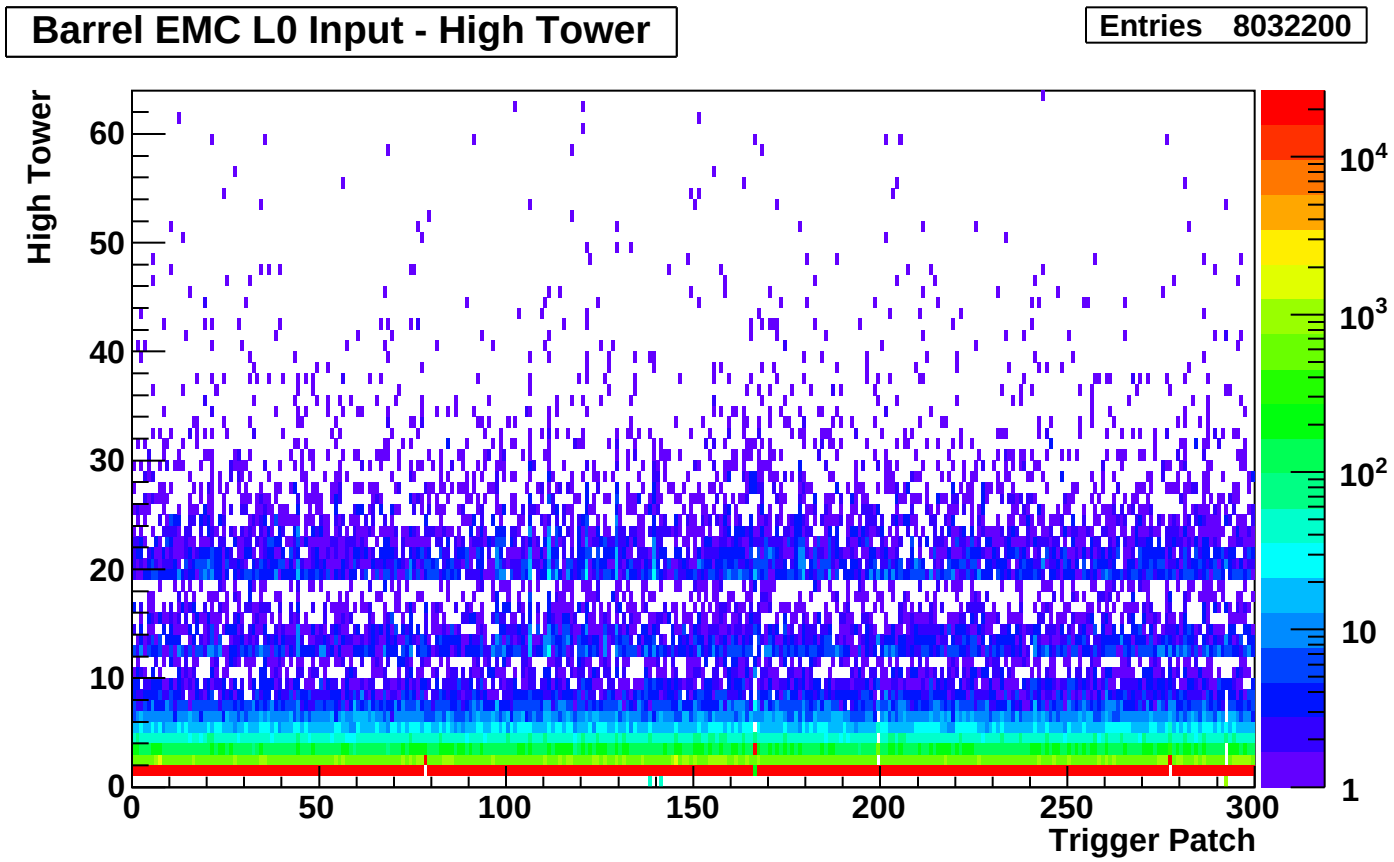

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

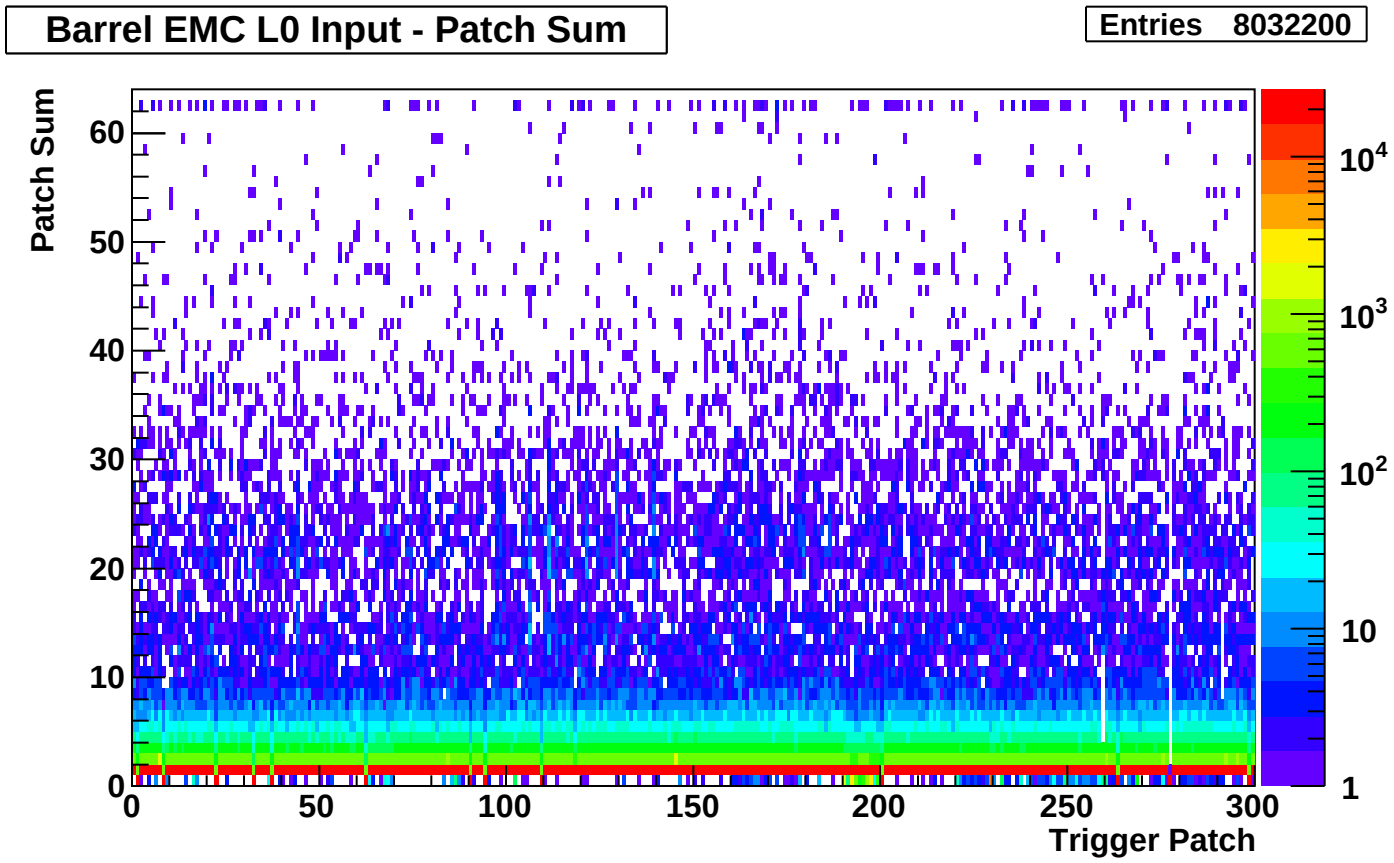

TCU bits
Entries 428384
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

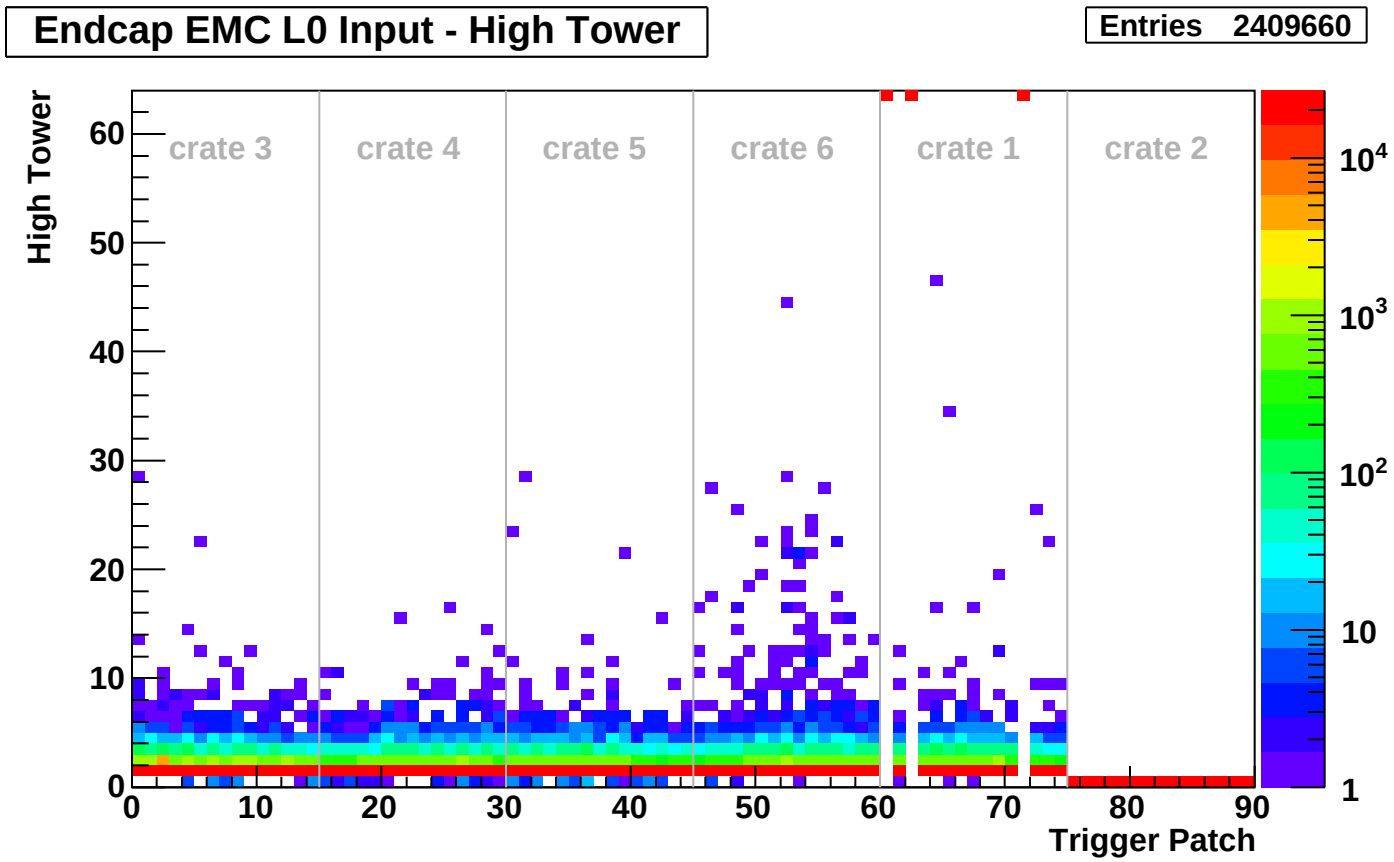

TCU bits
Entries 51233
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

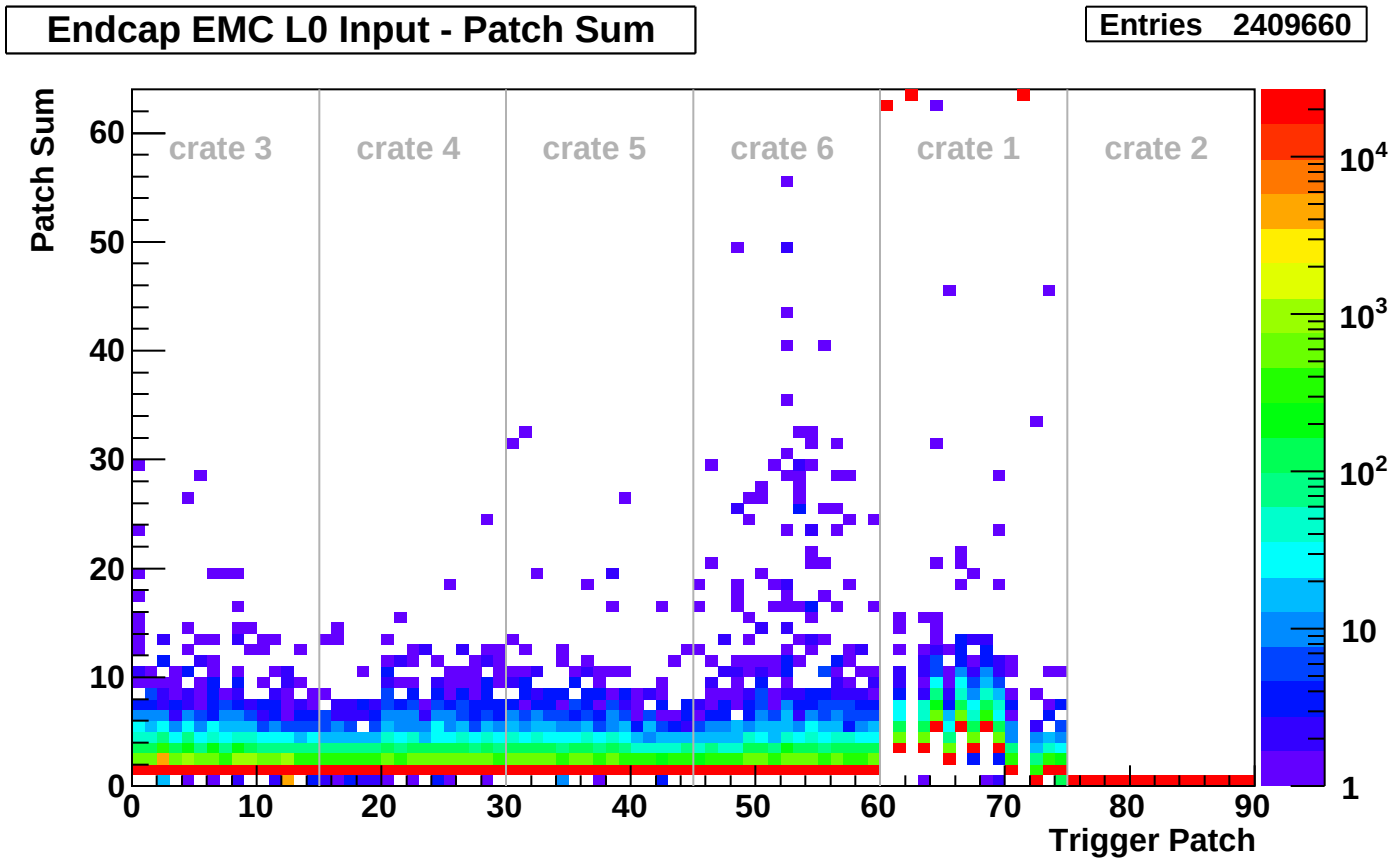

BunchId7Bit (BHT3)
Entries 455

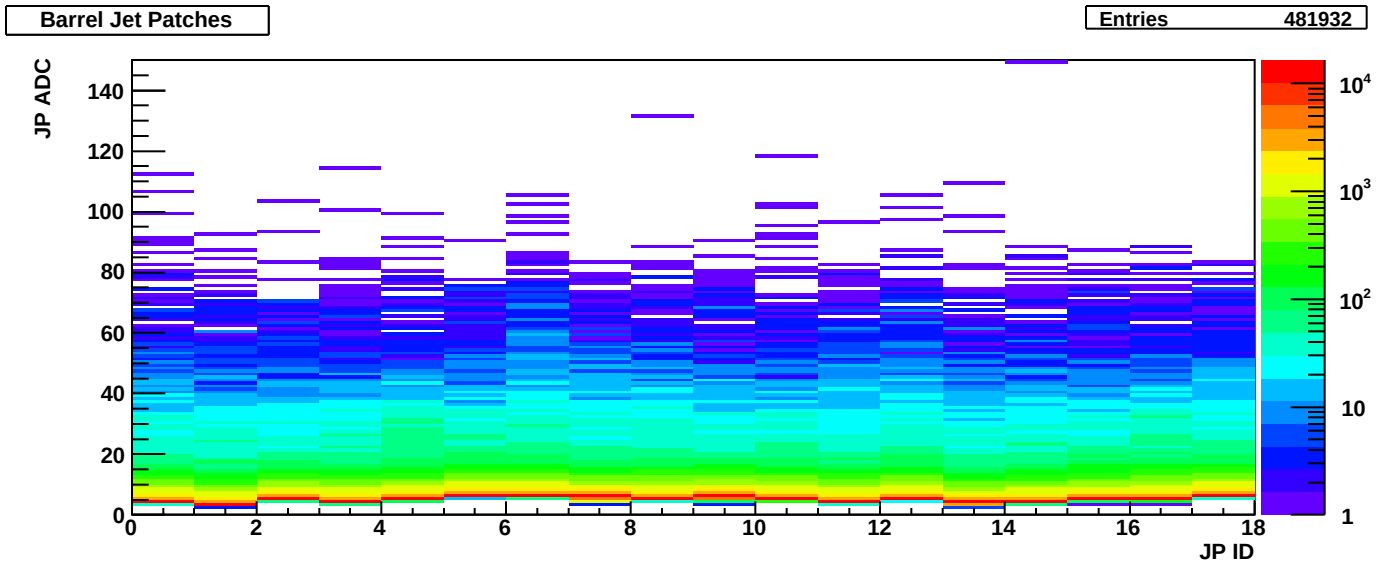

BunchId7Bit (all events)
Entries 26774

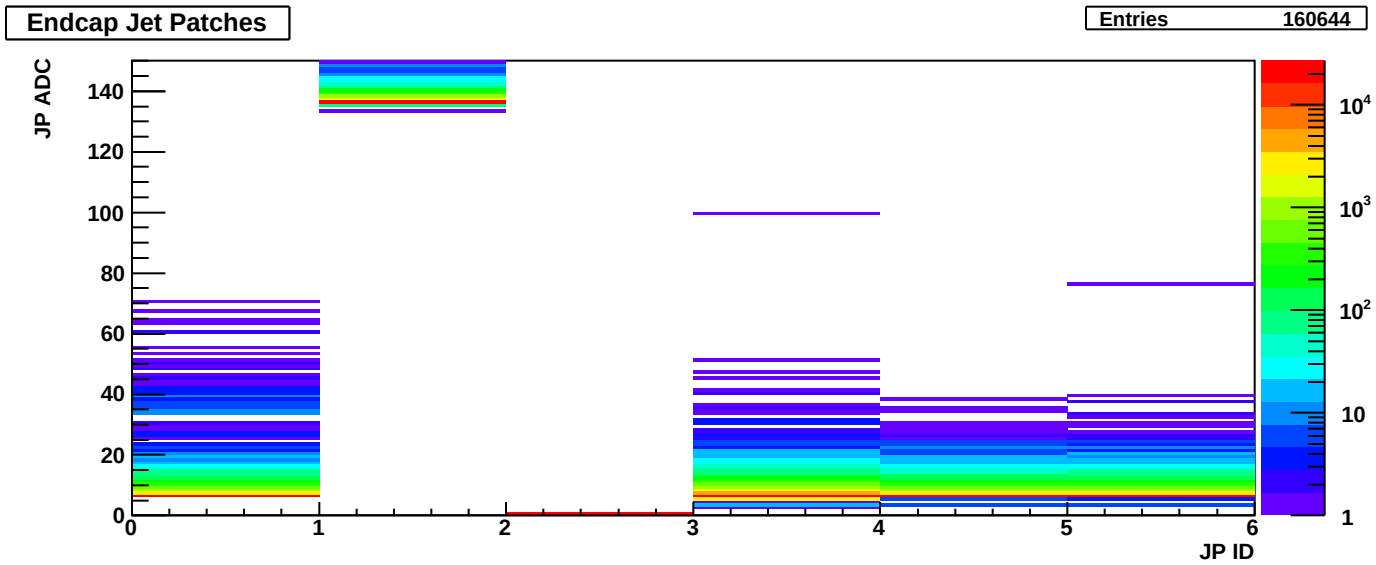

Barrel EMC L0 Input - High Tower
Entries 8032200
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

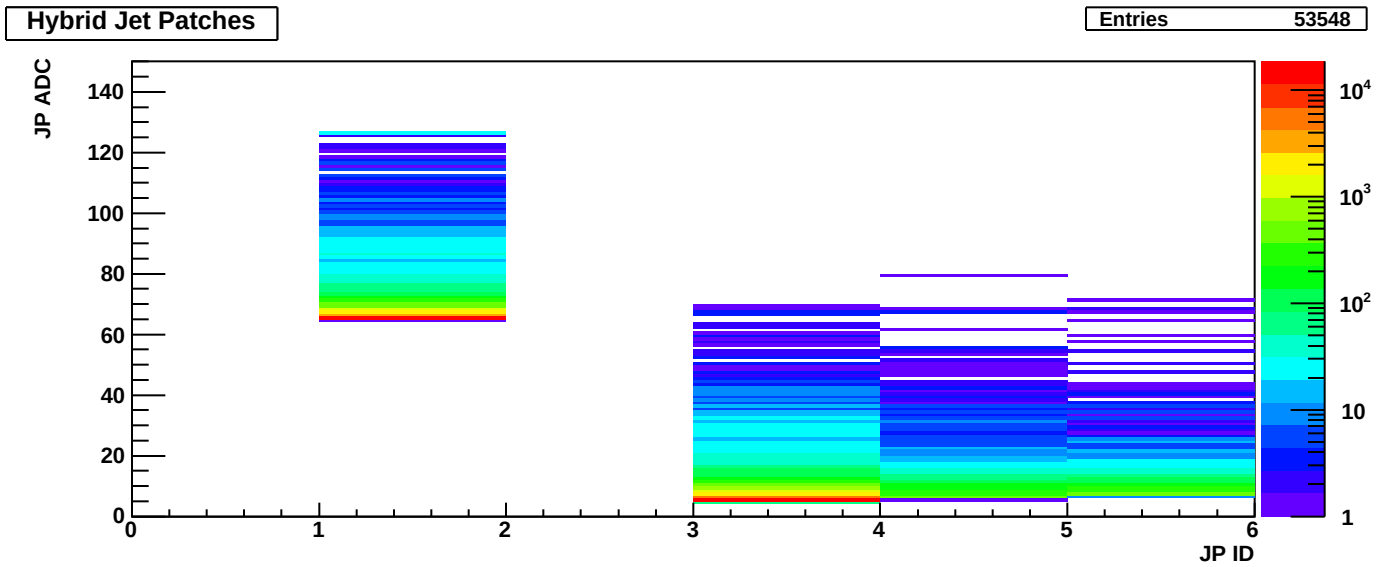

Barrel EMC L0 Input - Patch Sum
Entries 8032200
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

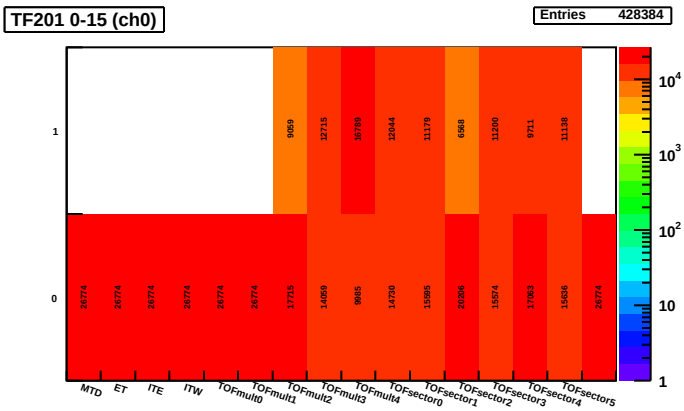

Endcap EMC L0 Input - High Tower
Entries 2409660
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

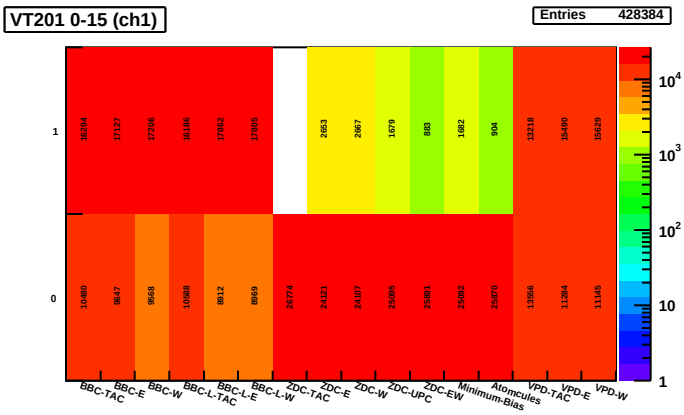

Endcap EMC L0 Input - Patch Sum
Entries 2409660
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

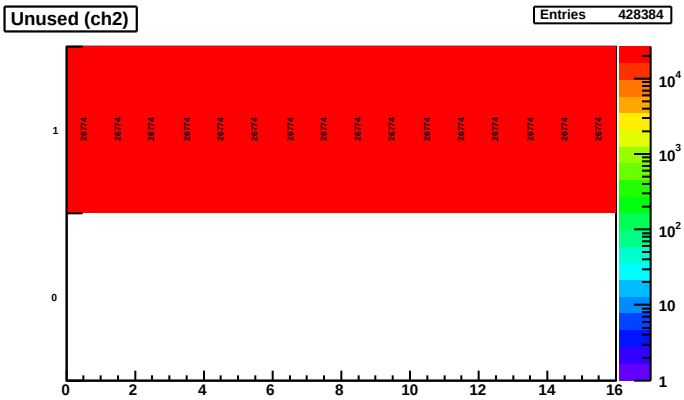

Barrel Jet Patches
Entries 481932
JP ADC
JP ID

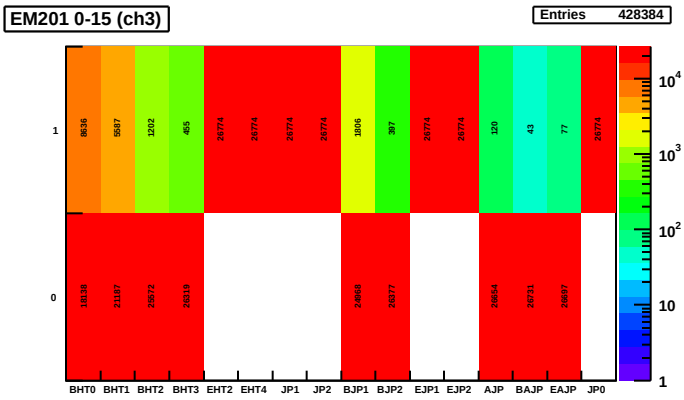

Endcap Jet Patches
Entries 160644
JP ADC
JP ID

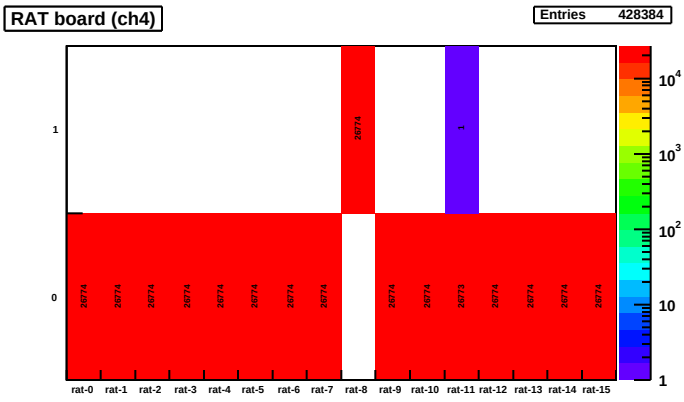

Hybrid Jet Patches
Entries 53548
JP ADC
JP ID

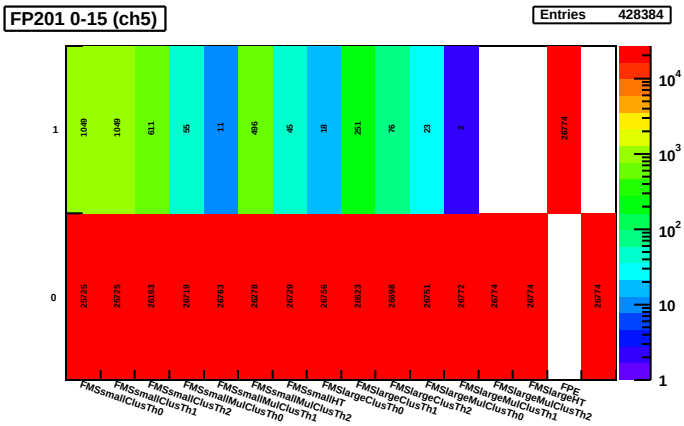

TF201 0-15 (ch0)
Entries 428384
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

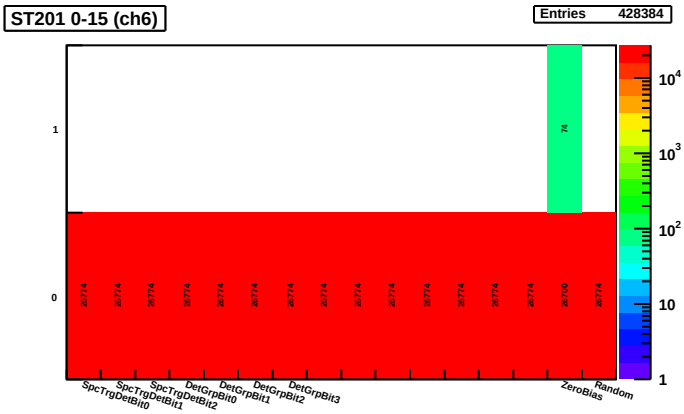

VT201 0-15 (ch1)
Entries 428384
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

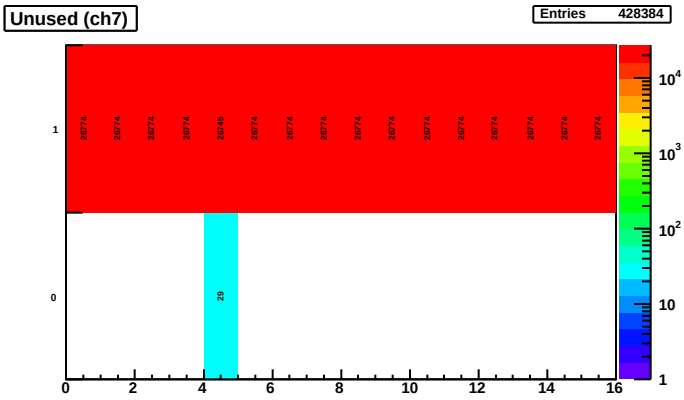

Unused (ch2)
Entries 428384

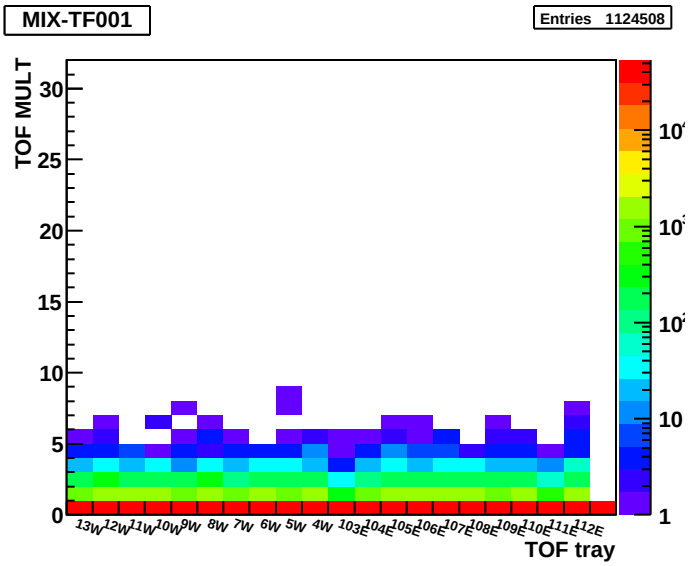

EM201 0-15 (ch3)
Entries 428384
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

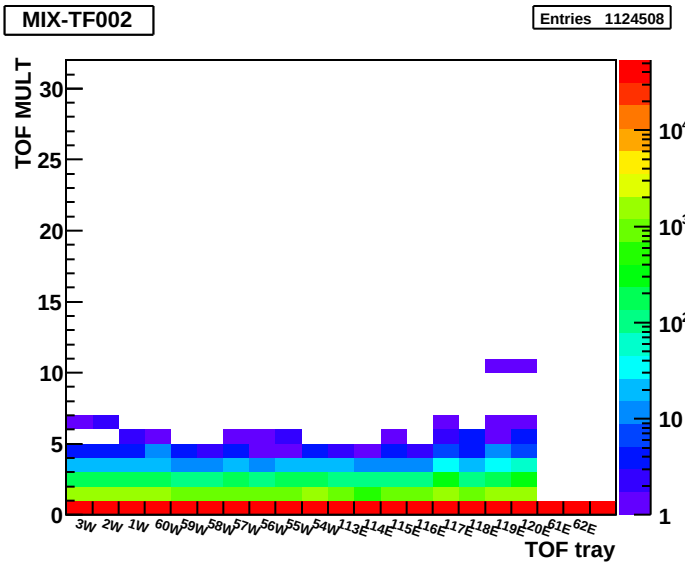

RAT board (ch4)
Entries 428384
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

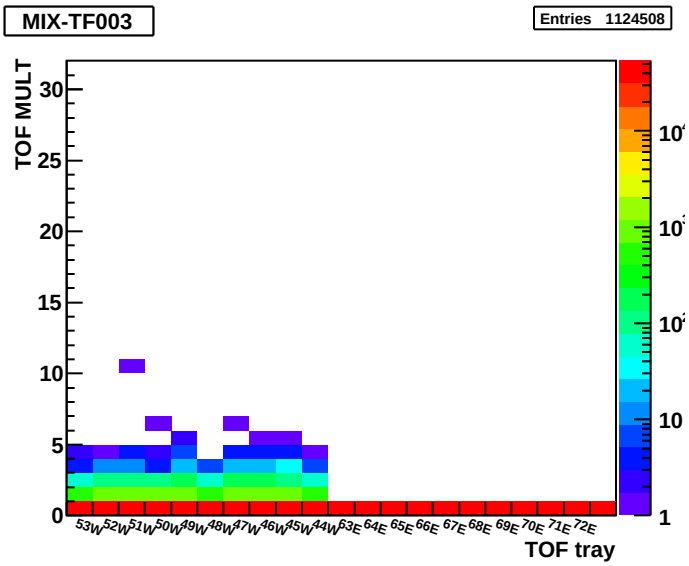

FP201 0-15 (ch5)
Entries 428384
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

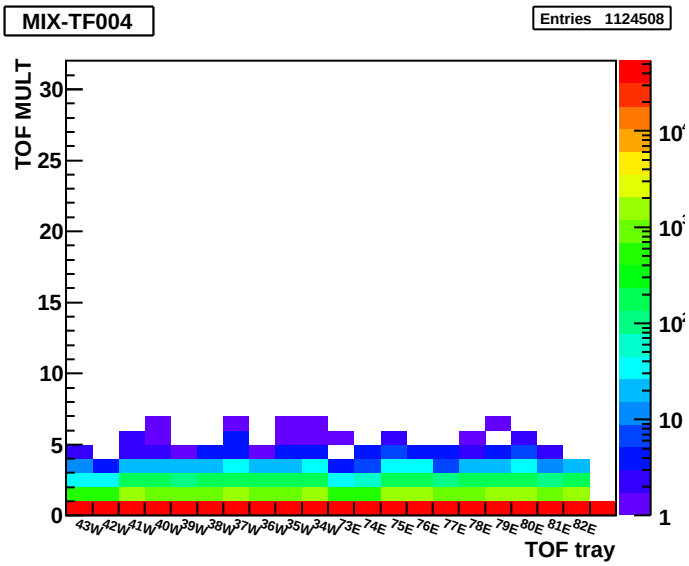

ST201 0-15 (ch6)
Entries 428384
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

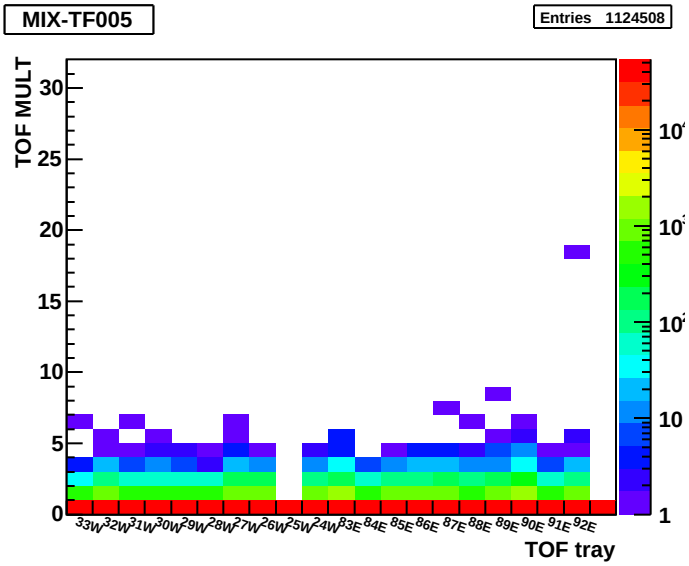

Unused (ch7)
Entries 428384

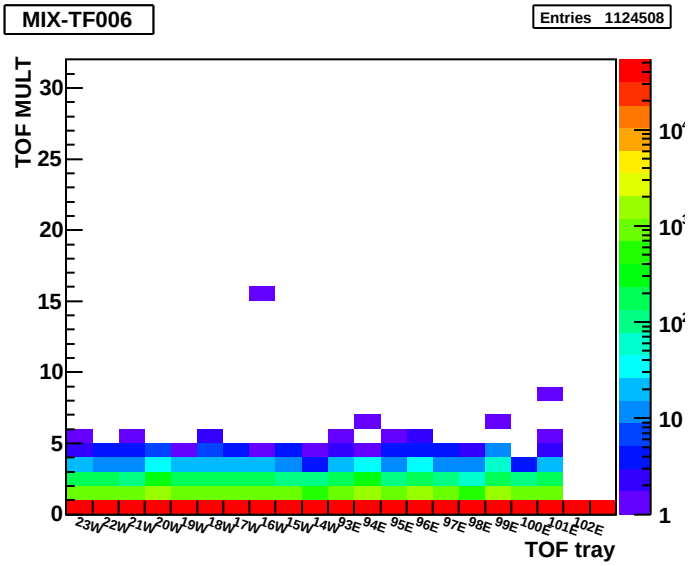

MIX-TF001
Entries 1124508
TOF MULT
TOF tray

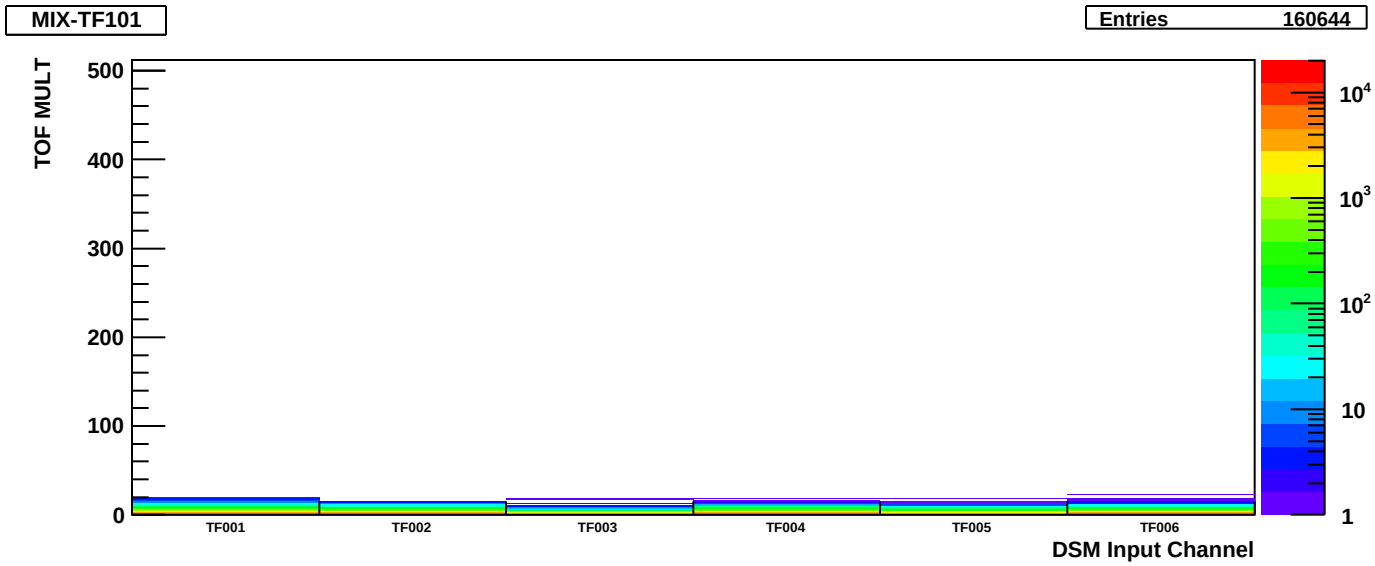

MIX-TF002
Entries 1124508
TOF MULT
TOF tray

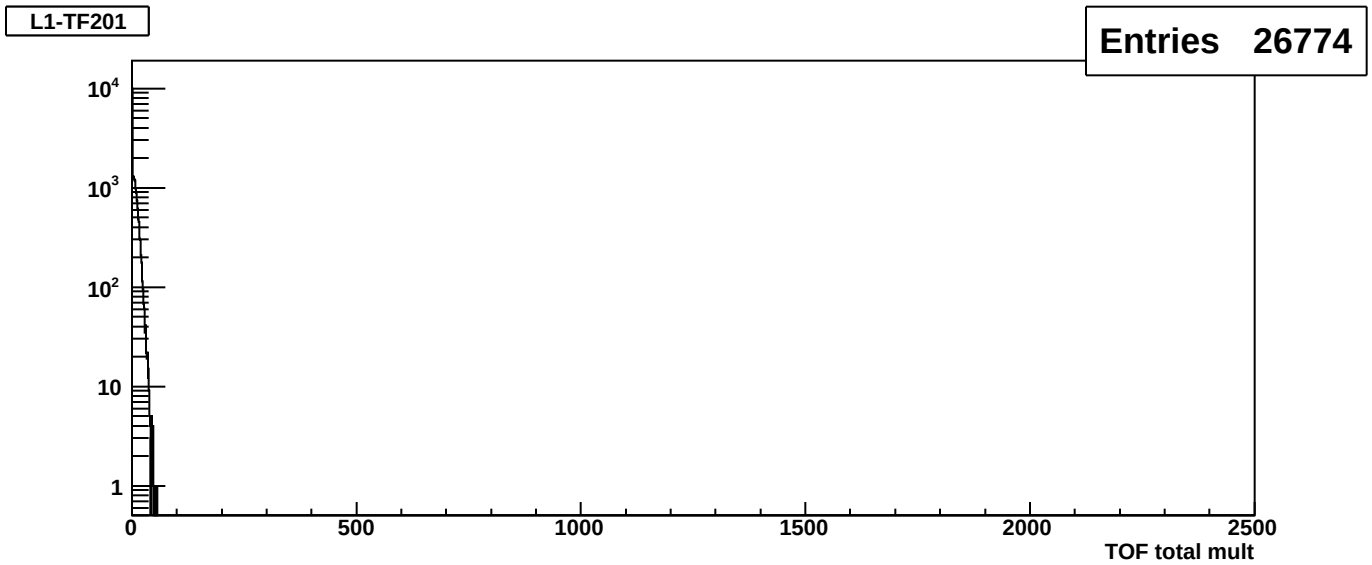

MIX-TF003
Entries 1124508
TOF MULT
TOF tray

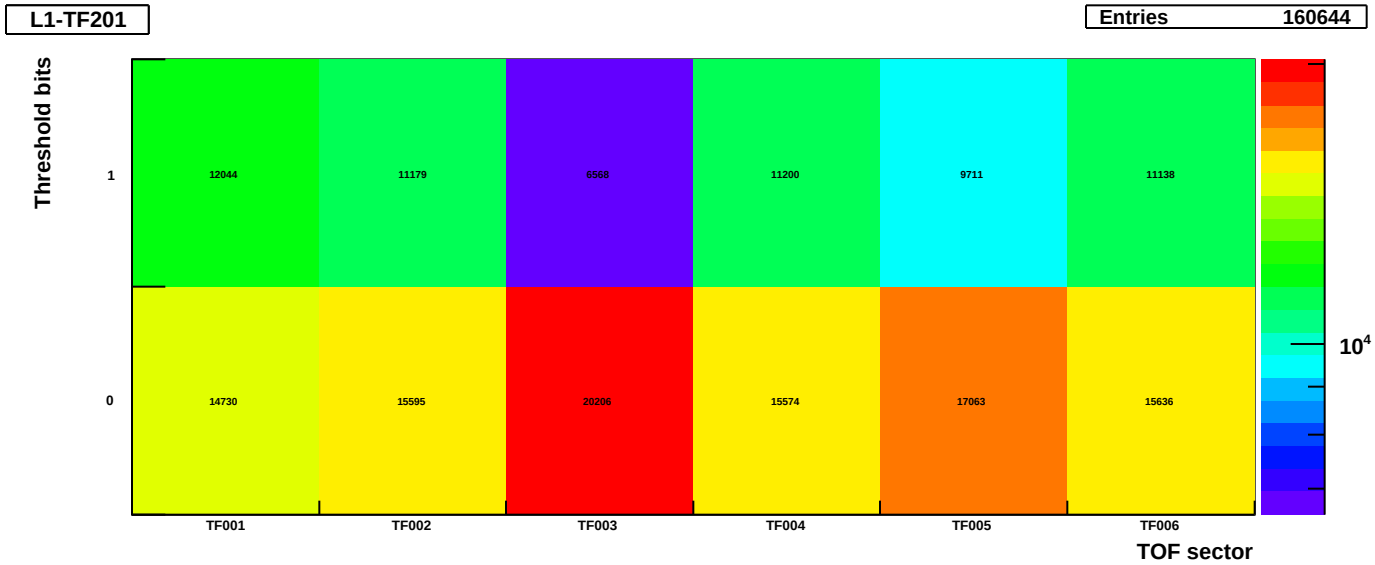

MIX-TF004
Entries 1124508
TOF MULT
TOF tray

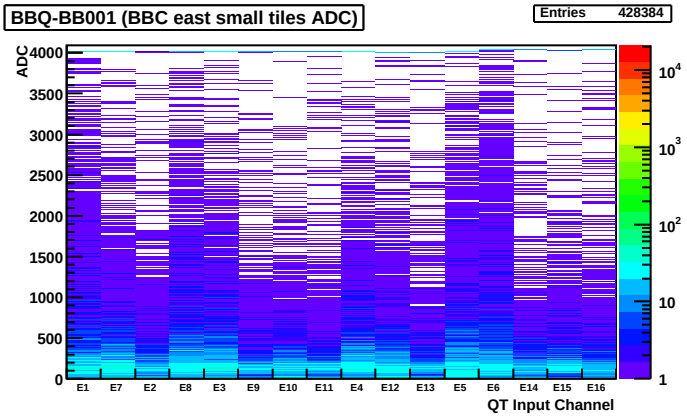

MIX-TF005
Entries 1124508
TOF MULT
TOF tray

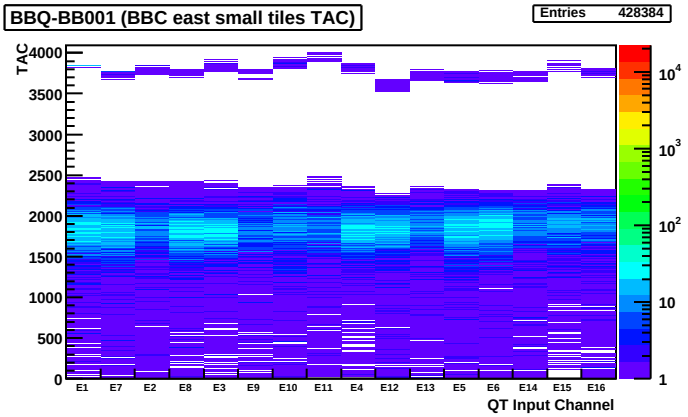

MIX-TF006
Entries 1124508
TOF MULT
TOF tray

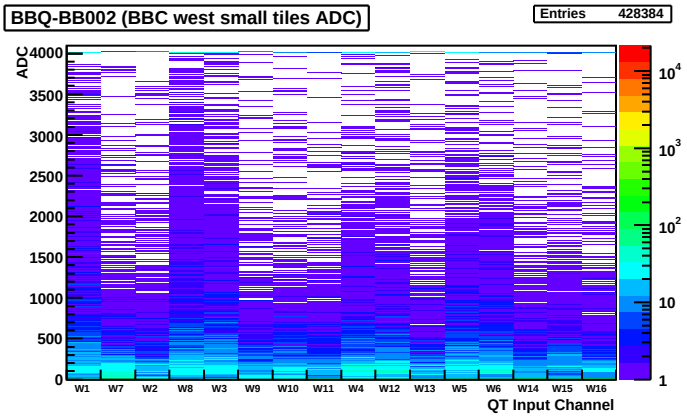

MIX-TF101
Entries 160644
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

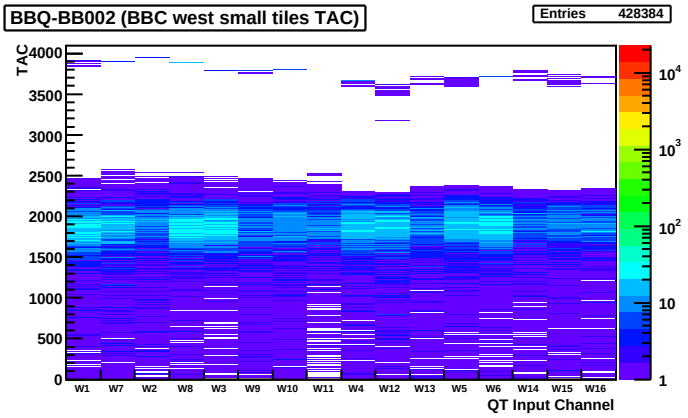

L1-TF201
Entries 26774
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

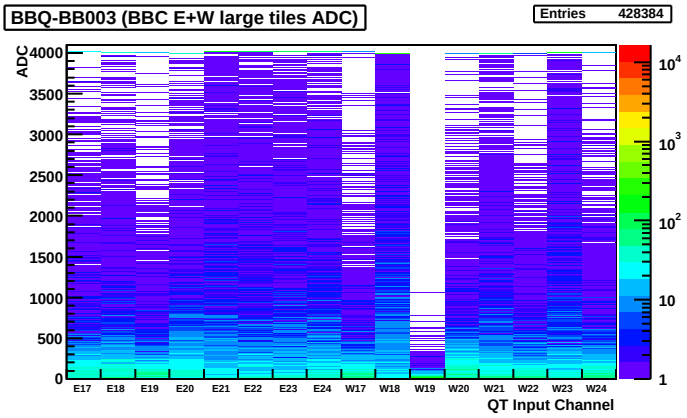

L1-TF201
Entries 160644
Threshold bits
1
0
12044
11179
6568
11200
9711
11138
14730
15595
20206
15574
17063
15636
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

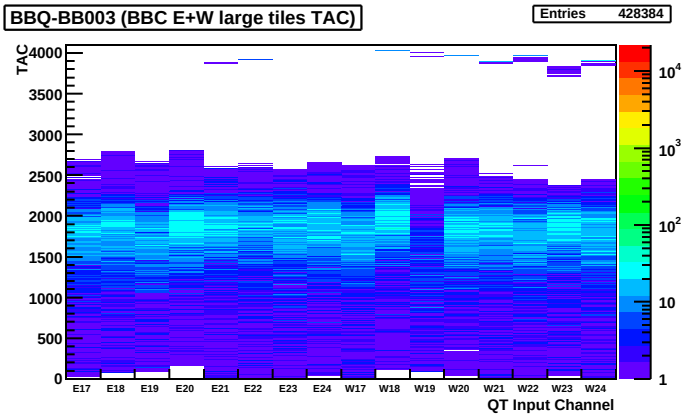

BBQ-BB001 (BBC east small tiles ADC)
Entries 428384
ADC
QT Input Channel

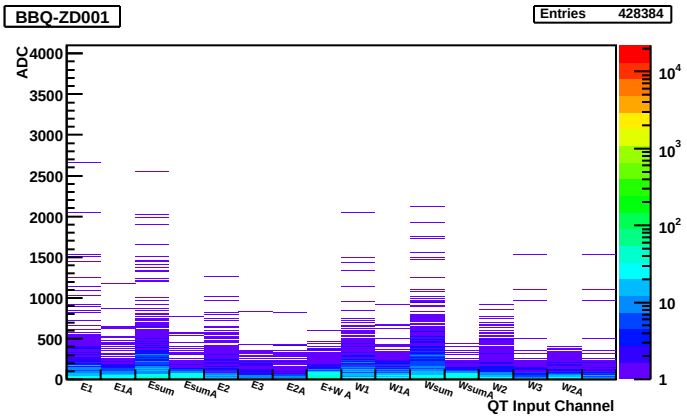

BBQ-BB001 (BBC east small tiles TAC)
Entries 428384
TAC
QT Input Channel

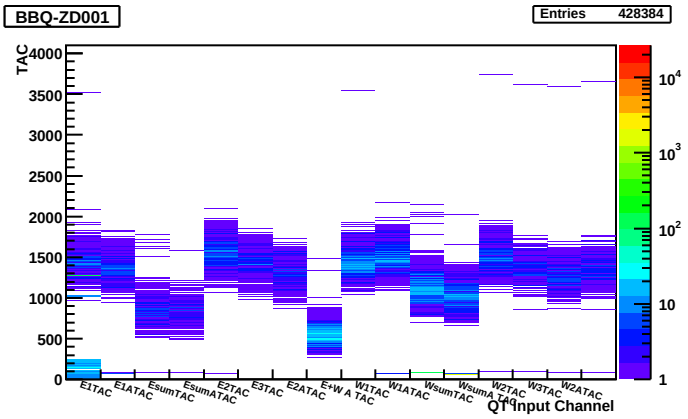

BBQ-BB002 (BBC west small tiles ADC)
Entries 428384
ADC
QT Input Channel

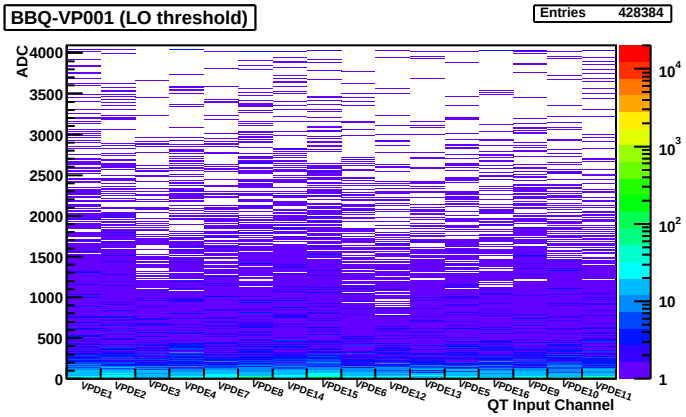

BBQ-BB002 (BBC west small tiles TAC)
Entries 428384
TAC
QT Input Channel

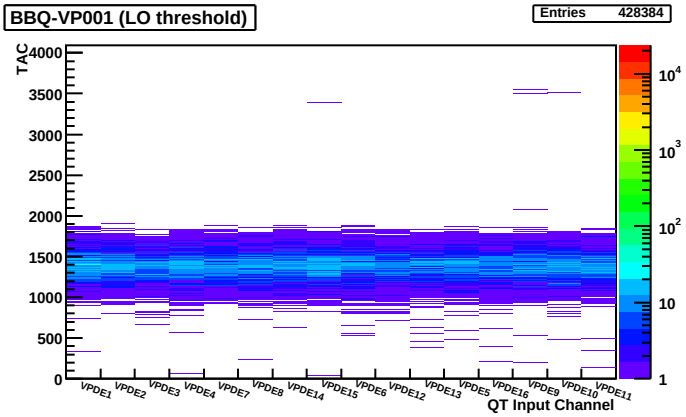

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 428384
ADC
QT Input Channel

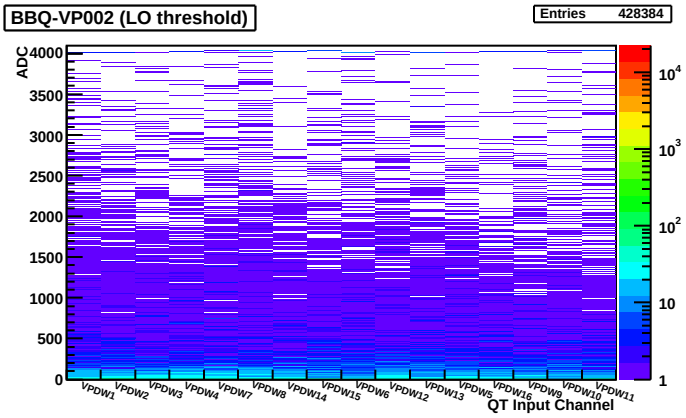

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 428384
TAC
QT Input Channel

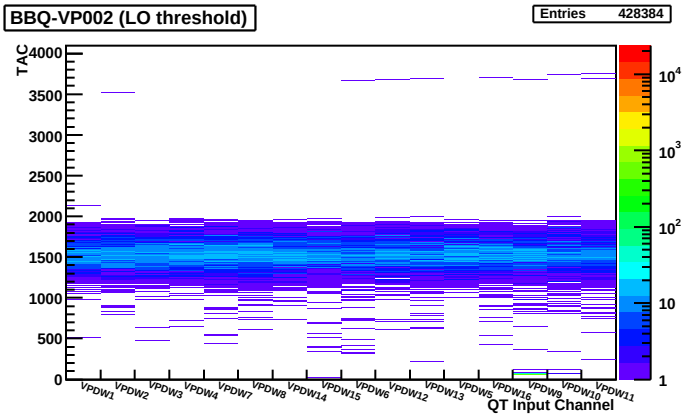

BBQ-ZD001
Entries 428384
ADC
QT Input Channel

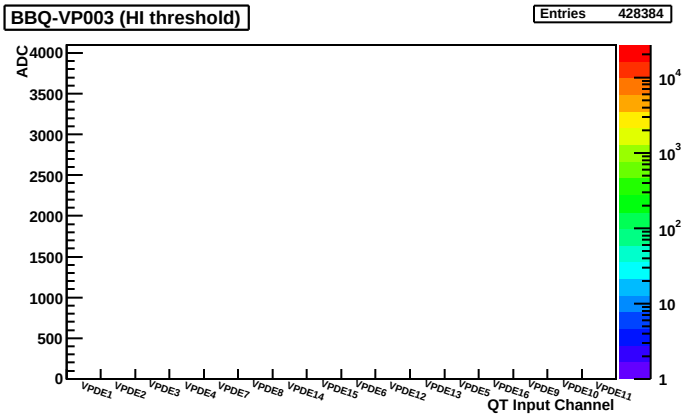

BBQ-ZD001
Entries 428384
TAC
QT Input Channel

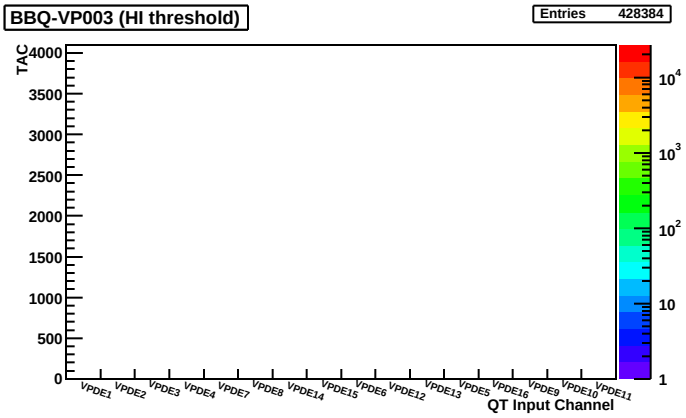

BBQ-VP001 (LO threshold)
Entries 428384
ADC
QT Input Channel

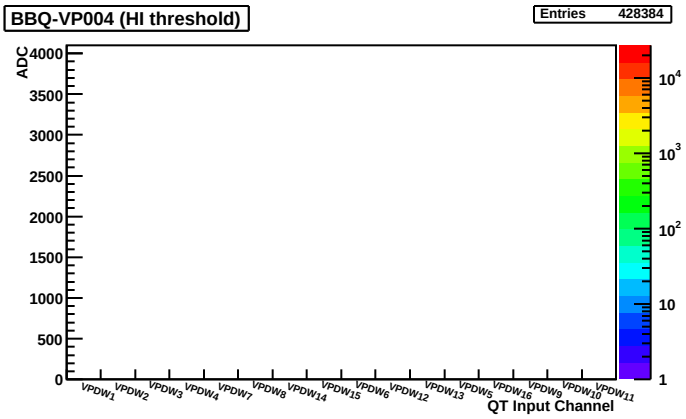

BBQ-VP001 (LO threshold)
Entries 428384
TAC
QT Input Channel

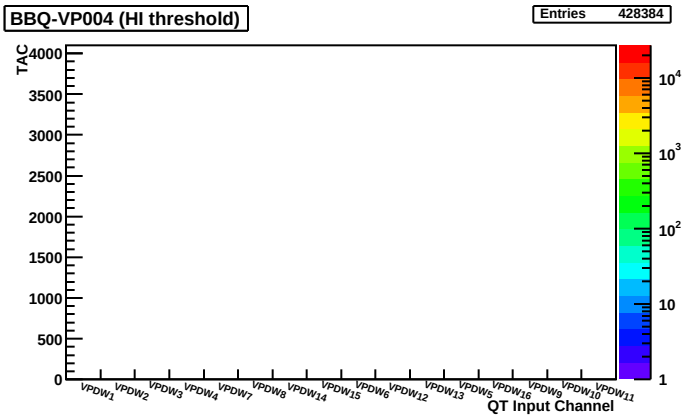

BBQ-VP002 (LO threshold)
Entries 428384
ADC
QT Input Channel

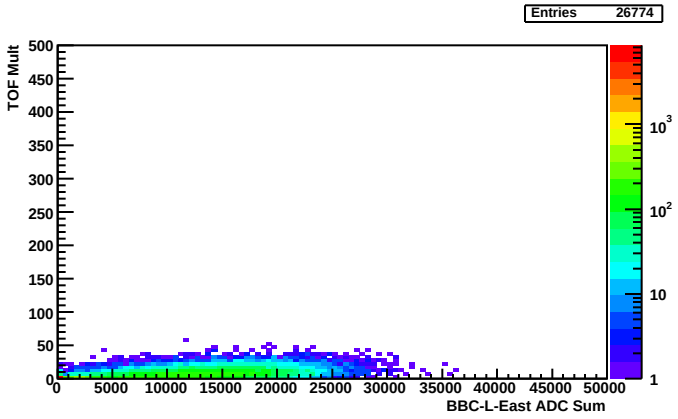

BBQ-VP002 (LO threshold)
Entries 428384
TAC
QT Input Channel

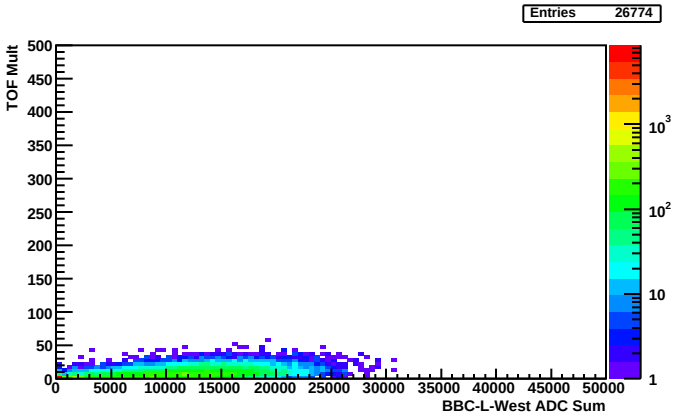

BBQ-VP003 (HI threshold)
Entries 428384
ADC
QT Input Channel

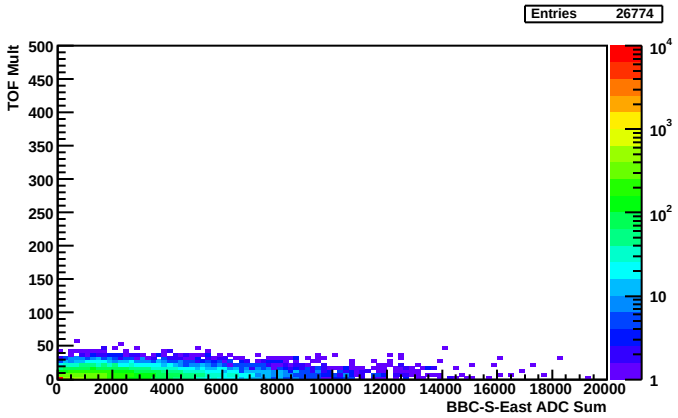

BBQ-VP003 (HI threshold)
Entries 428384
TAC
QT Input Channel

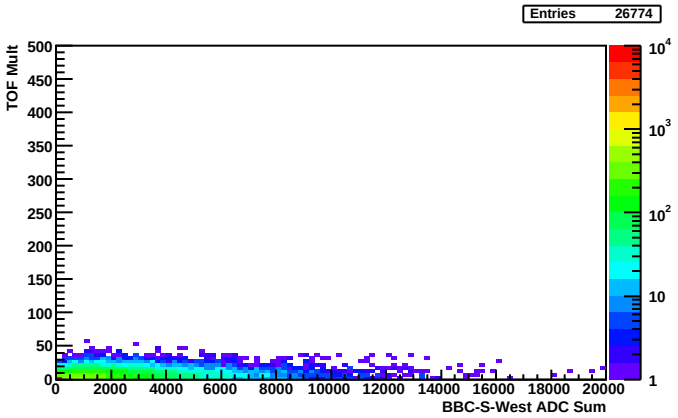

BBQ-VP004 (HI threshold)
Entries 428384
ADC
QT Input Channel

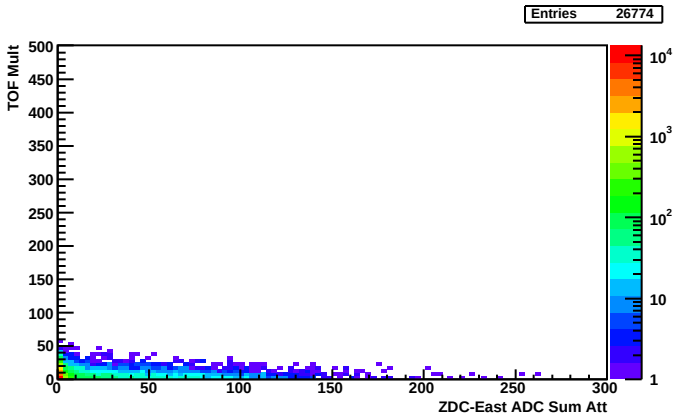

BBQ-VP004 (HI threshold)
Entries 428384
TAC
QT Input Channel

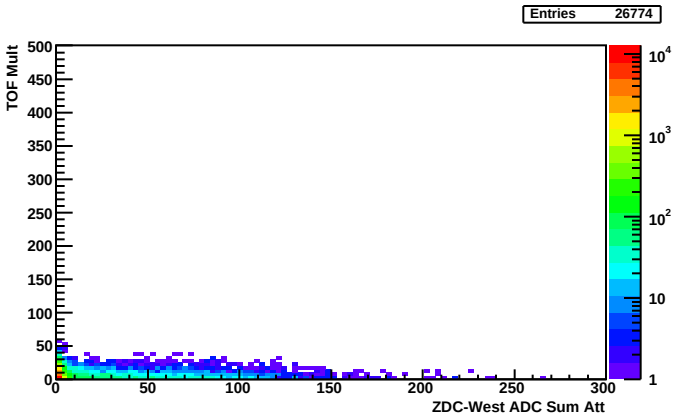

Entries 26774
TOF Mult
BBC-L-East ADC Sum

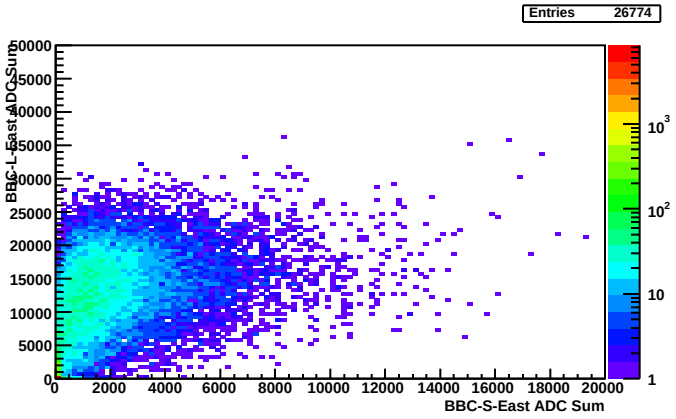

Entries 26774
TOF Mult
BBC-L-West ADC Sum

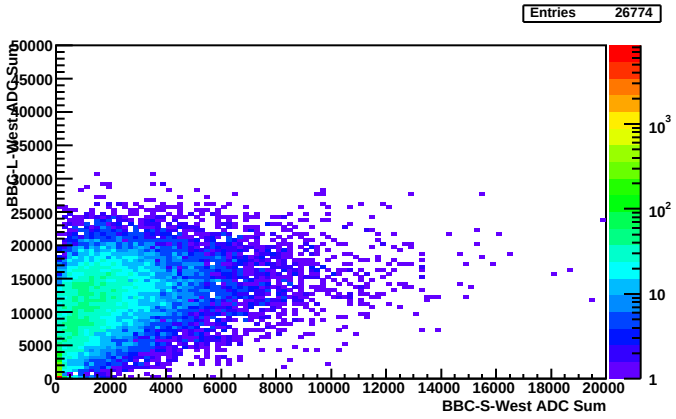

Entries 26774
TOF Mult
BBC-S-East ADC Sum

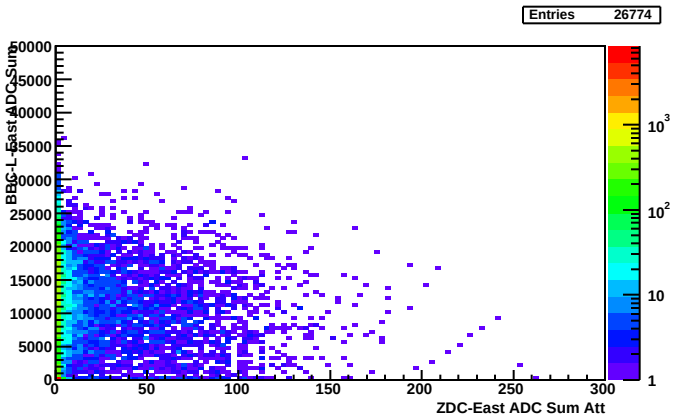

Entries 26774
TOF Mult
BBC-S-West ADC Sum

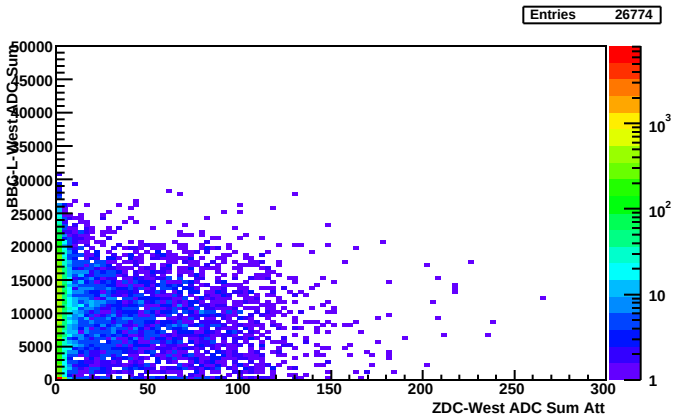

Entries 26774
TOF Mult
ZDC-East ADC Sum Att

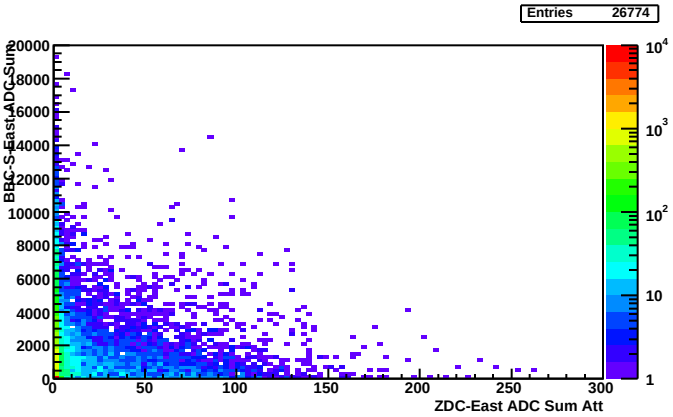

Entries 26774
TOF Mult
ZDC-West ADC Sum Att

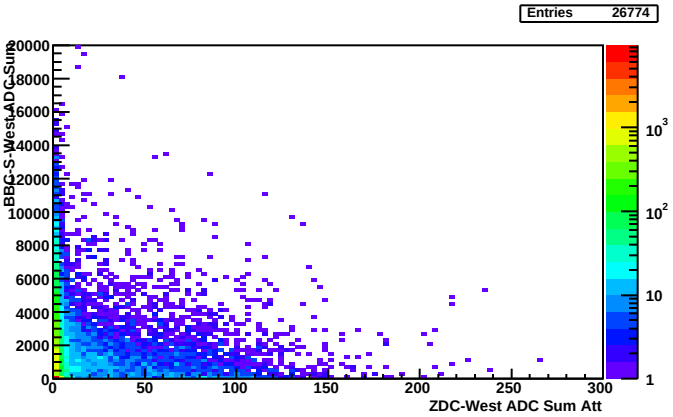

Entries 26774
BBC-L-East ADC Sum
BBC-S-East ADC Sum

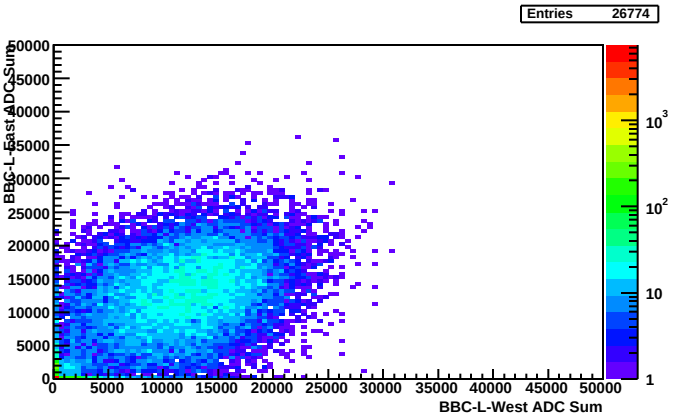

Entries 26774
BBC-L-West ADC Sum
BBC-S-West ADC Sum

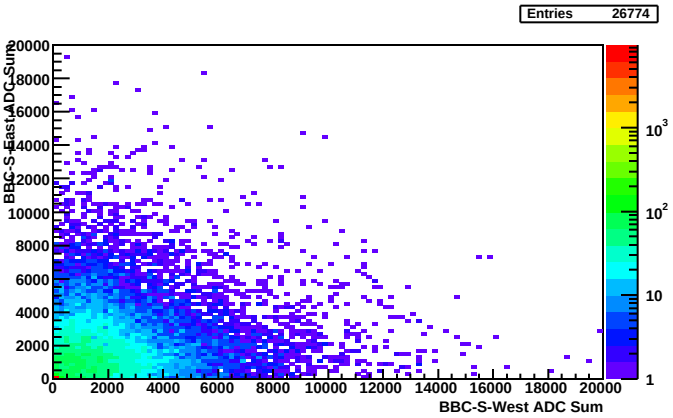

Entries 26774
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

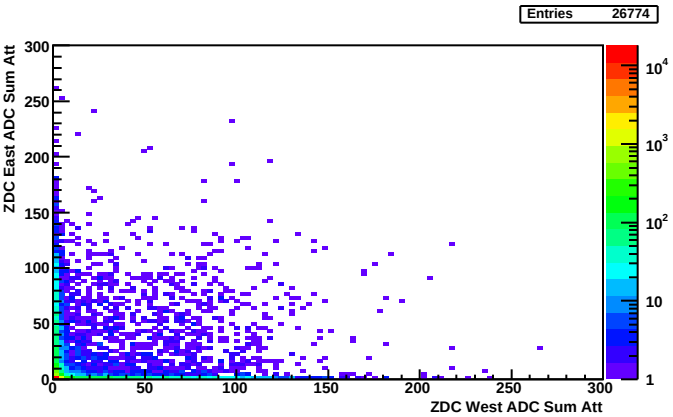

Entries 26774
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

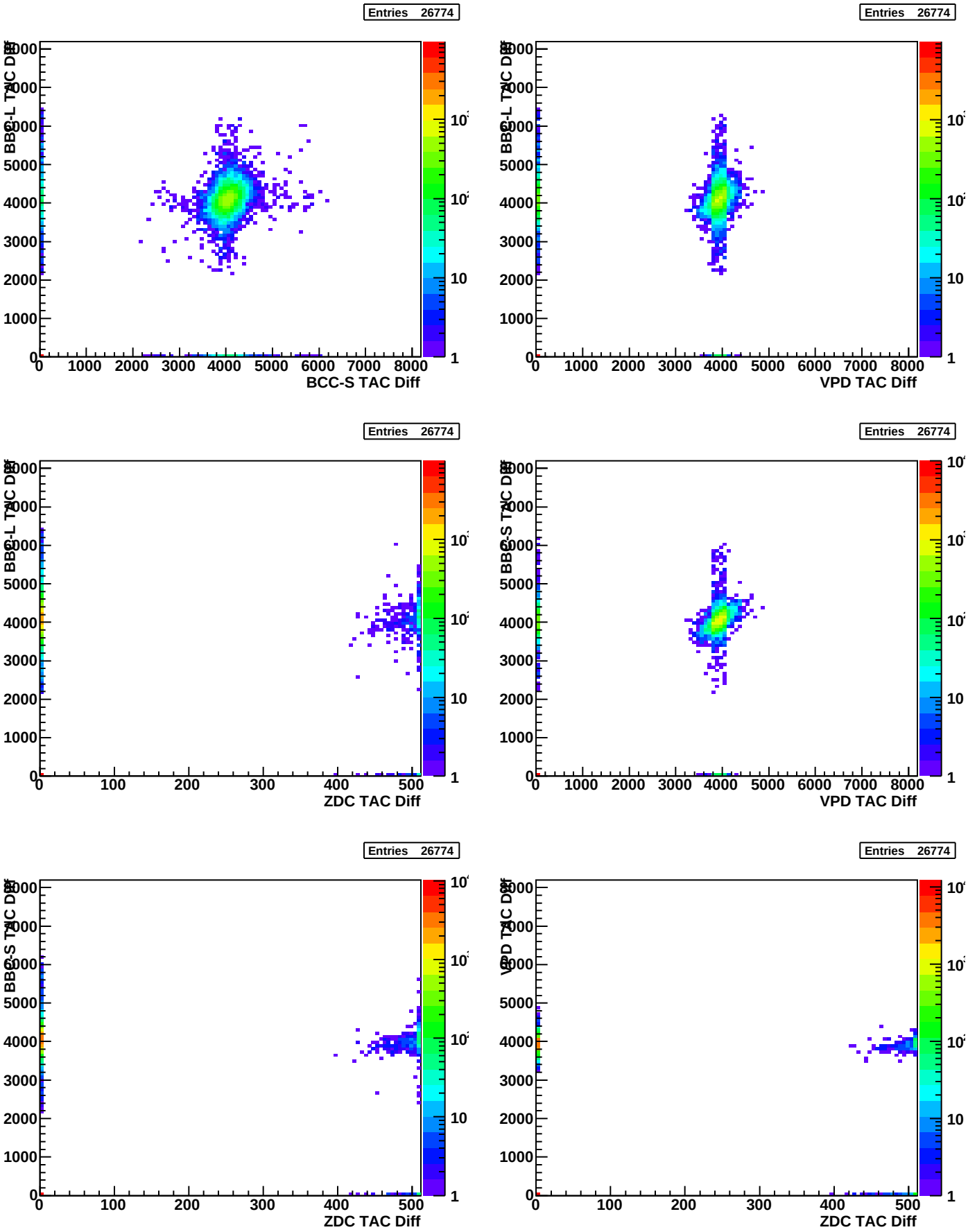

Entries 26774
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

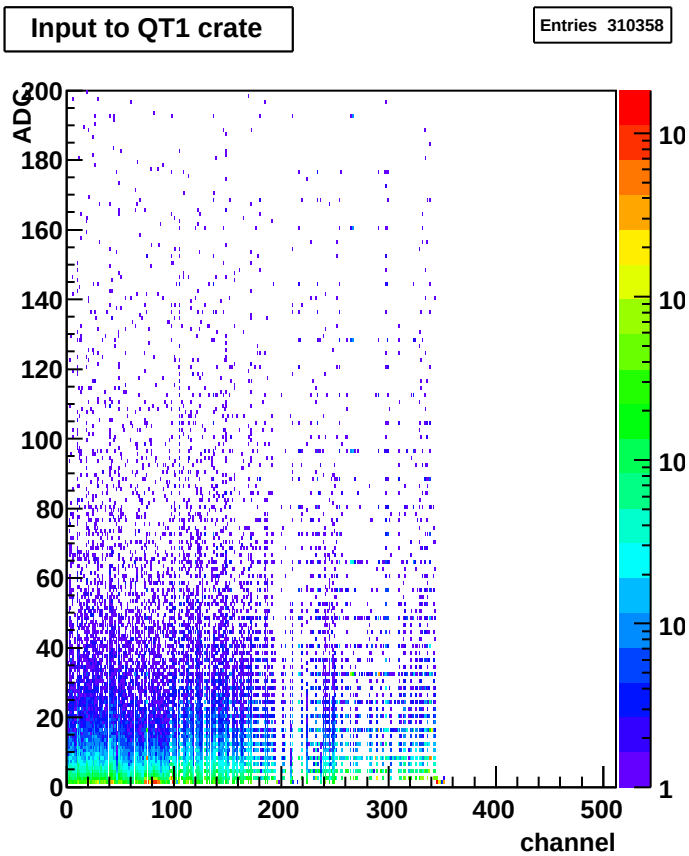

Entries 26774
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

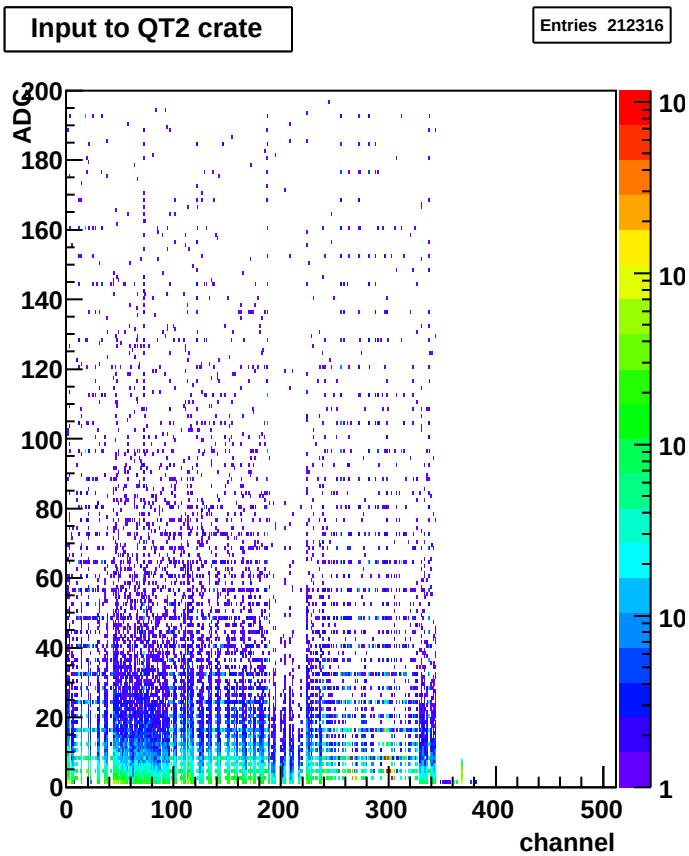

Entries 26774
BBC-L-East ADC Sum
BBC-L-West ADC Sum

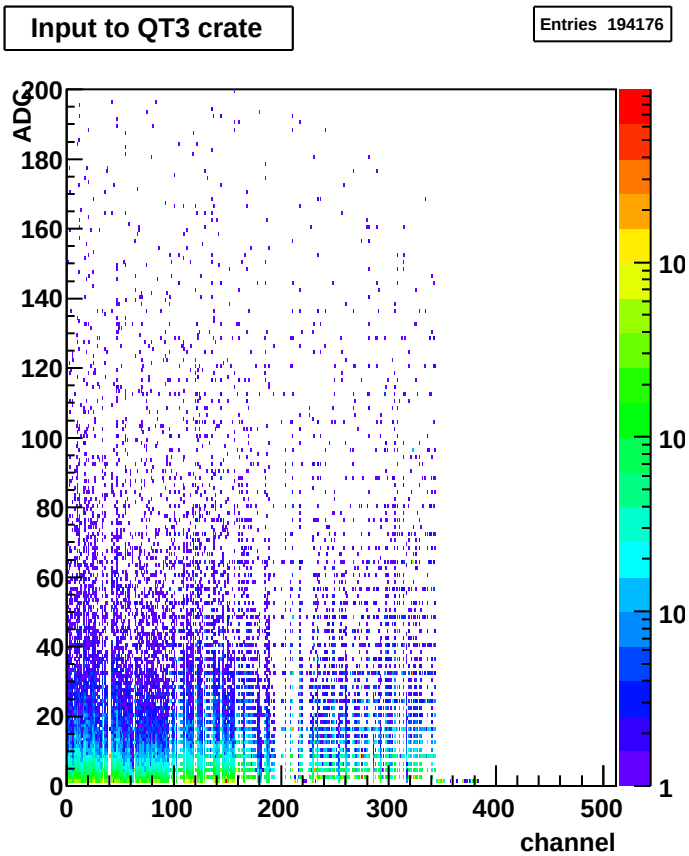

Entries 26774
BBC-S-East ADC Sum
BBC-S-West ADC Sum

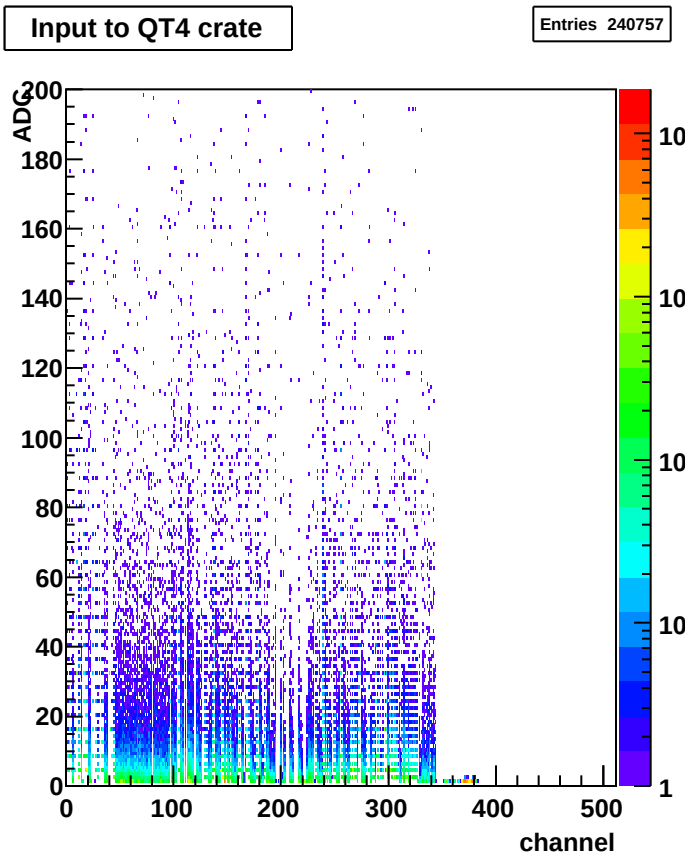

Entries 26774
ZDC East ADC Sum Att
ZDC West ADC Sum Att

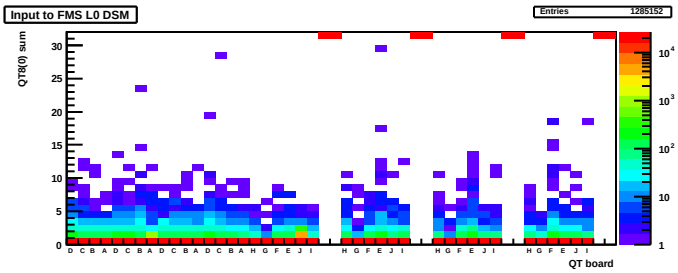

Entries 26774
BBC-L TAC Diff
BCC-S TAC Diff
Entries 26774
BBC-L TAC Diff
VPD TAC Diff
Entries 26774
BBC-L TAC Diff
ZDC TAC Diff
Entries 26774
BBC-S TAC Diff
VPD TAC Diff
Entries 26774
BBC-S TAC Diff
ZDC TAC Diff
Entries 26774
VPD TAC Diff
ZDC TAC Diff

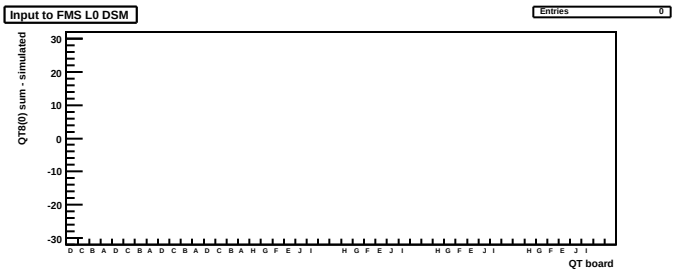

Input to QT1 crate
Entries 310358
ADC
channel

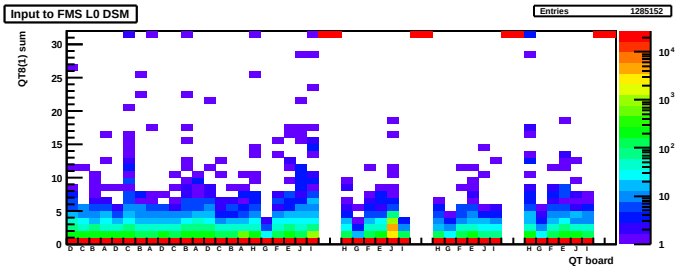

Input to QT2 crate
Entries 212316
ADC
channel

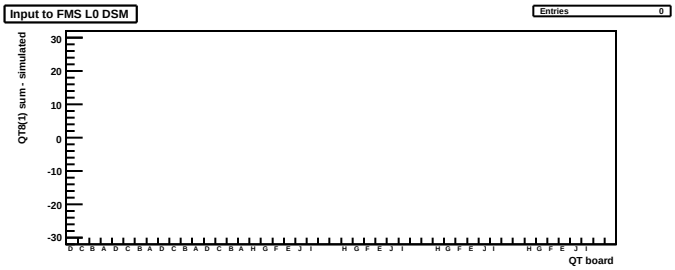

Input to QT3 crate
Entries 194176
ADC
channel

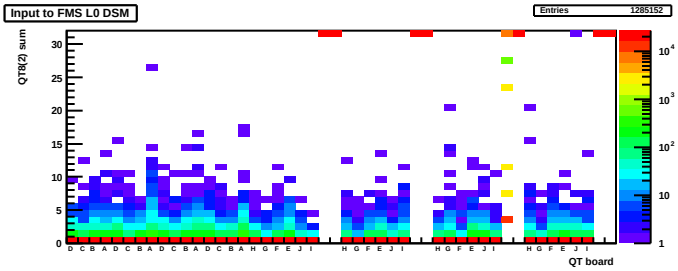

Input to QT4 crate
Entries 240757
ADC
channel

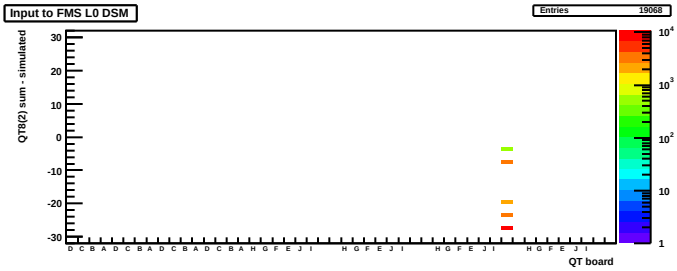

Input to FMS L0 DSM
Entries 1285152
QT8(0) sum
QT board

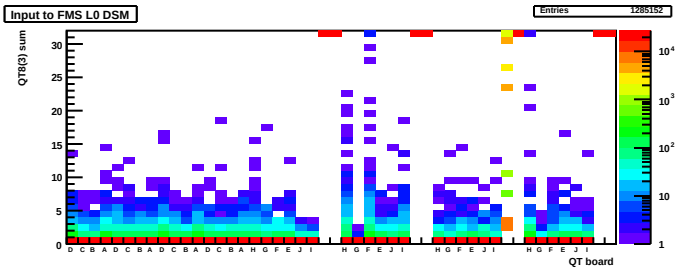

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

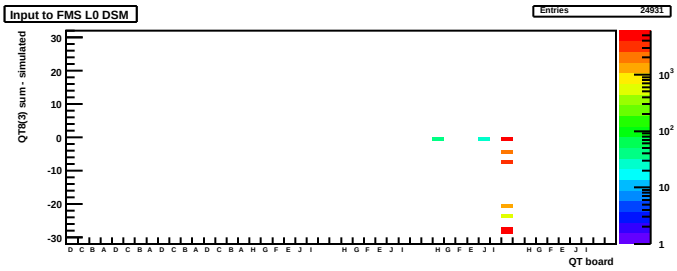

Input to FMS L0 DSM
Entries 1285152
QT8(1) sum
QT board

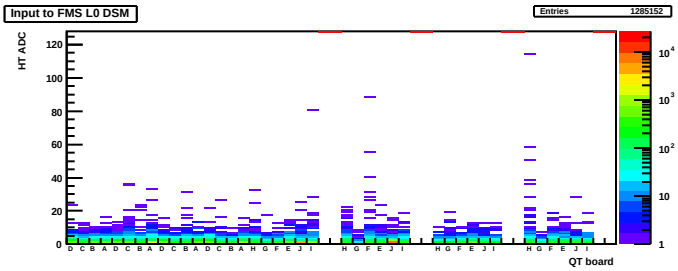

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

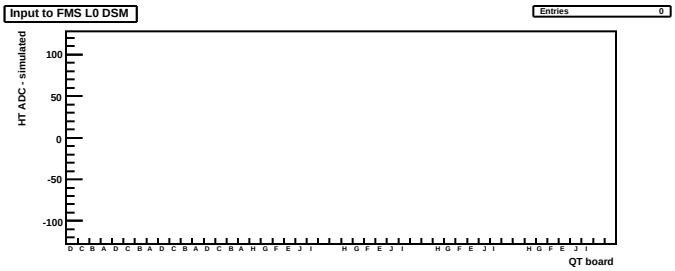

Input to FMS L0 DSM
Entries 1285152
QT8(2) sum
QT board

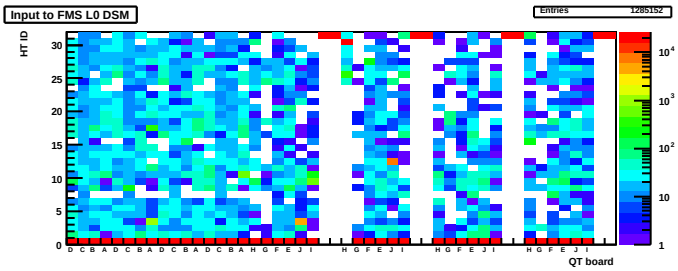

Input to FMS L0 DSM
Entries 19068
QT8(2) sum - simulated
QT board

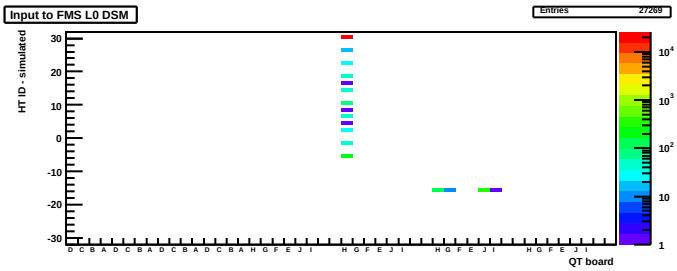

Input to FMS L0 DSM
Entries 1285152
QT8(3) sum
QT board

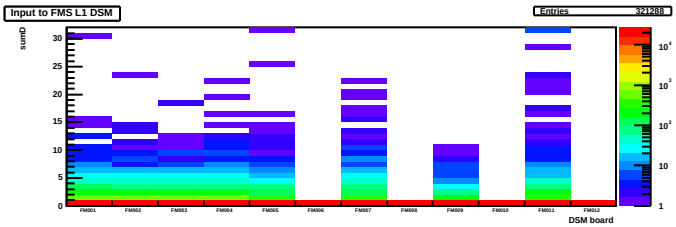

Input to FMS L0 DSM
Entries 24931
QT8(3) sum - simulated
QT board

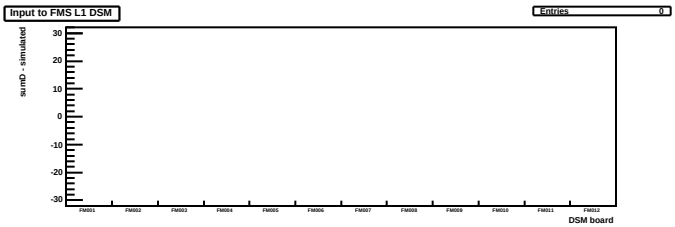

Input to FMS L0 DSM
Entries 1285152
HT ADC
QT board

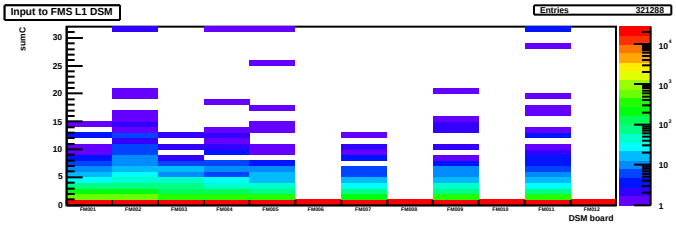

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

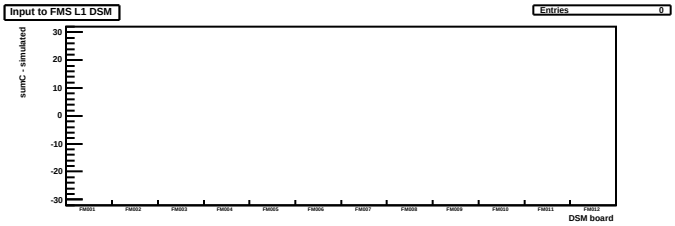

Input to FMS L0 DSM
Entries 1285152
HT ID
QT board

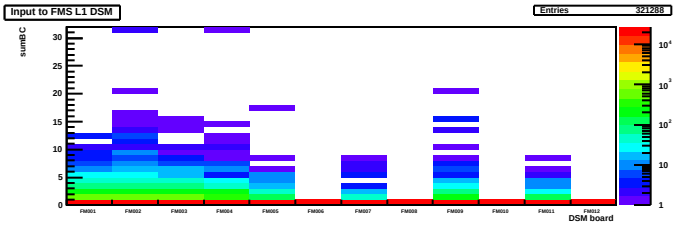

Input to FMS L0 DSM
Entries 27269
HT ID - simulated
QT board

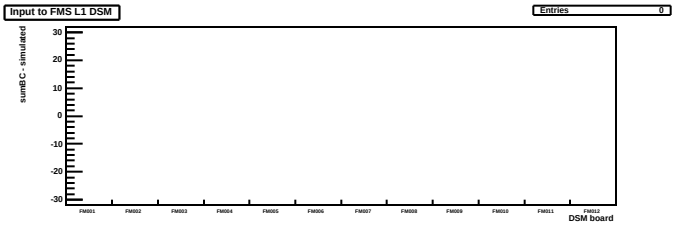

Input to FMS L1 DSM
Entries 321288
sumD
DSM board

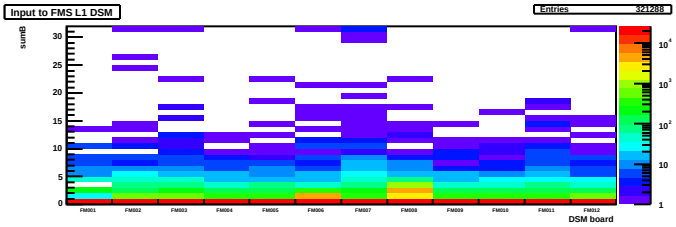

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

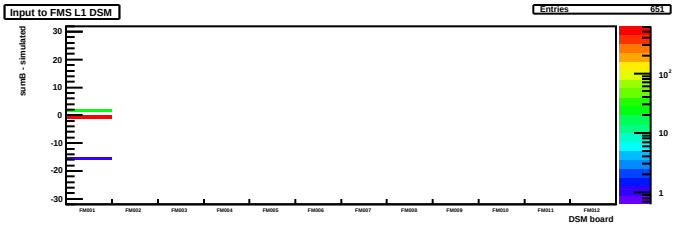

Input to FMS L1 DSM
Entries 321288
sumC
DSM board

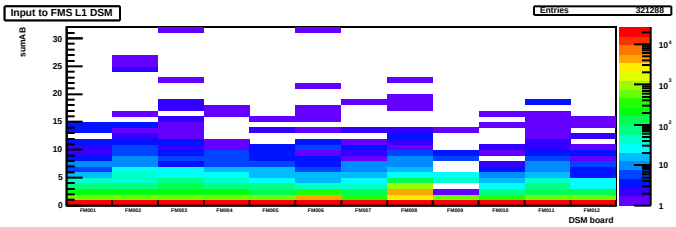

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

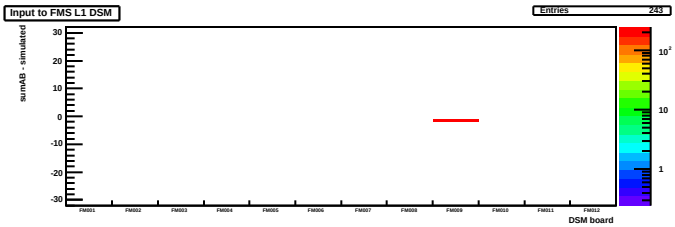

Input to FMS L1 DSM
Entries 321288
sumBC
DSM board

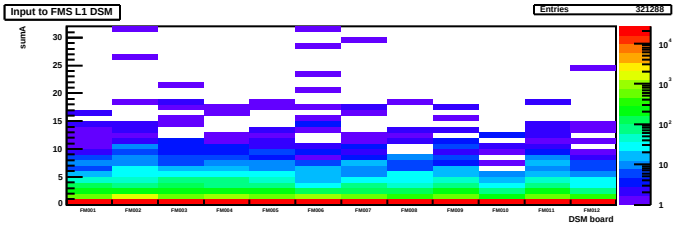

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

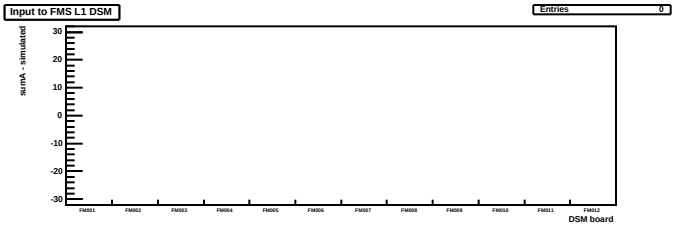

Input to FMS L1 DSM
Entries 321288
sumB
DSM board

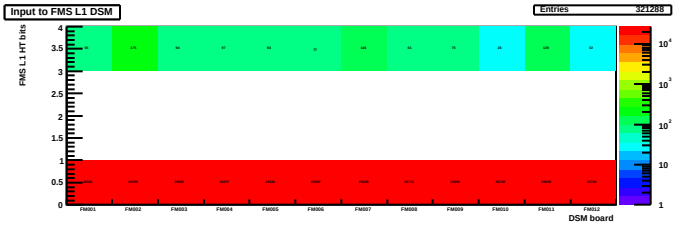

Input to FMS L1 DSM
Entries 651
sumB - simulated
DSM board

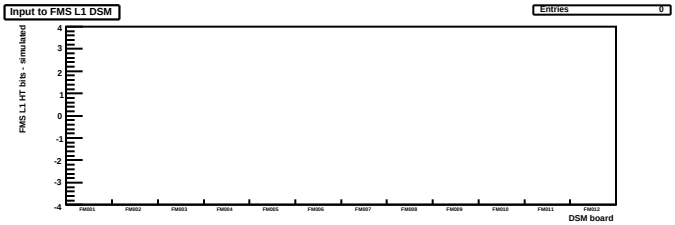

Input to FMS L1 DSM
Entries 321288
sumAB
DSM board

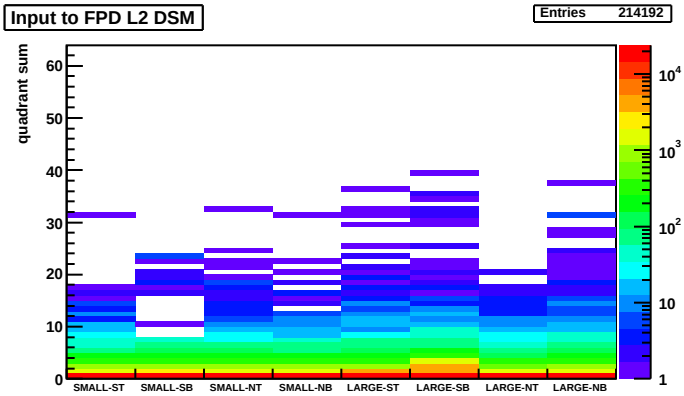

Input to FMS L1 DSM
Entries 243
sumAB - simulated
DSM board

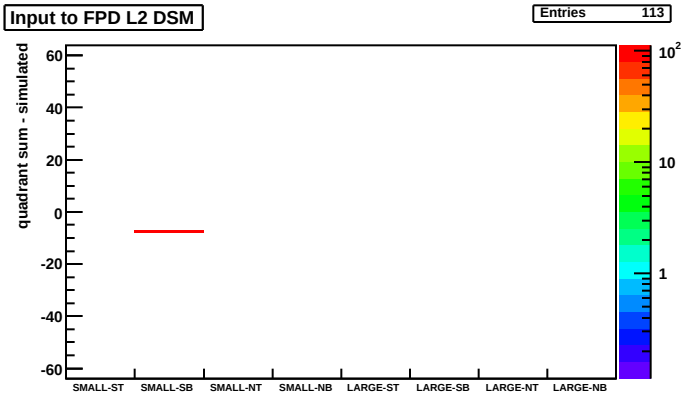

Input to FMS L1 DSM
Entries 321288
sumA
DSM board

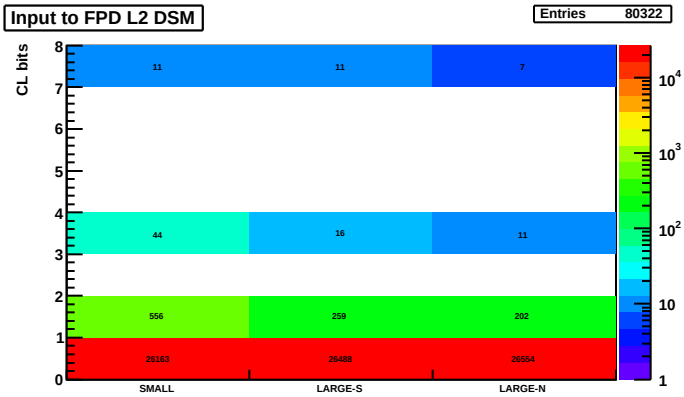

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

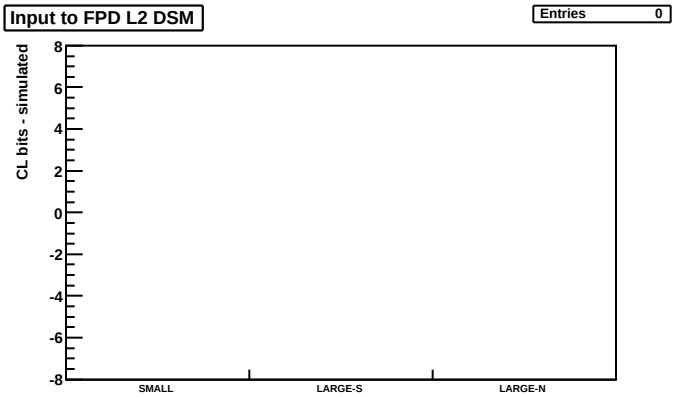

Input to FMS L1 DSM
Entries 321288
FMS L1 HT bits
DSM board

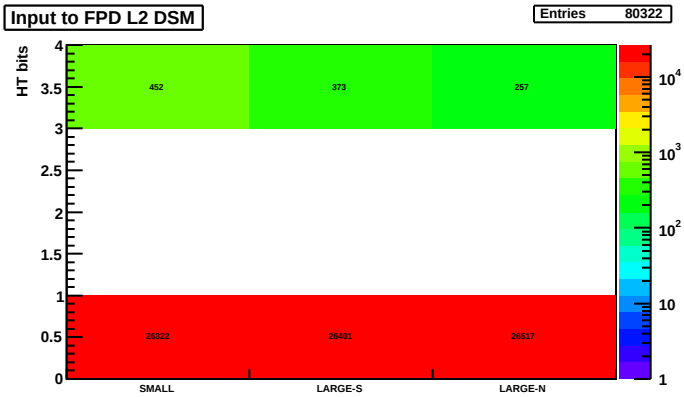

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

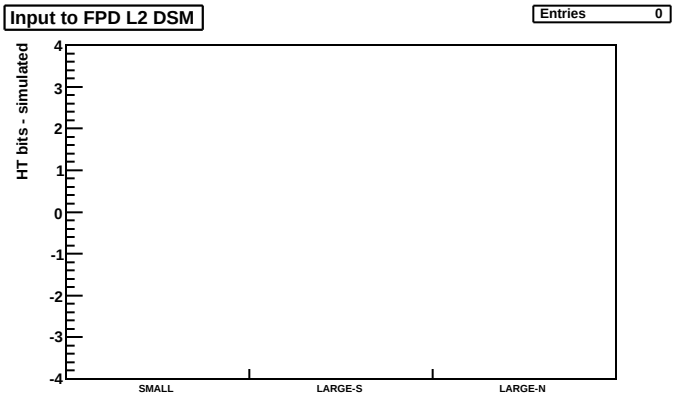

Input to FPD L2 DSM
Entries 214192
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

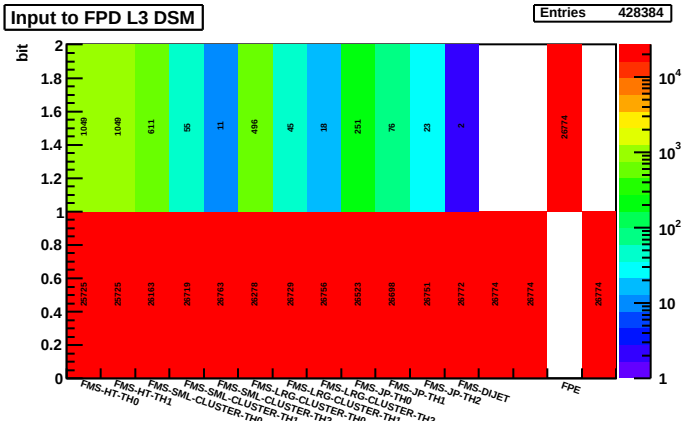

Input to FPD L2 DSM
Entries 113
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

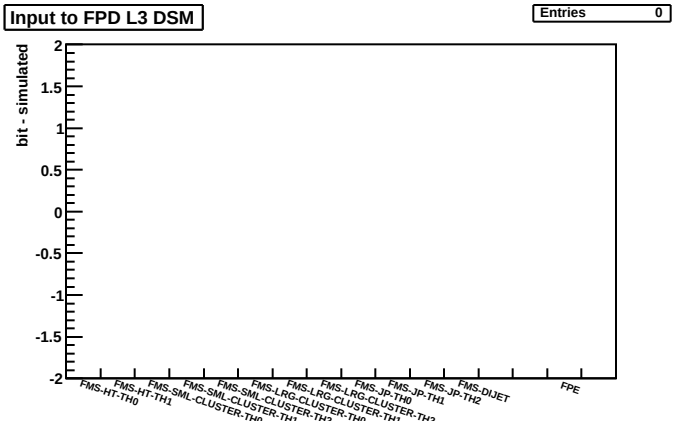

Input to FPD L2 DSM
Entries 80322
CL bits
11
11
7
44
16
11
556
259
202
26163
26488
26554
SMALL
LARGE-S
LARGE-N

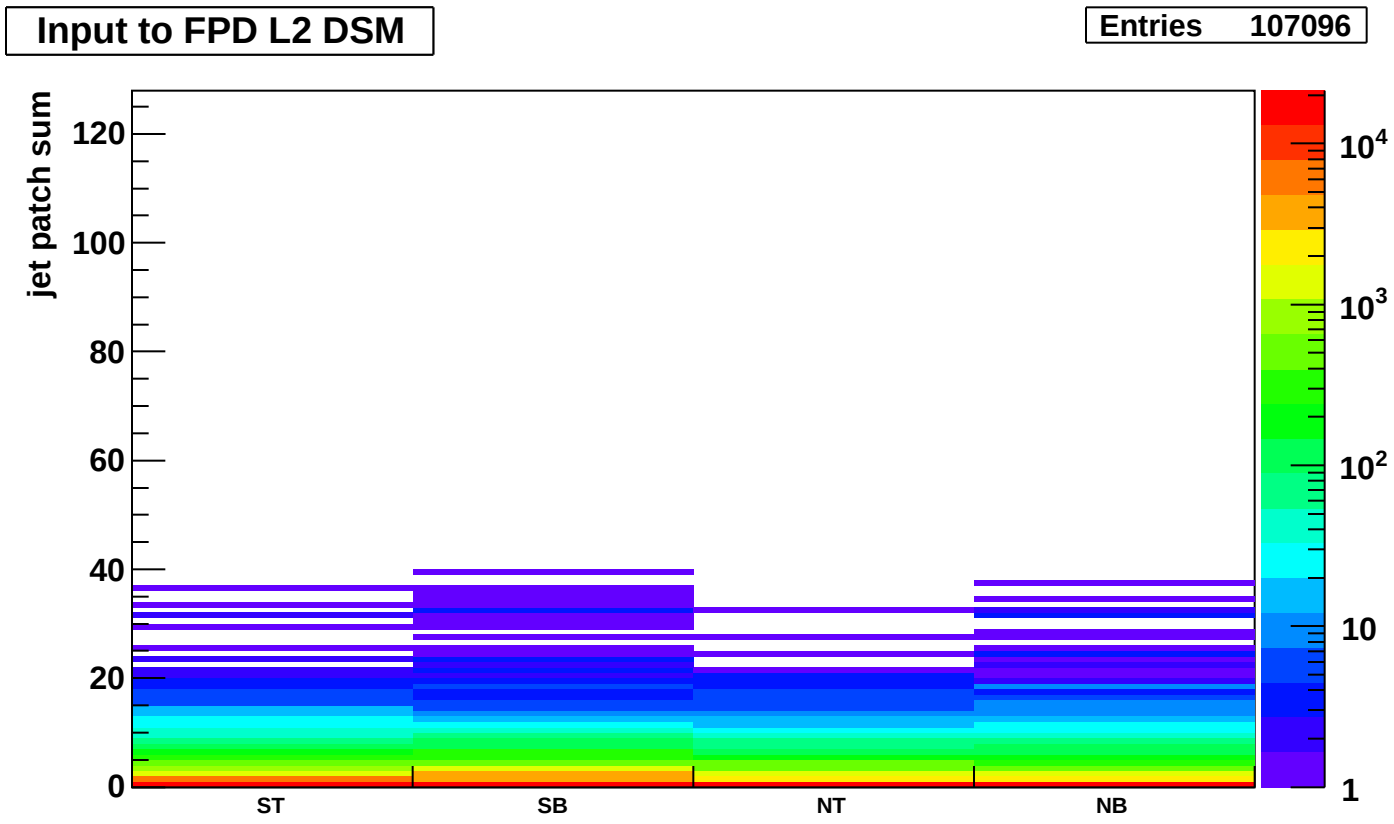

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

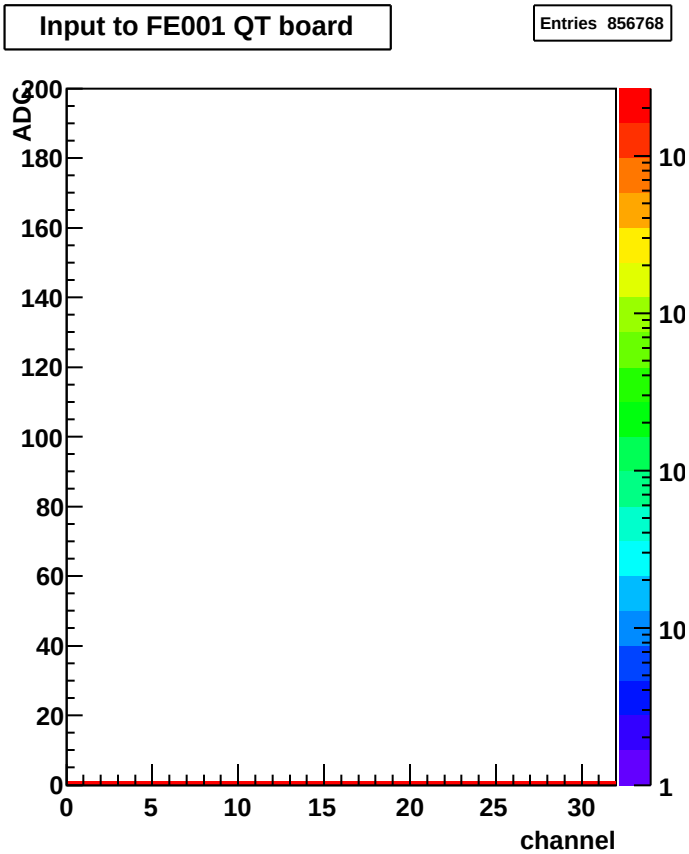

Input to FPD L2 DSM
Entries 80322
HT bits
452
373
257
26322
26401
26517
SMALL
LARGE-S
LARGE-N

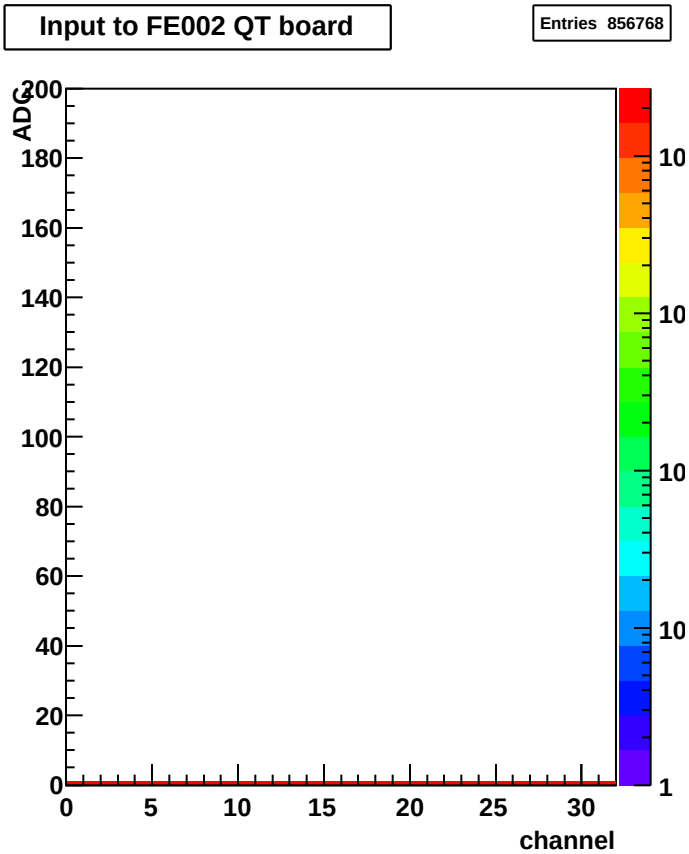

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

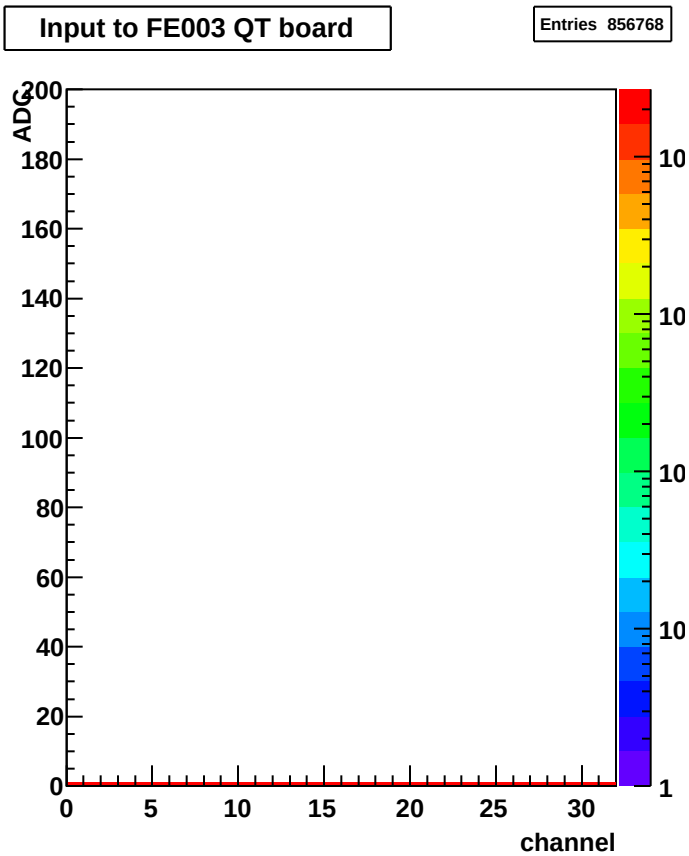

Input to FPD L3 DSM
Entries 428384
bit
1048
1049
611
55
11
496
45
18
251
76
23
2
25974
25725
25725
26163
26719
26763
26278
26729
26756
26523
26598
26751
26772
26774
26774
26774
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

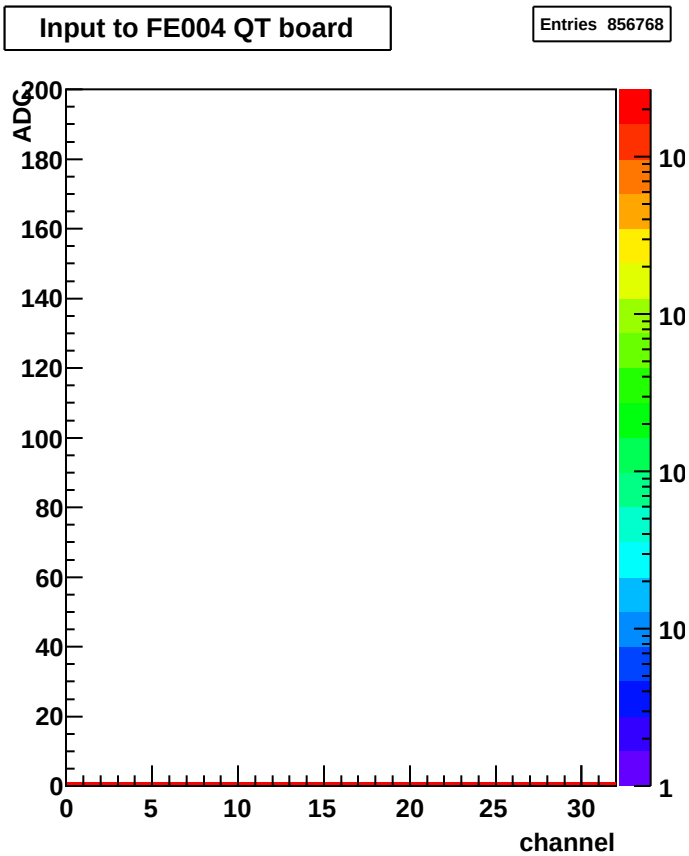

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

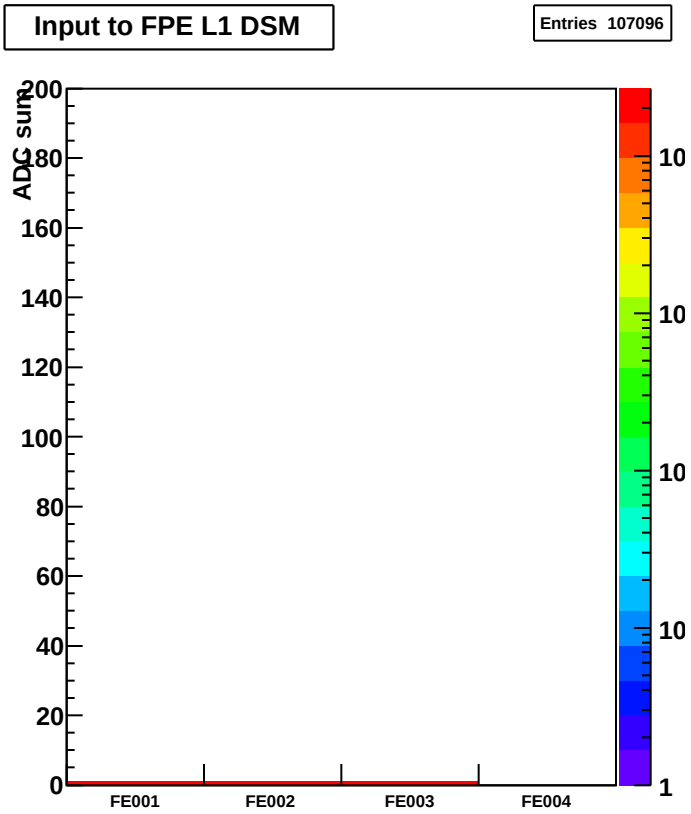

Input to FPD L2 DSM
Entries 107096
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

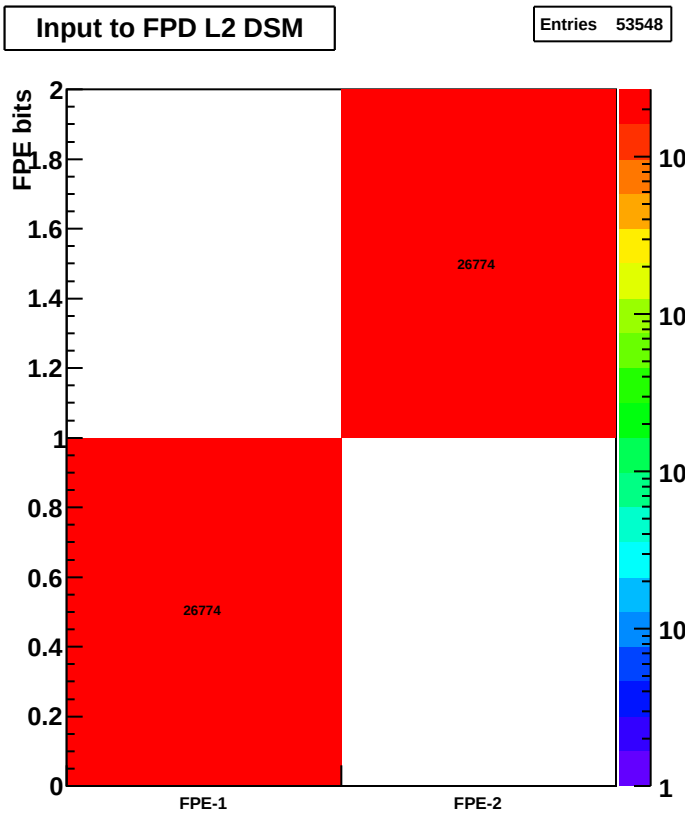

Input to FE001 QT board
Entries 856768
ADC
channel

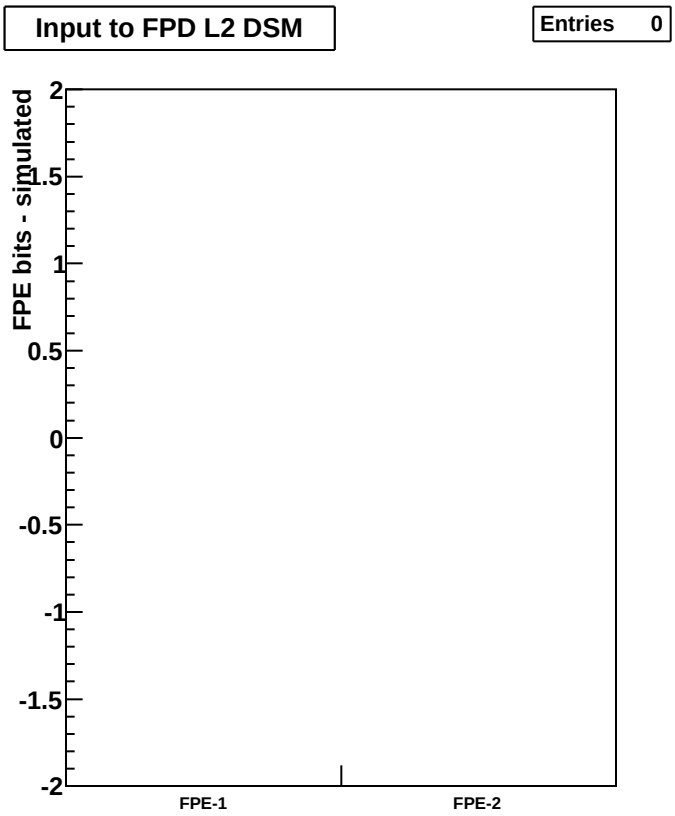

Input to FE002 QT board
Entries 856768
ADC
channel


Input to FE003 QT board
Entries 856768
ADC
channel


Input to FE004 QT board
Entries 856768
ADC
channel


Input to FPE L1 DSM
Entries 107096
ADC sum
200
180
160
140
120
100
80
60
40
20
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 53548
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
26774
26774
FPE-1
FPE-2
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2