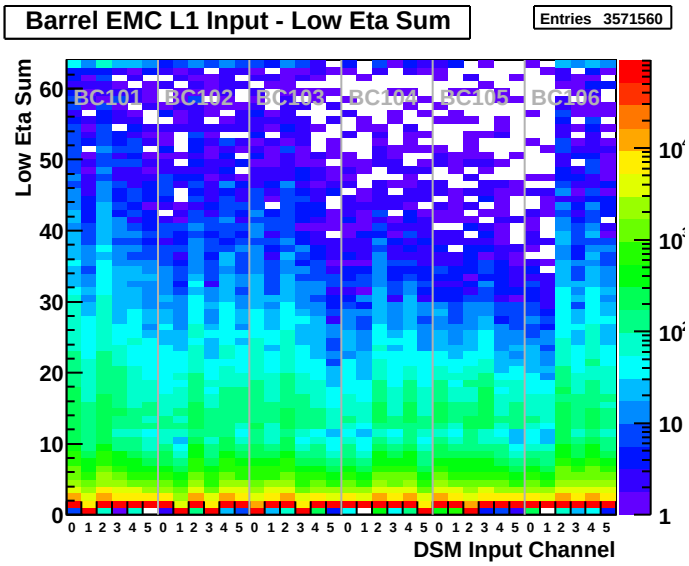
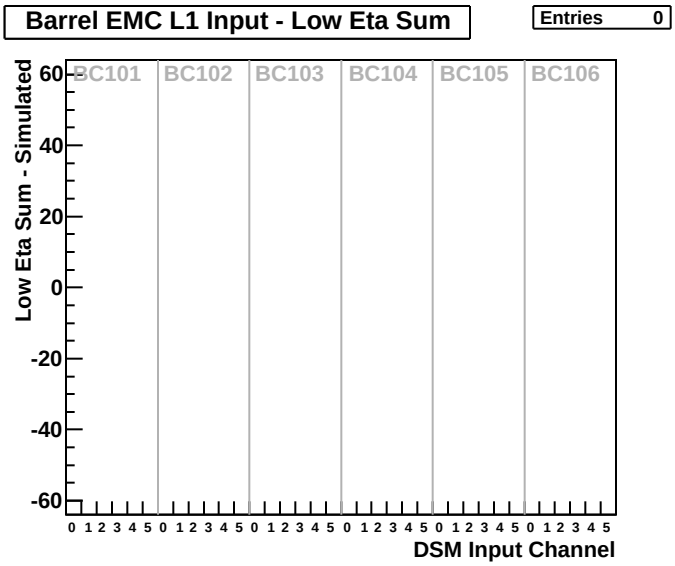
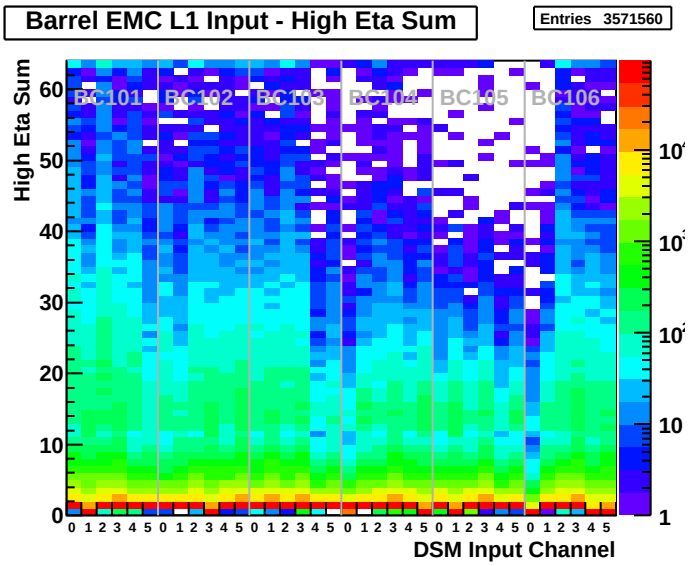
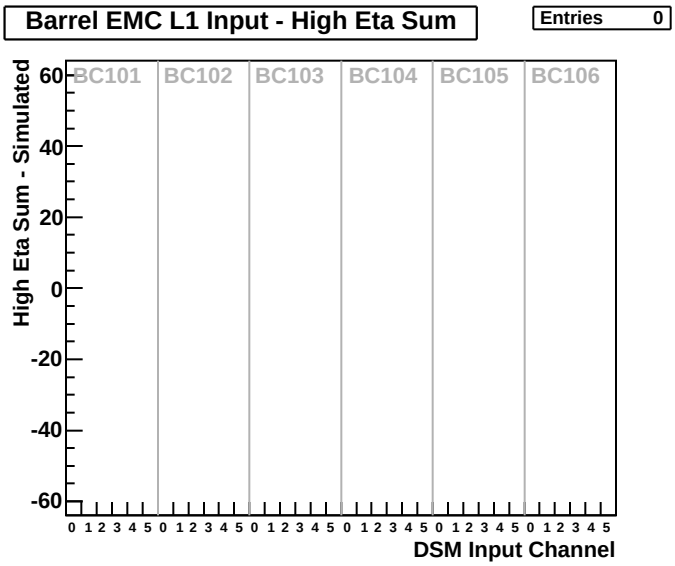
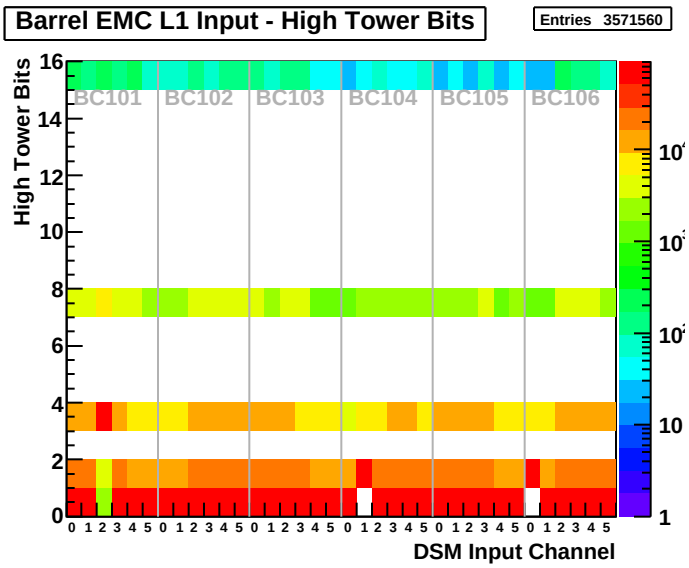
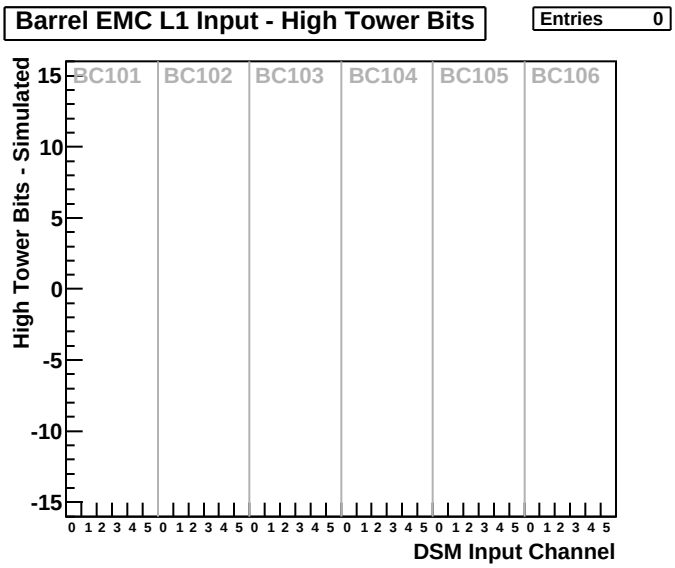

Barrel EMC L1 Input - Low Eta Sum
Entries 3571560
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

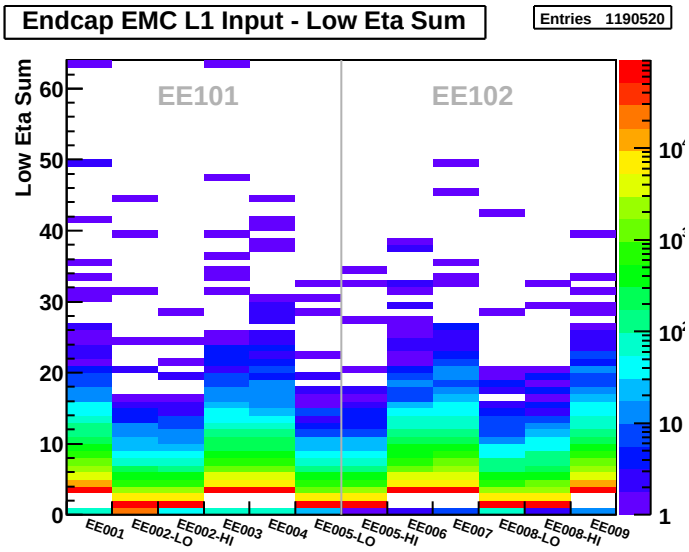

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

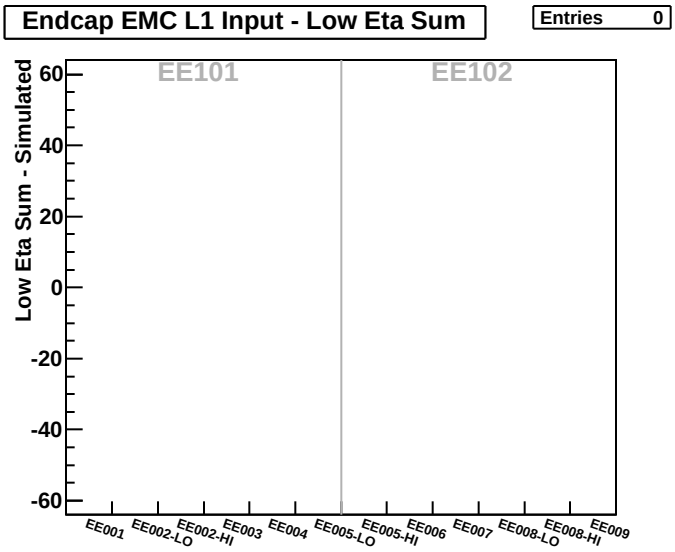

Barrel EMC L1 Input - High Eta Sum
Entries 3571560
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

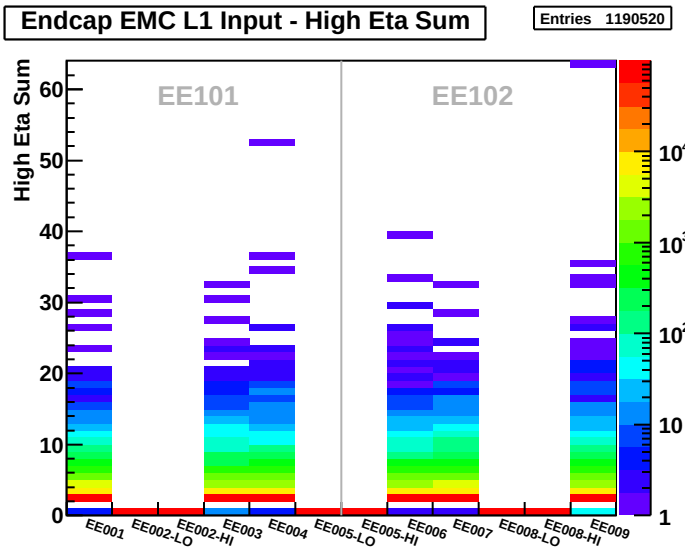

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

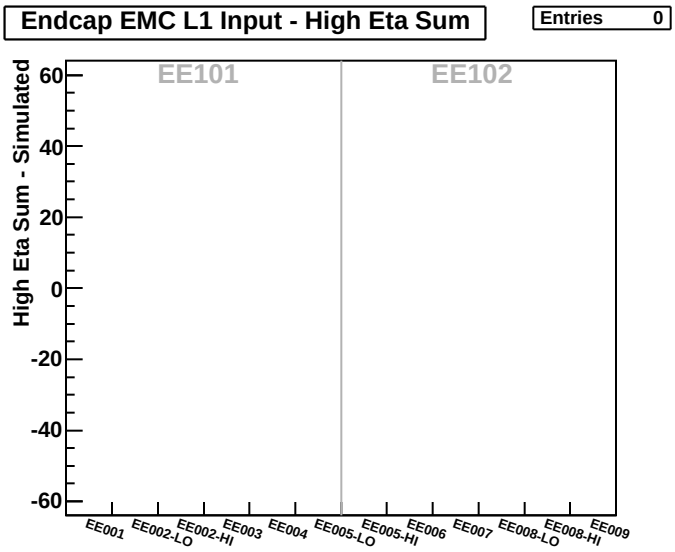

Barrel EMC L1 Input - High Tower Bits
Entries 3571560
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

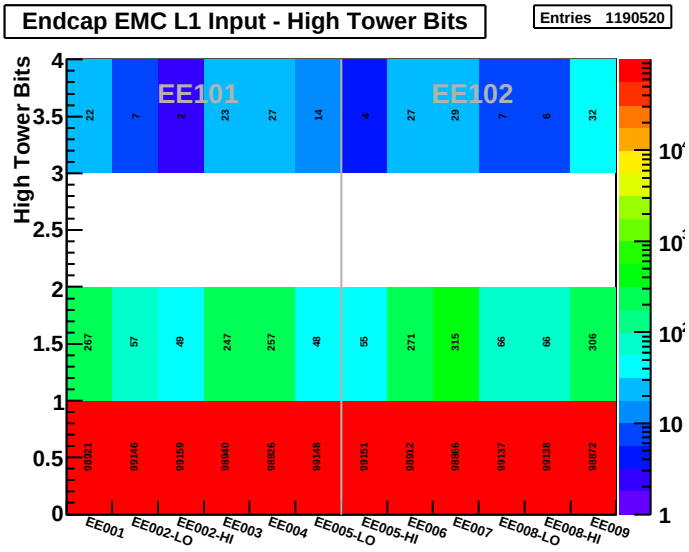

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

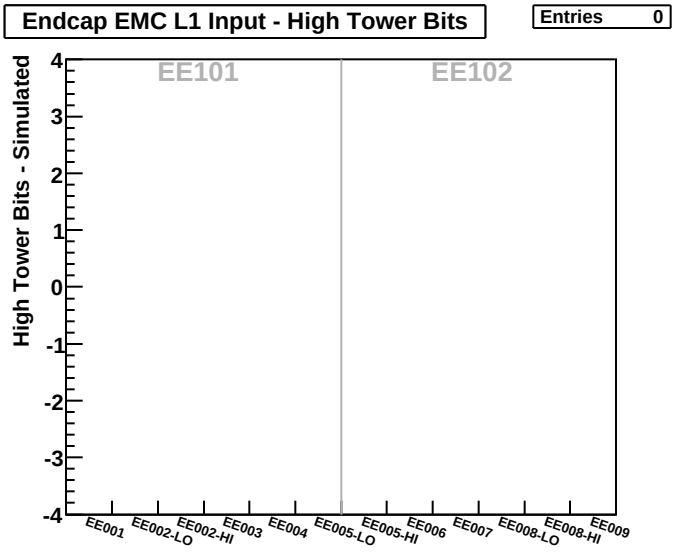

Endcap EMC L1 Input - Low Eta Sum
Entries 1190520
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

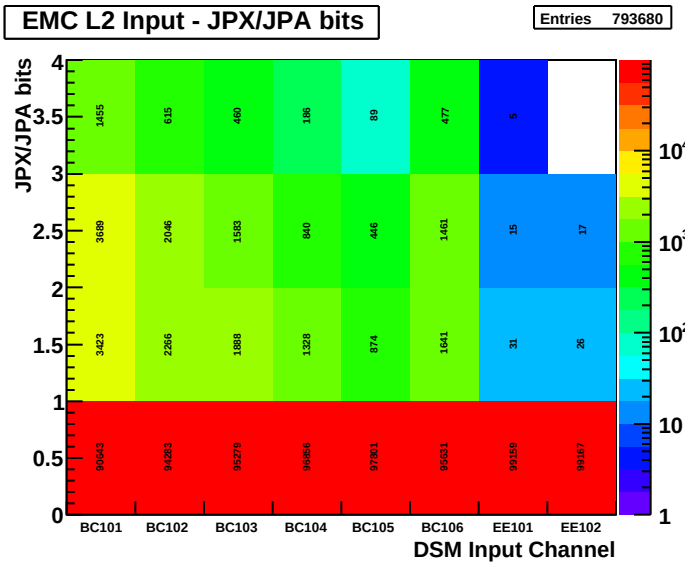

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

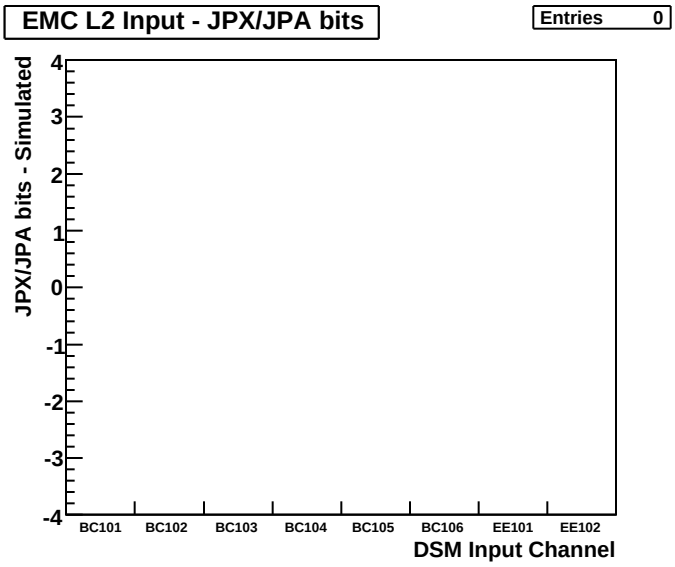

Endcap EMC L1 Input - High Eta Sum
Entries 1190520
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

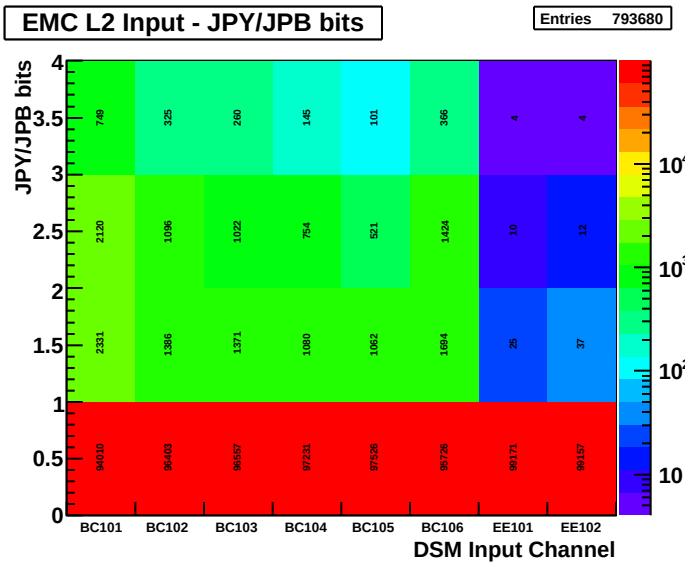

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

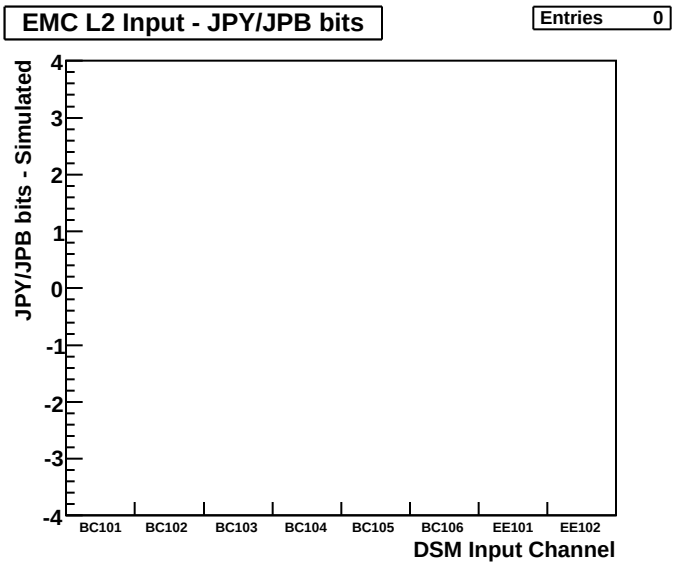

Endcap EMC L1 Input - High Tower Bits
Entries 1190520
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

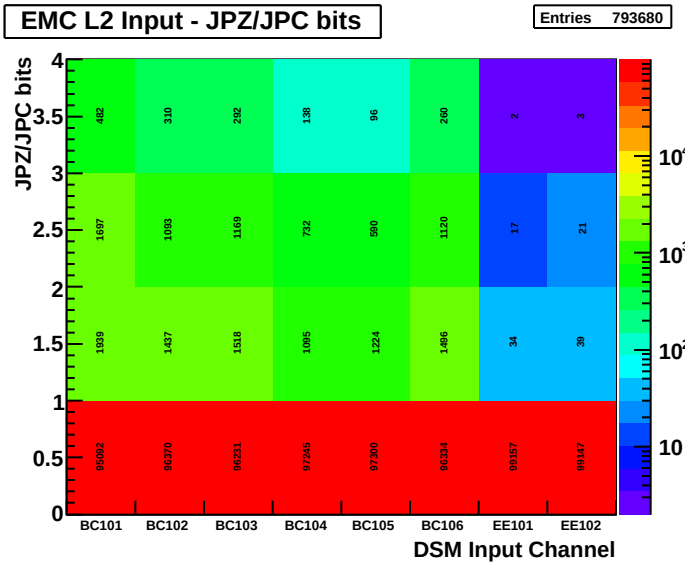

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

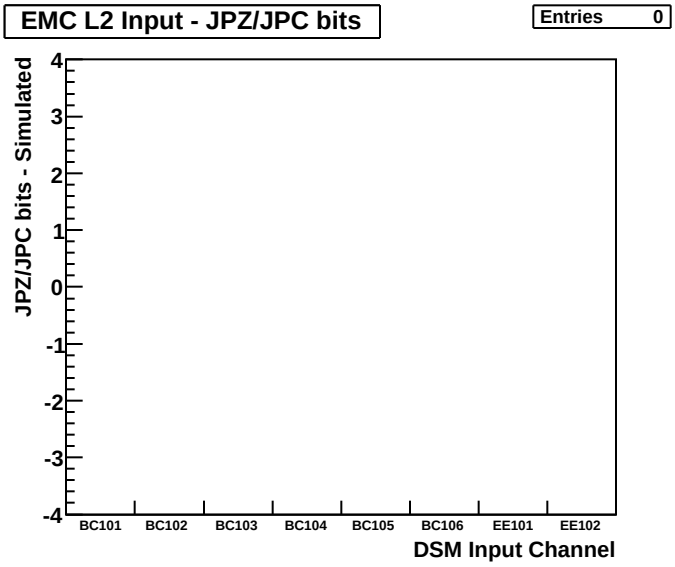

EMC L2 Input - JPX/JPA bits
Entries 793680
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1455 615 460 186 89 477 5
3689 2046 1583 840 446 1461 15 17
3423 2266 1888 1328 874 1641 31 26
90643 94283 95279 96856 97801 95631 99159 99167

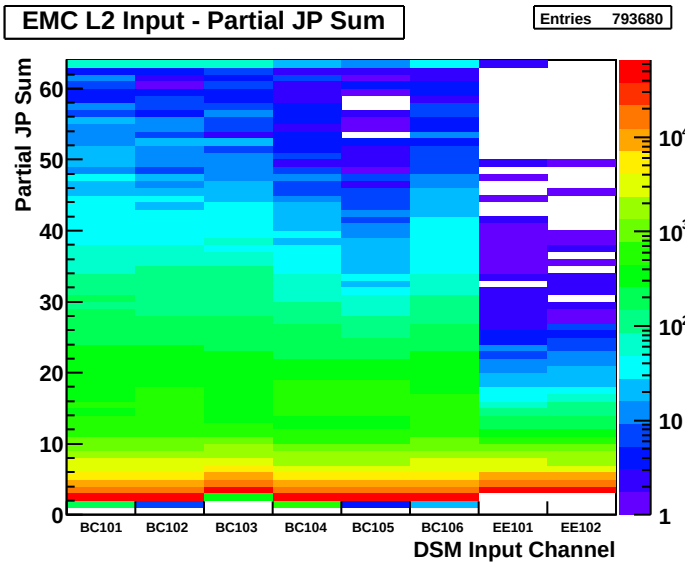

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

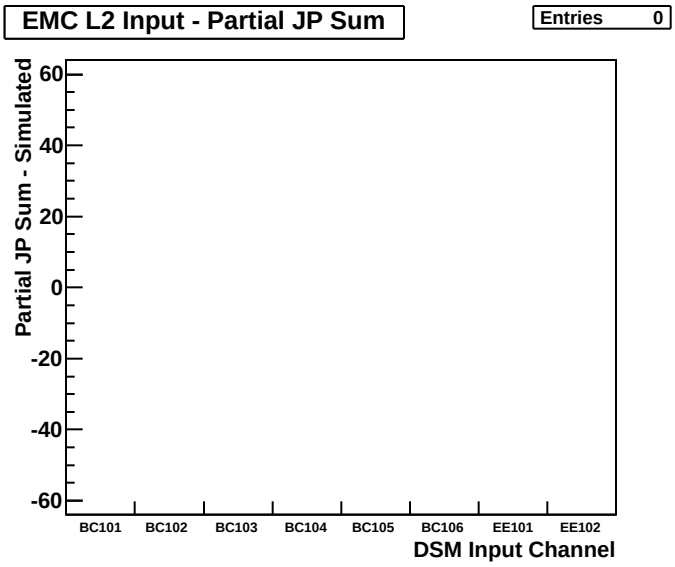

EMC L2 Input - JPY/JPB bits
Entries 793680
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
749 325 260 145 101 366 4 4
2120 1096 1022 754 521 1424 10 12
2331 1386 1371 1080 1062 1694 25 37
94010 96403 96557 97231 97526 95726 99171 99157

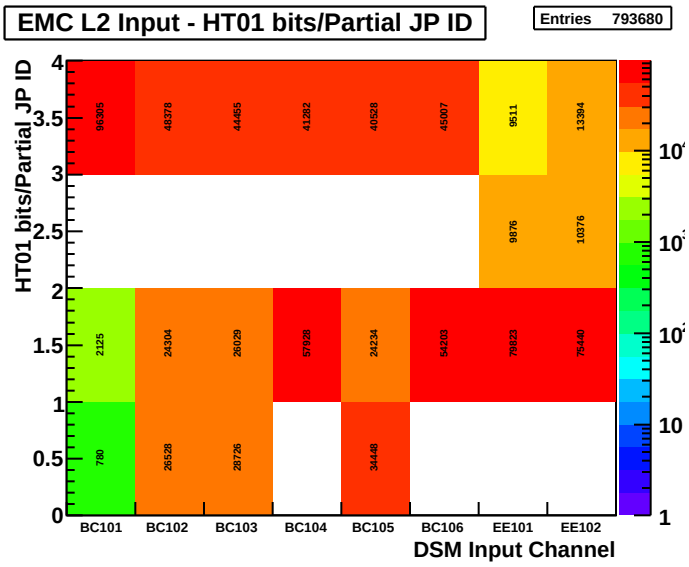

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

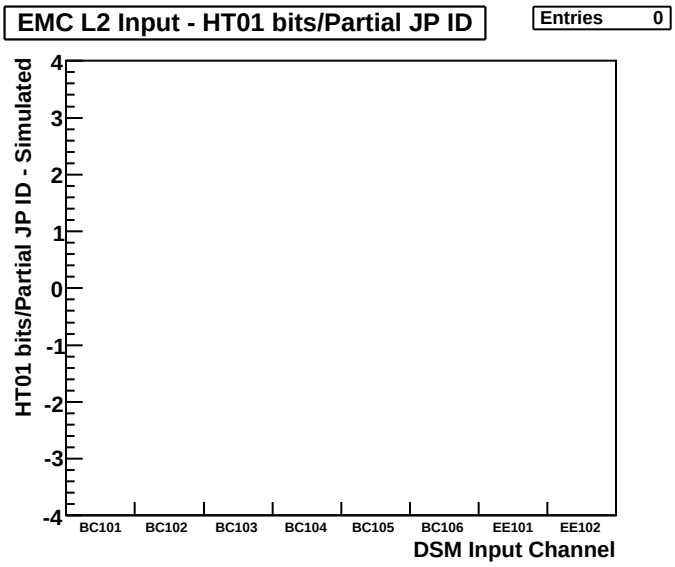

EMC L2 Input - JPZ/JPC bits
Entries 793680
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
482 310 292 138 96 260 2 3
1697 1093 1169 732 590 1120 17 21
1939 1437 1518 1095 1224 1496 34 39
95092 96270 96231 97245 97300 96334 99157 99147

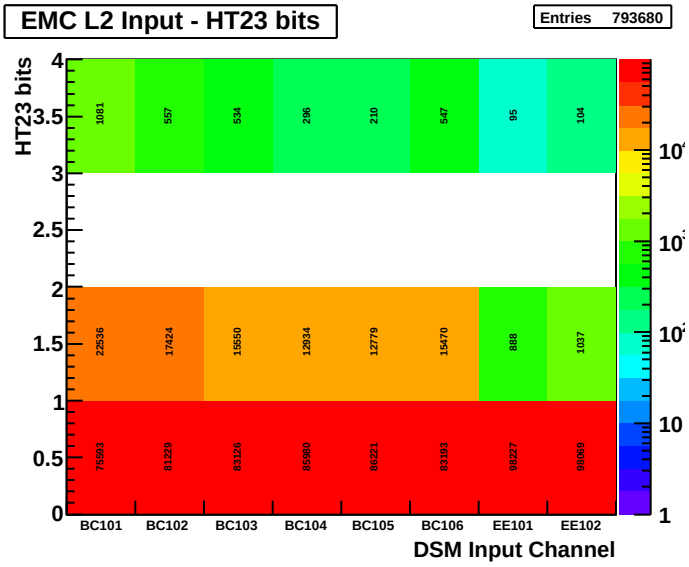

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

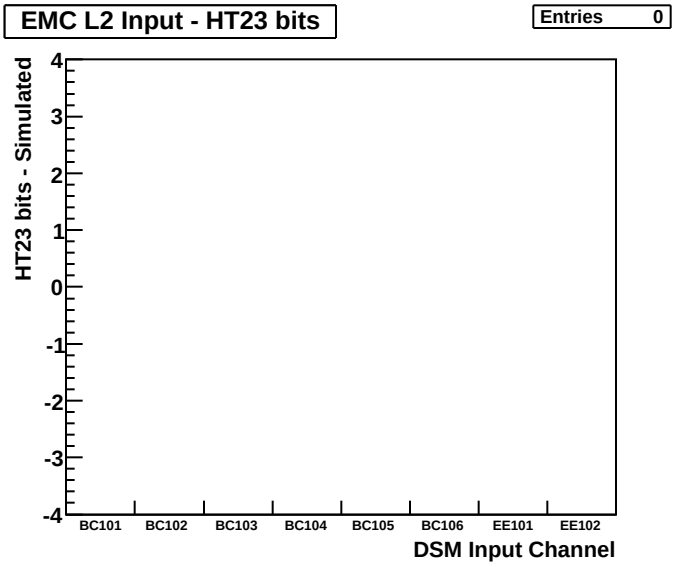

EMC L2 Input - Partial JP Sum
Entries 793680
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

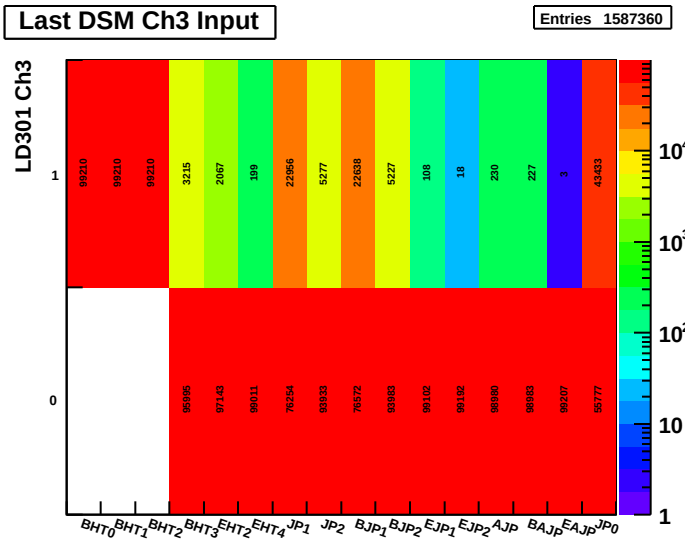

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

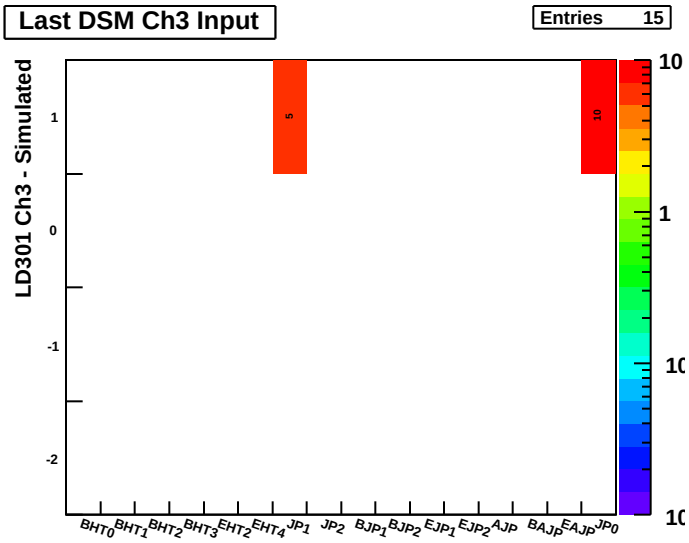

EMC L2 Input - HT01 bits/Partial JP ID
Entries 793680
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

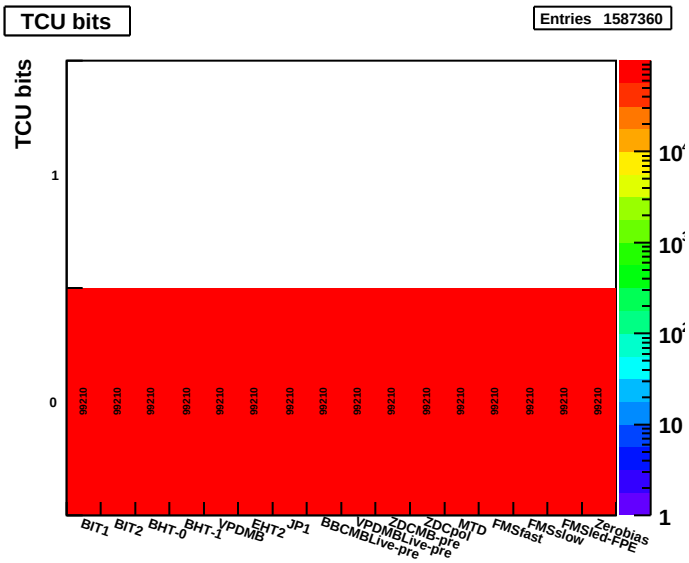

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

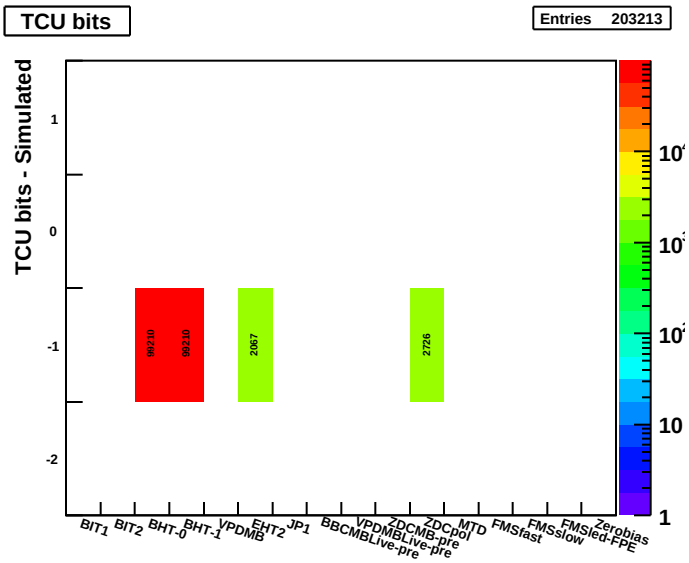

EMC L2 Input - HT23 bits
Entries 793680
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

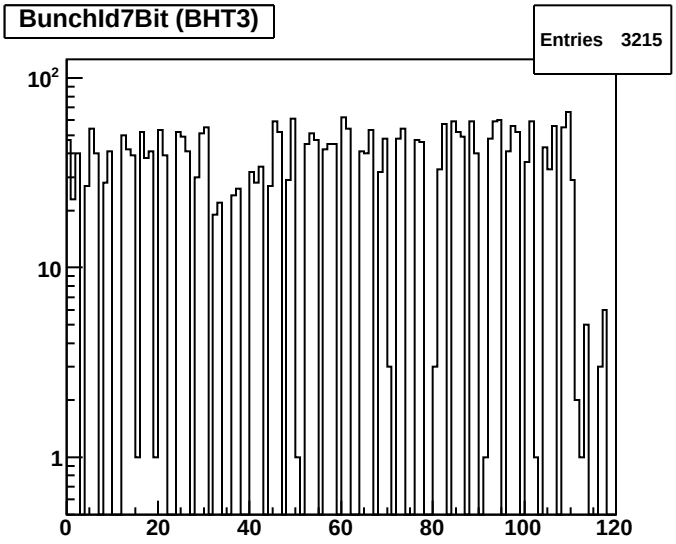

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

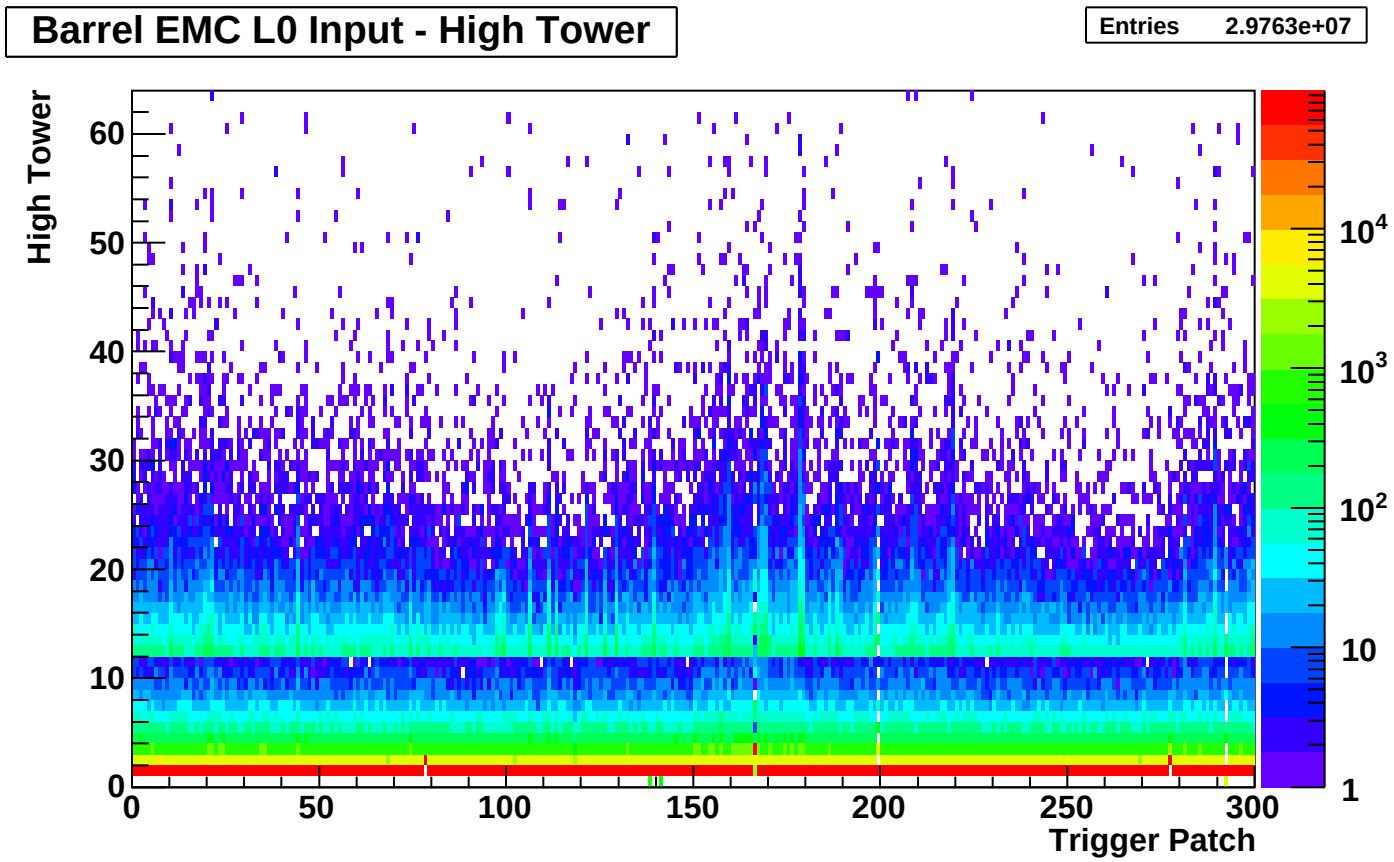

Last DSM Ch3 Input
Entries 1587360
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

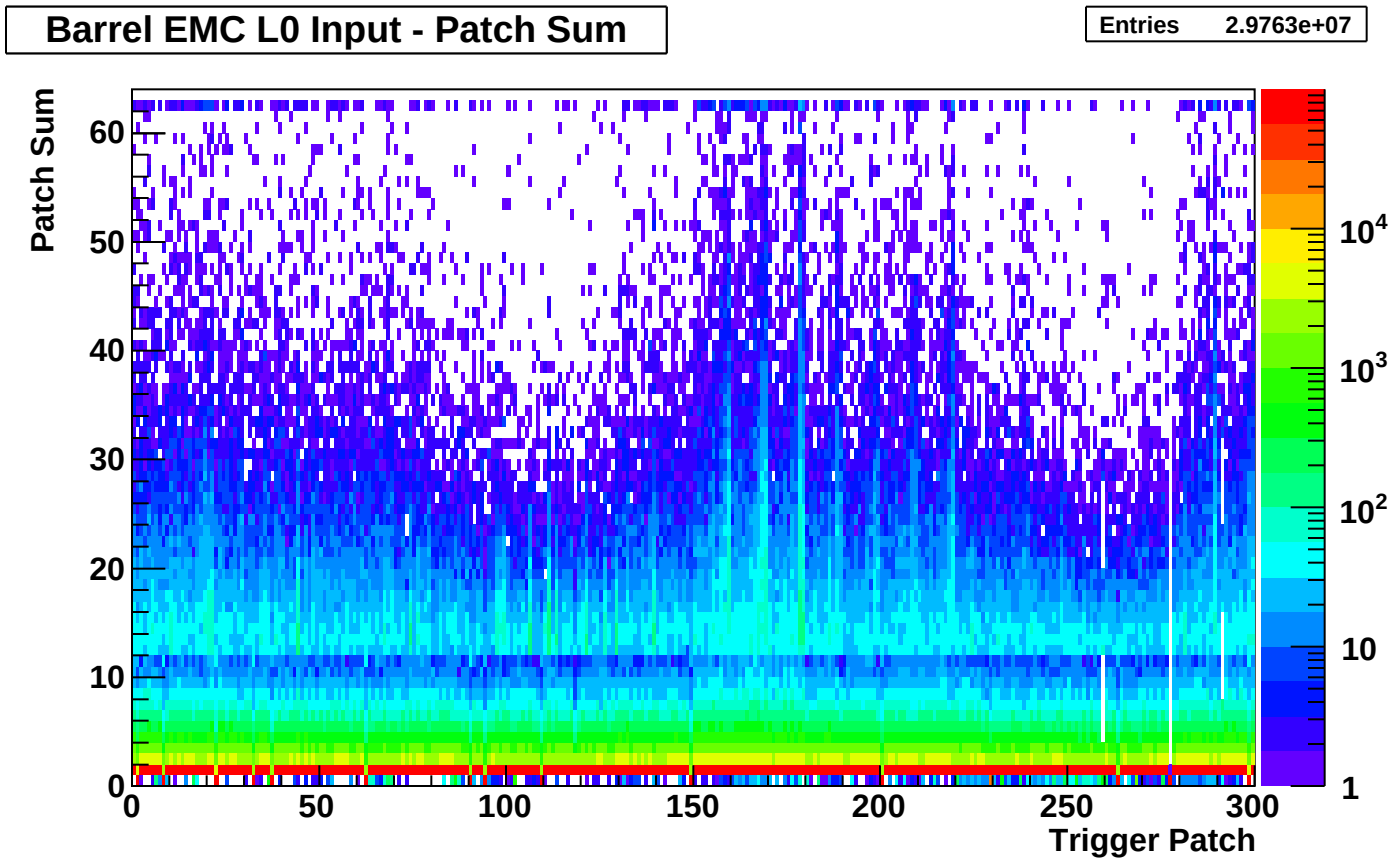

Last DSM Ch3 Input
Entries 15
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

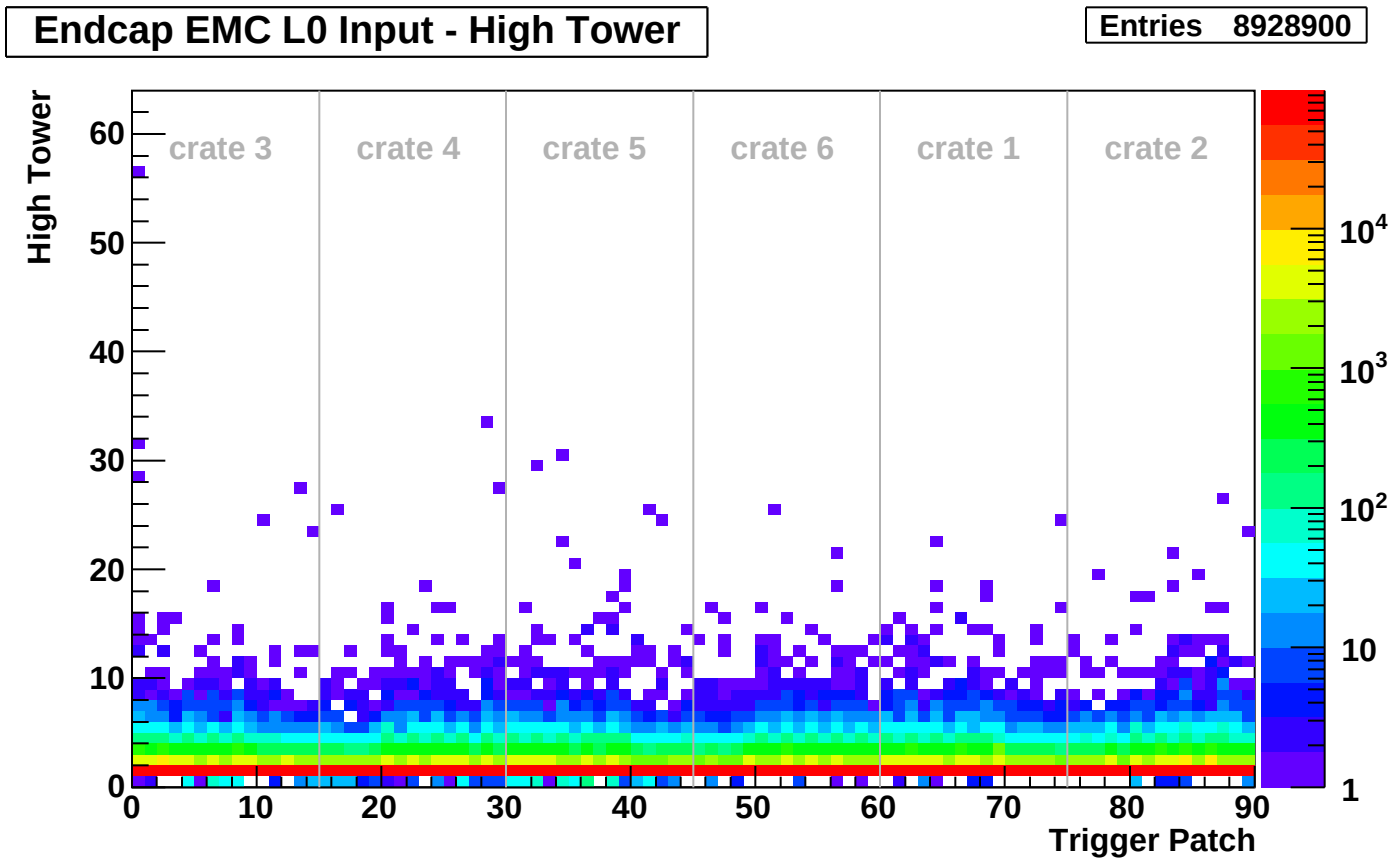

TCU bits
Entries 1587360
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

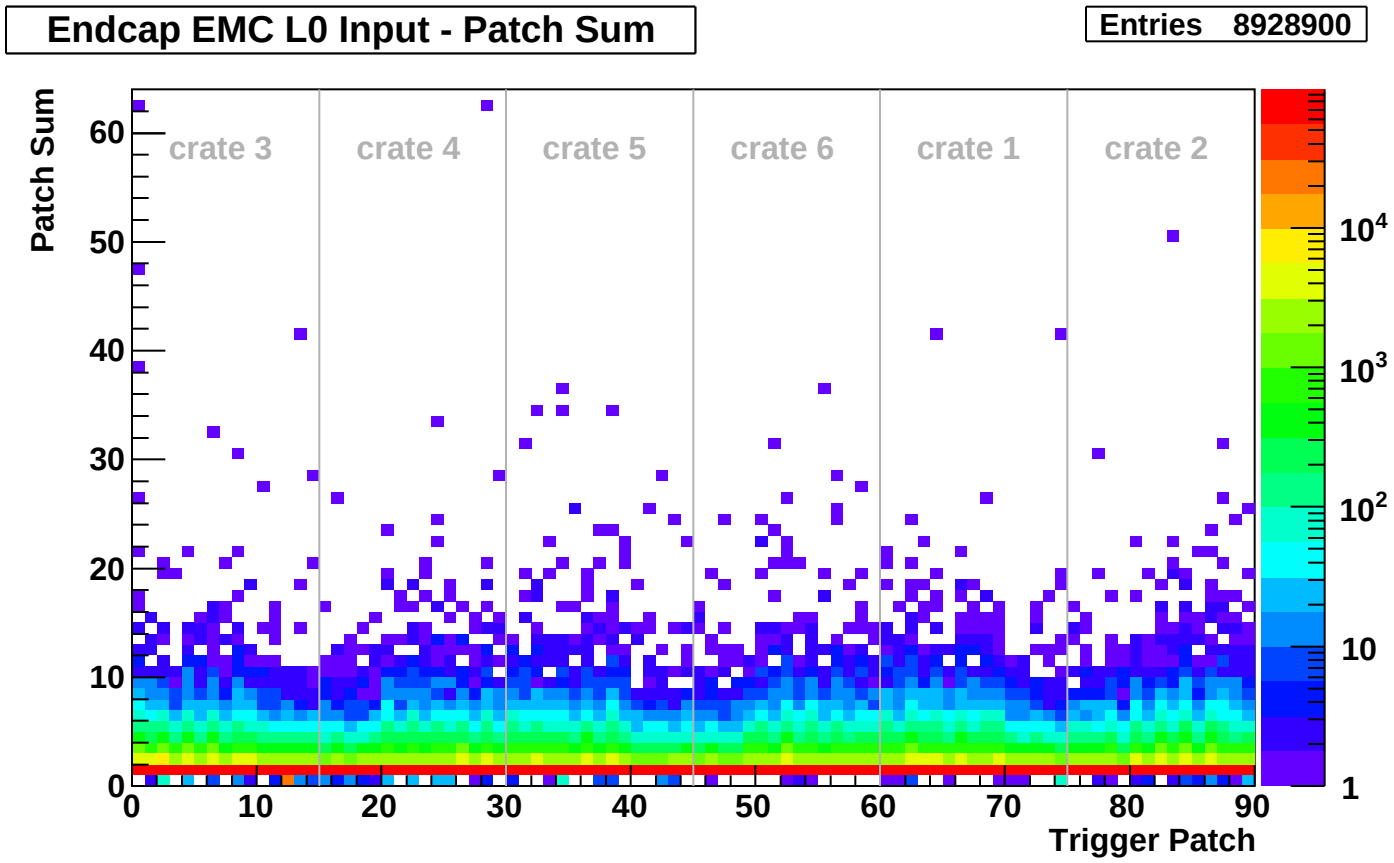

TCU bits
Entries 203213
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

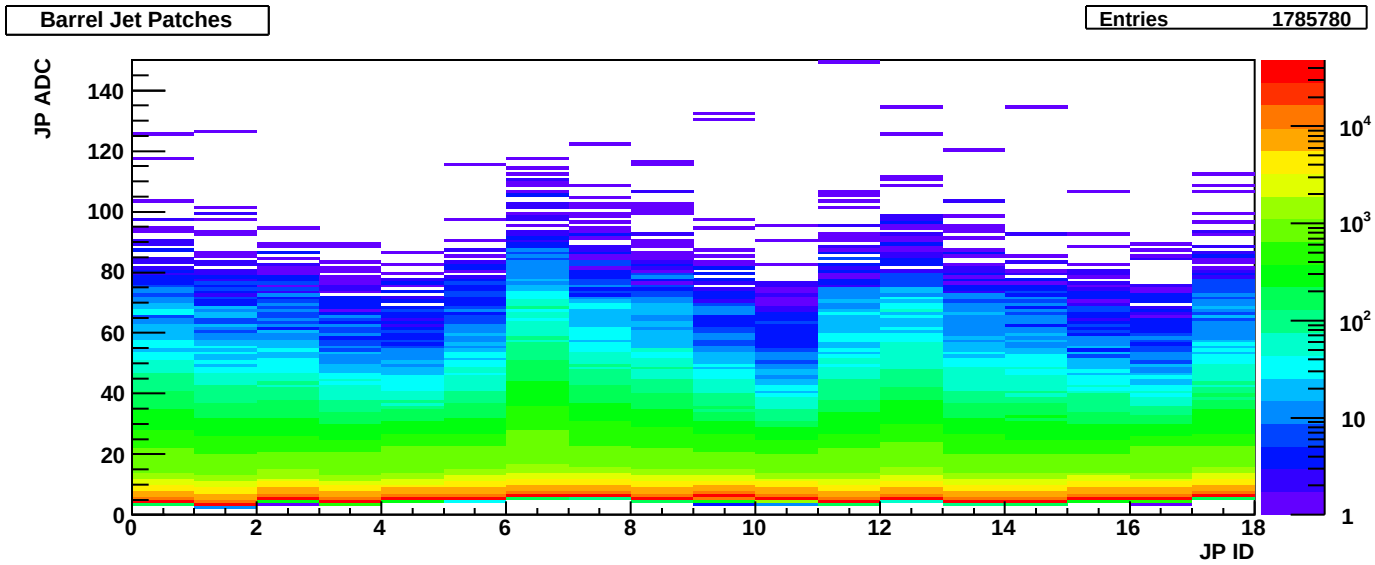

BunchId7Bit (BHT3)
Entries 3215

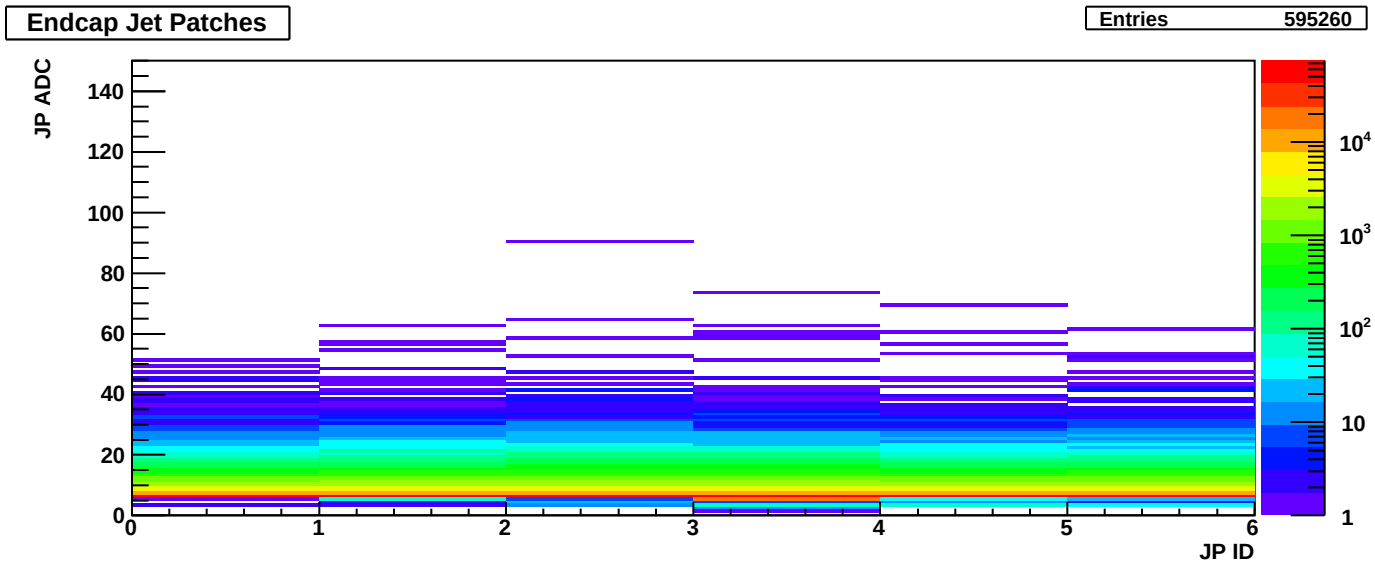

Barrel EMC L0 Input - High Tower
Entries 2.9763e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

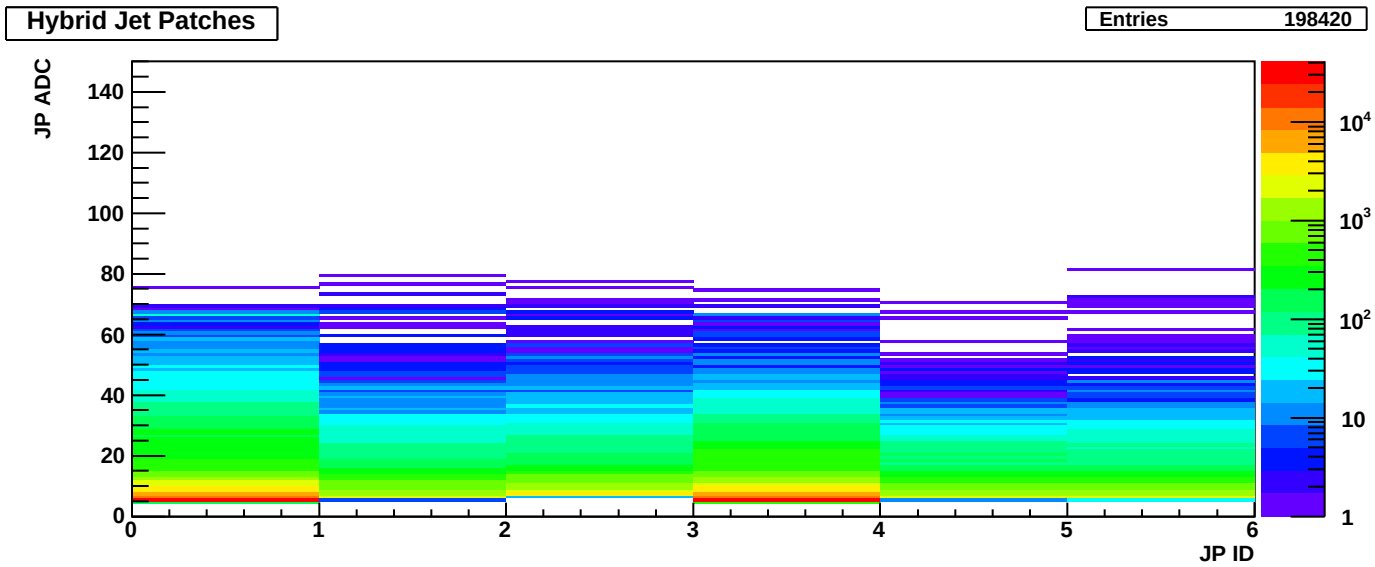

Barrel EMC L0 Input - Patch Sum
Entries 2.9763e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

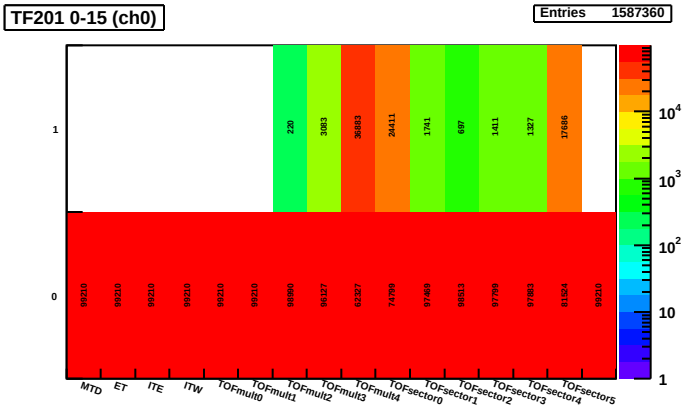

Endcap EMC L0 Input - High Tower
Entries 8928900
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

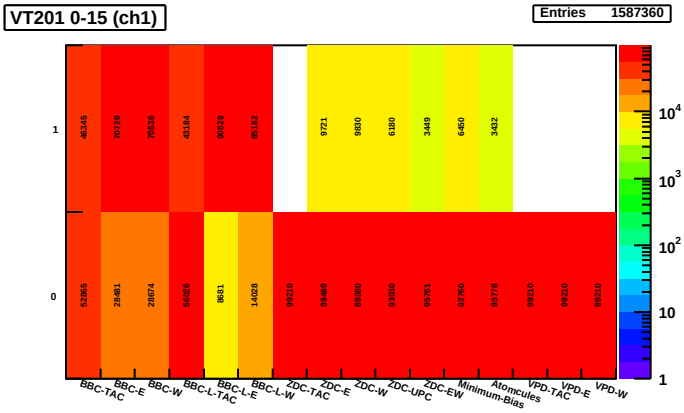

Endcap EMC L0 Input - Patch Sum
Entries 8928900
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

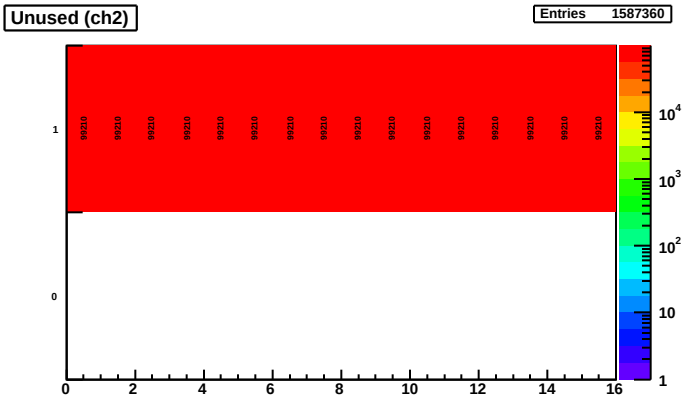

Barrel Jet Patches
Entries 1785780
JP ADC
JP ID

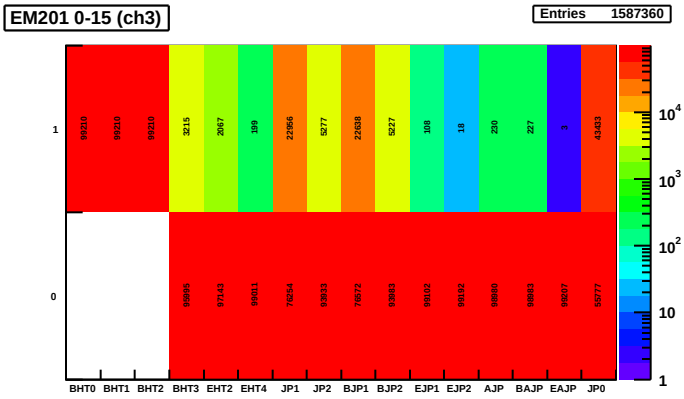

Endcap Jet Patches
Entries 595260
JP ADC
JP ID

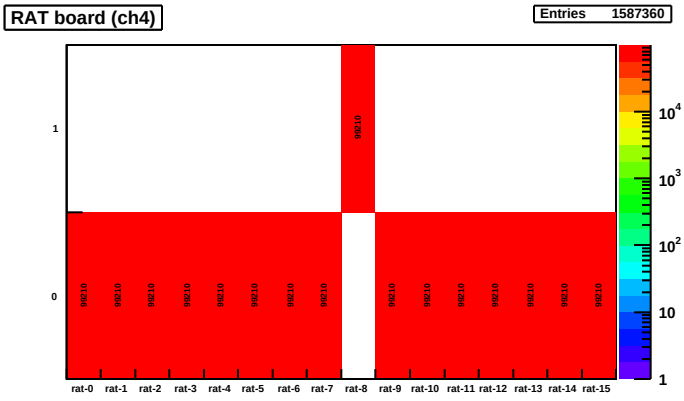

Hybrid Jet Patches
Entries 198420
JP ADC
JP ID

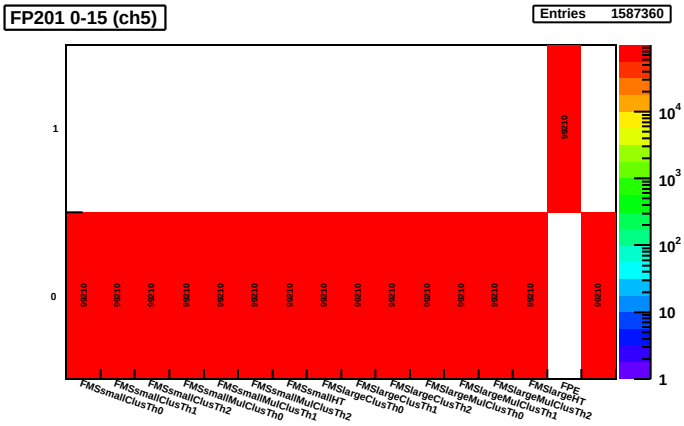

TF201 0-15 (ch0)
Entries 1587360
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

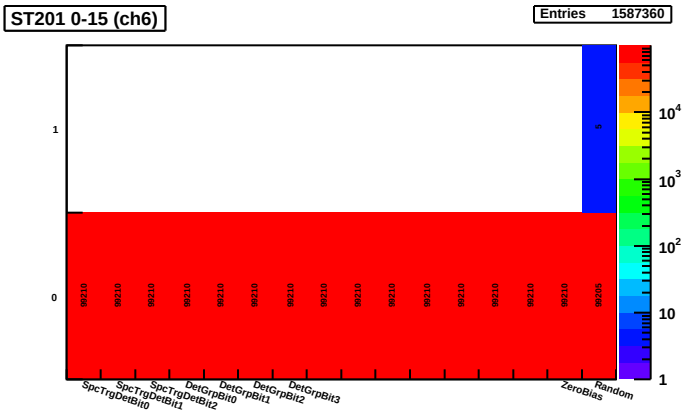

VT201 0-15 (ch1)
Entries 1587360
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

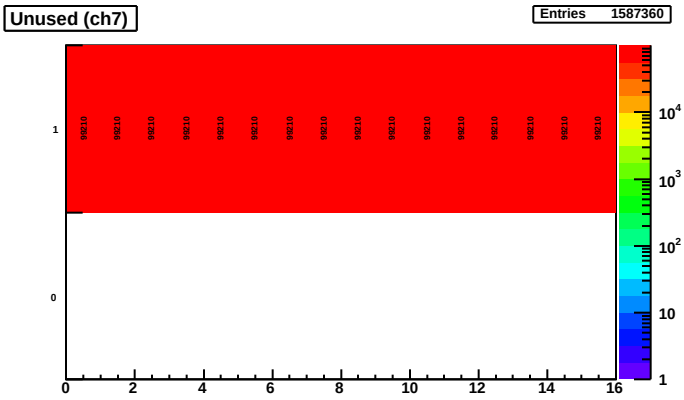

Unused (ch2)
Entries 1587360

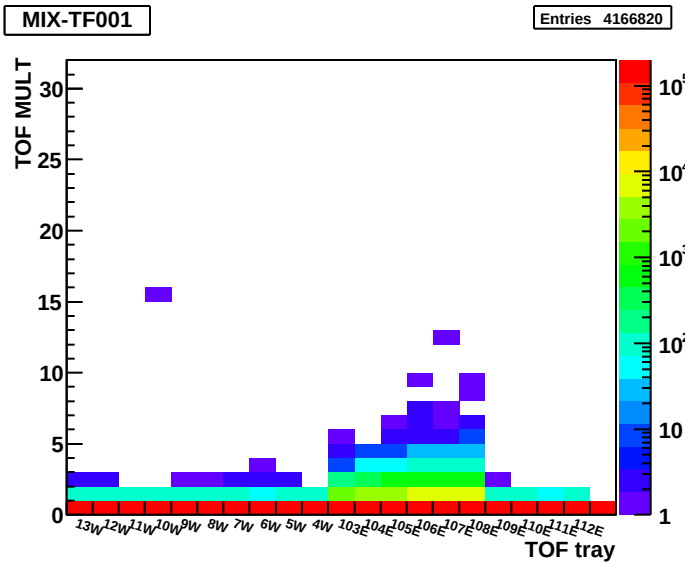

EM201 0-15 (ch3)
Entries 1587360
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

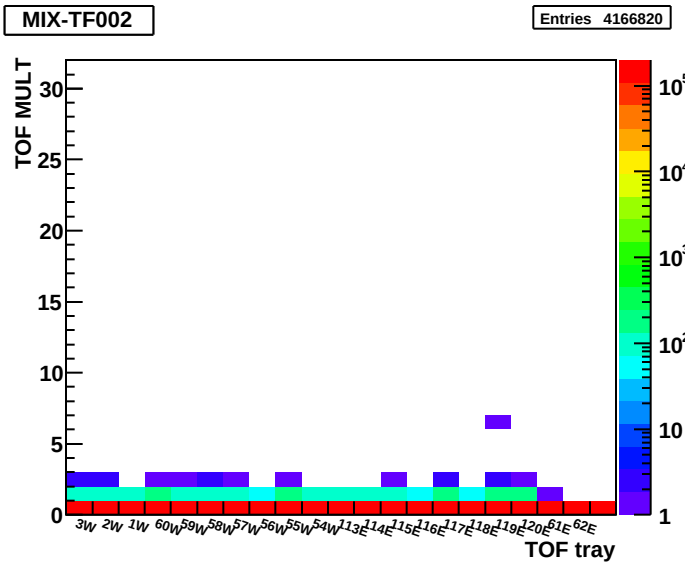

RAT board (ch4)
Entries 1587360
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

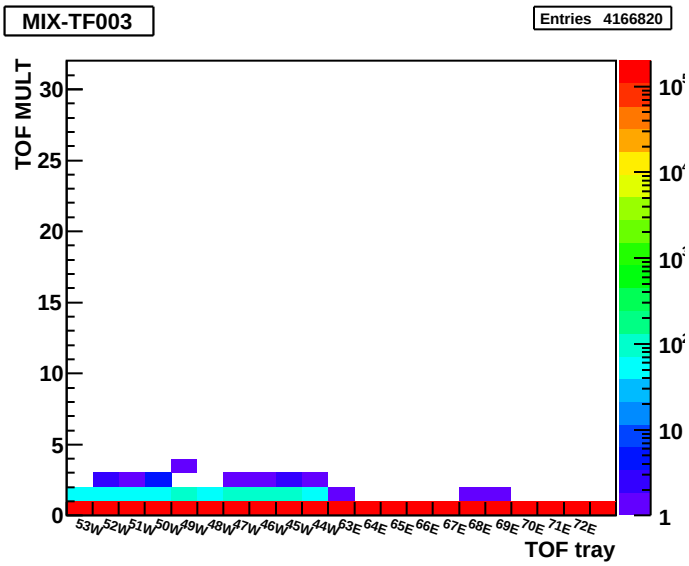

FP201 0-15 (ch5)
Entries 1587360
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

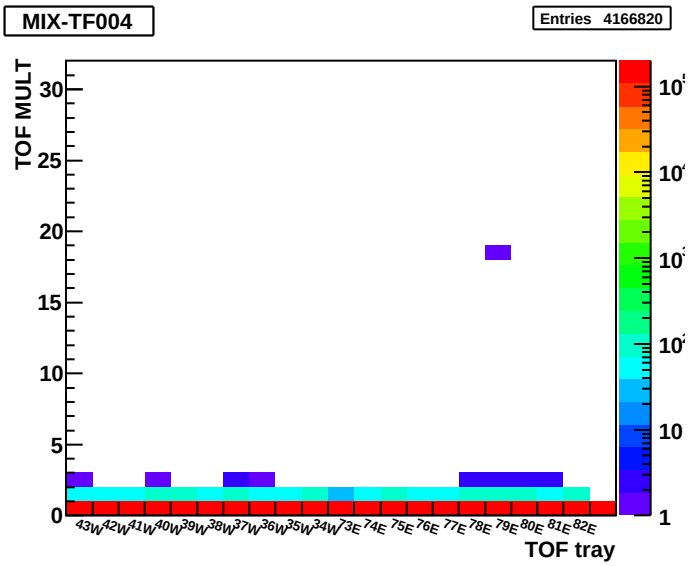

ST201 0-15 (ch6)
Entries 1587360
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

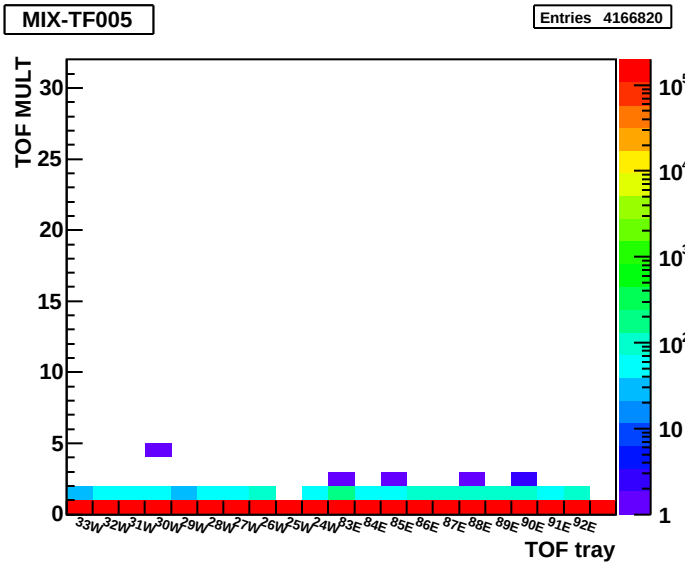

Unused (ch7)
Entries 1587360

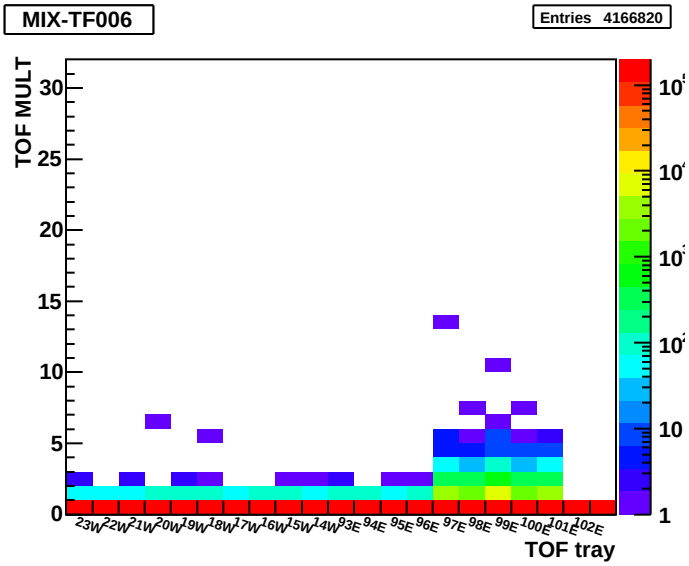

MIX-TF001
Entries 4166820
TOF MULT
TOF tray

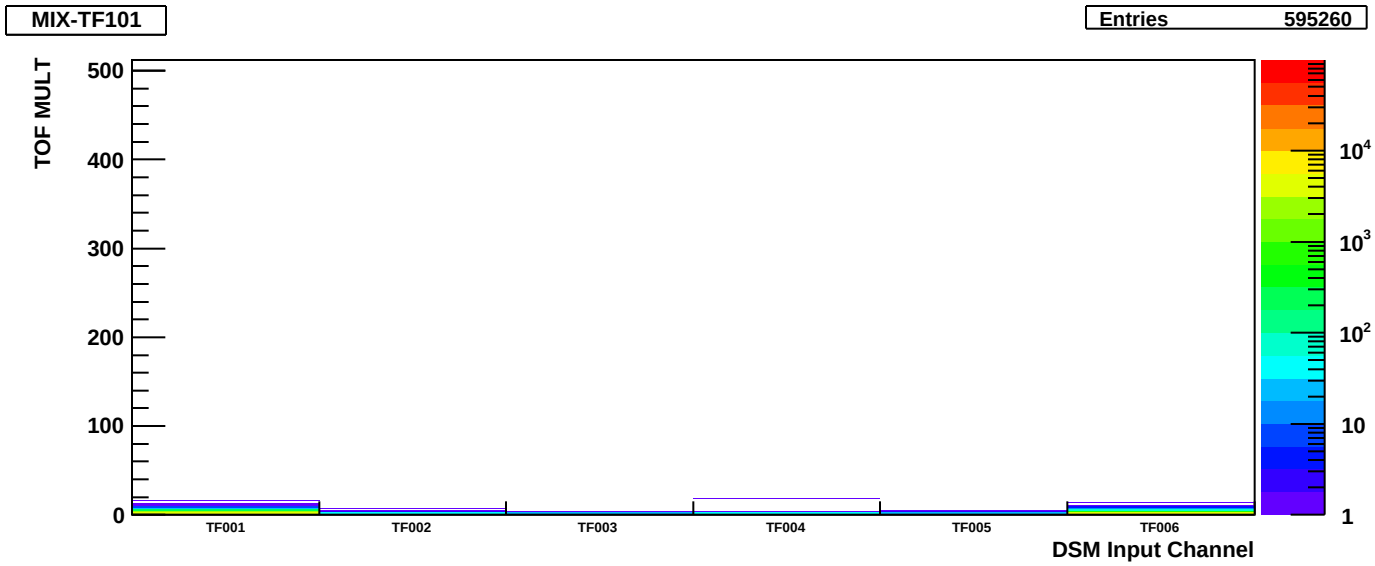

MIX-TF002
Entries 4166820
TOF MULT
TOF tray

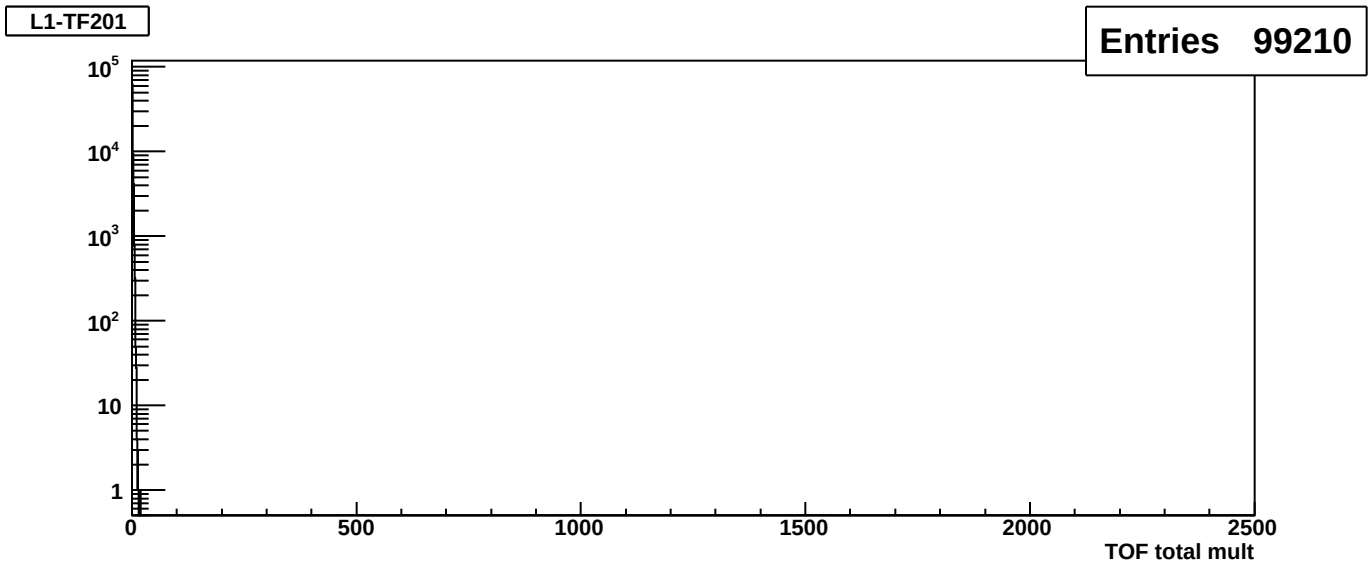

MIX-TF003
Entries 4166820
TOF MULT
TOF tray

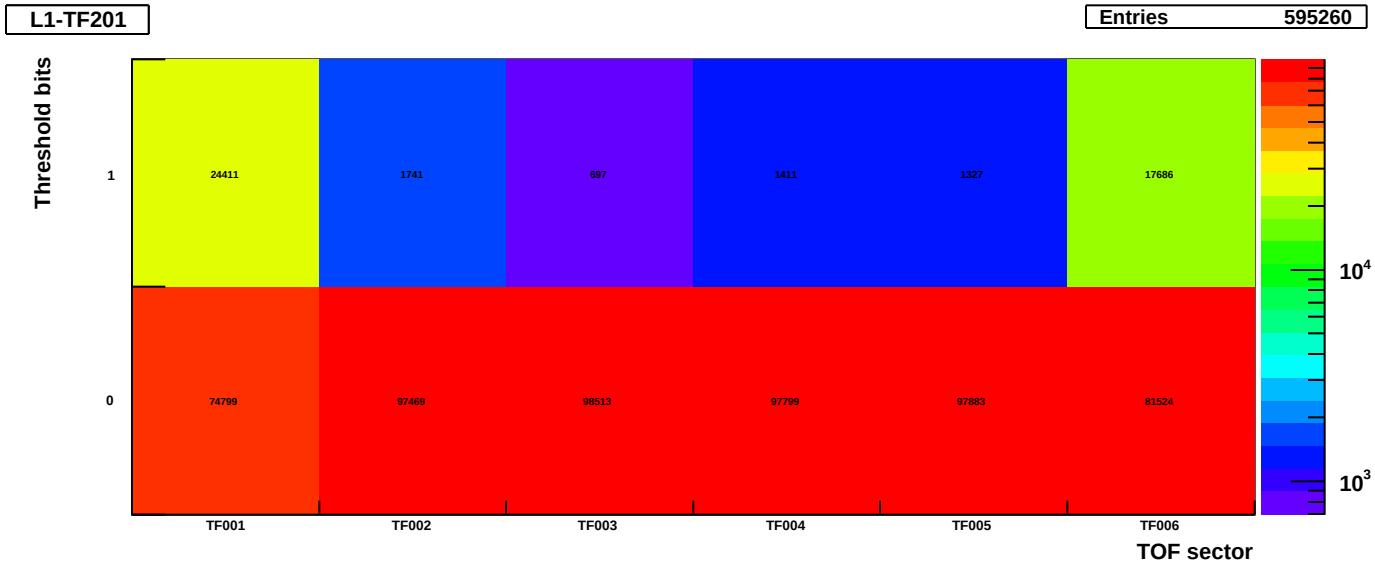

MIX-TF004
Entries 4166820
TOF MULT
TOF tray

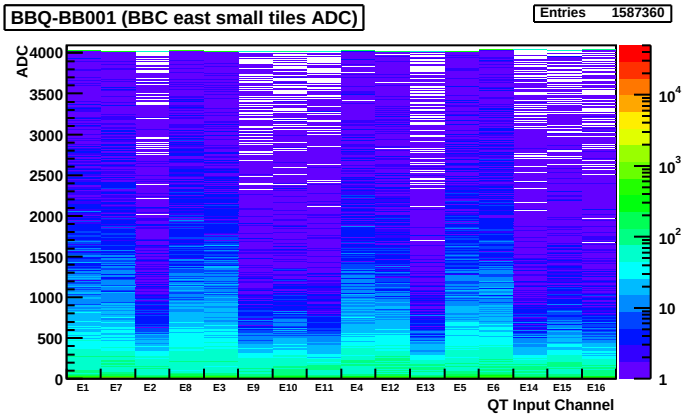

MIX-TF005
Entries 4166820
TOF MULT
TOF tray

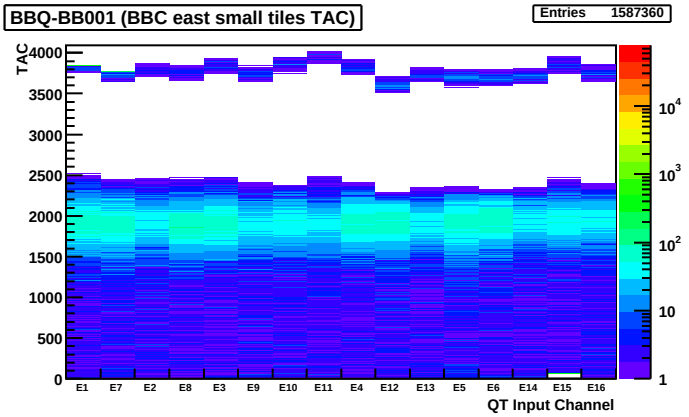

MIX-TF006
Entries 4166820
TOF MULT
TOF tray

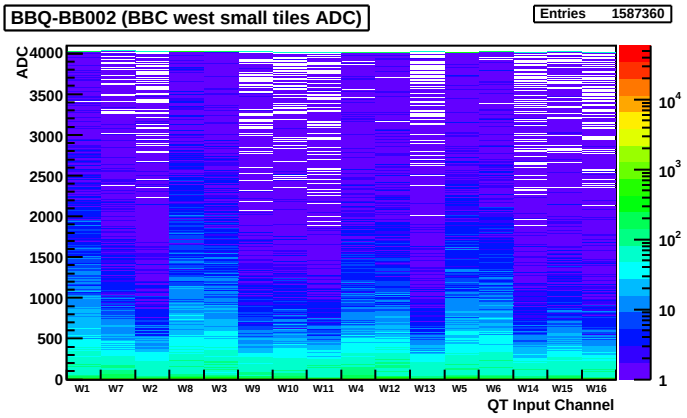

MIX-TF101
Entries 595260
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

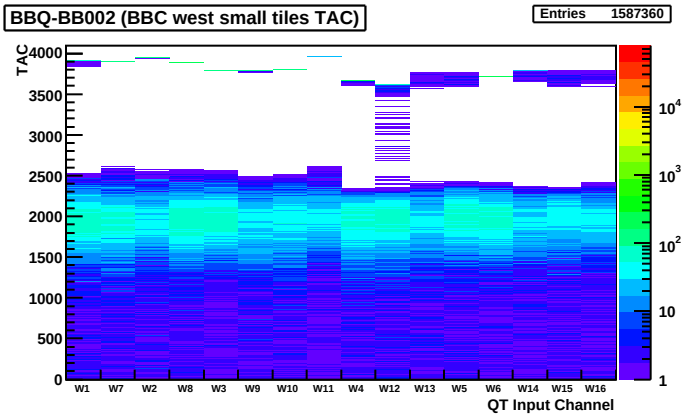

L1-TF201
Entries 99210
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

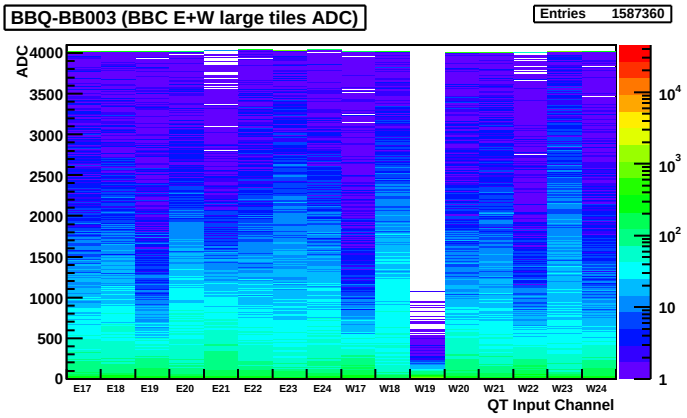

L1-TF201
Entries 595260
Threshold bits
1
0
24411
1741
697
1411
1327
17686
74799
97469
98513
97799
97883
81524
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

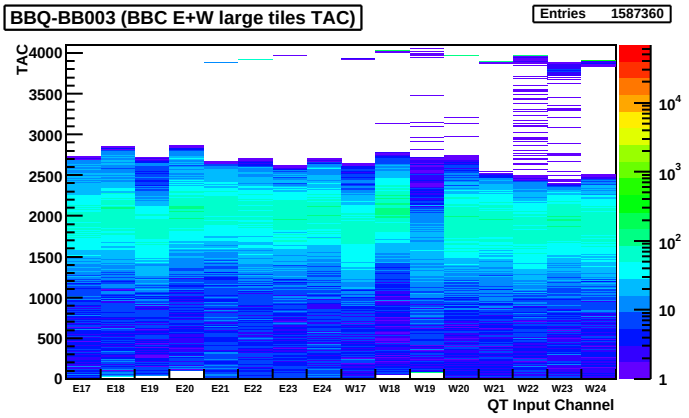

BBQ-BB001 (BBC east small tiles ADC)
Entries 1587360
ADC
QT Input Channel

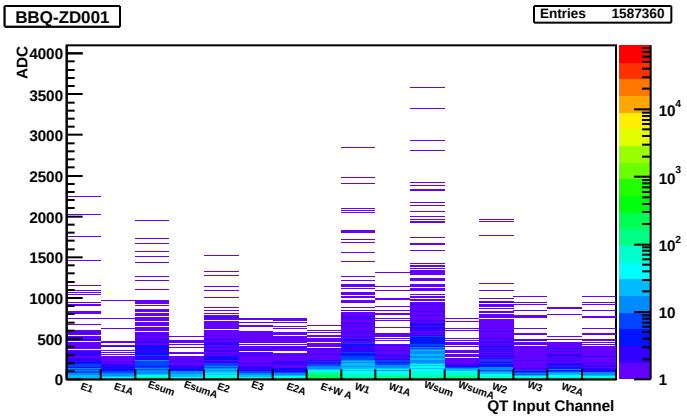

BBQ-BB001 (BBC east small tiles TAC)
Entries 1587360
TAC
QT Input Channel

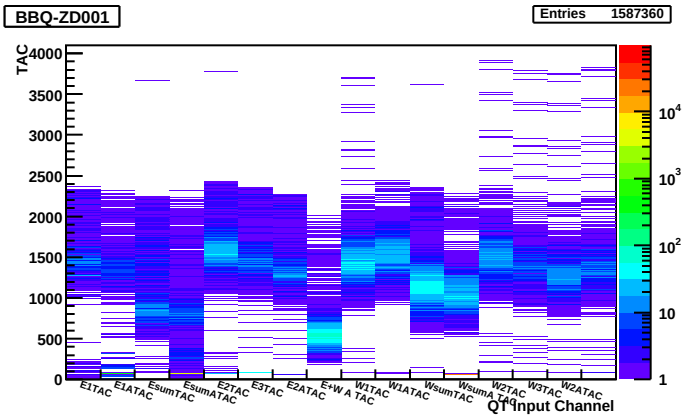

BBQ-BB002 (BBC west small tiles ADC)
Entries 1587360
ADC
QT Input Channel

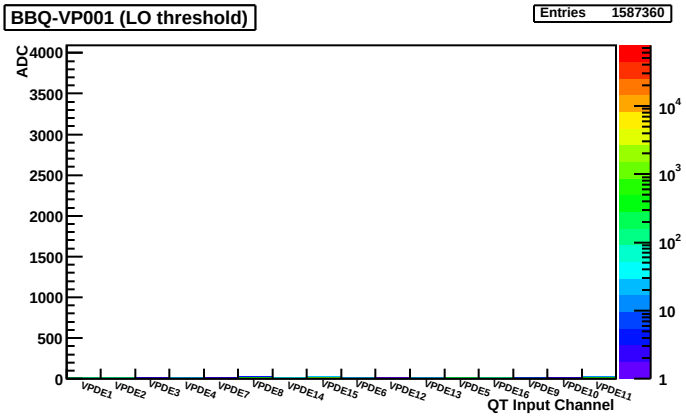

BBQ-BB002 (BBC west small tiles TAC)
Entries 1587360
TAC
QT Input Channel

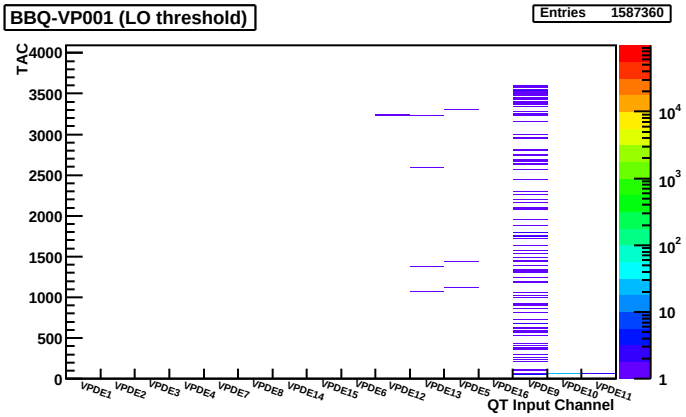

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1587360
ADC
QT Input Channel

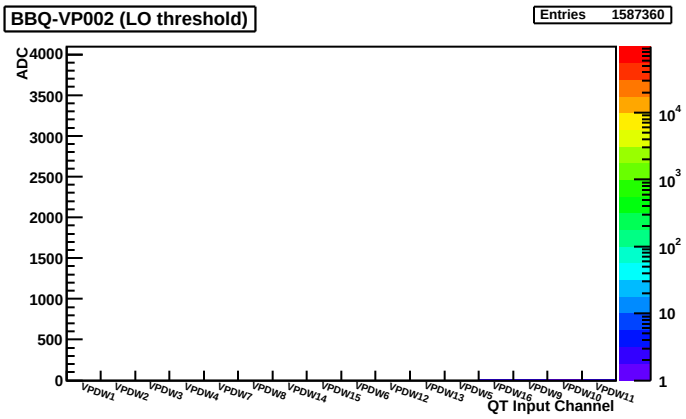

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1587360
TAC
QT Input Channel

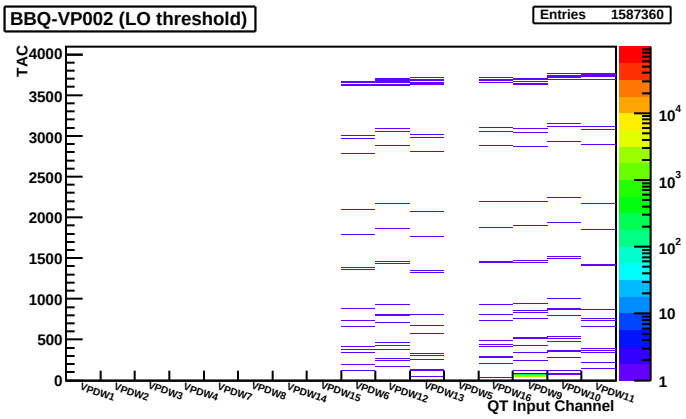

BBQ-ZD001
Entries 1587360
ADC
QT Input Channel

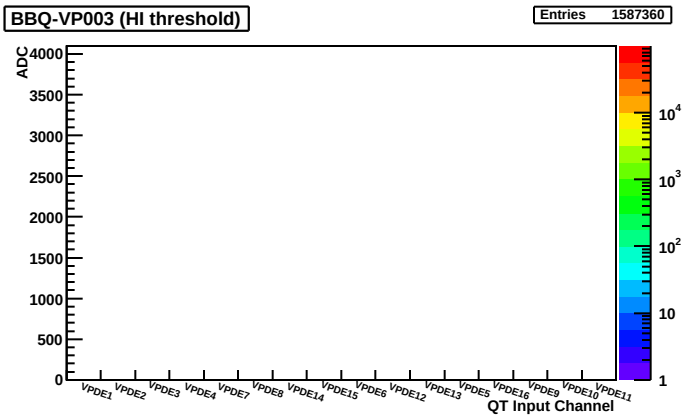

BBQ-ZD001
Entries 1587360
TAC
QT Input Channel

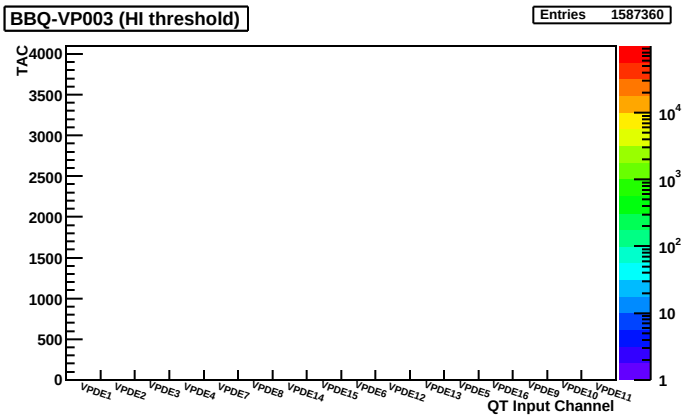

BBQ-VP001 (LO threshold)
Entries 1587360
ADC
QT Input Channel

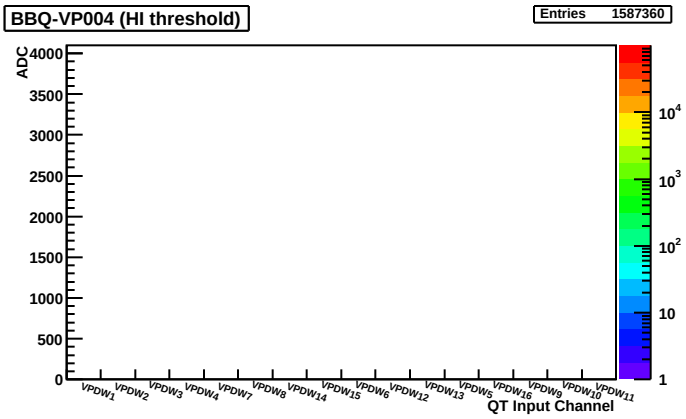

BBQ-VP001 (LO threshold)
Entries 1587360
TAC
QT Input Channel

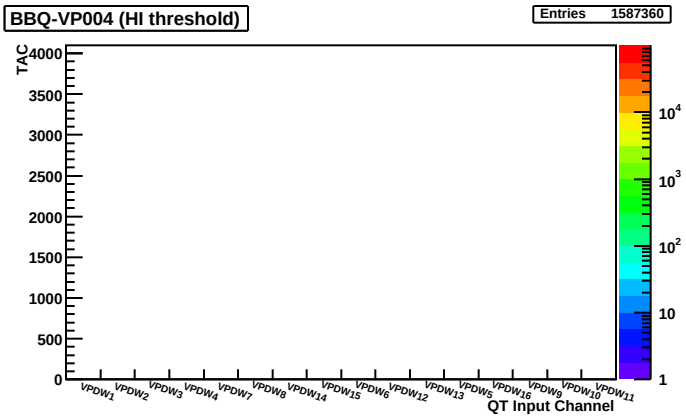

BBQ-VP002 (LO threshold)
Entries 1587360
ADC
QT Input Channel

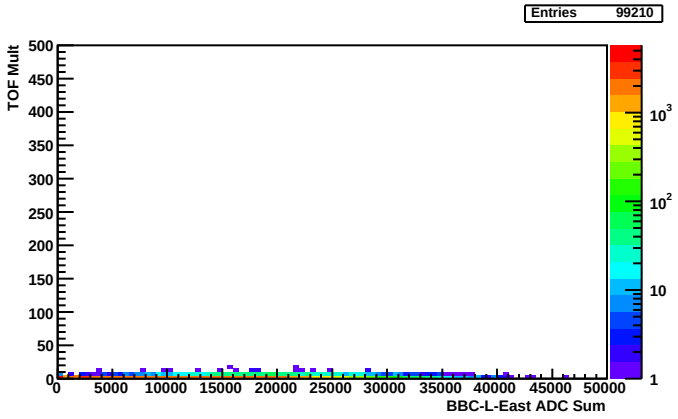

BBQ-VP002 (LO threshold)
Entries 1587360
TAC
QT Input Channel

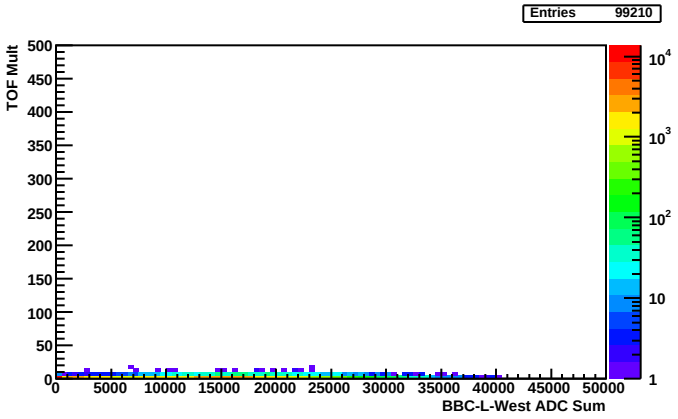

BBQ-VP003 (HI threshold)
Entries 1587360
ADC
QT Input Channel

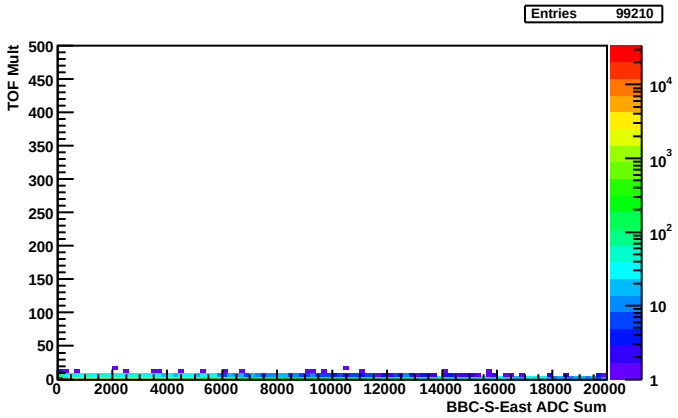

BBQ-VP003 (HI threshold)
Entries 1587360
TAC
QT Input Channel

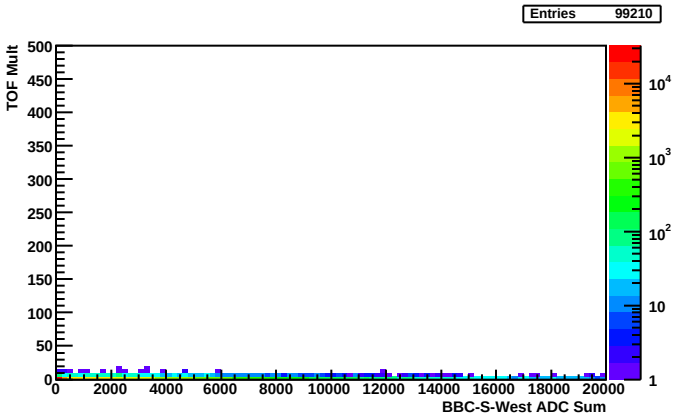

BBQ-VP004 (HI threshold)
Entries 1587360
ADC
QT Input Channel

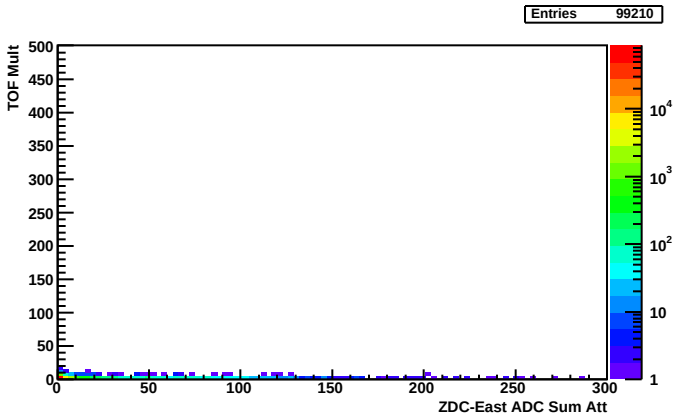

BBQ-VP004 (HI threshold)
Entries 1587360
TAC
QT Input Channel

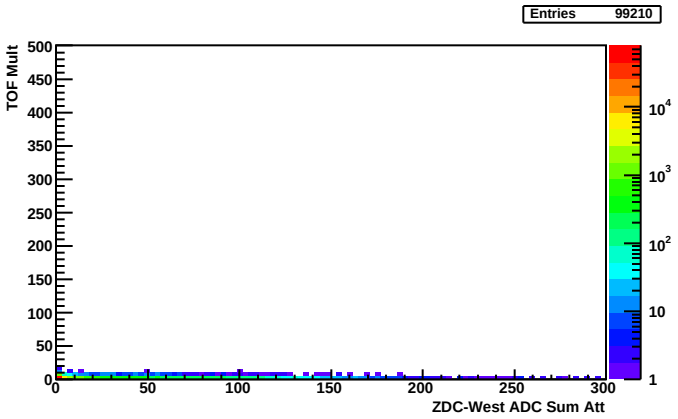

Entries 99210
TOF Mult
BBC-L-East ADC Sum

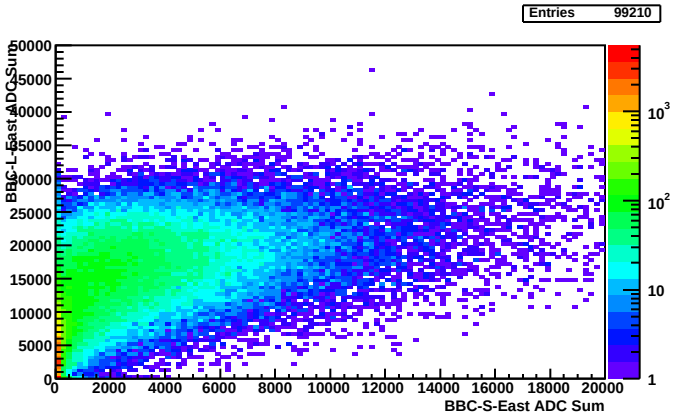

Entries 99210
TOF Mult
BBC-L-West ADC Sum

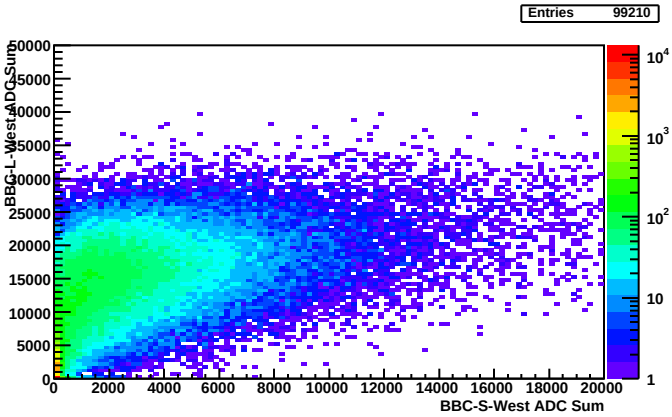

Entries 99210
TOF Mult
BBC-S-East ADC Sum

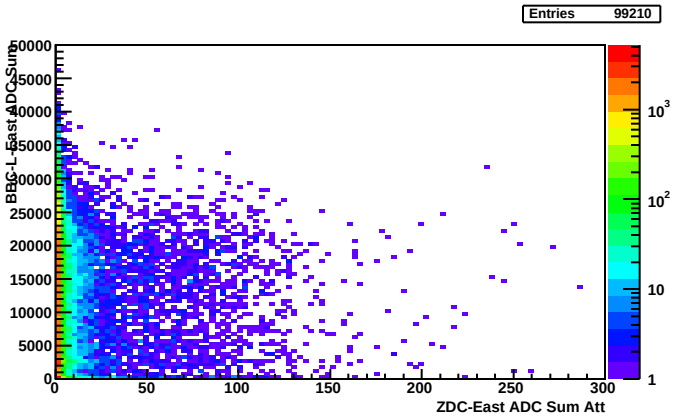

Entries 99210
TOF Mult
BBC-S-West ADC Sum

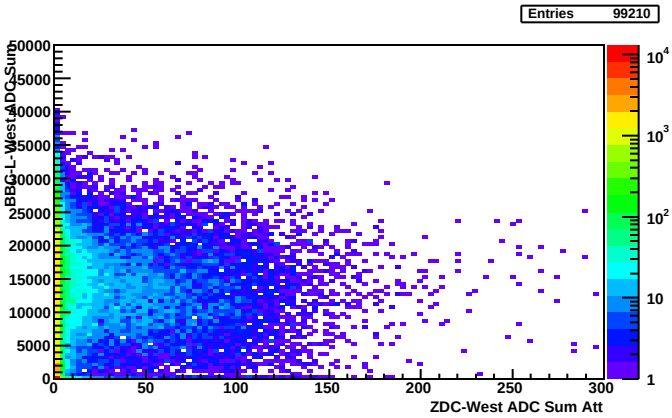

Entries 99210
TOF Mult
ZDC-East ADC Sum Att

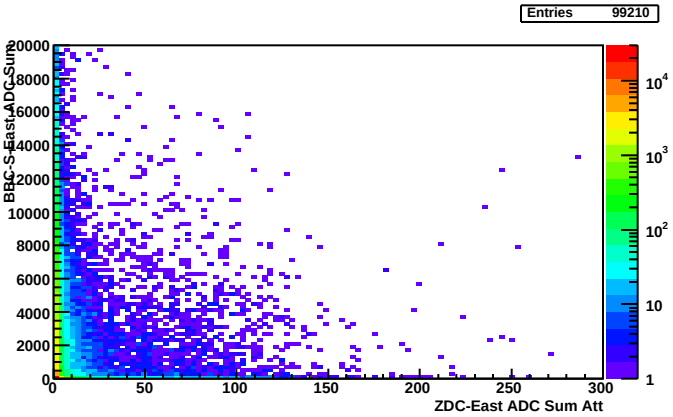

Entries 99210
TOF Mult
ZDC-West ADC Sum Att

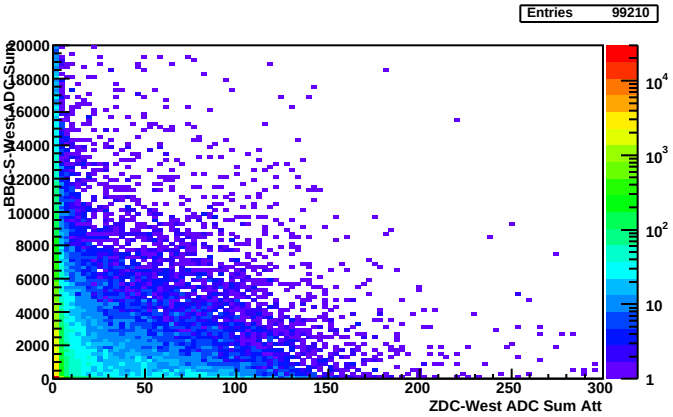

Entries 99210
BBC-L-East ADC Sum
BBC-S-East ADC Sum

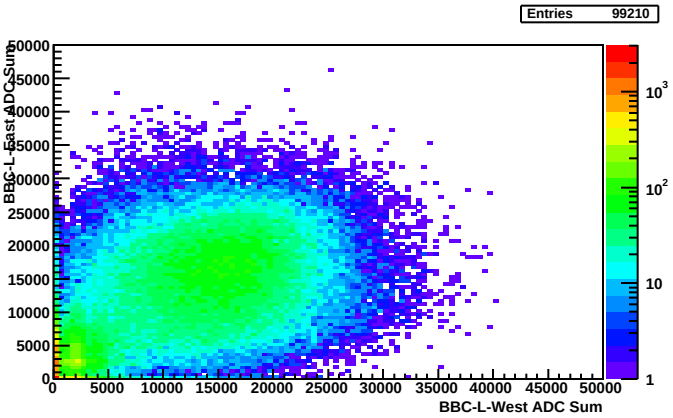

Entries 99210
BBC-L-West ADC Sum
BBC-S-West ADC Sum

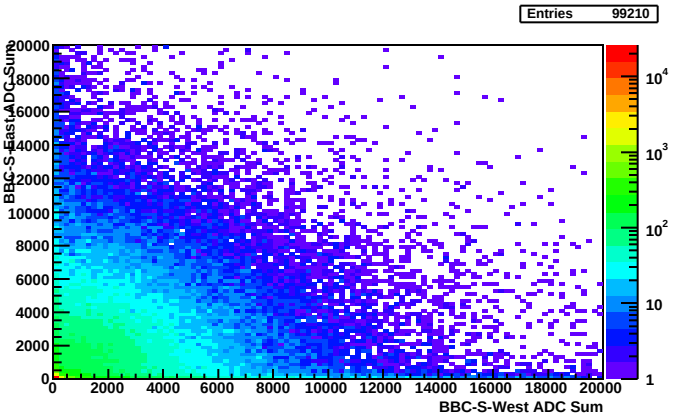

Entries 99210
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

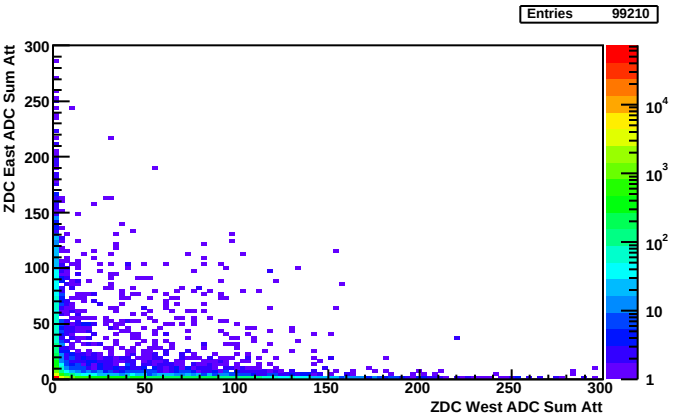

Entries 99210
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

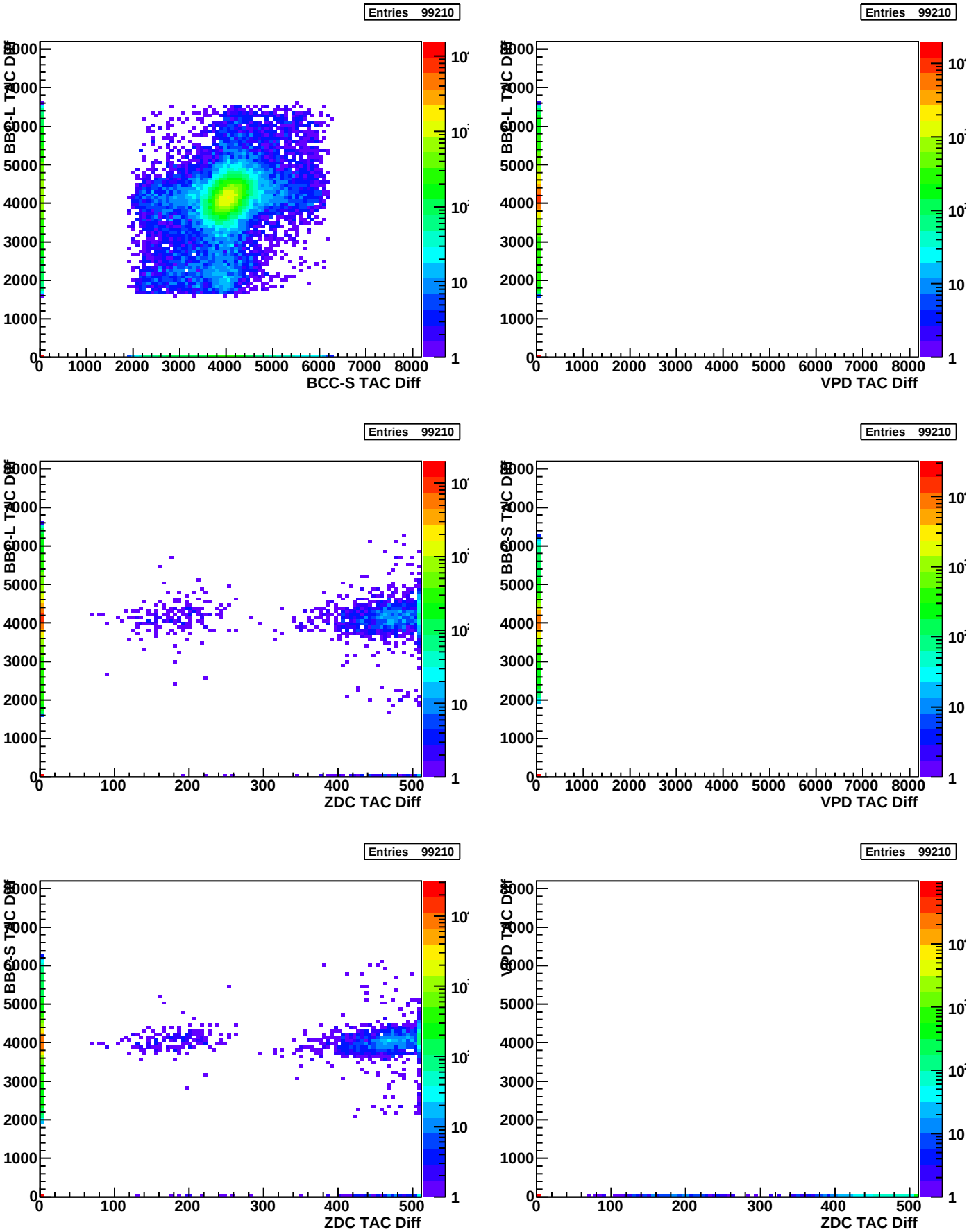

Entries 99210
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

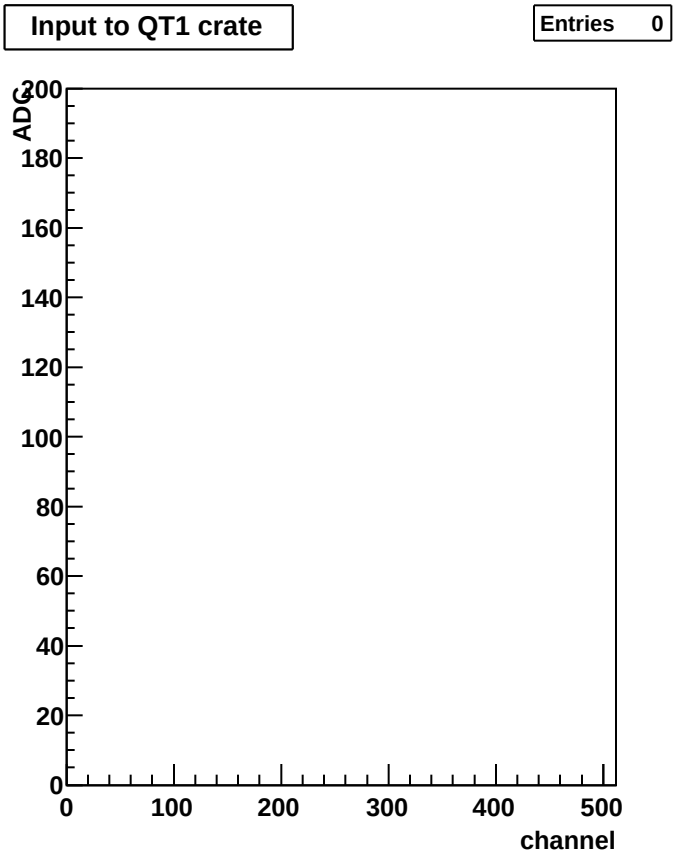

Entries 99210
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

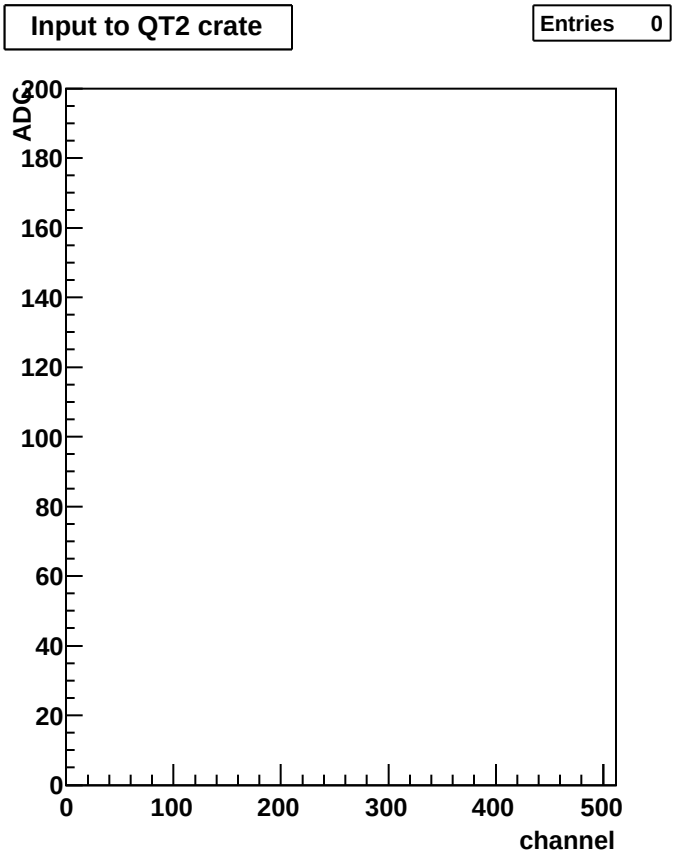

Entries 99210
BBC-L-East ADC Sum
BBC-L-West ADC Sum

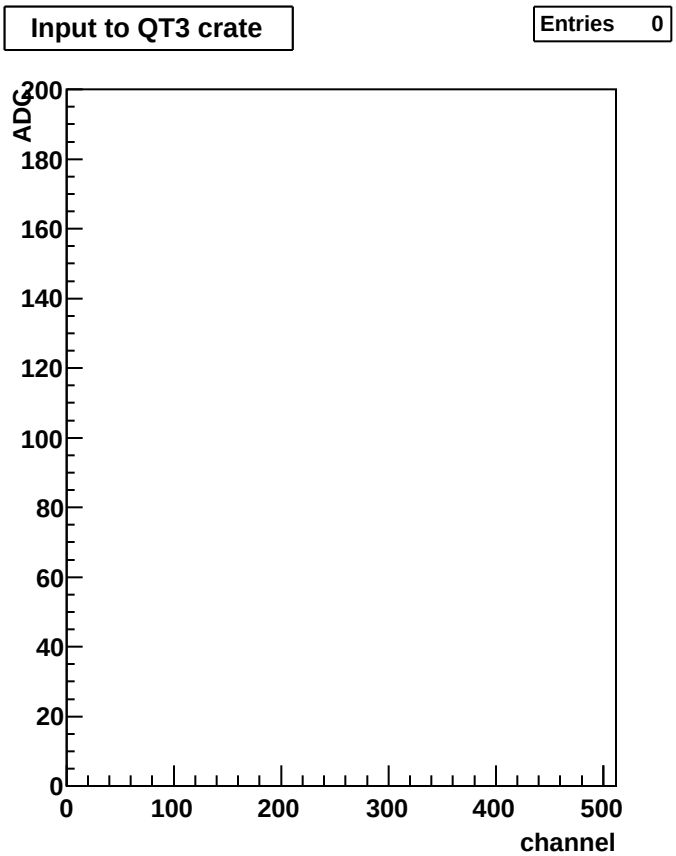

Entries 99210
BBC-S-East ADC Sum
BBC-S-West ADC Sum

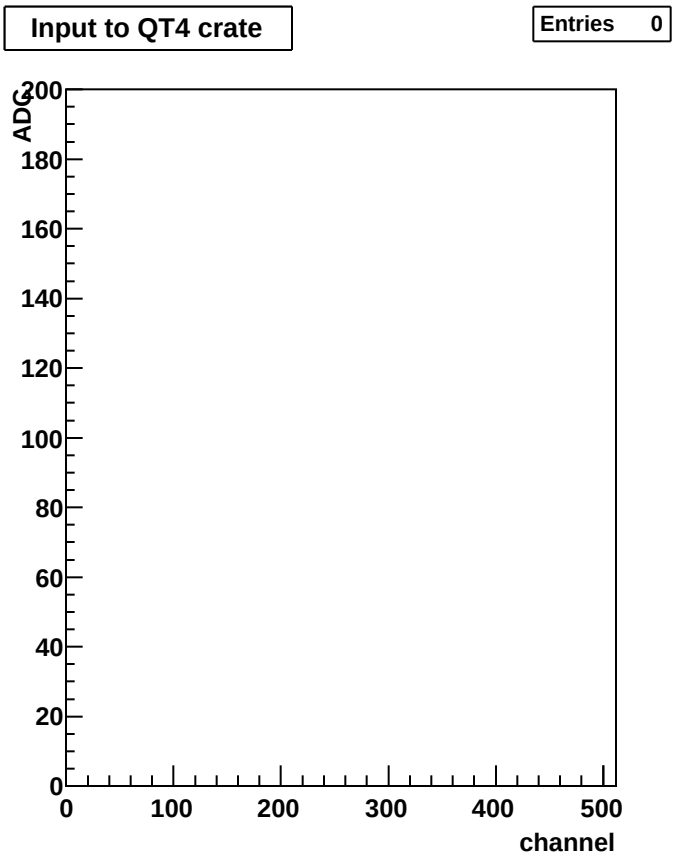

Entries 99210
ZDC East ADC Sum Att
ZDC West ADC Sum Att

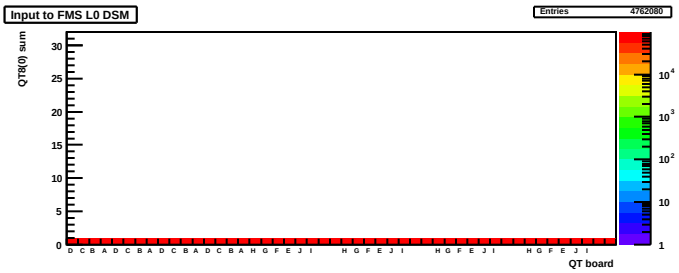

Entries 99210
BBC-L TAC Diff
BCC-S TAC Diff
Entries 99210
BBC-L TAC Diff
VPD TAC Diff
Entries 99210
BBC-L TAC Diff
ZDC TAC Diff
Entries 99210
BBC-S TAC Diff
VPD TAC Diff
Entries 99210
BBC-S TAC Diff
ZDC TAC Diff
Entries 99210
VPD TAC Diff
ZDC TAC Diff

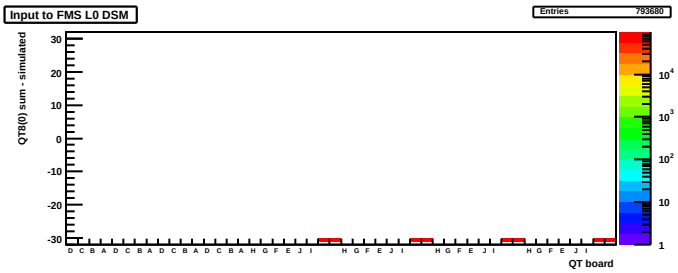

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

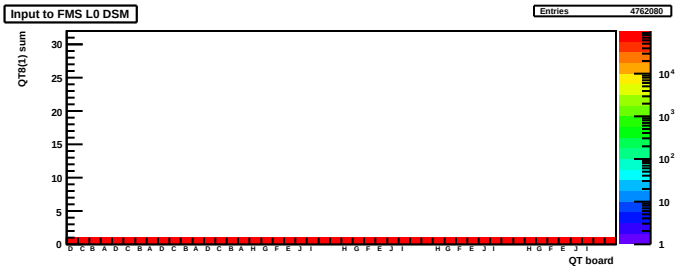

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

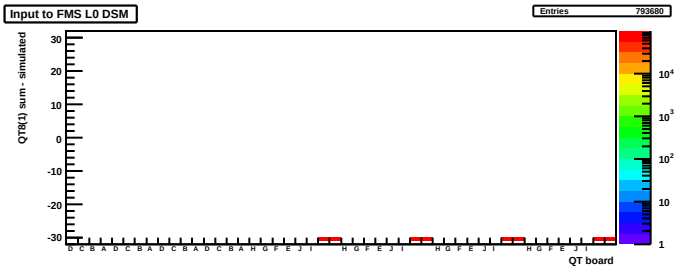

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

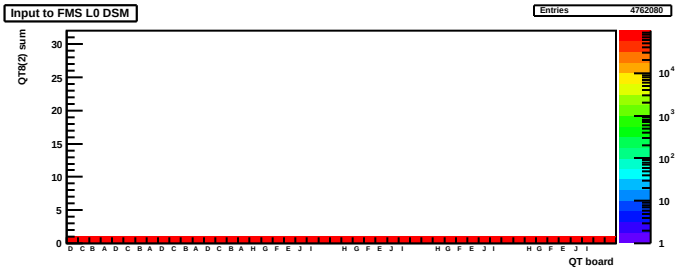

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

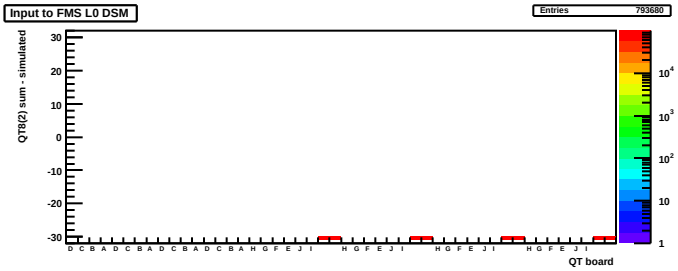

Input to FMS L0 DSM
Entries 4762080
QT8(0) sum
QT board

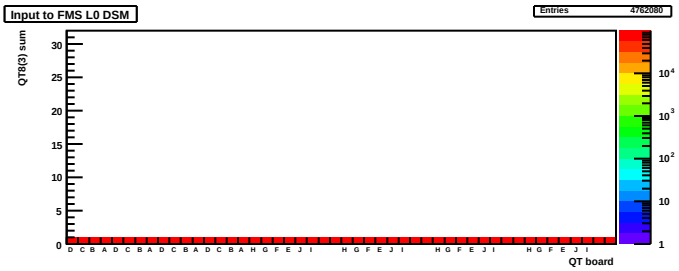

Input to FMS L0 DSM
Entries 793680
QT8(0) sum - simulated
QT board

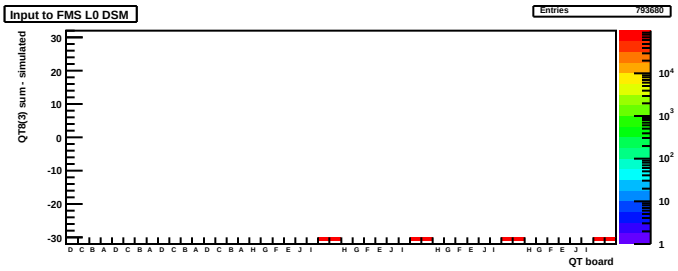

Input to FMS L0 DSM
Entries 4762080
QT8(1) sum
QT board

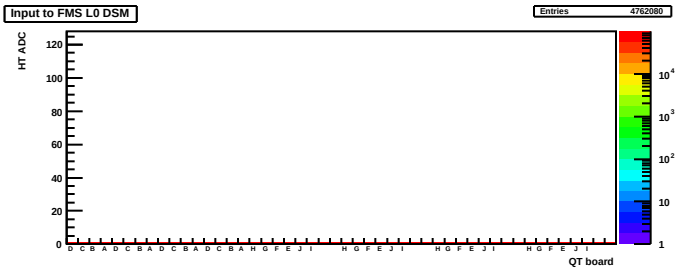

Input to FMS L0 DSM
Entries 793680
QT8(1) sum - simulated
QT board

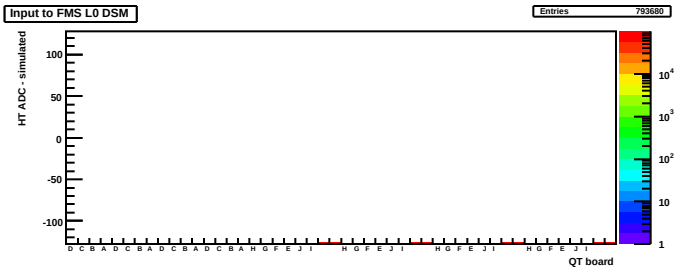

Input to FMS L0 DSM
Entries 4762080
QT8(2) sum
QT board

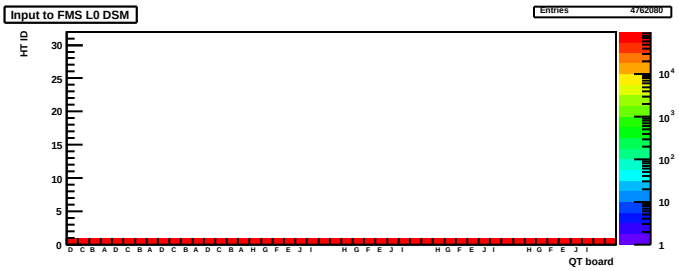

Input to FMS L0 DSM
Entries 793680
QT8(2) sum - simulated
QT board

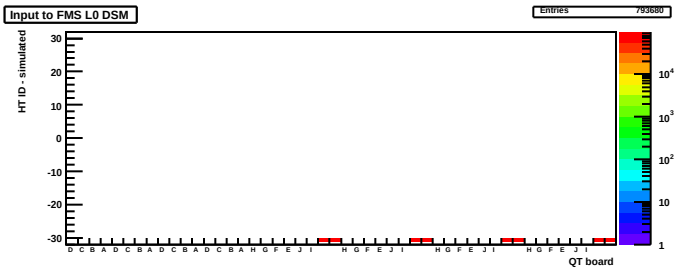

Input to FMS L0 DSM
Entries 4762080
QT8(3) sum
QT board

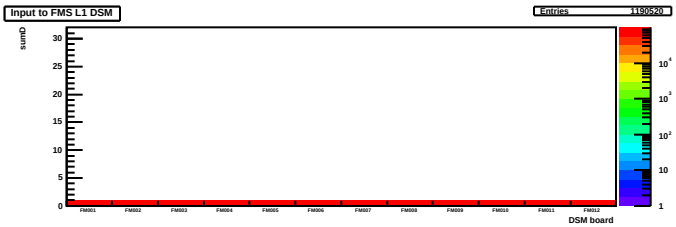

Input to FMS L0 DSM
Entries 793680
QT8(3) sum - simulated
QT board

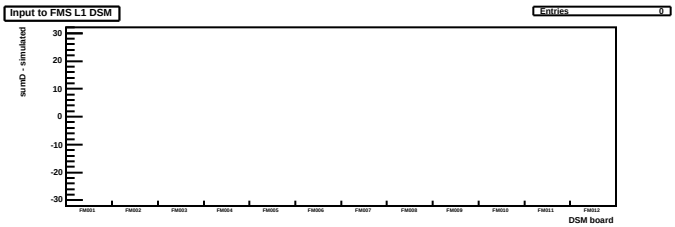

Input to FMS L0 DSM
Entries 4762080
HT ADC
QT board

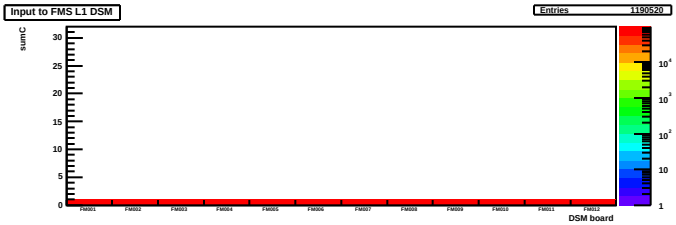

Input to FMS L0 DSM
Entries 793680
HT ADC - simulated
QT board

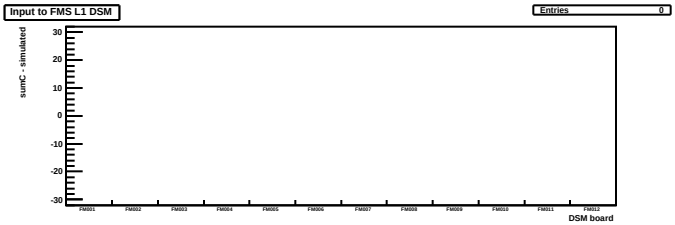

Input to FMS L0 DSM
Entries 4762080
HT ID
QT board

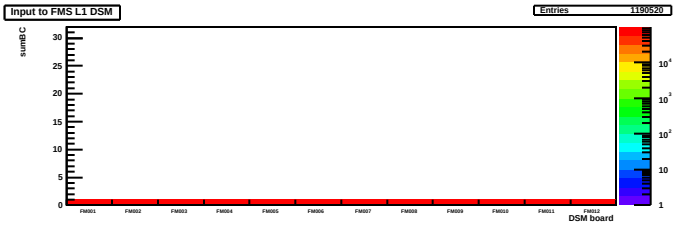

Input to FMS L0 DSM
Entries 793680
HT ID - simulated
QT board

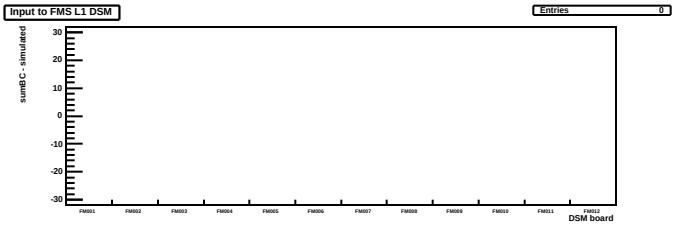

Input to FMS L1 DSM
Entries 1190520
sumD
DSM board

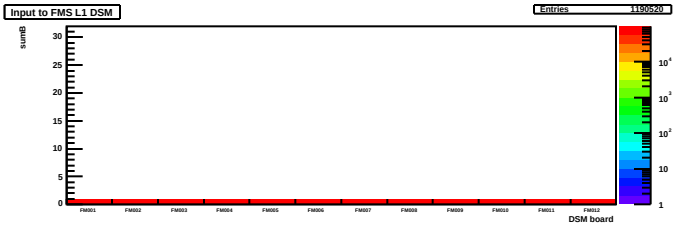

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

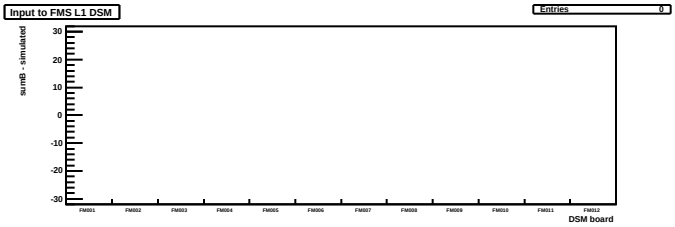

Input to FMS L1 DSM
Entries 1190520
sumC
DSM board

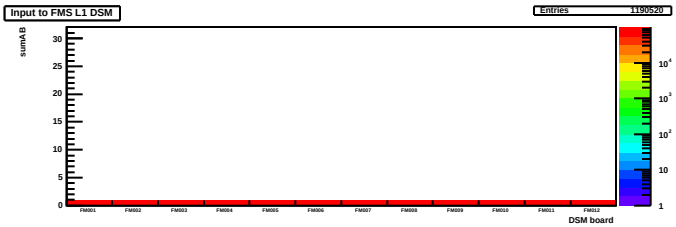

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

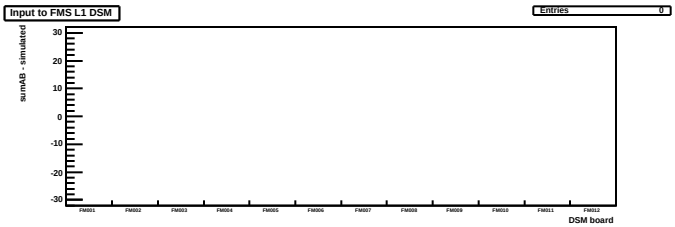

Input to FMS L1 DSM
Entries 1190520
sumBC
DSM board

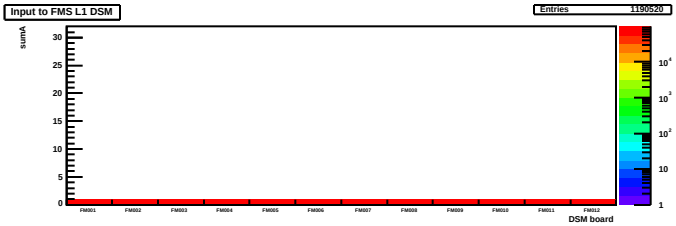

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

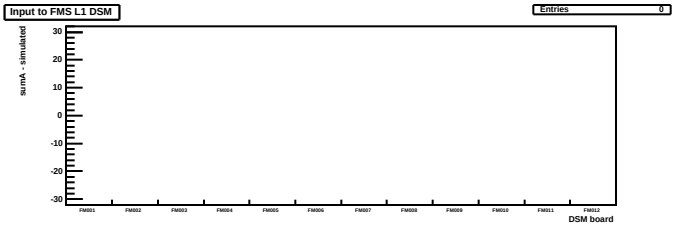

Input to FMS L1 DSM
Entries 1190520
sumB
DSM board

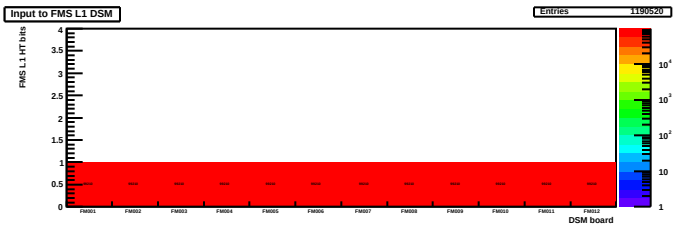

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

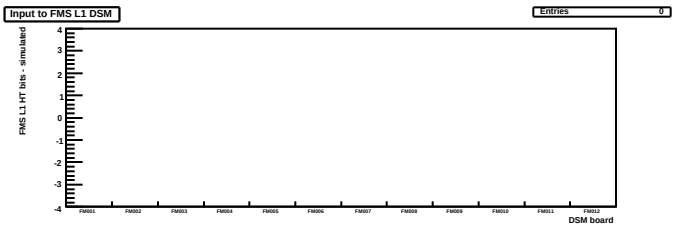

Input to FMS L1 DSM
Entries 1190520
sumAB
DSM board

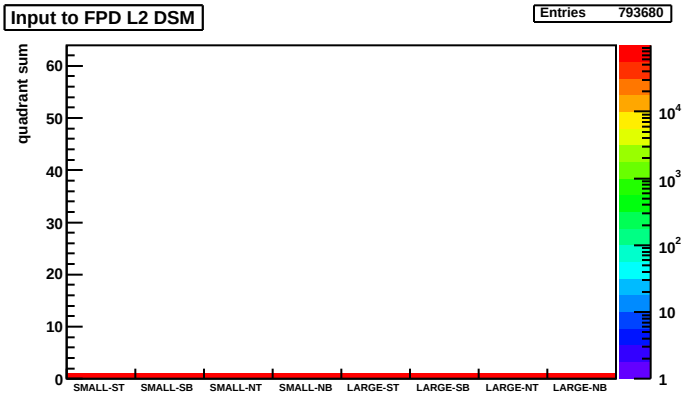

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

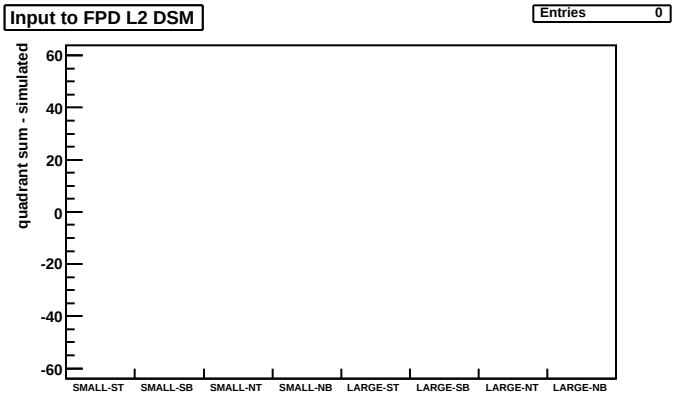

Input to FMS L1 DSM
Entries 1190520
sumA
DSM board

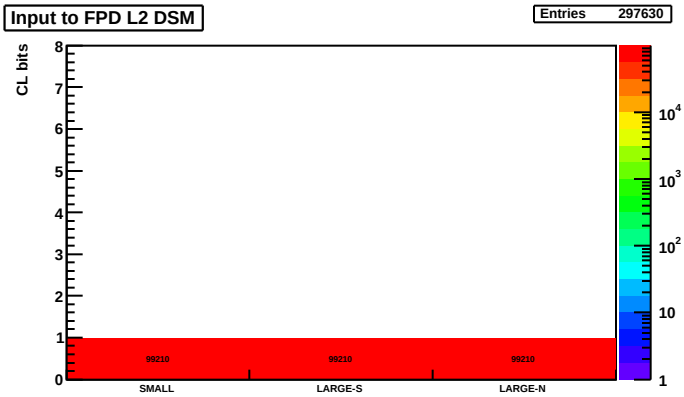

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

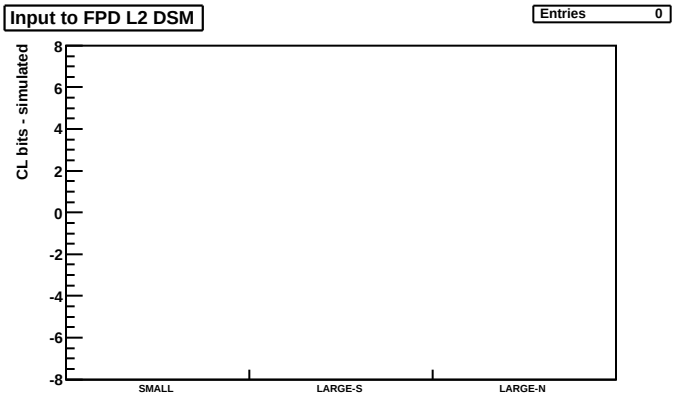

Input to FMS L1 DSM
Entries 1190520
FMS L1 HT bits
DSM board

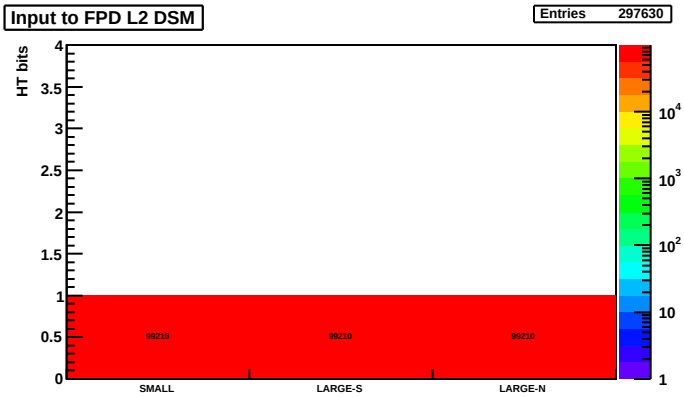

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

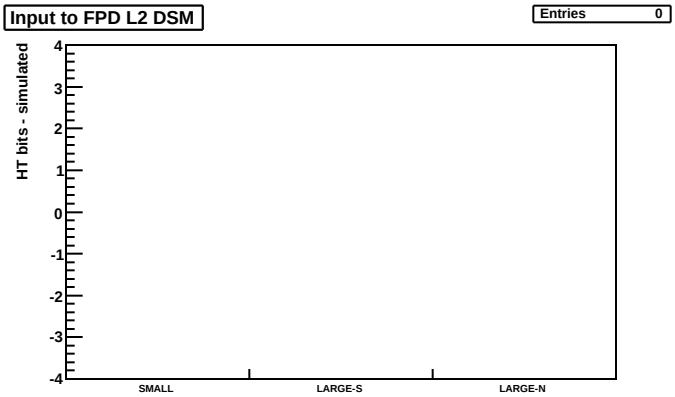

Input to FPD L2 DSM
Entries 793680
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

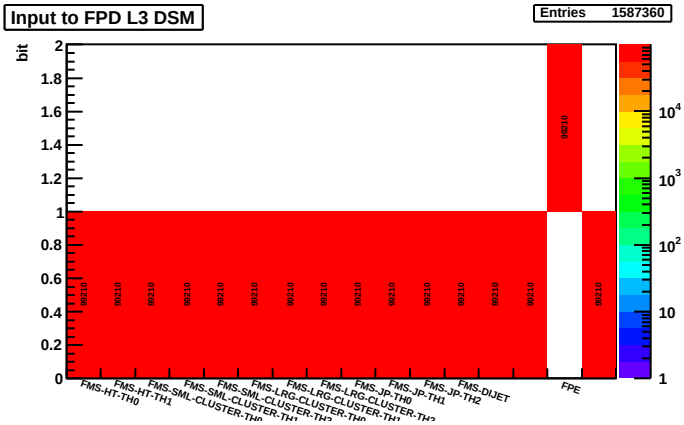

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

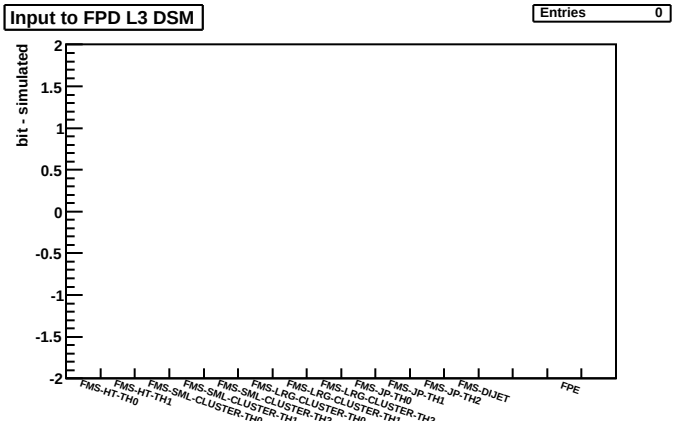

Input to FPD L2 DSM
Entries 297630
CL bits
99210
99210
99210
SMALL
LARGE-S
LARGE-N

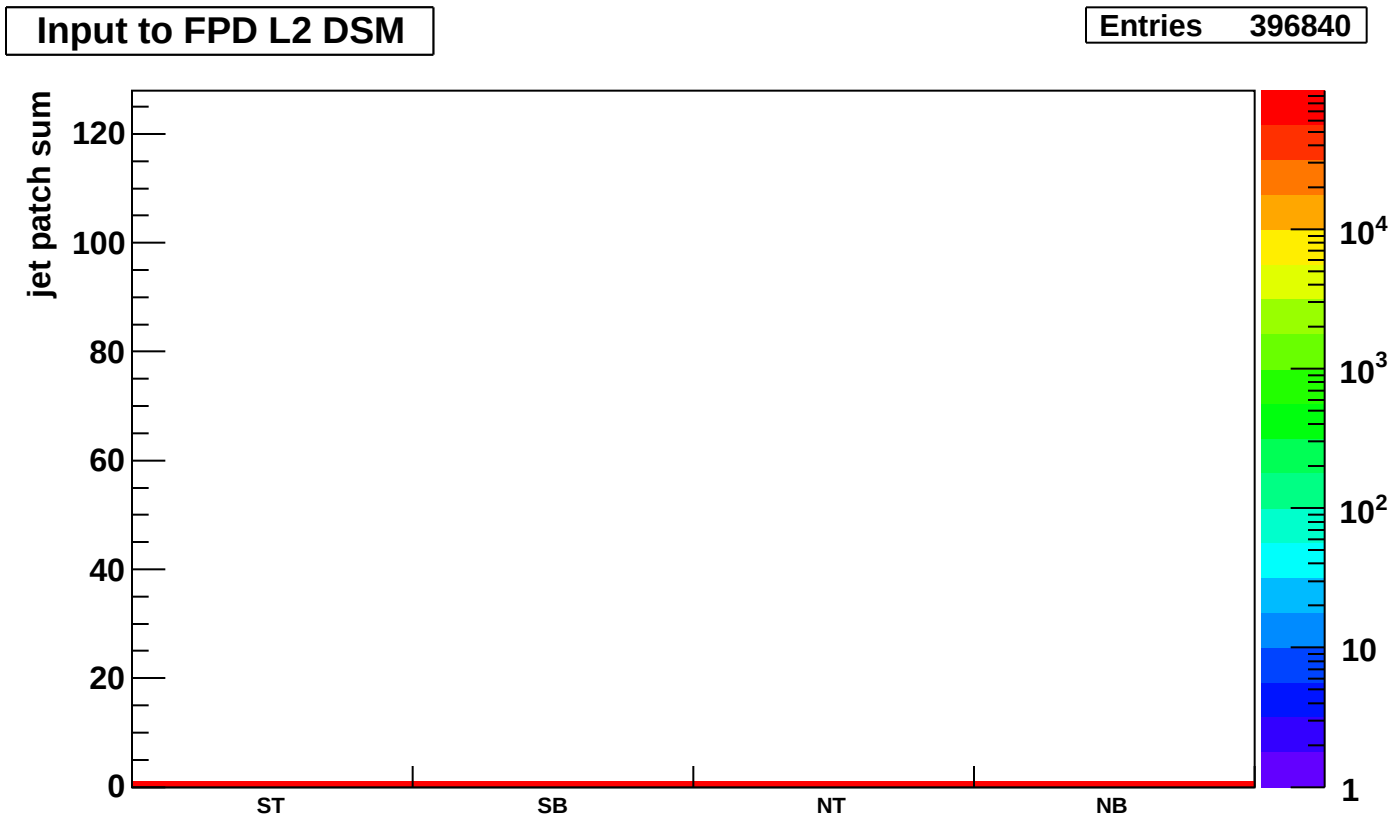

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

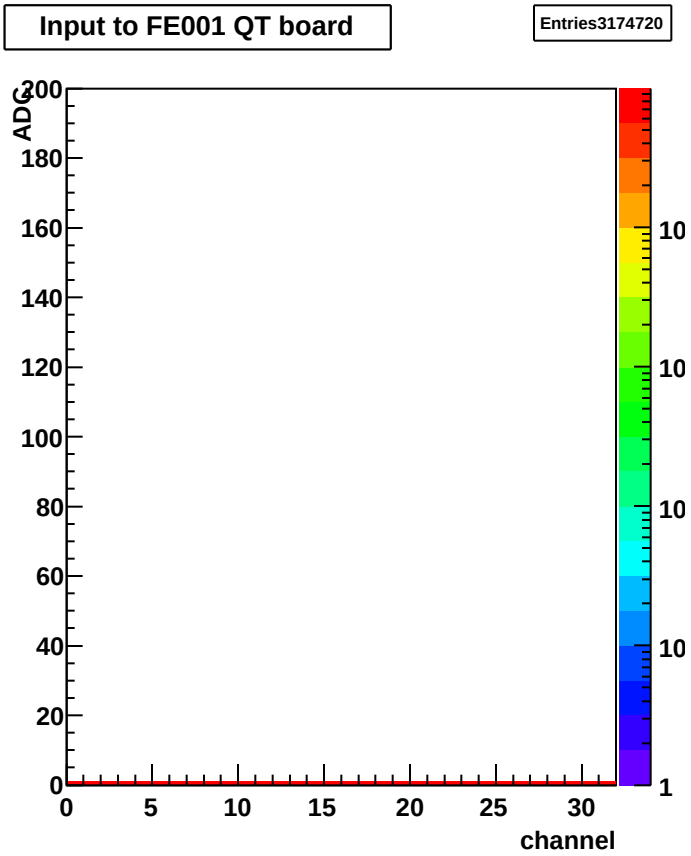

Input to FPD L2 DSM
Entries 297630
HT bits
99210
99210
99210
SMALL
LARGE-S
LARGE-N

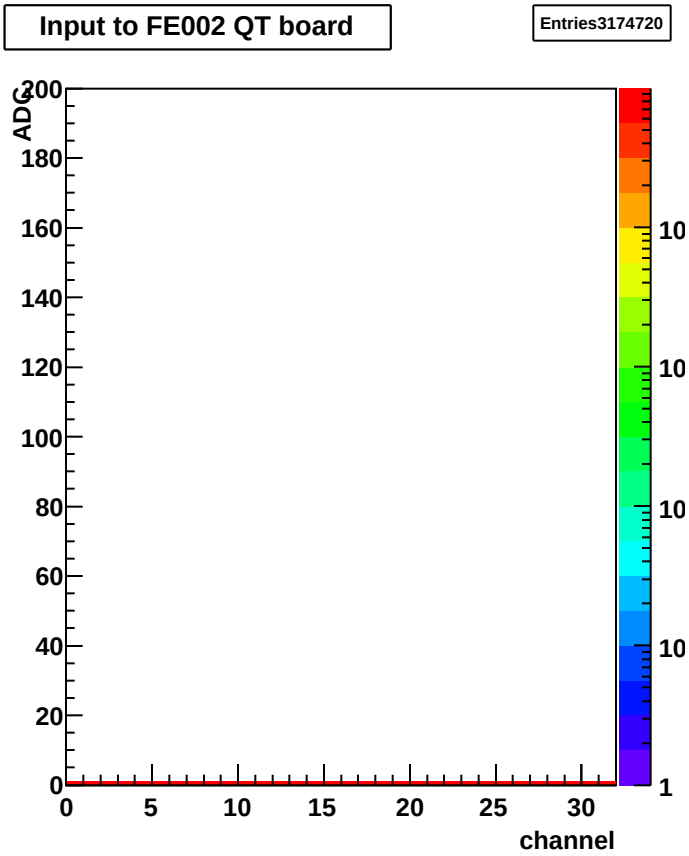

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

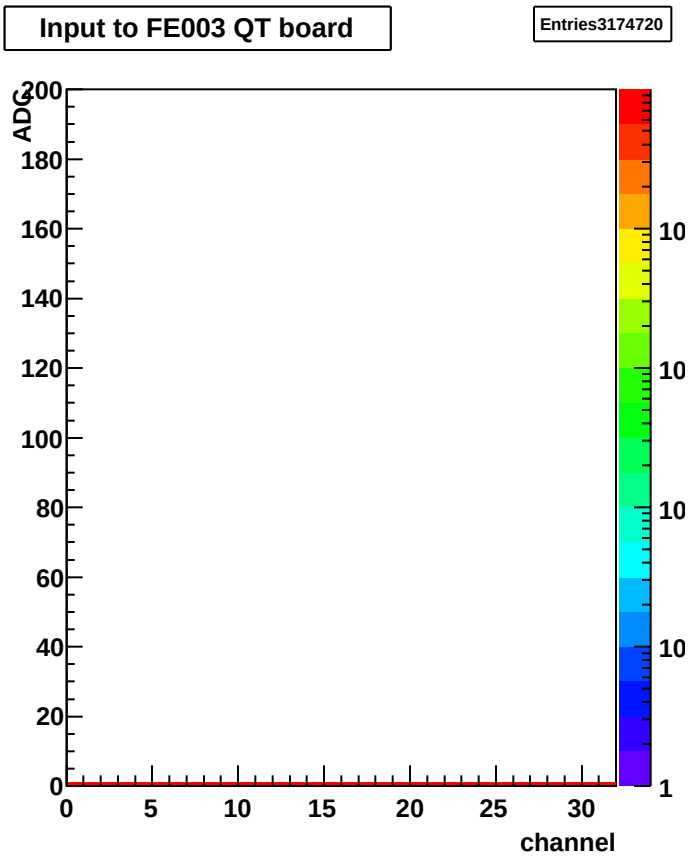

Input to FPD L3 DSM
Entries 1587360
bit
99210
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

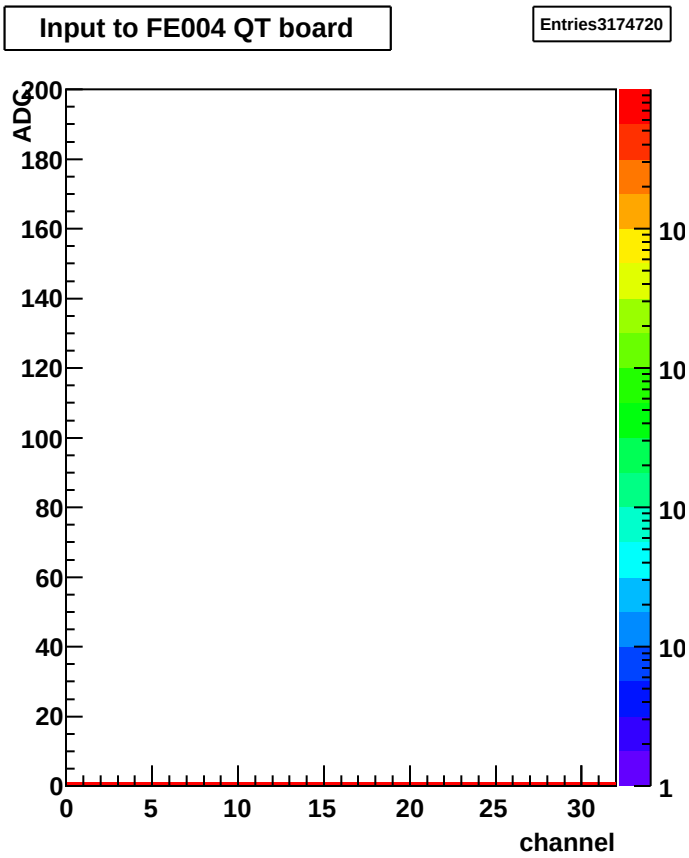

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

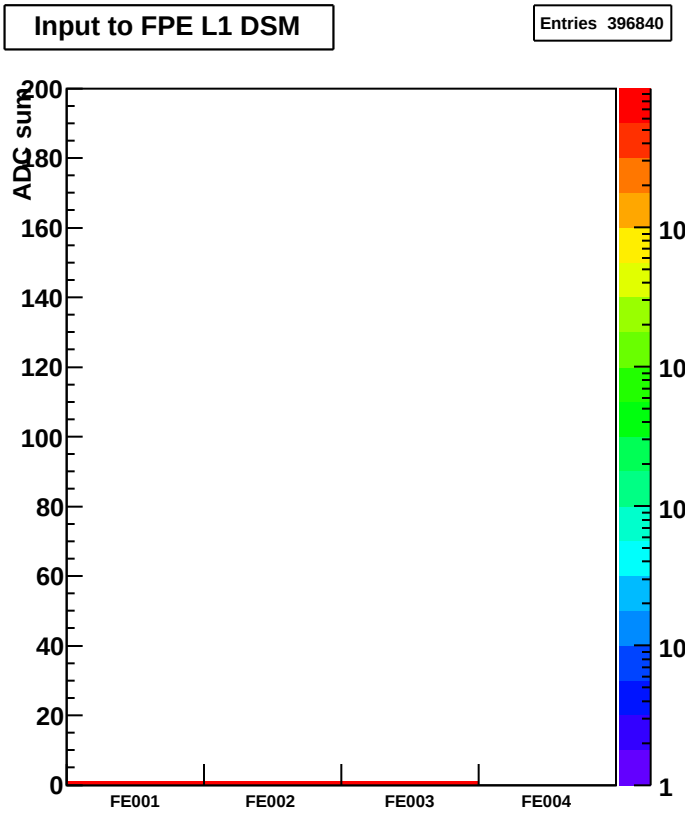

Input to FPD L2 DSM
Entries 396840
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

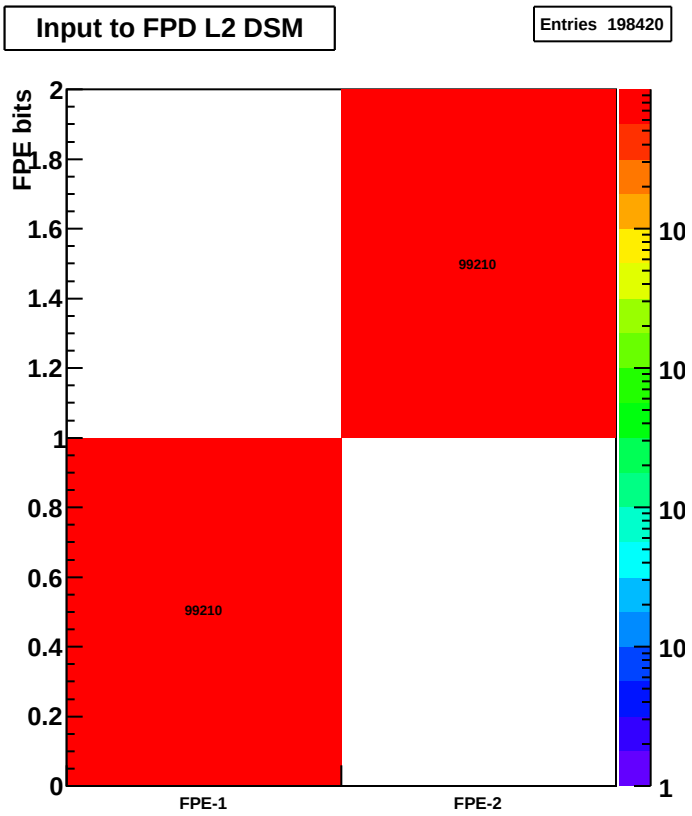

Input to FE001 QT board
Entries3174720
ADC
channel

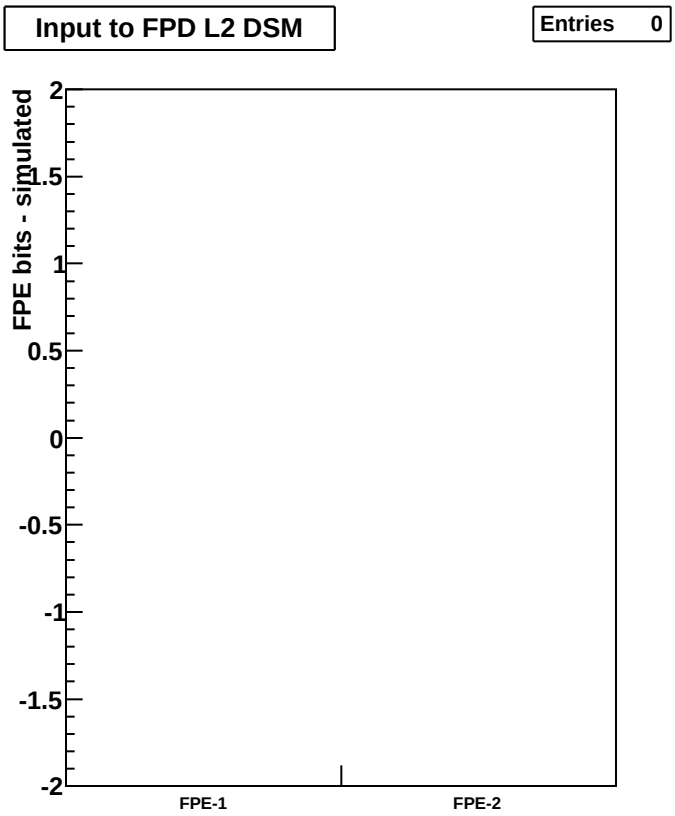

Input to FE002 QT board
Entries3174720
ADC
channel


Input to FE003 QT board
Entries3174720
ADC
channel


Input to FE004 QT board
Entries3174720
ADC
channel


Input to FPE L1 DSM
Entries 396840
ADC sum
200
180
160
140
120
100
80
60
40
20
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 198420
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
99210
99210
FPE-1
FPE-2
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2