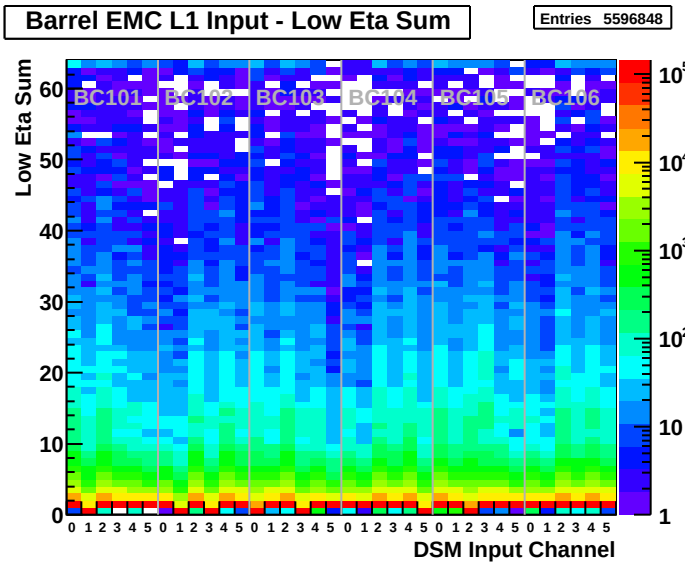
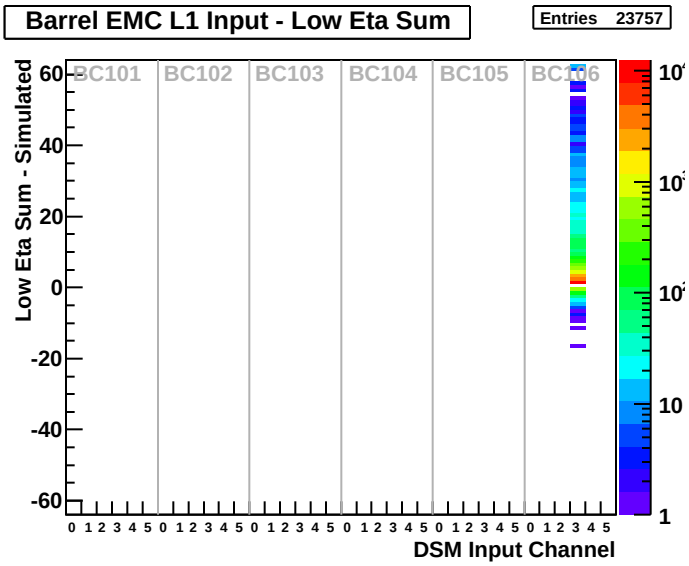
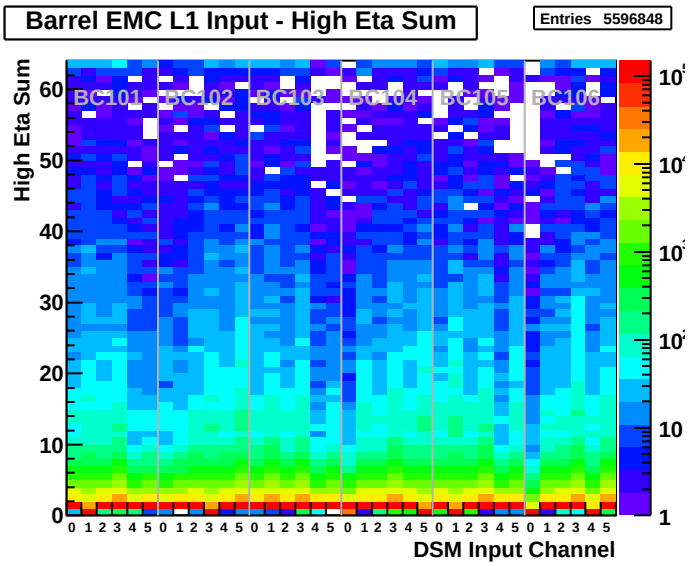
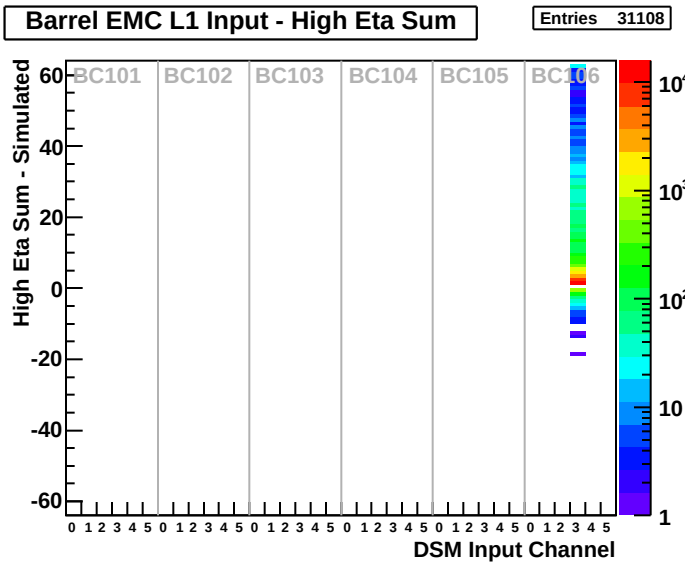
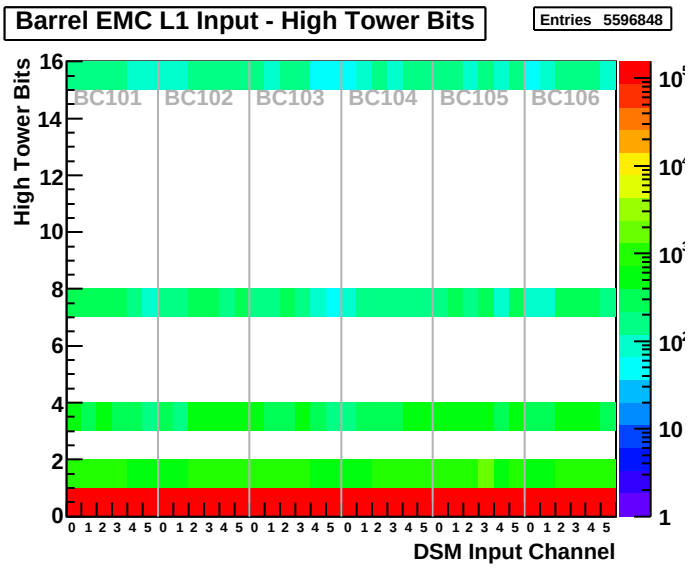
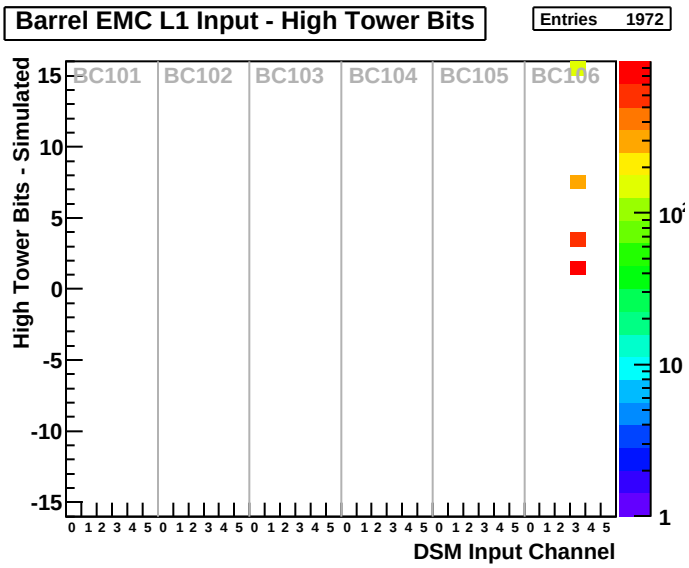

Barrel EMC L1 Input - Low Eta Sum
Entries 5596848
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

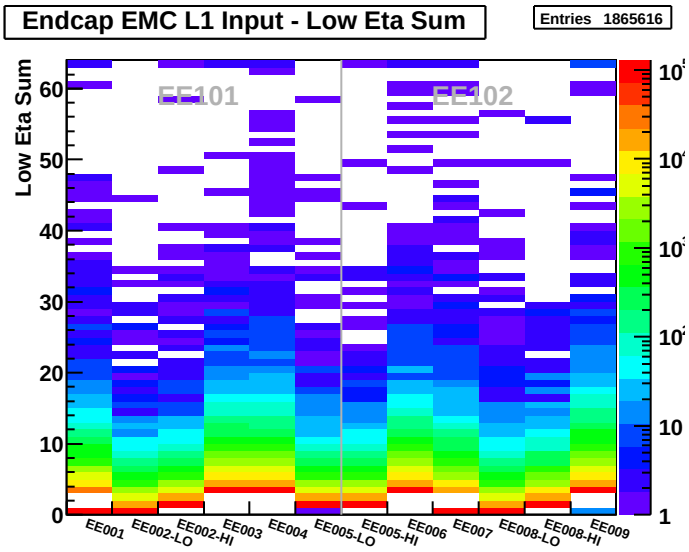

Barrel EMC L1 Input - Low Eta Sum
Entries 23757
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

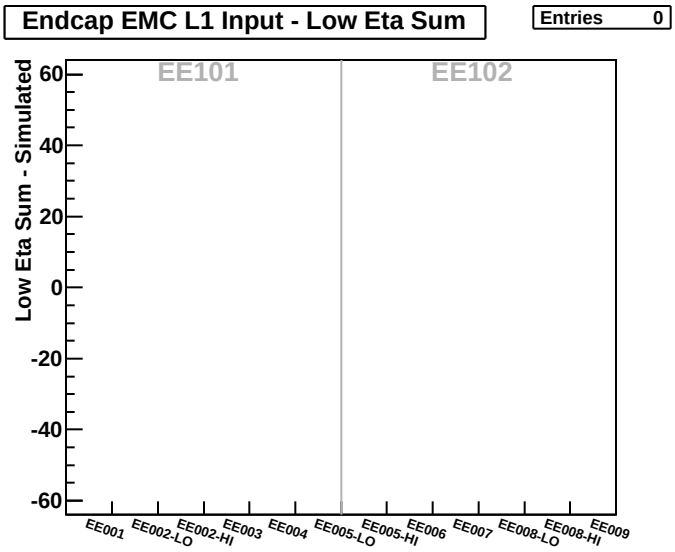

Barrel EMC L1 Input - High Eta Sum
Entries 5596848
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

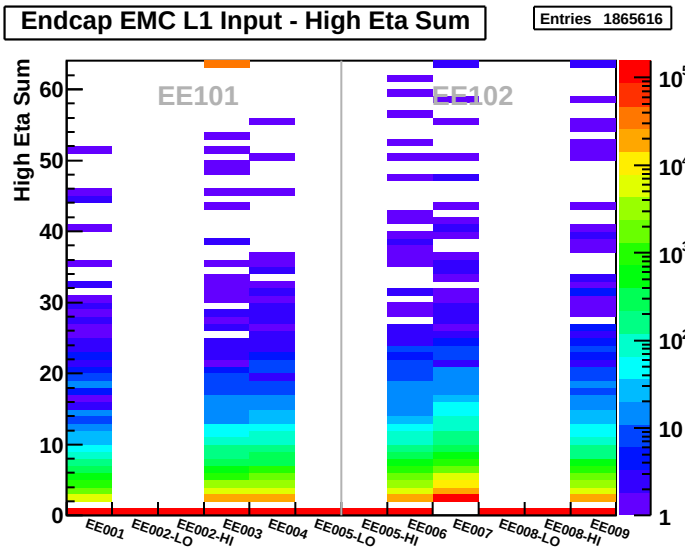

Barrel EMC L1 Input - High Eta Sum
Entries 31108
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

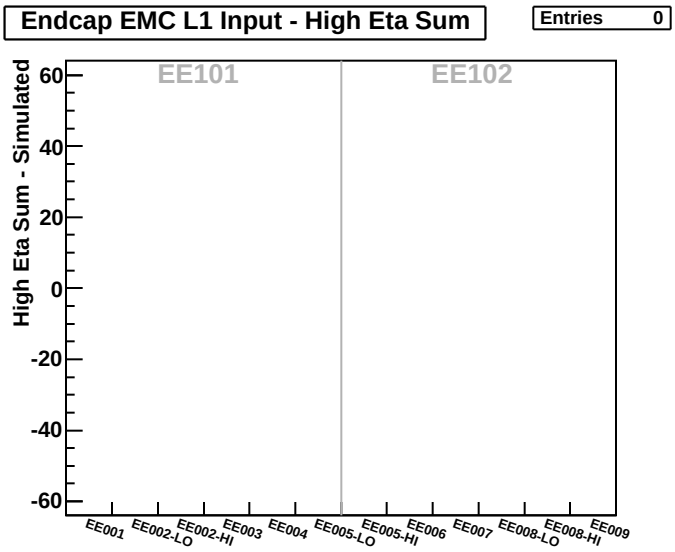

Barrel EMC L1 Input - High Tower Bits
Entries 5596848
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

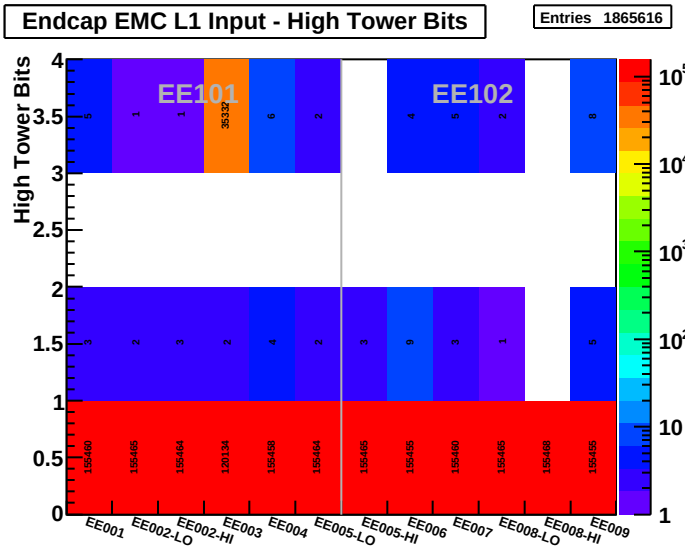

Barrel EMC L1 Input - High Tower Bits
Entries 1972
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

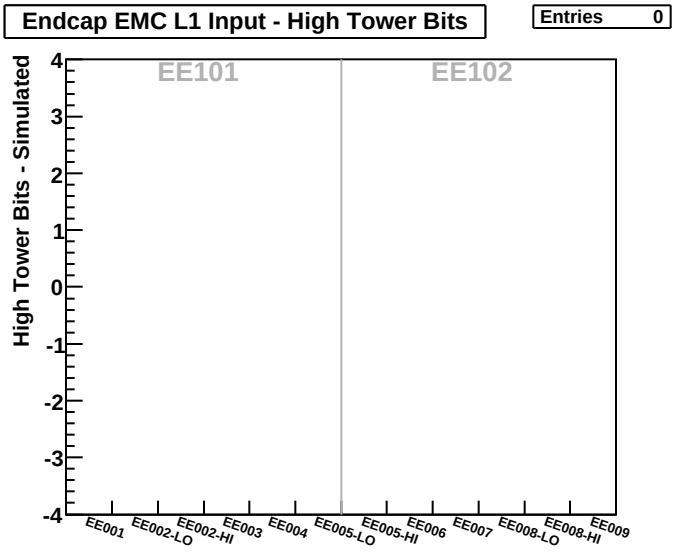

Endcap EMC L1 Input - Low Eta Sum
Entries 1865616
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

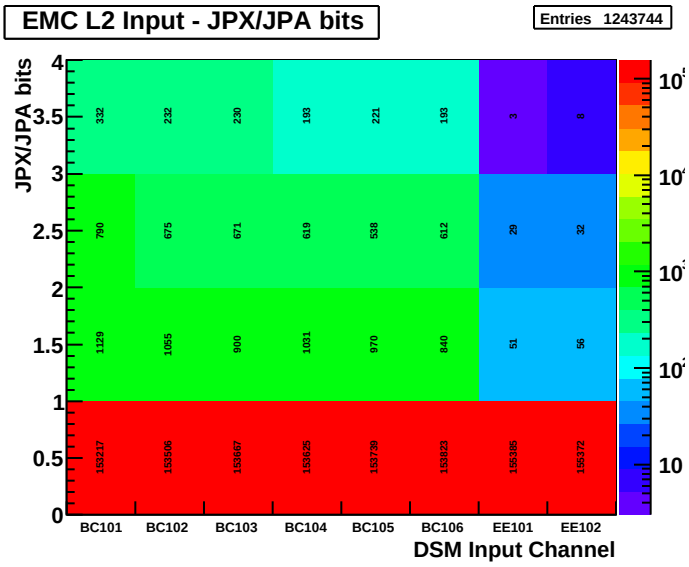

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

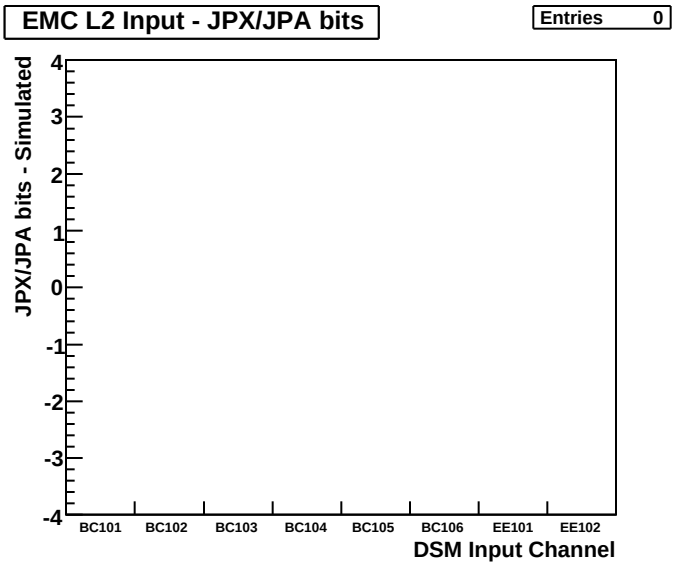

Endcap EMC L1 Input - High Eta Sum
Entries 1865616
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

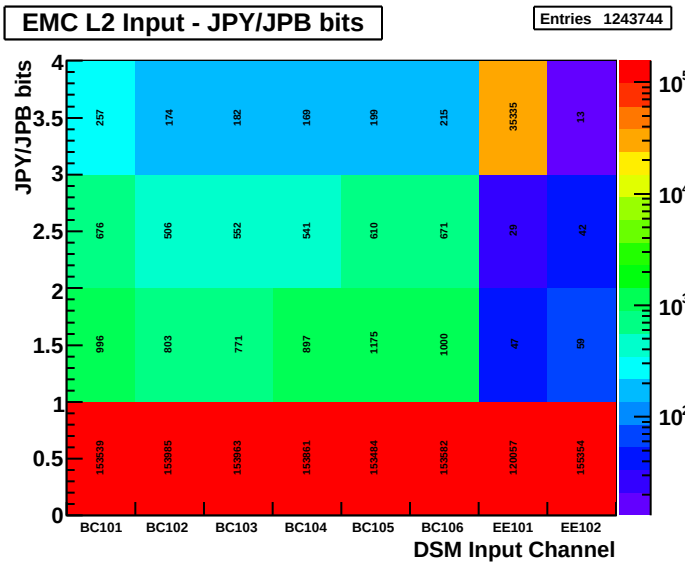

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

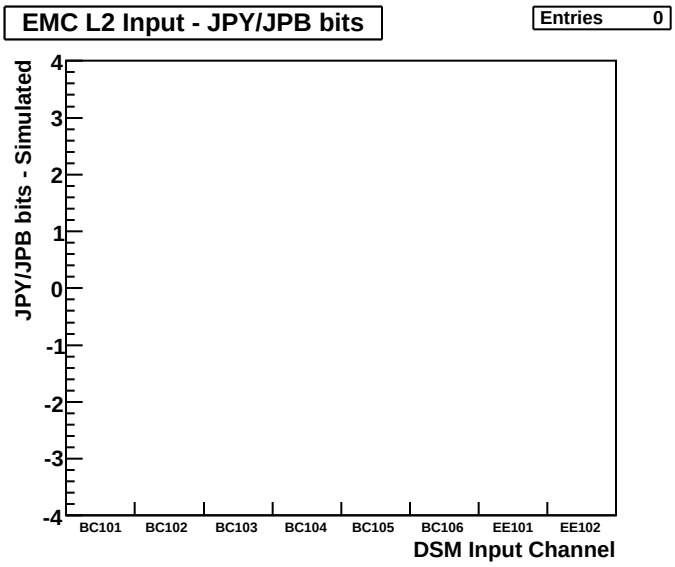

Endcap EMC L1 Input - High Tower Bits
Entries 1865616
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

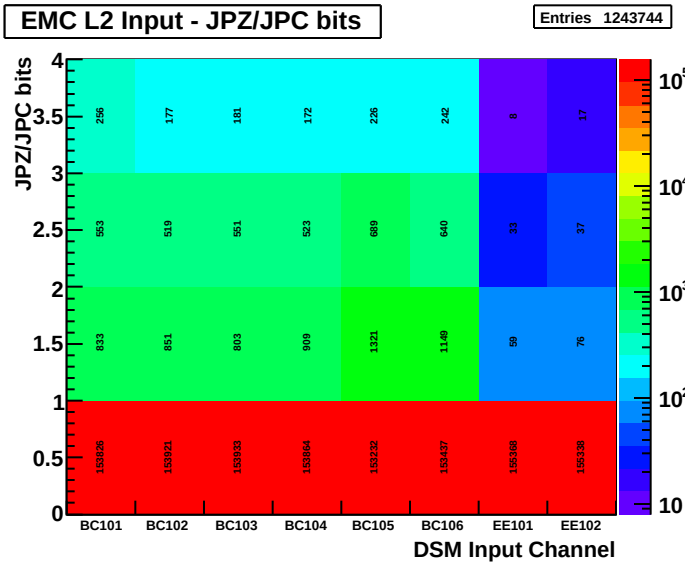

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

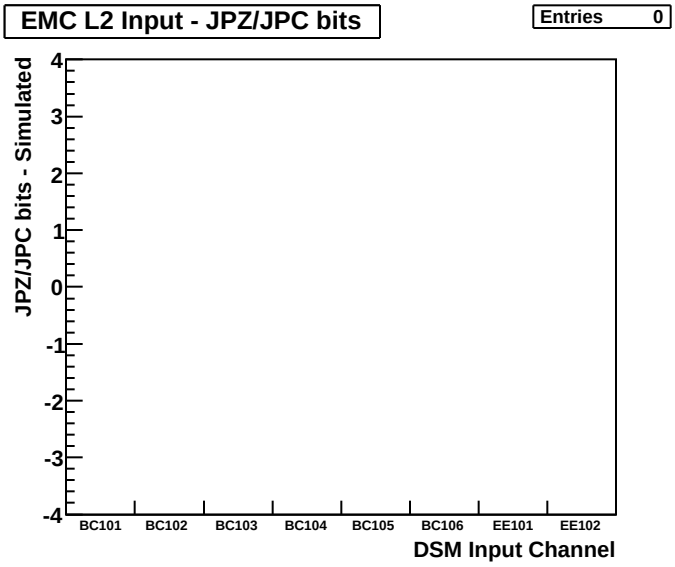

EMC L2 Input - JPX/JPA bits
Entries 1243744
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
332 232 230 193 221 193 3 8
790 675 671 619 538 612 29 32
1129 1055 900 1031 970 840 51 56
153217 153506 153667 153625 153739 153823 155385 155372

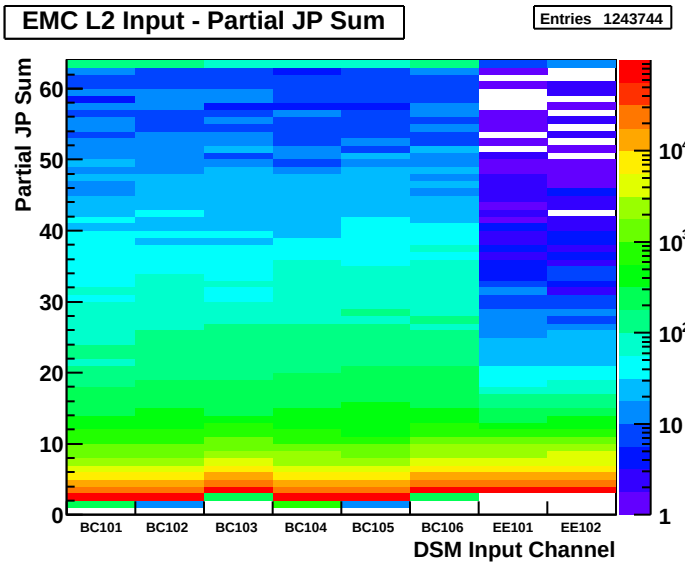

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

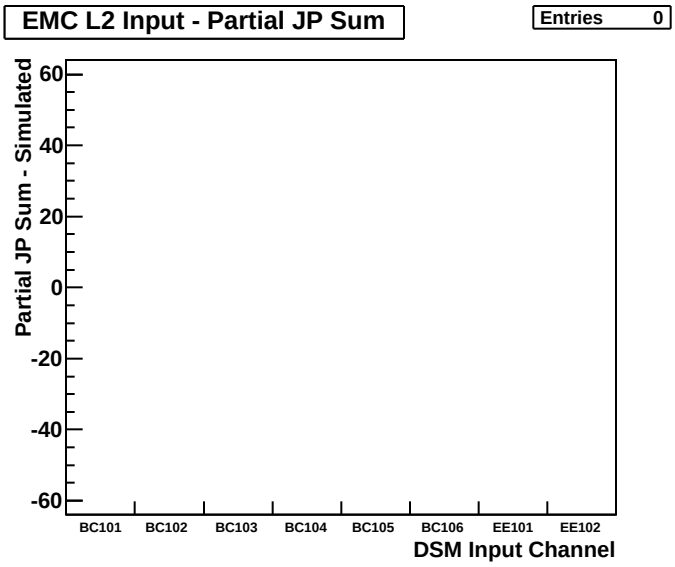

EMC L2 Input - JPY/JPB bits
Entries 1243744
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
257 174 182 169 199 215 35335 13
676 506 552 541 610 671 29 42
996 803 771 897 1175 1000 47 59
155539 153985 153963 153861 153484 153582 120057 155354

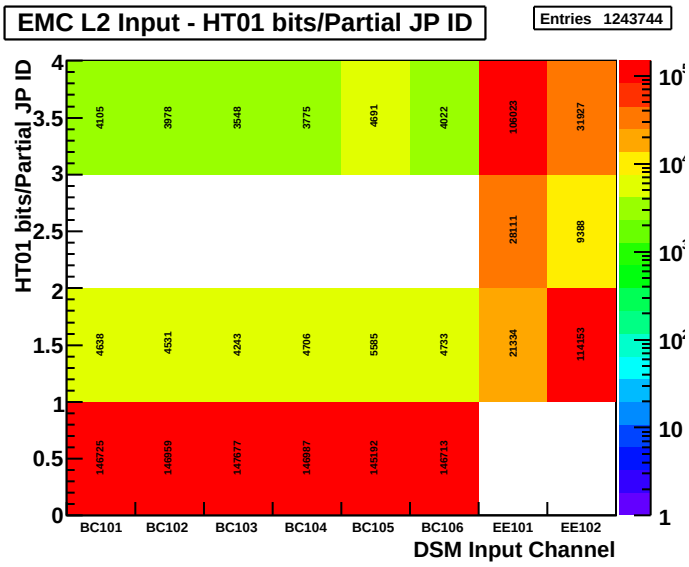

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

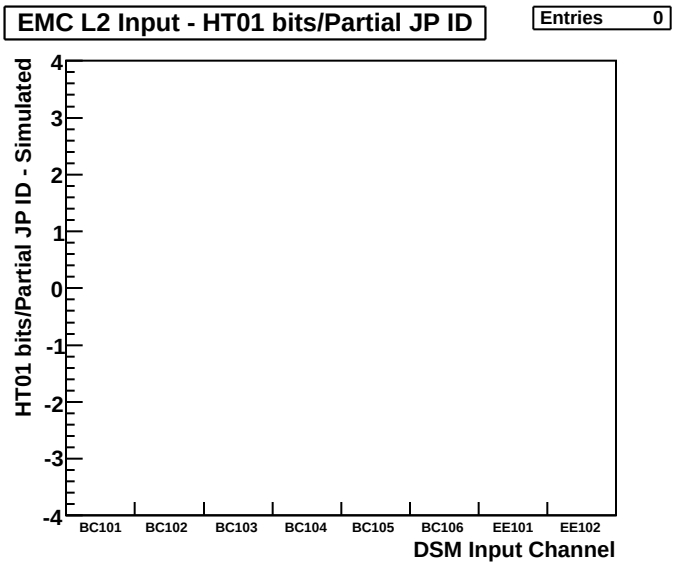

EMC L2 Input - JPZ/JPC bits
Entries 1243744
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
256 177 181 172 226 242 8 17
553 519 551 523 689 640 33 37
833 851 803 909 1321 1149 59 76
153926 153921 153933 153864 153232 153437 155388 155338

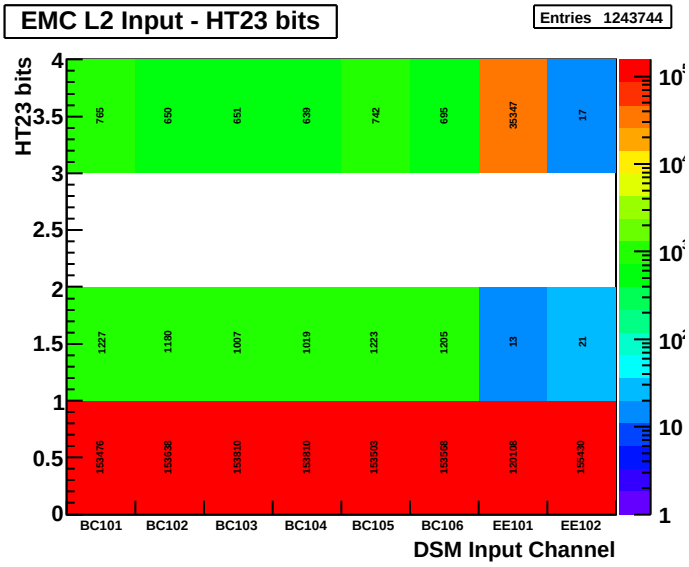

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

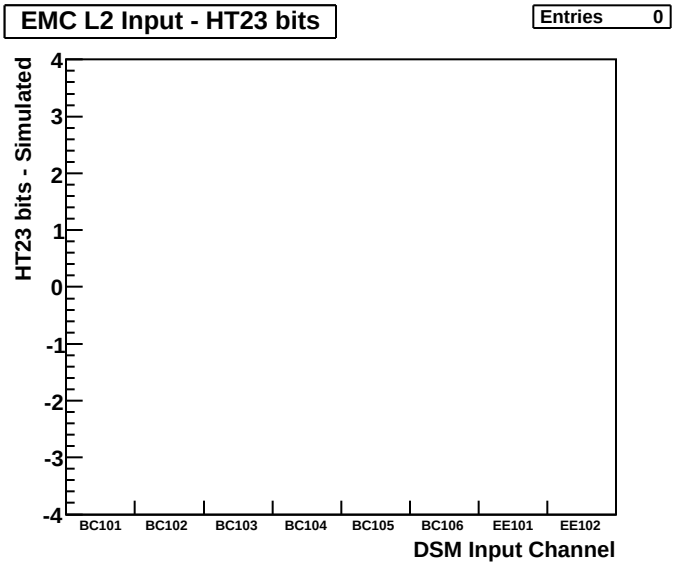

EMC L2 Input - Partial JP Sum
Entries 1243744
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

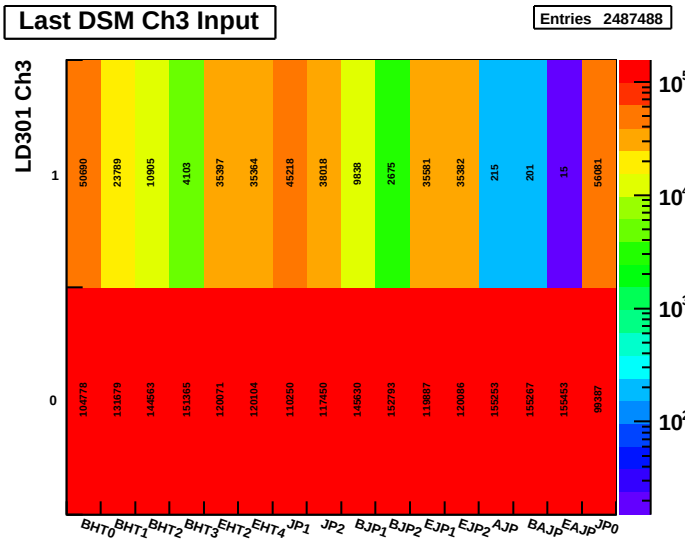

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

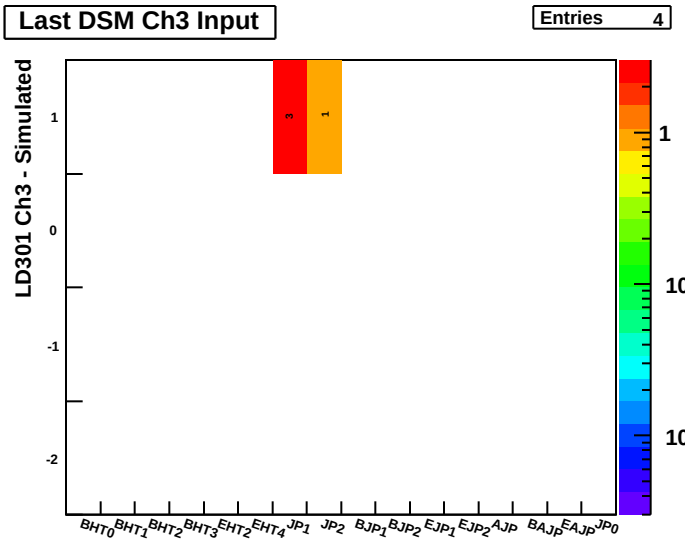

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1243744
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

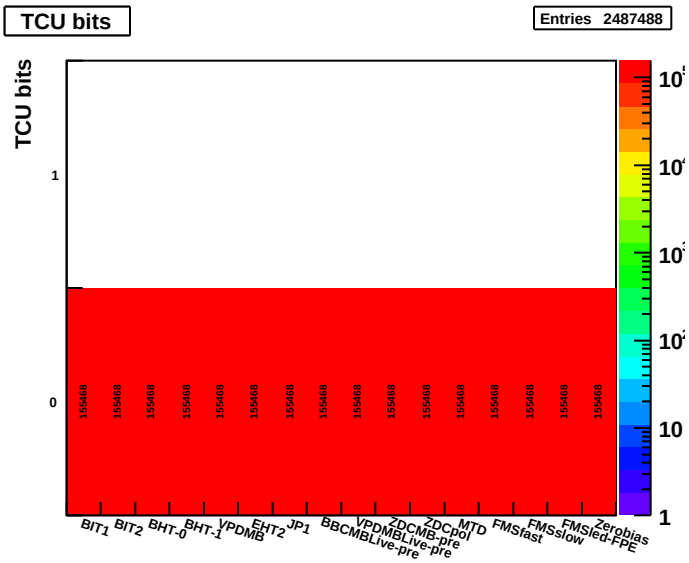

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

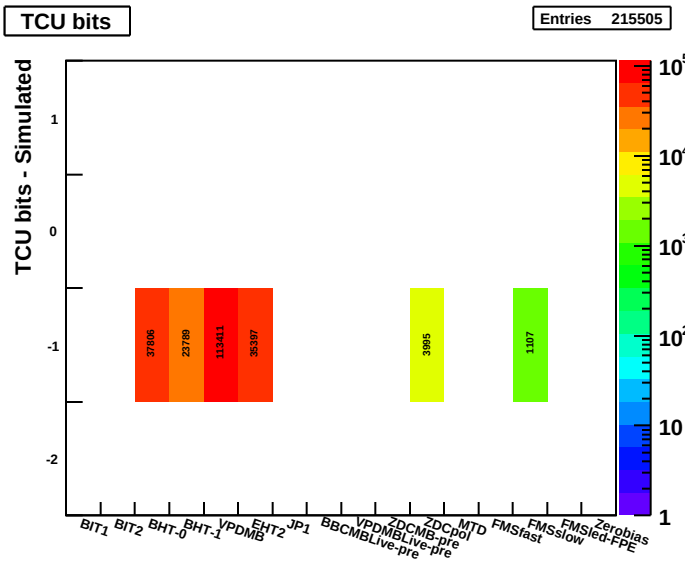

EMC L2 Input - HT23 bits
Entries 1243744
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

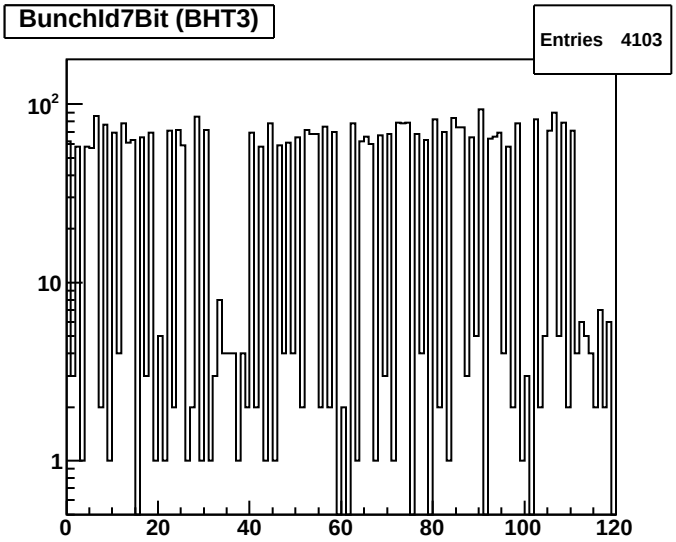

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

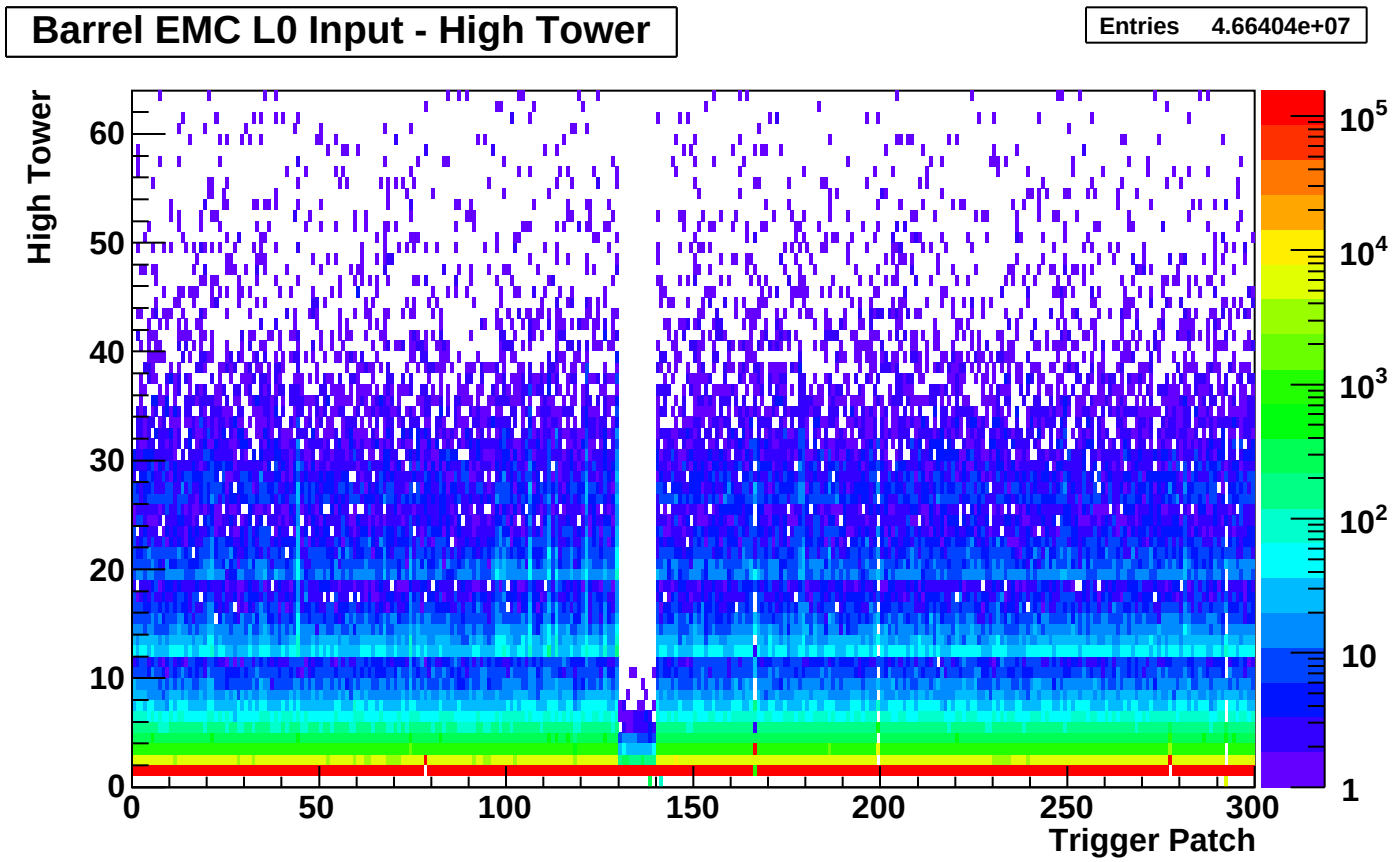

Last DSM Ch3 Input
Entries 2487488
LD301 Ch3
50680 23789 10905 4103 35397 35364 45218 38018 9838 2675 35581 35382 215 201 15 56081
104778 131679 144563 151365 120071 120104 110250 117450 145630 152783 118887 120086 155253 155267 155453 99387
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

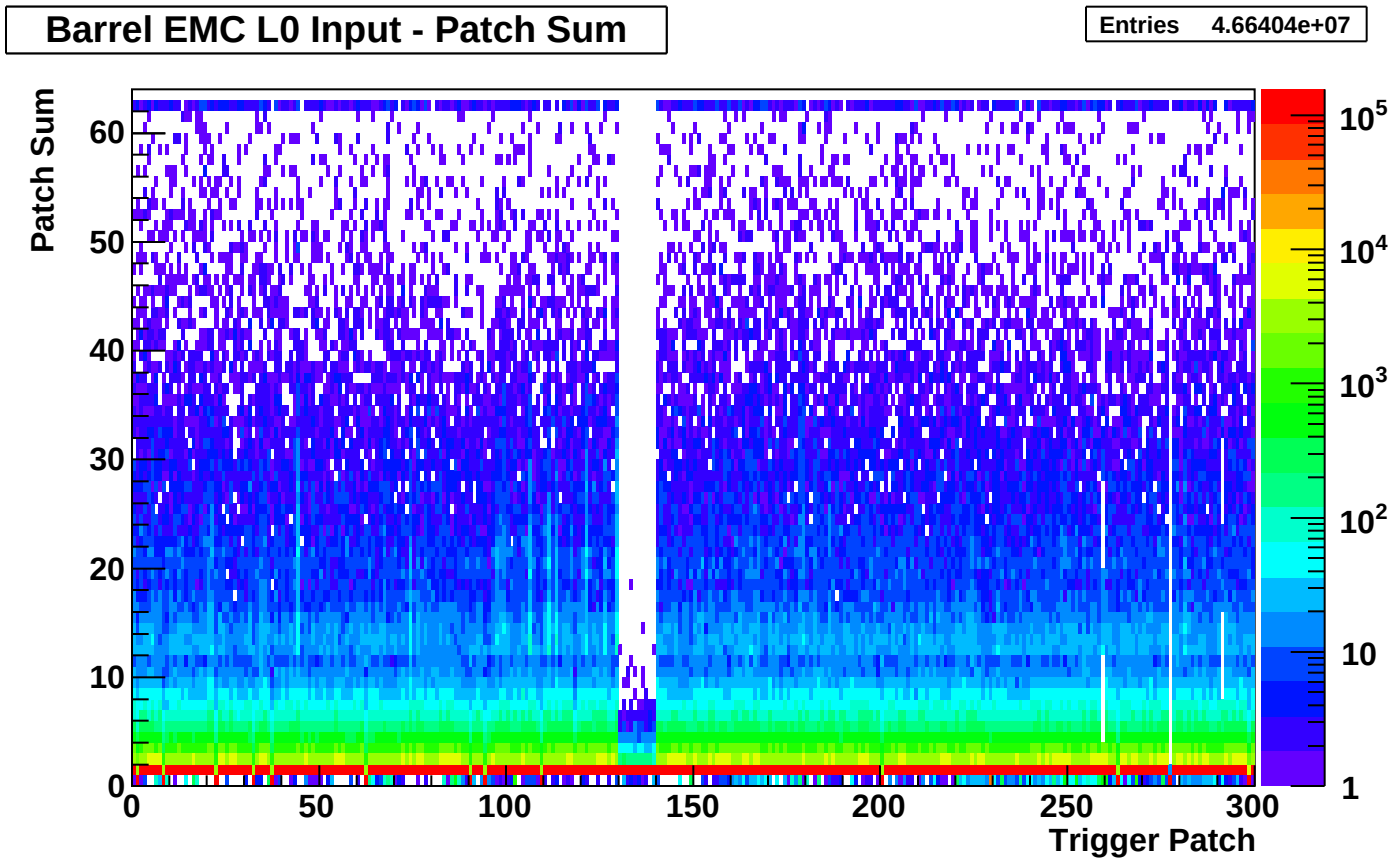

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
3 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

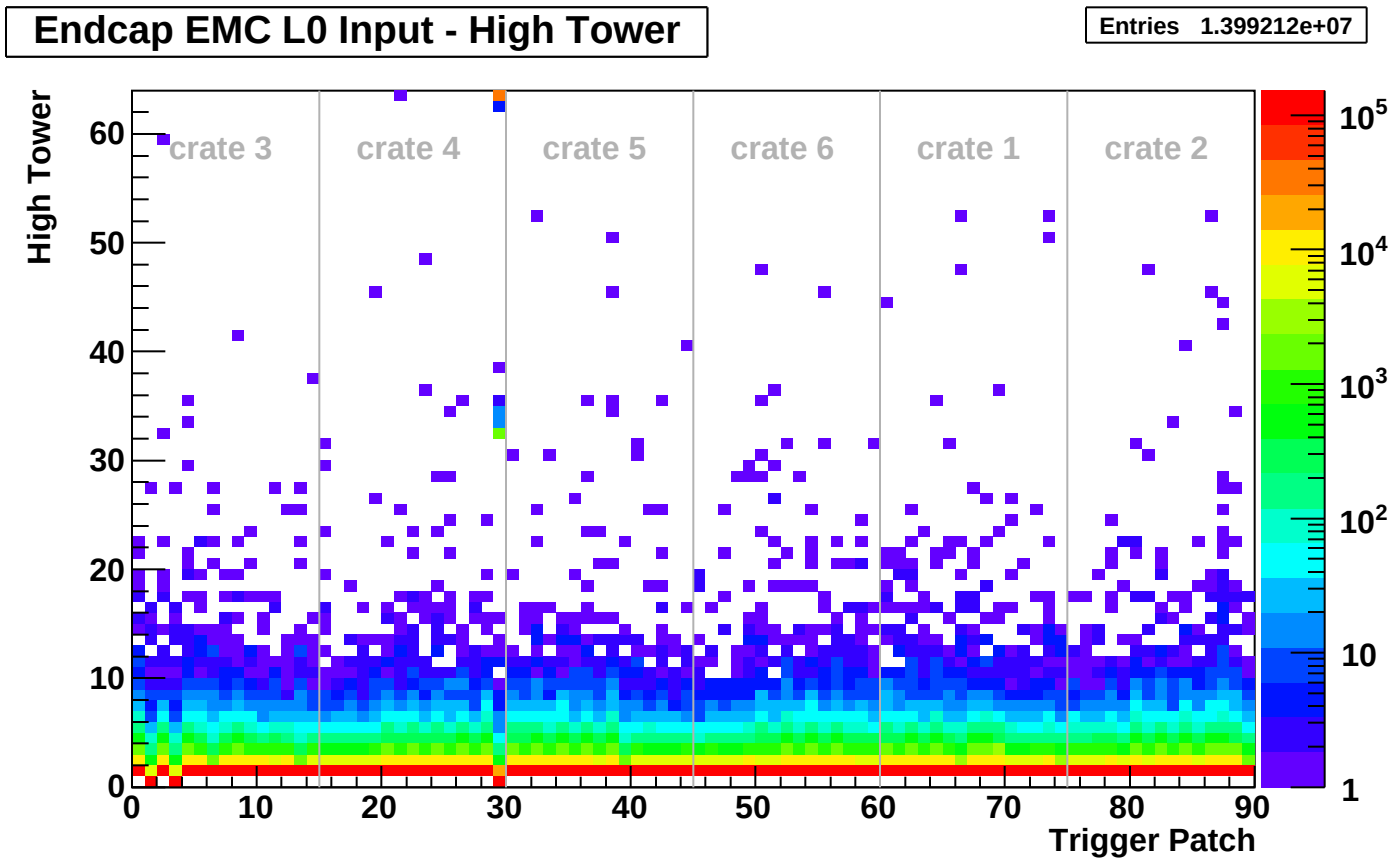

TCU bits
Entries 2487488
TCU bits
155468 155468 155468 155468 155468 155468 155468 155468 155468 155468 155468 155468 155468 155468 155468 155468
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

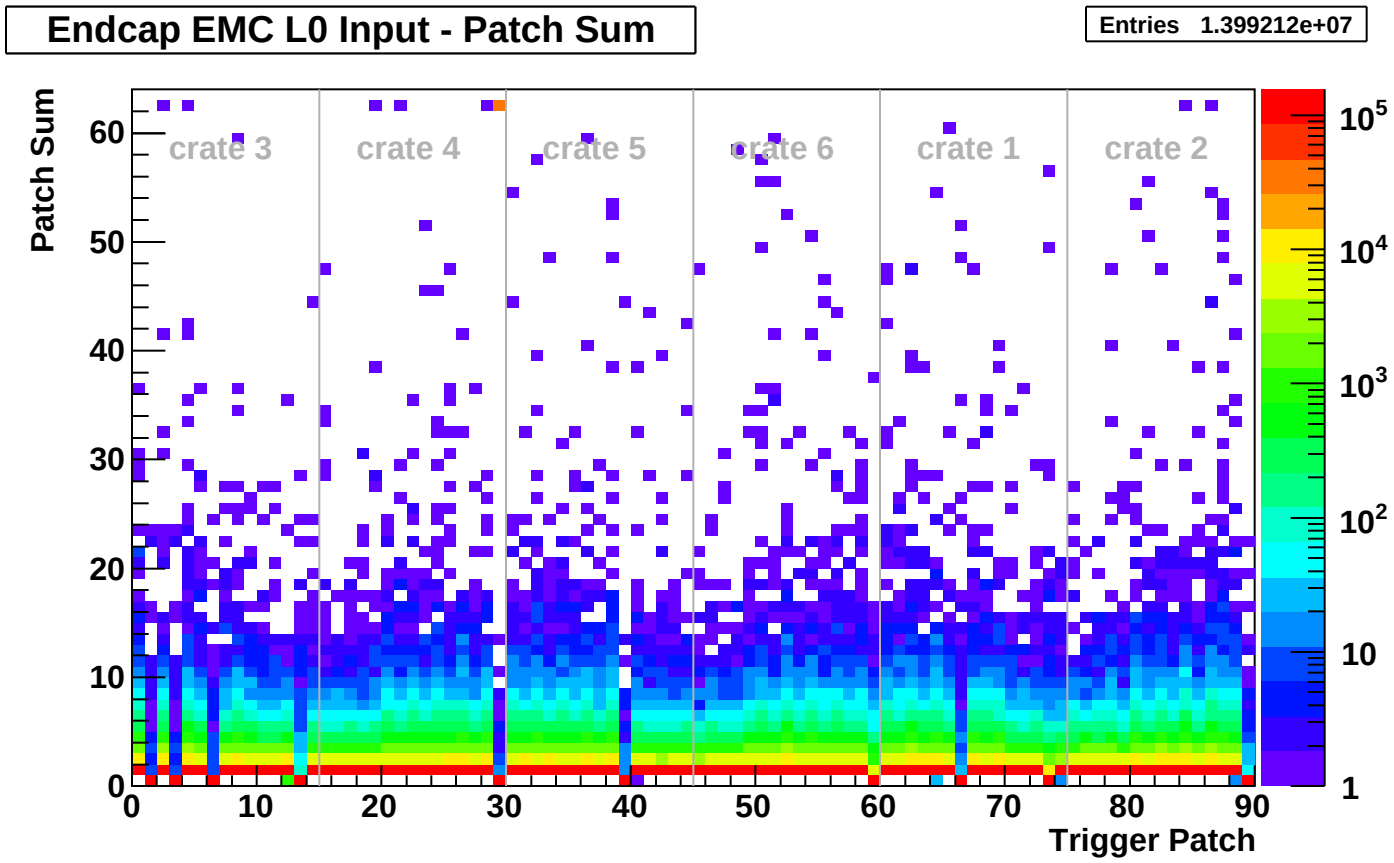

TCU bits
Entries 215505
TCU bits - Simulated
37806 23789 113411 35397 3995 1107
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

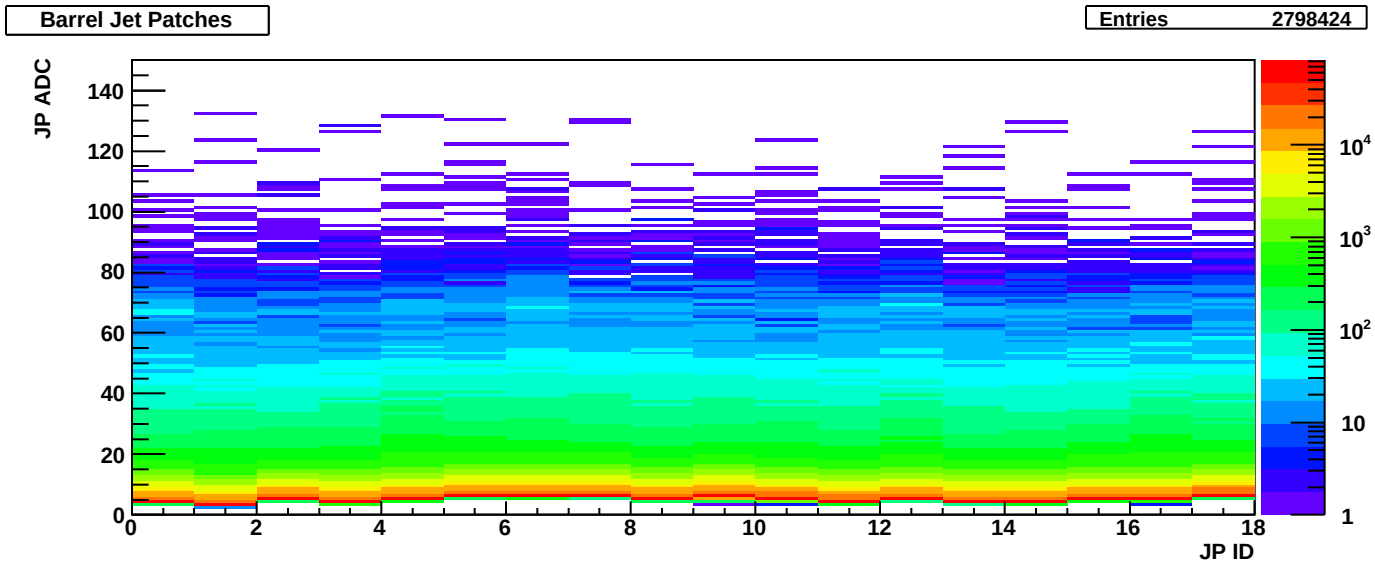

BunchId7Bit (BHT3)
Entries 4103

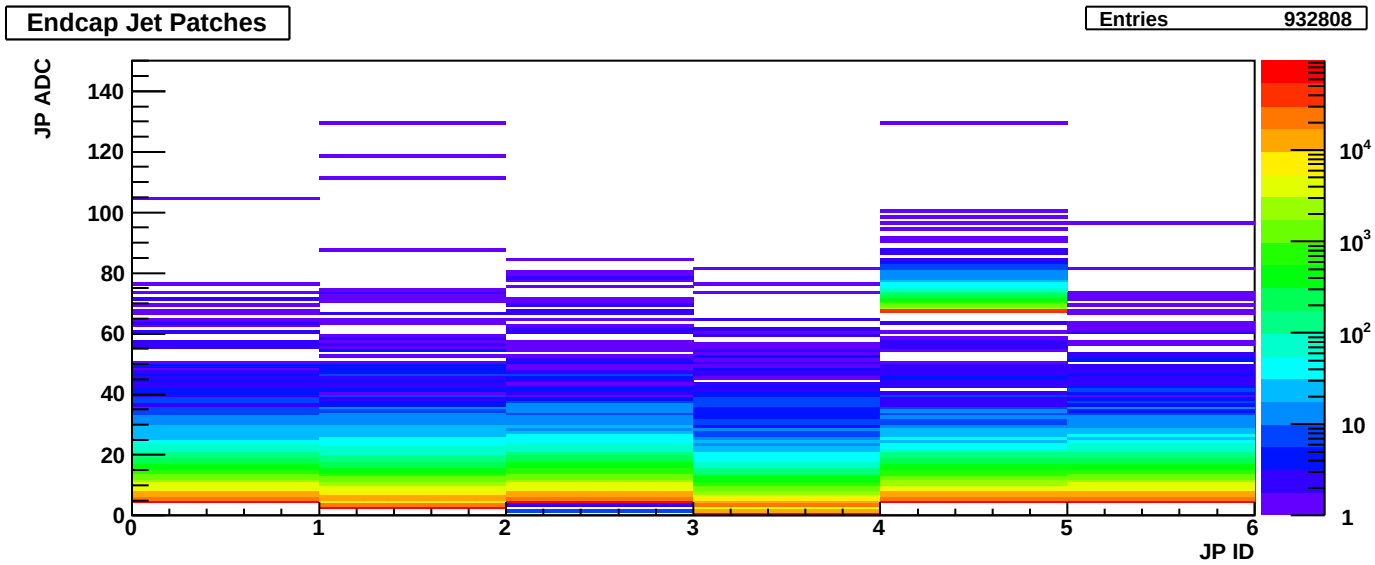

Barrel EMC L0 Input - High Tower
Entries 4.66404e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

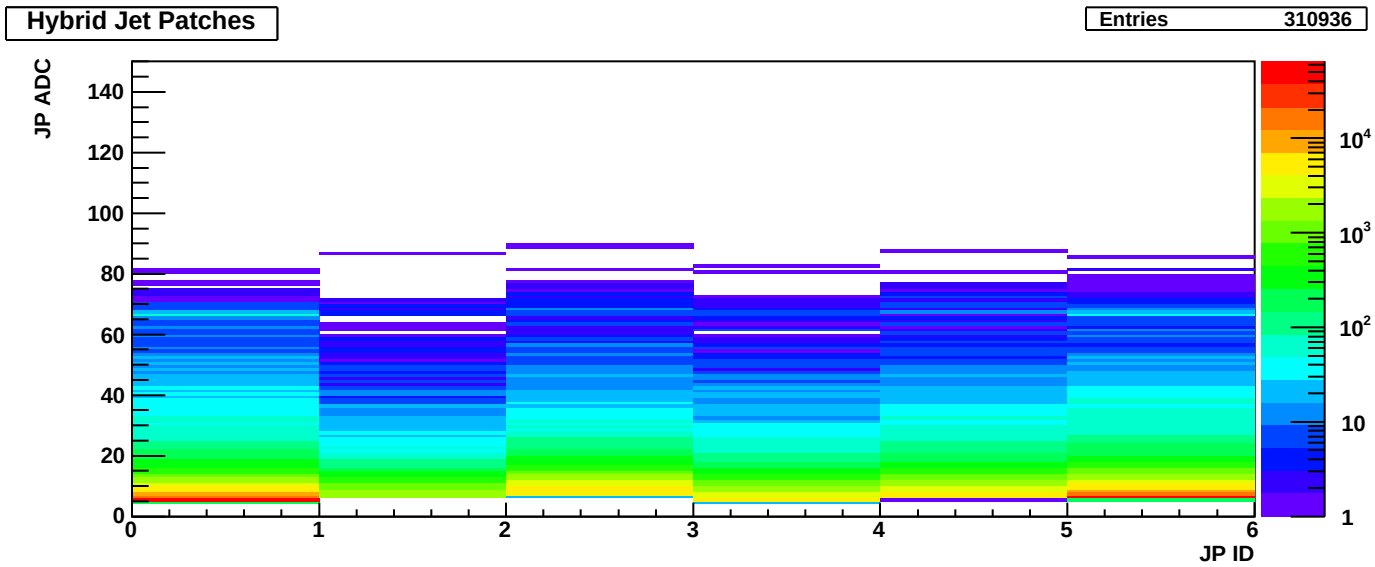

Barrel EMC L0 Input - Patch Sum
Entries 4.66404e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

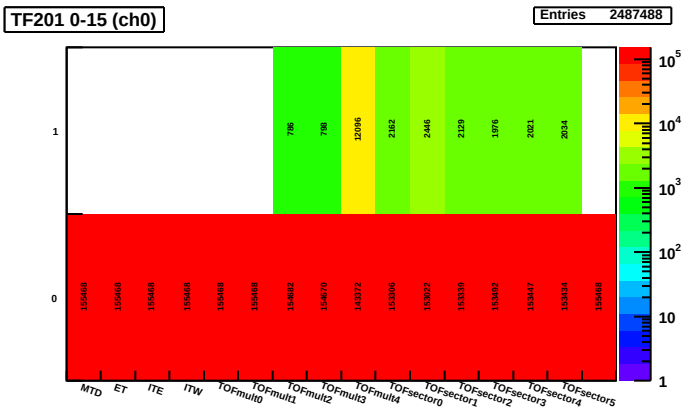

Endcap EMC L0 Input - High Tower
Entries 1.399212e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

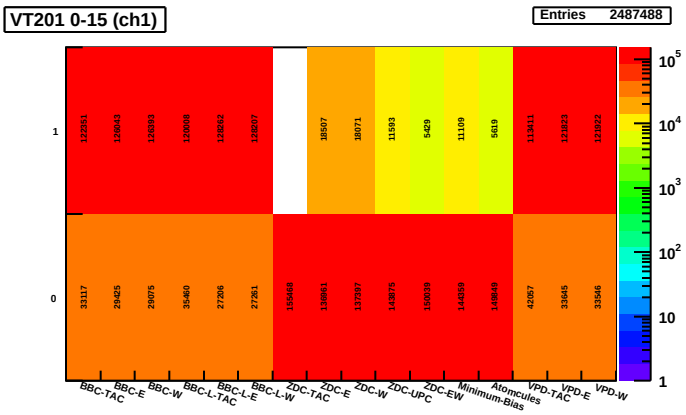

Endcap EMC L0 Input - Patch Sum
Entries 1.399212e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

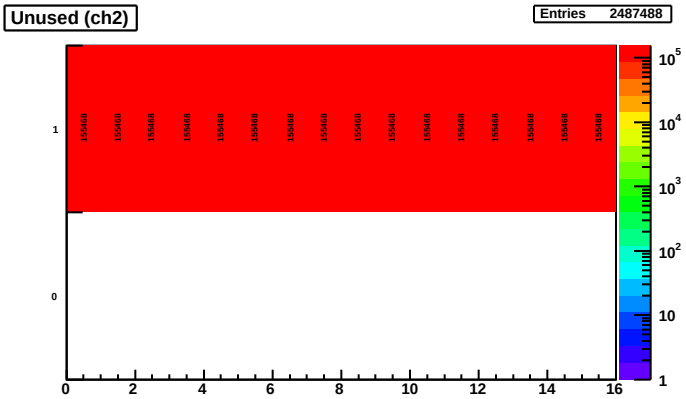

Barrel Jet Patches
Entries 2798424
JP ADC
JP ID

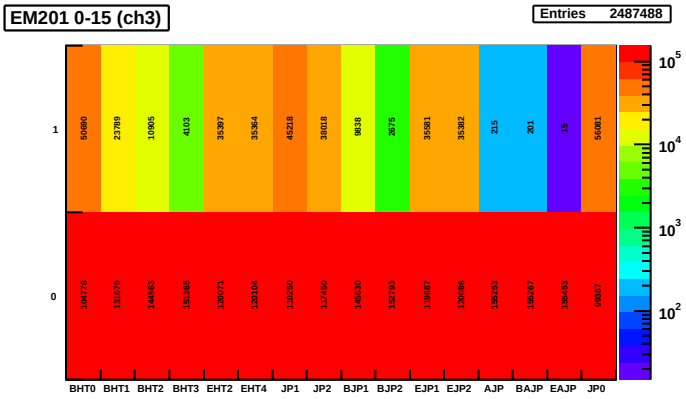

Endcap Jet Patches
Entries 932808
JP ADC
JP ID

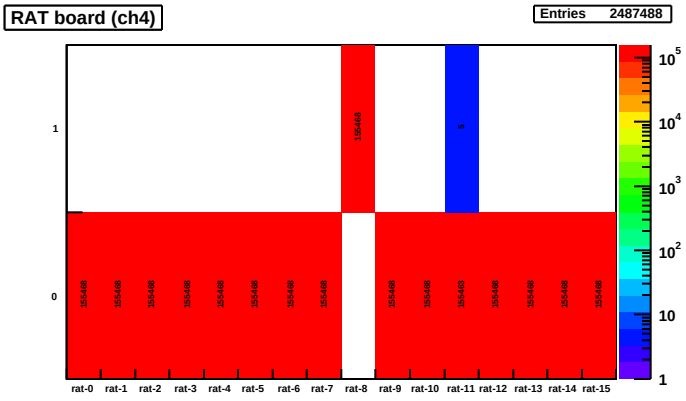

Hybrid Jet Patches
Entries 310936
JP ADC
JP ID

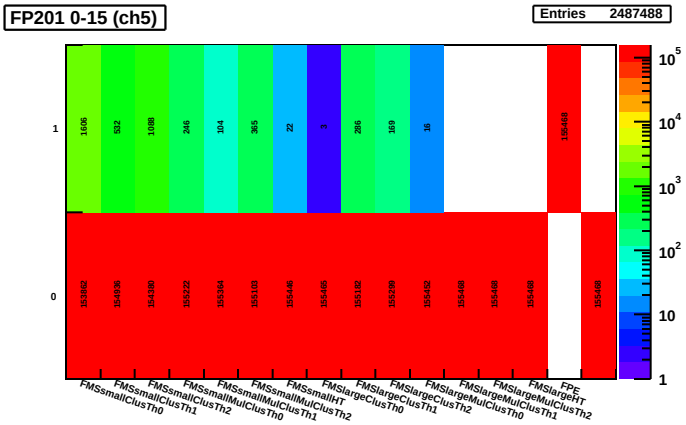

TF201 0-15 (ch0)
Entries 2487488
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

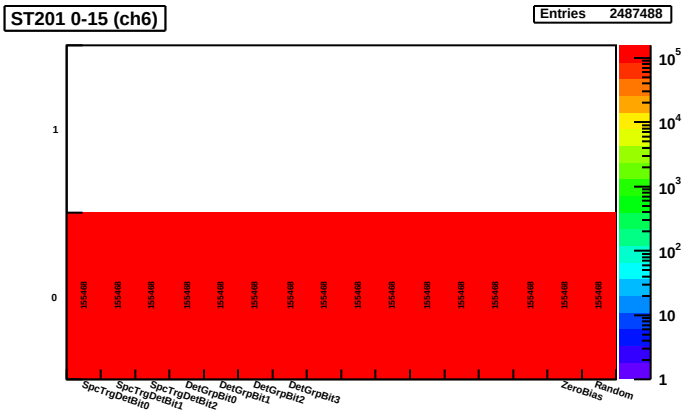

VT201 0-15 (ch1)
Entries 2487488
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

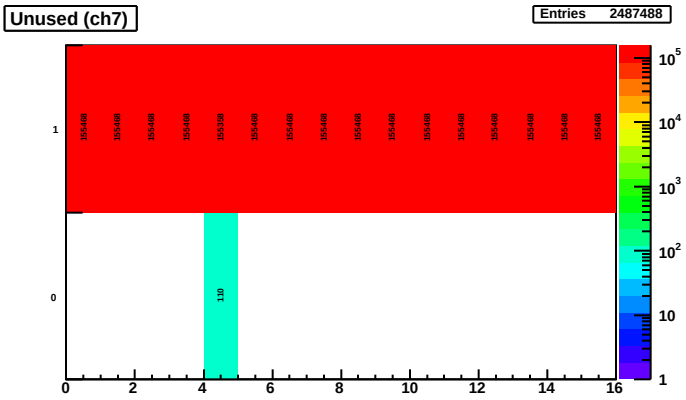

Unused (ch2)
Entries 2487488

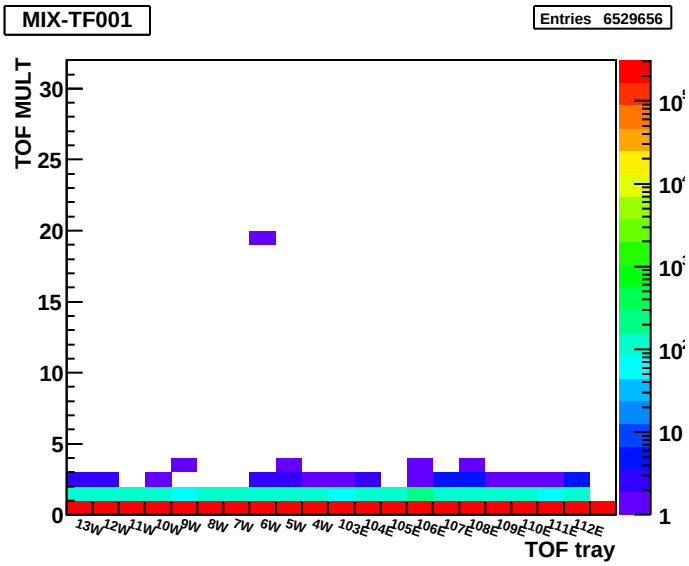

EM201 0-15 (ch3)
Entries 2487488
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

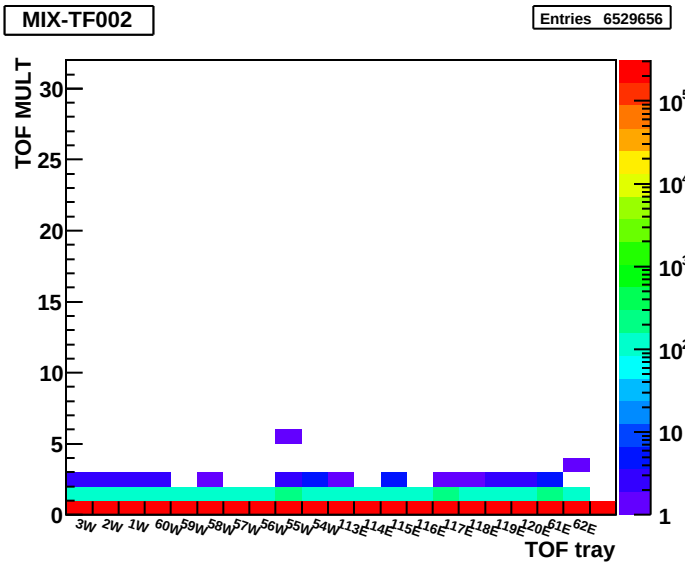

RAT board (ch4)
Entries 2487488
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

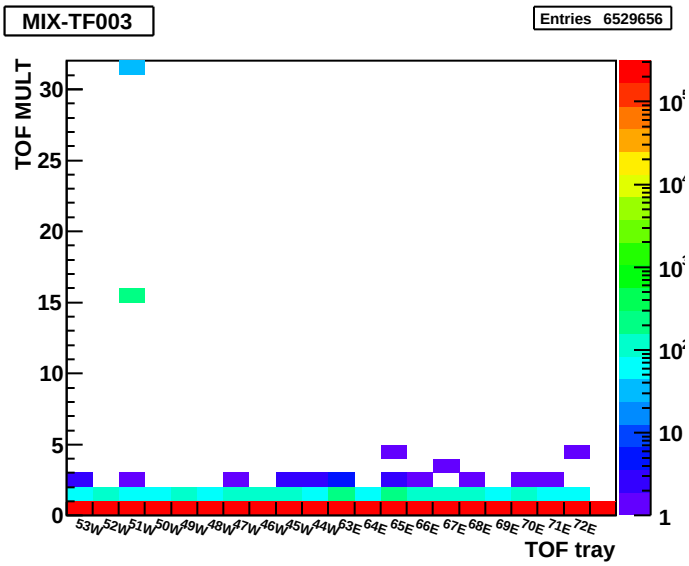

FP201 0-15 (ch5)
Entries 2487488
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

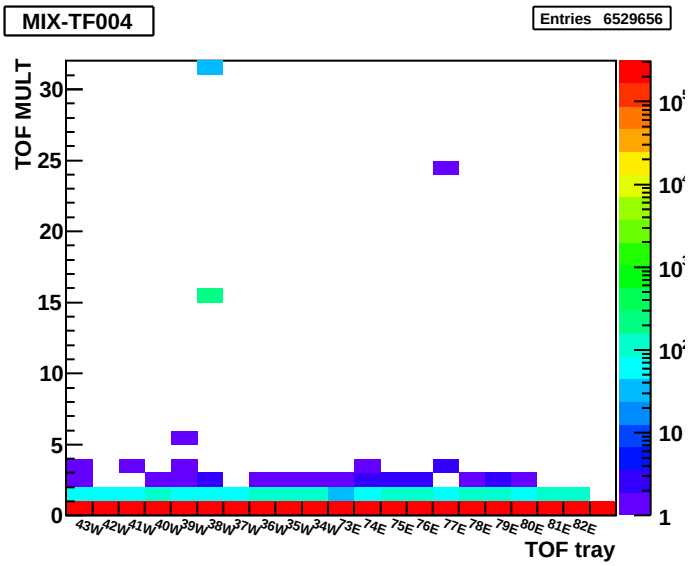

ST201 0-15 (ch6)
Entries 2487488
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

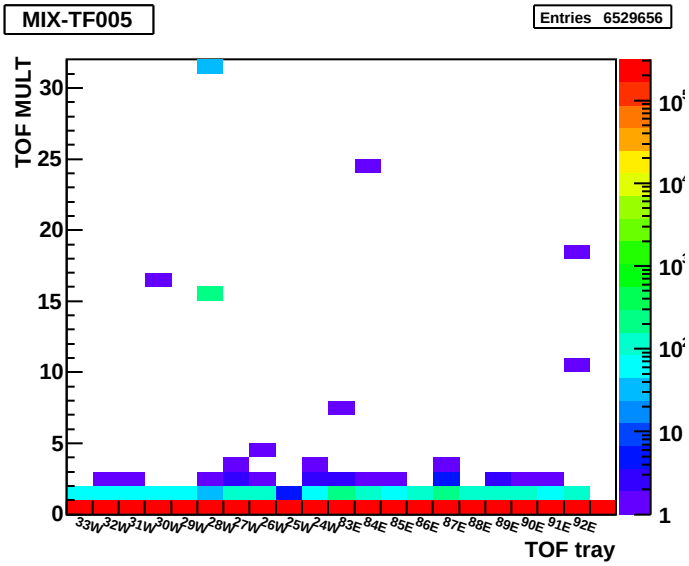

Unused (ch7)
Entries 2487488

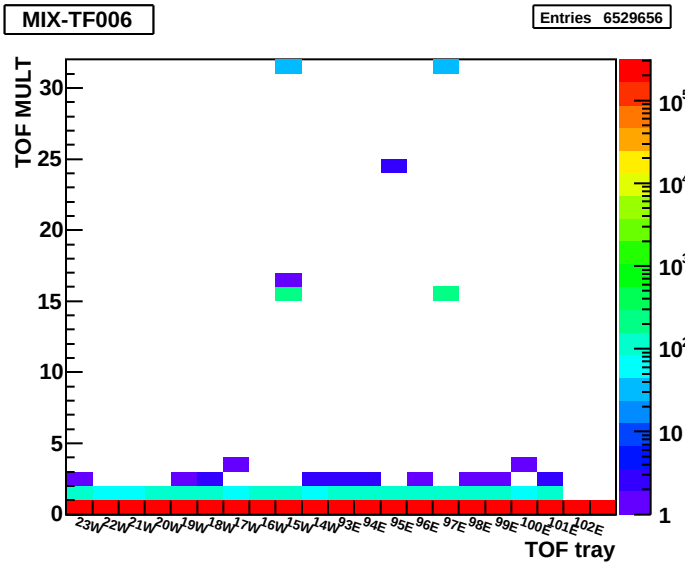

MIX-TF001
Entries 6529656
TOF MULT
TOF tray

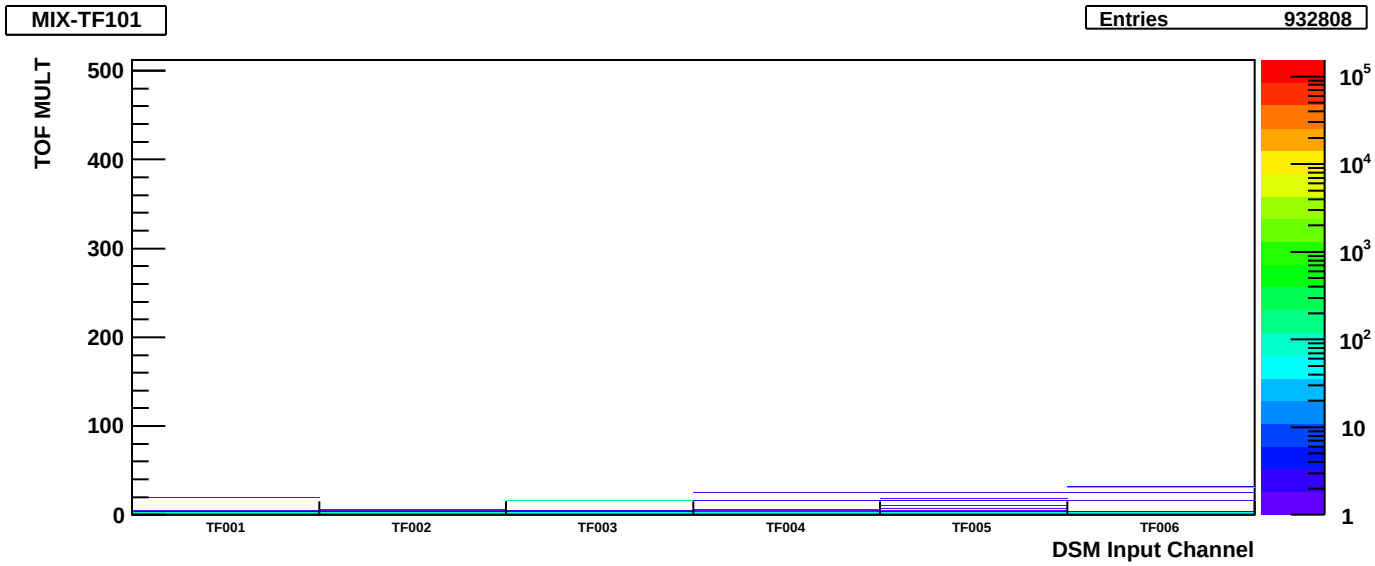

MIX-TF002
Entries 6529656
TOF MULT
TOF tray

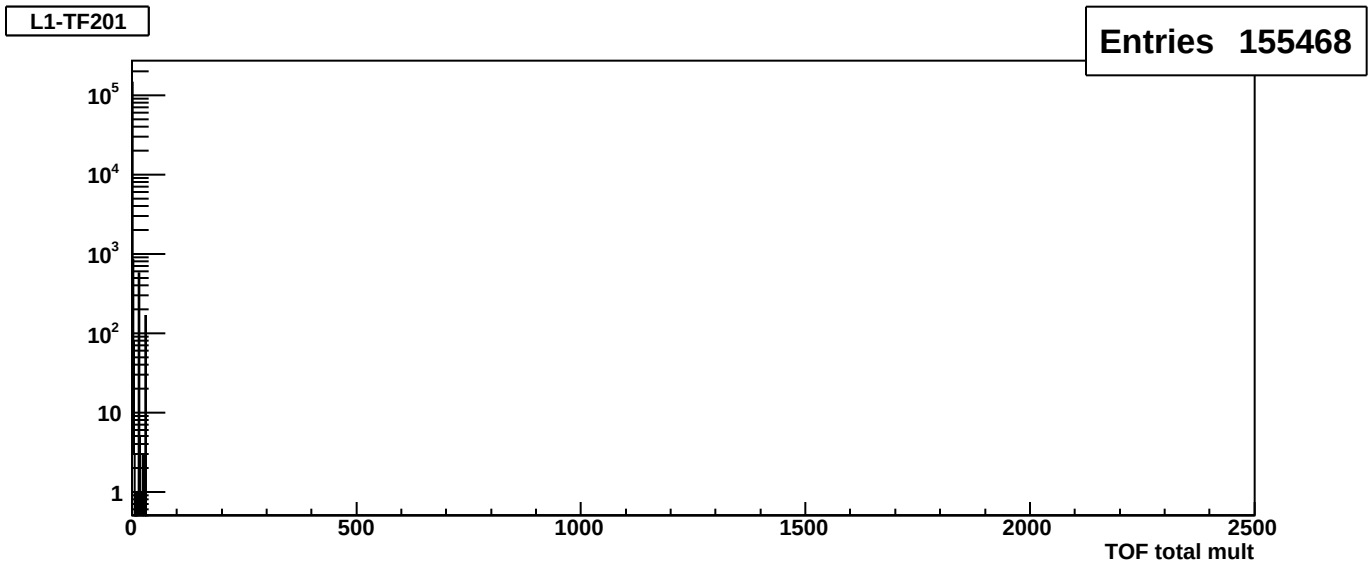

MIX-TF003
Entries 6529656
TOF MULT
TOF tray

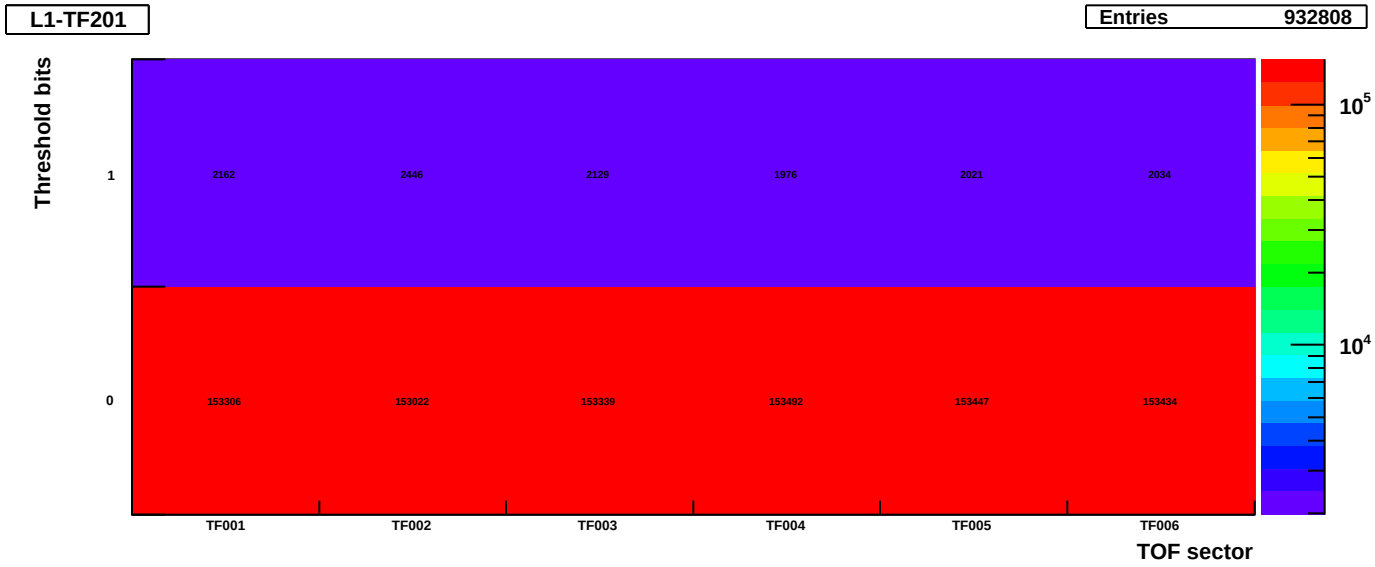

MIX-TF004
Entries 6529656
TOF MULT
TOF tray

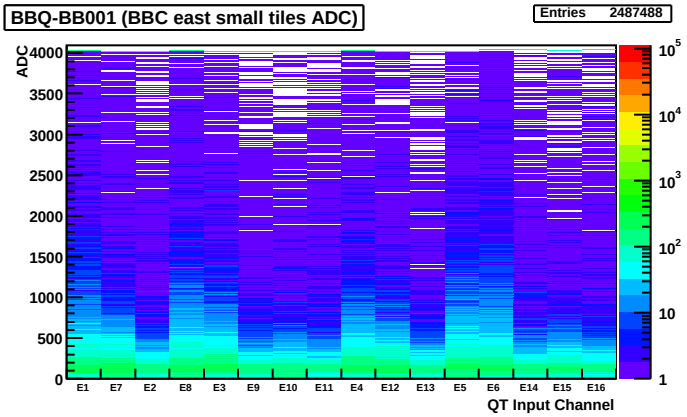

MIX-TF005
Entries 6529656
TOF MULT
TOF tray

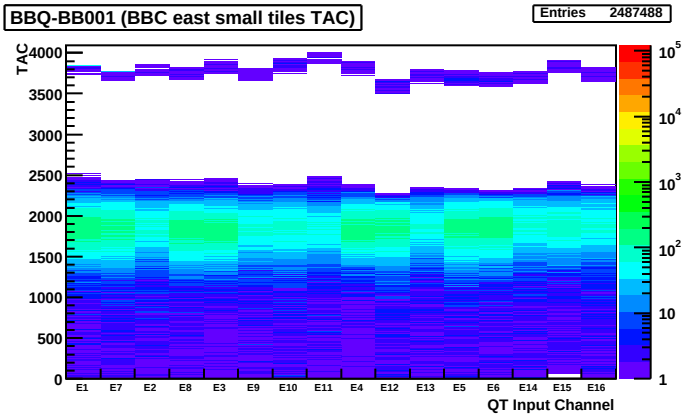

MIX-TF006
Entries 6529656
TOF MULT
TOF tray

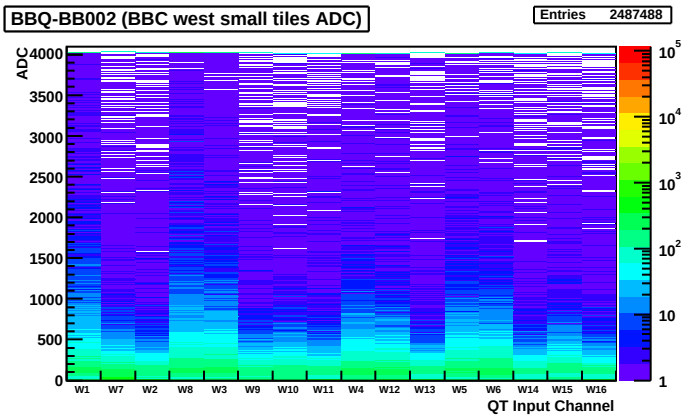

MIX-TF101
Entries 932808
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

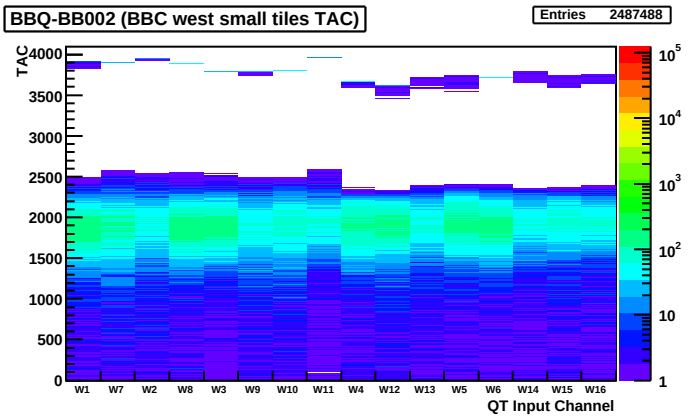

L1-TF201
Entries 155468
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

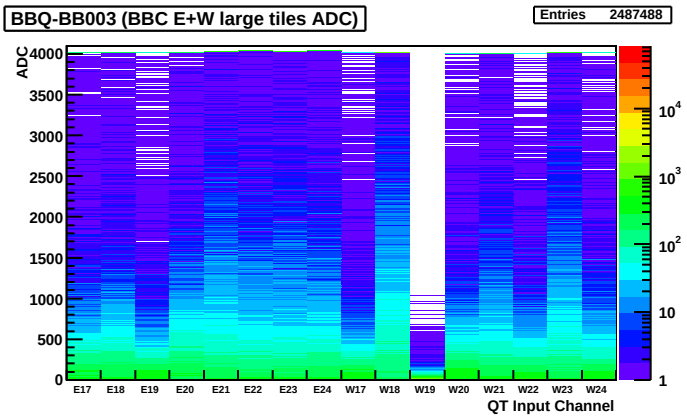

L1-TF201
Entries 932808
Threshold bits
1
0
2162
2446
2129
1976
2021
2034
153306
153022
153339
153492
153447
153434
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

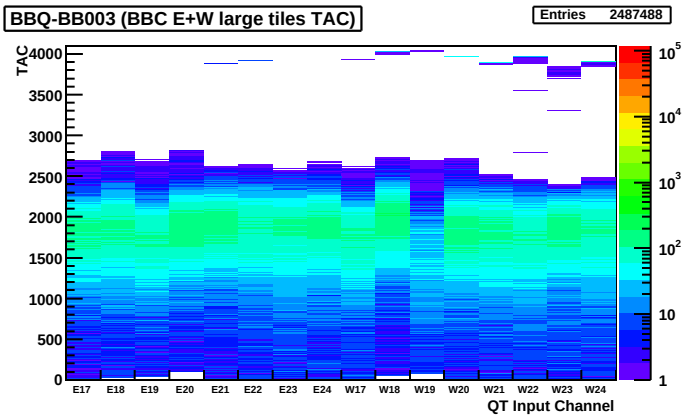

BBQ-BB001 (BBC east small tiles ADC)
Entries 2487488
ADC
QT Input Channel

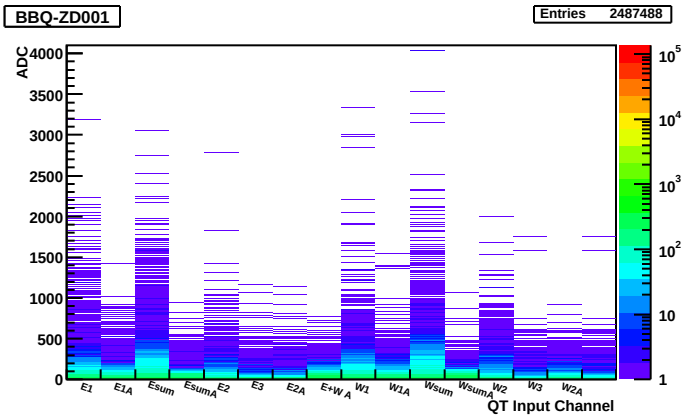

BBQ-BB001 (BBC east small tiles TAC)
Entries 2487488
TAC
QT Input Channel

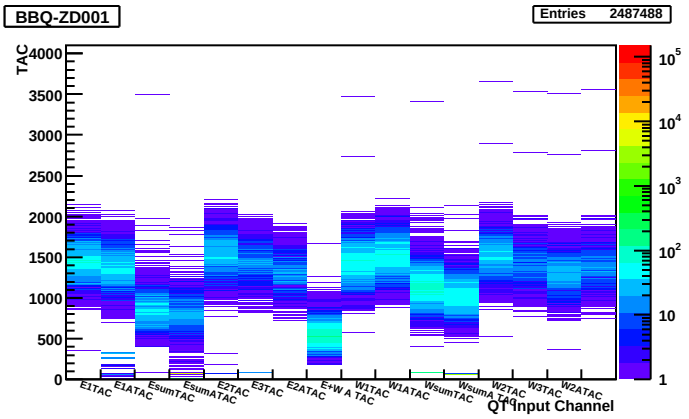

BBQ-BB002 (BBC west small tiles ADC)
Entries 2487488
ADC
QT Input Channel

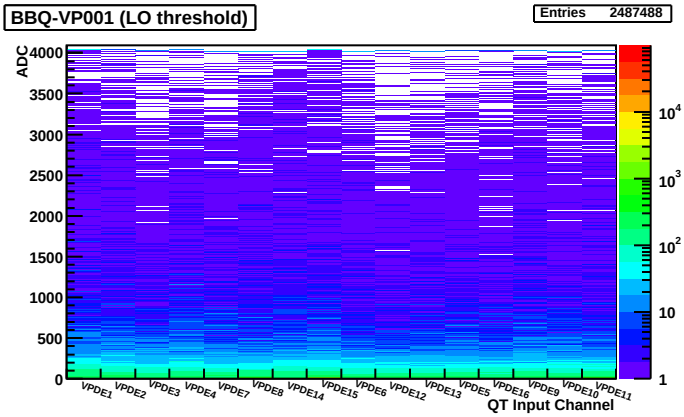

BBQ-BB002 (BBC west small tiles TAC)
Entries 2487488
TAC
QT Input Channel

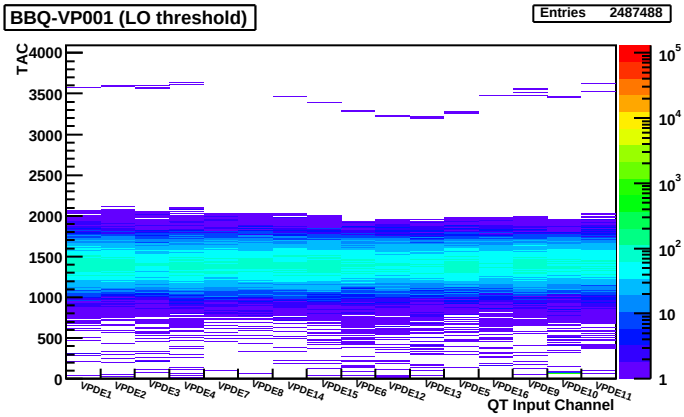

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2487488
ADC
QT Input Channel

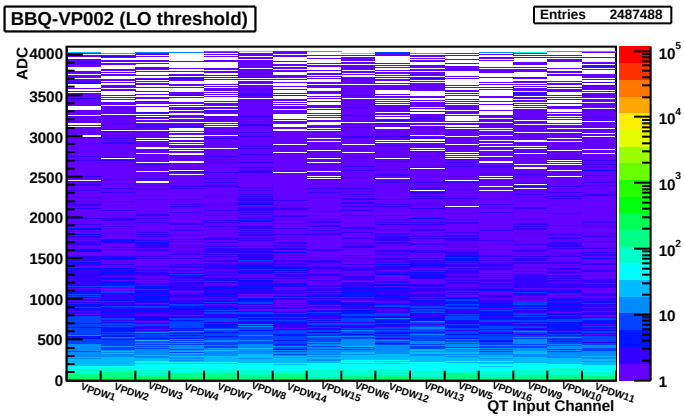

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2487488
TAC
QT Input Channel

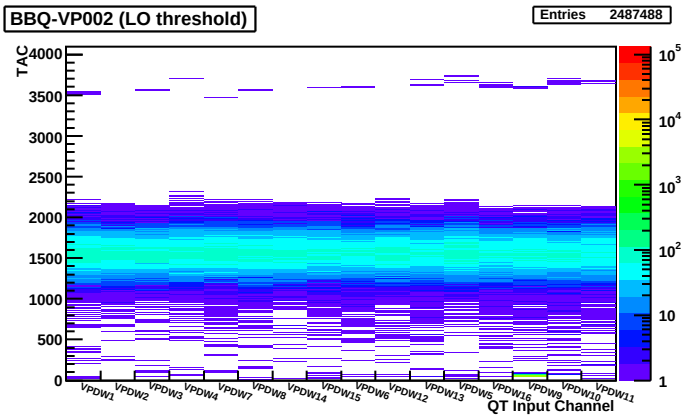

BBQ-ZD001
Entries 2487488
ADC
QT Input Channel

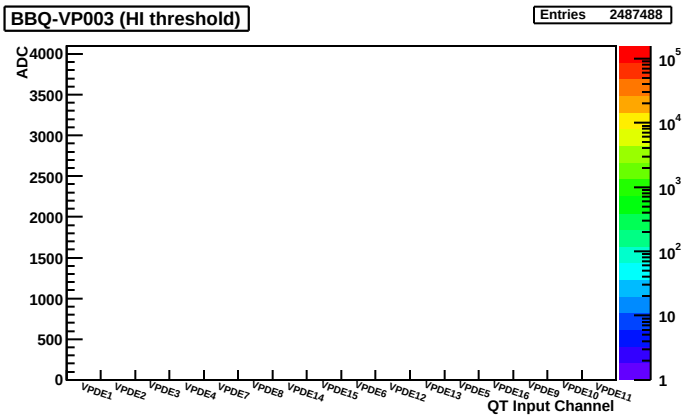

BBQ-ZD001
Entries 2487488
TAC
QT Input Channel

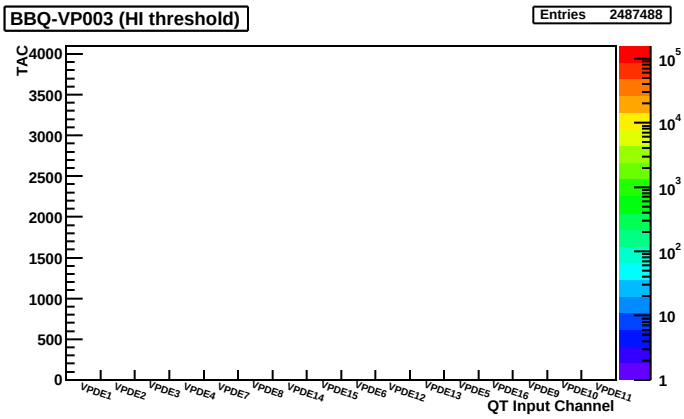

BBQ-VP001 (LO threshold)
Entries 2487488
ADC
QT Input Channel

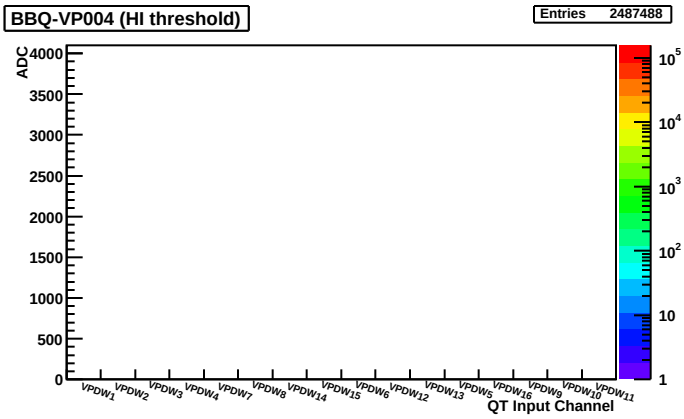

BBQ-VP001 (LO threshold)
Entries 2487488
TAC
QT Input Channel

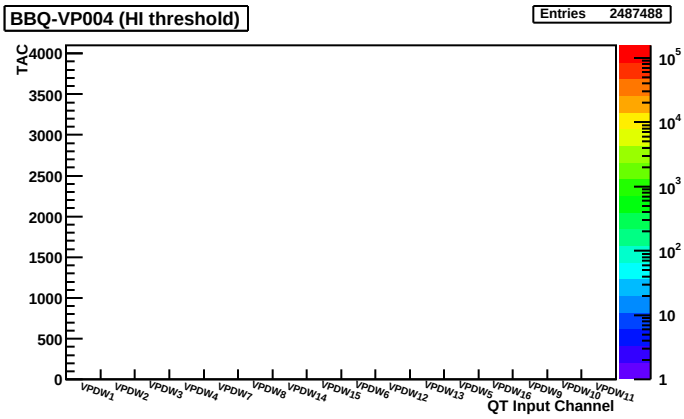

BBQ-VP002 (LO threshold)
Entries 2487488
ADC
QT Input Channel

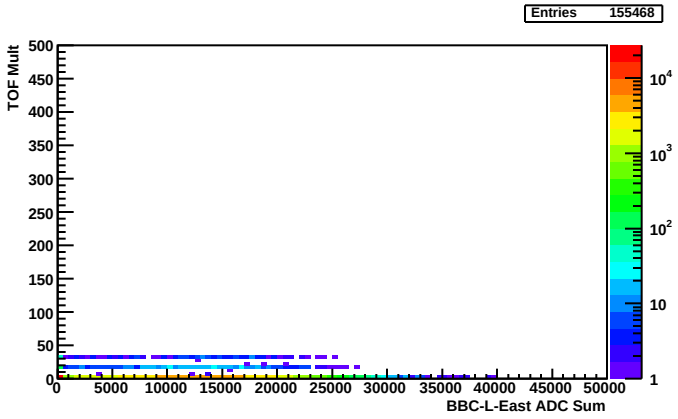

BBQ-VP002 (LO threshold)
Entries 2487488
TAC
QT Input Channel

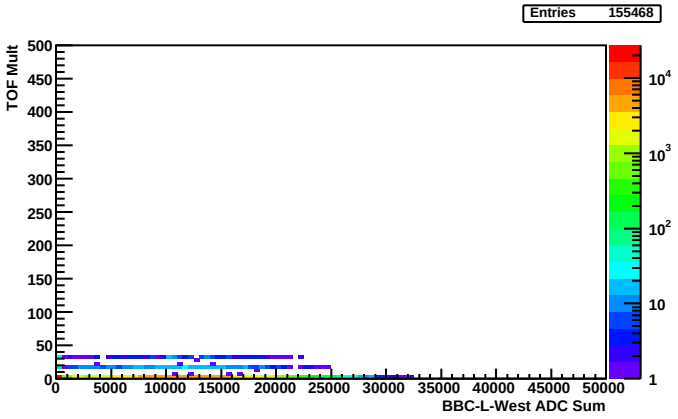

BBQ-VP003 (HI threshold)
Entries 2487488
ADC
QT Input Channel

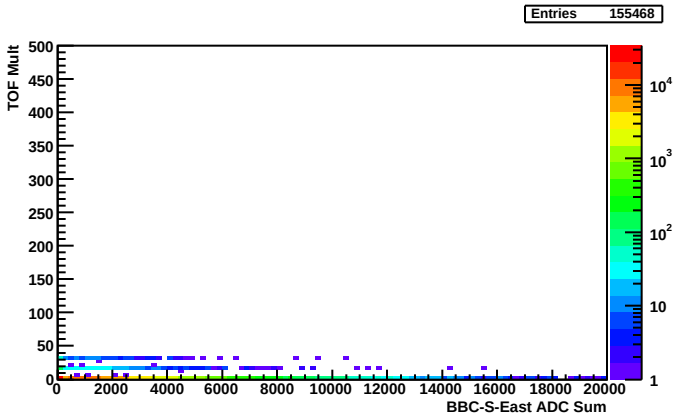

BBQ-VP003 (HI threshold)
Entries 2487488
TAC
QT Input Channel

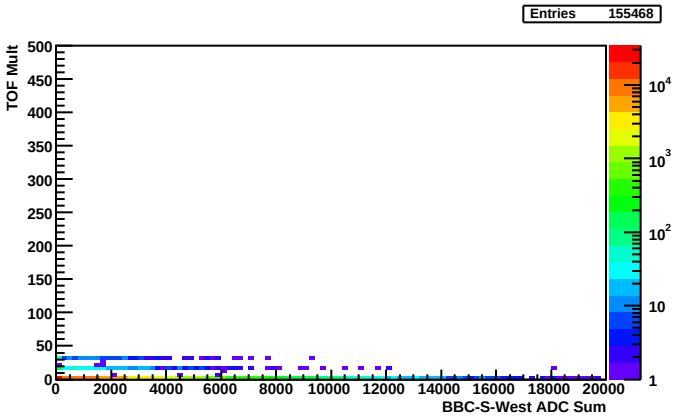

BBQ-VP004 (HI threshold)
Entries 2487488
ADC
QT Input Channel

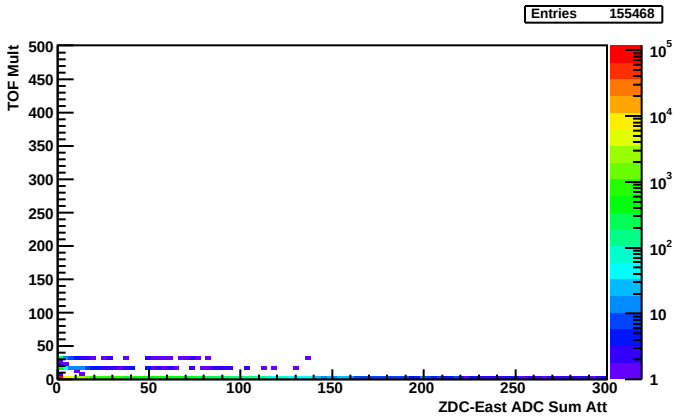

BBQ-VP004 (HI threshold)
Entries 2487488
TAC
QT Input Channel

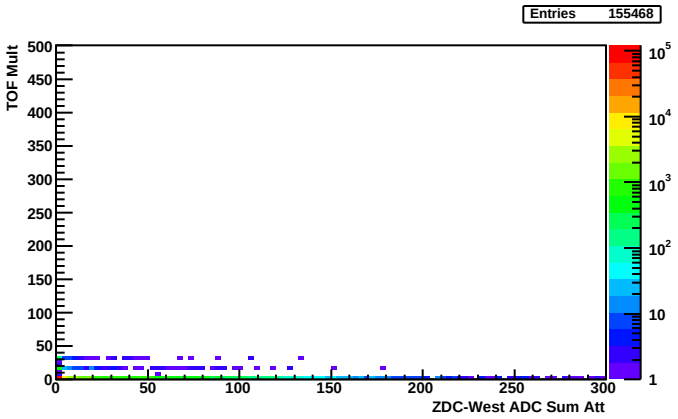

Entries 155468
TOF Mult
BBC-L-East ADC Sum

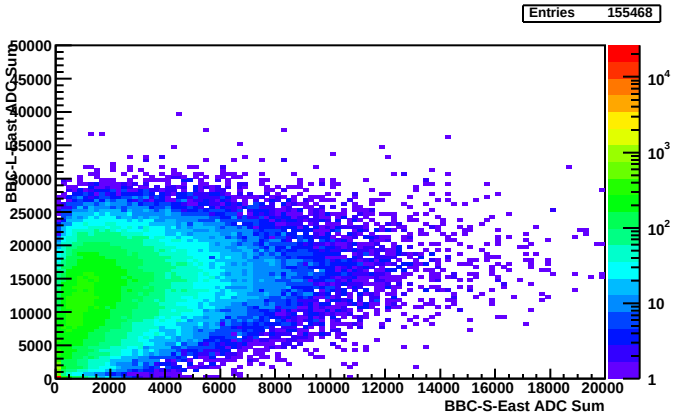

Entries 155468
TOF Mult
BBC-L-West ADC Sum

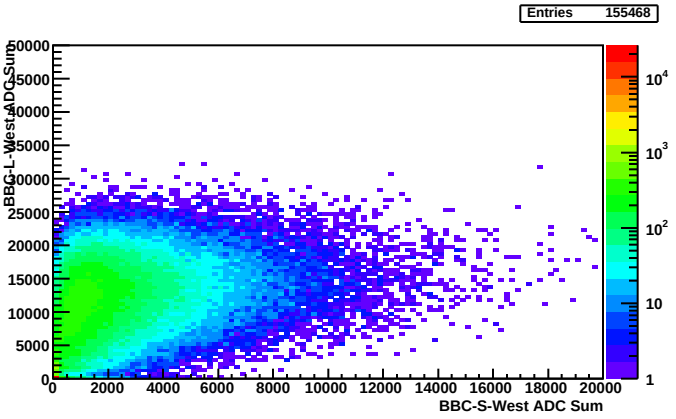

Entries 155468
TOF Mult
BBC-S-East ADC Sum

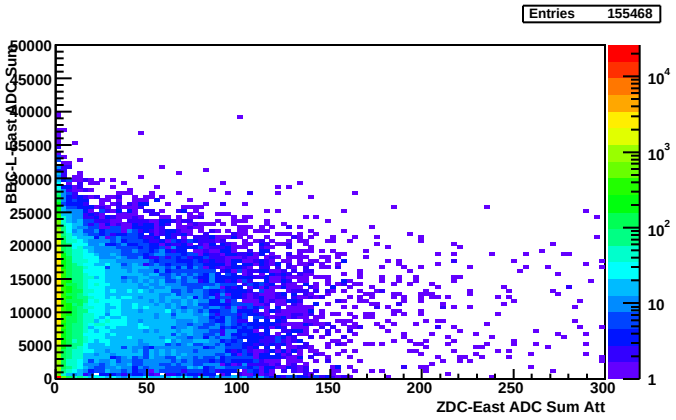

Entries 155468
TOF Mult
BBC-S-West ADC Sum

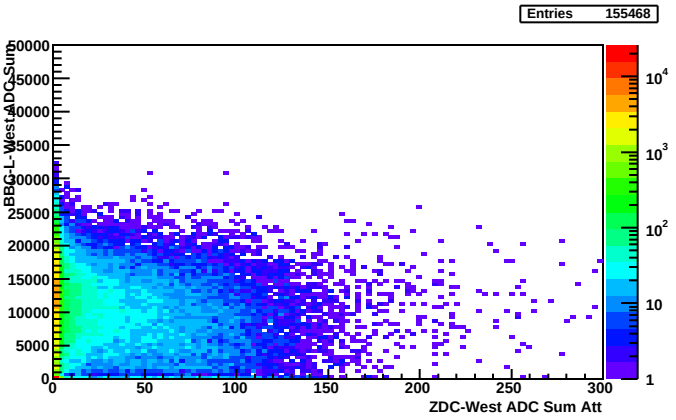

Entries 155468
TOF Mult
ZDC-East ADC Sum Att

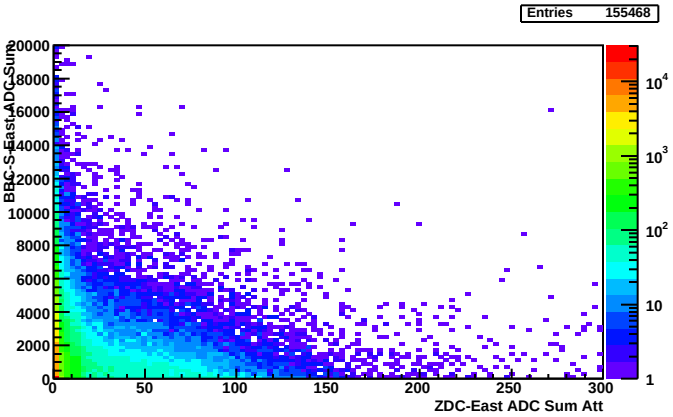

Entries 155468
TOF Mult
ZDC-West ADC Sum Att

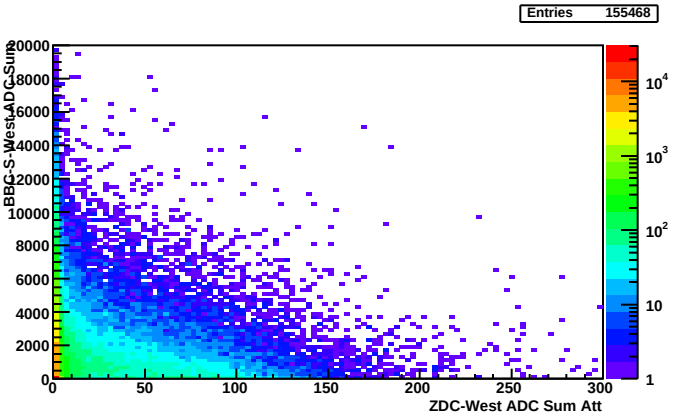

Entries 155468
BBC-L-East ADC Sum
BBC-S-East ADC Sum

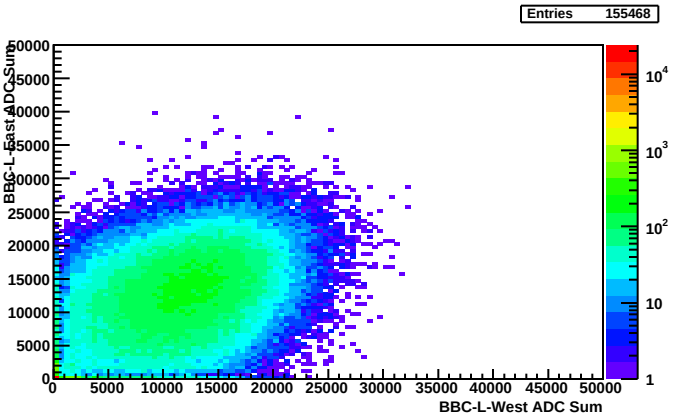

Entries 155468
BBC-L-West ADC Sum
BBC-S-West ADC Sum

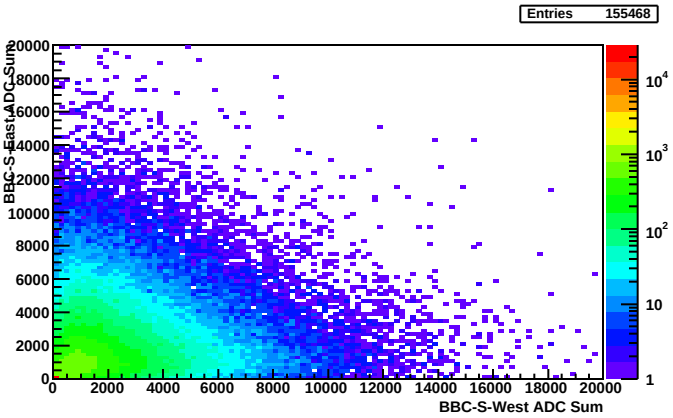

Entries 155468
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

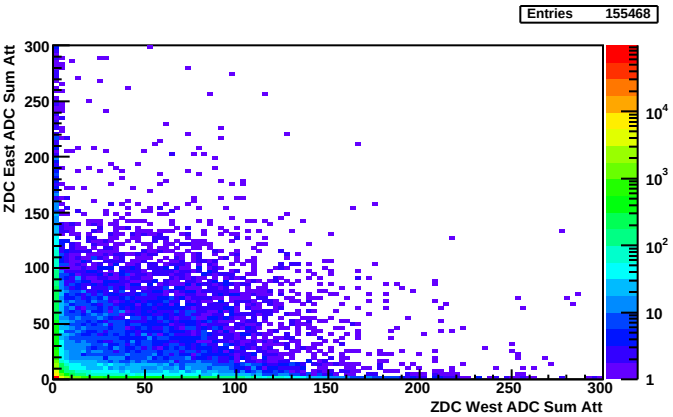

Entries 155468
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

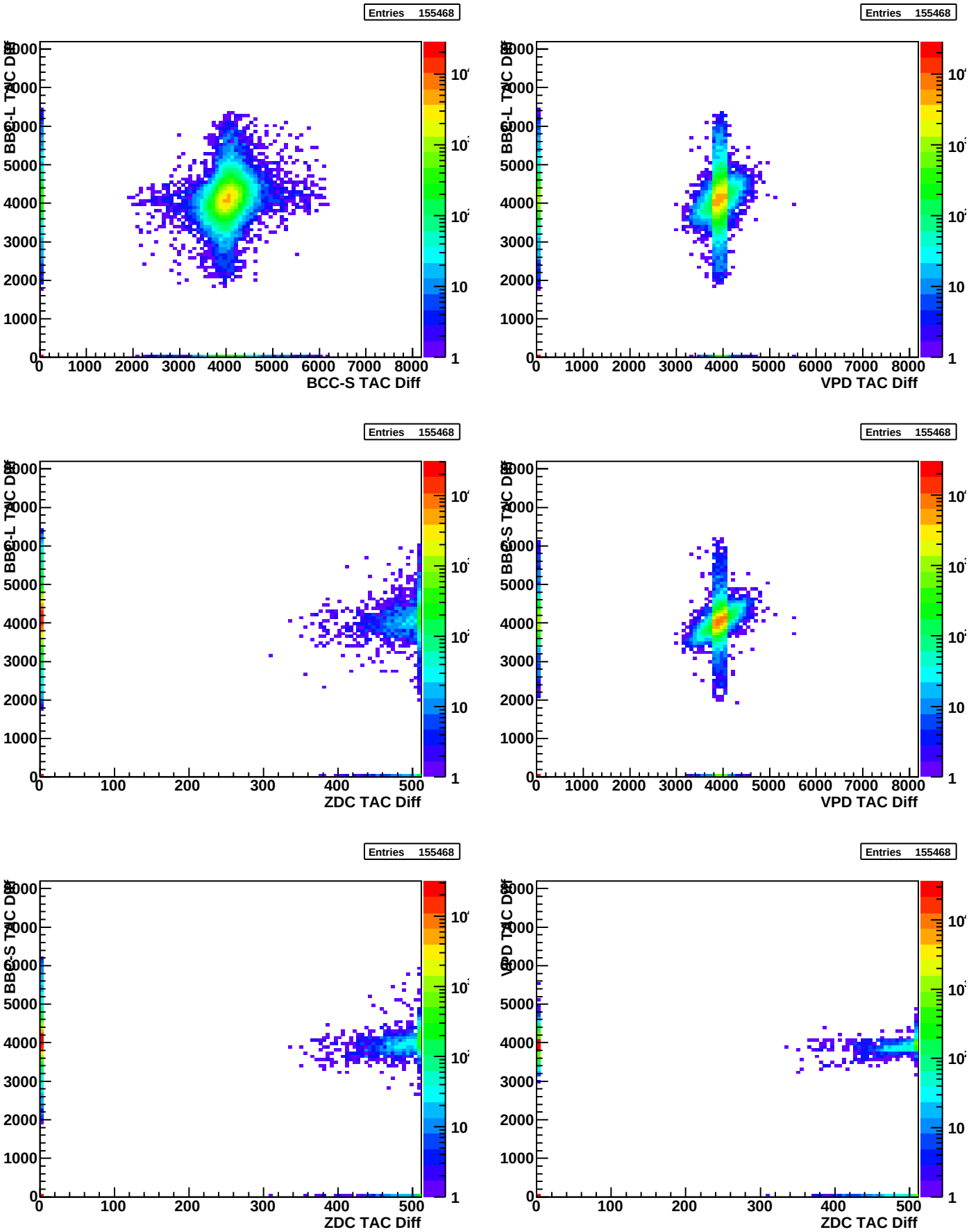

Entries 155468
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

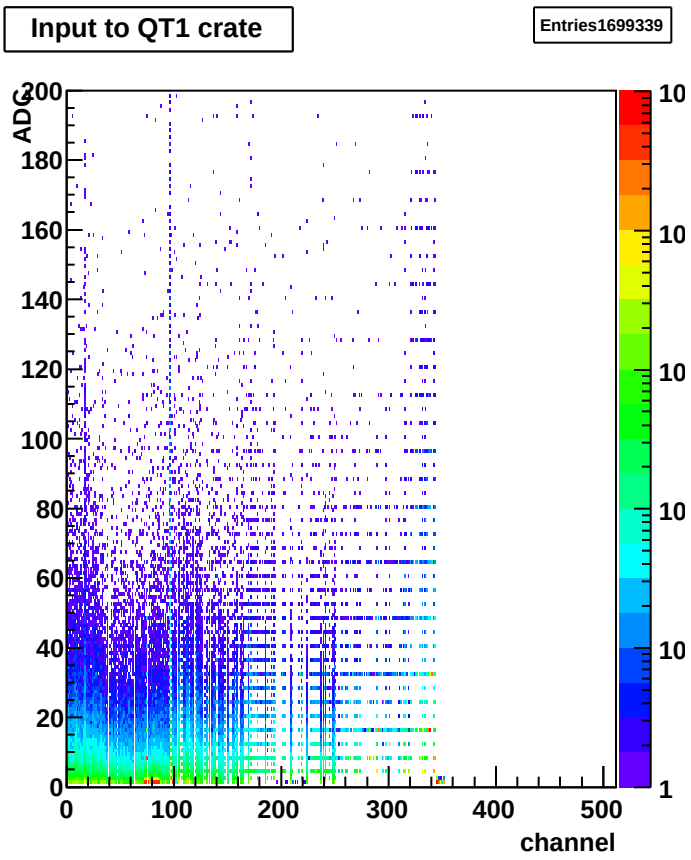

Entries 155468
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

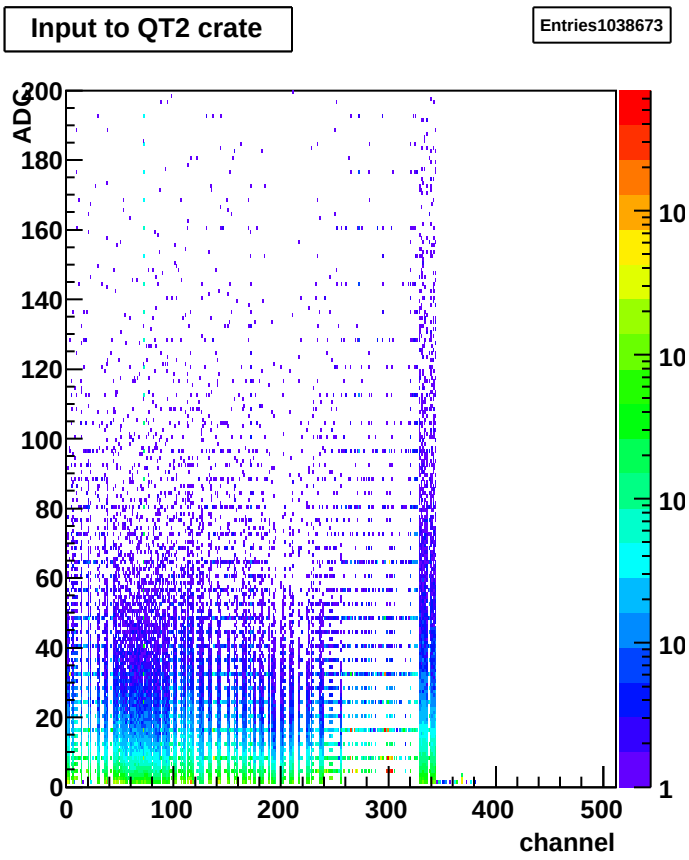

Entries 155468
BBC-L-East ADC Sum
BBC-L-West ADC Sum

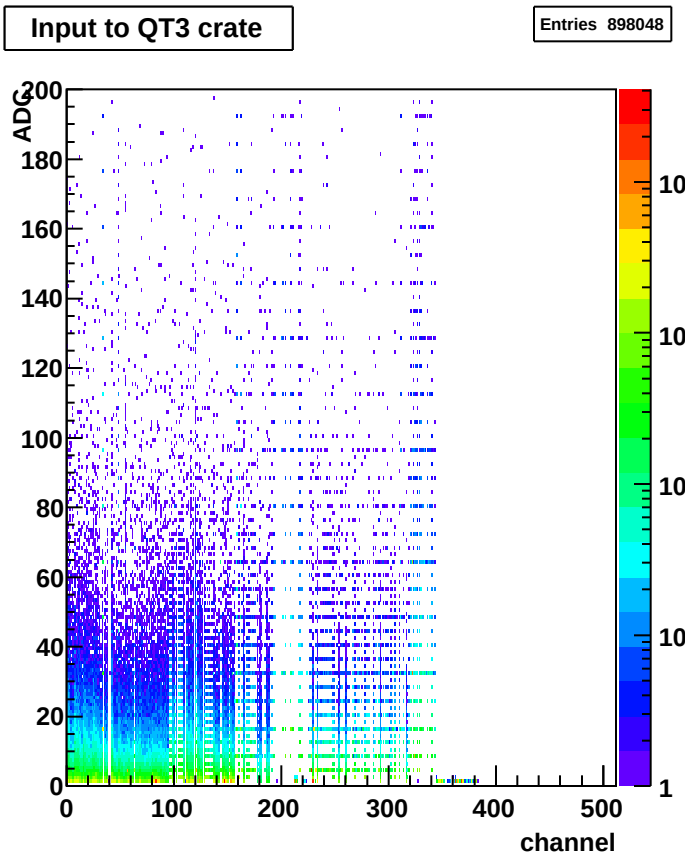

Entries 155468
BBC-S-East ADC Sum
BBC-S-West ADC Sum

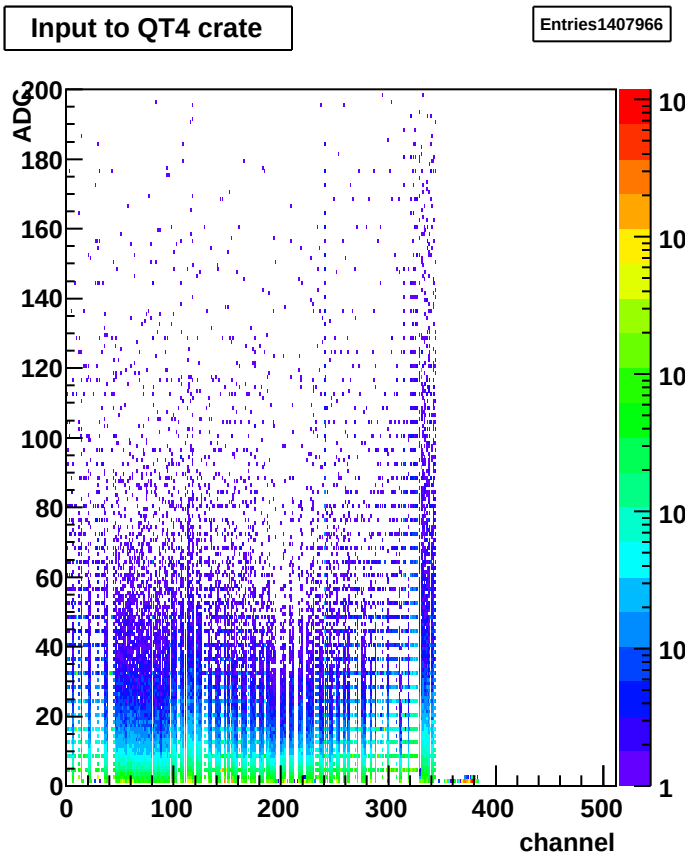

Entries 155468
ZDC East ADC Sum Att
ZDC West ADC Sum Att

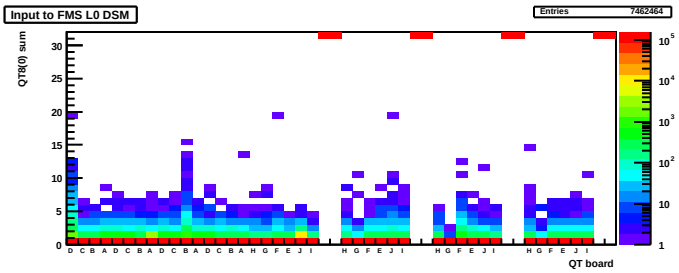

Entries 155468
BBC-L TAC Diff
BCC-S TAC Diff
Entries 155468
BBC-L TAC Diff
VPD TAC Diff
Entries 155468
BBC-L TAC Diff
ZDC TAC Diff
Entries 155468
BBC-S TAC Diff
VPD TAC Diff
Entries 155468
BBC-S TAC Diff
ZDC TAC Diff
Entries 155468
VPD TAC Diff
ZDC TAC Diff

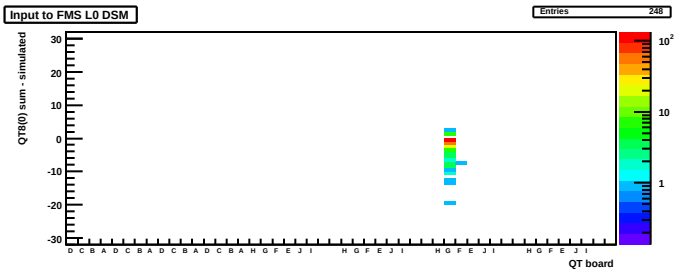

Input to QT1 crate
Entries1699339
ADC
channel

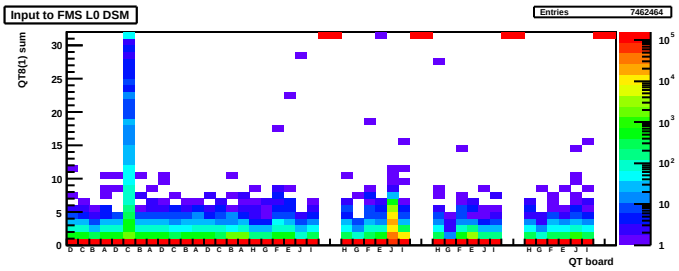

Input to QT2 crate
Entries1038673
ADC
channel

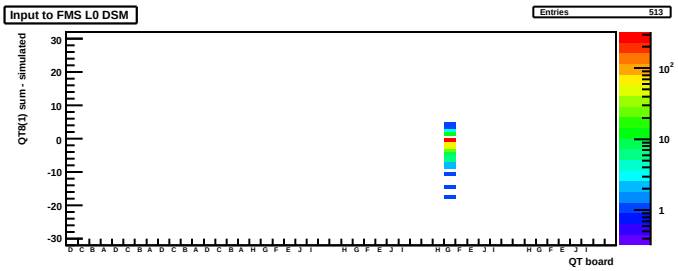

Input to QT3 crate
Entries 898048
ADC
channel

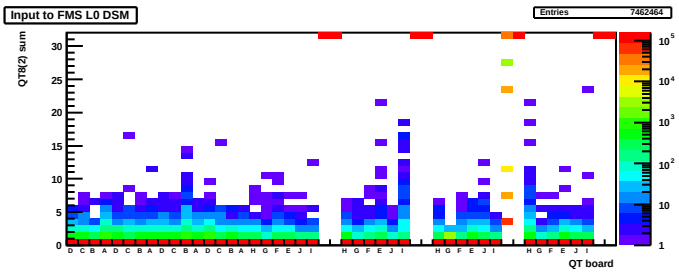

Input to QT4 crate
Entries1407966
ADC
channel

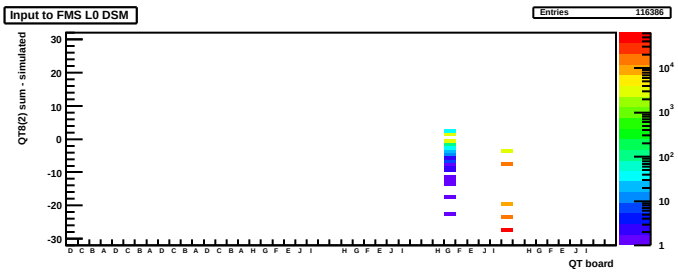

Input to FMS L0 DSM
Entries 7462464
QT8(0) sum
QT board

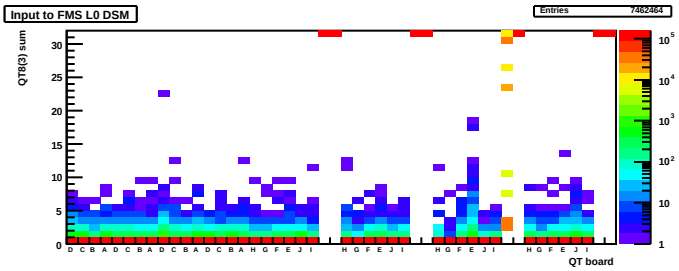

Input to FMS L0 DSM
Entries 248
QT8(0) sum - simulated
QT board

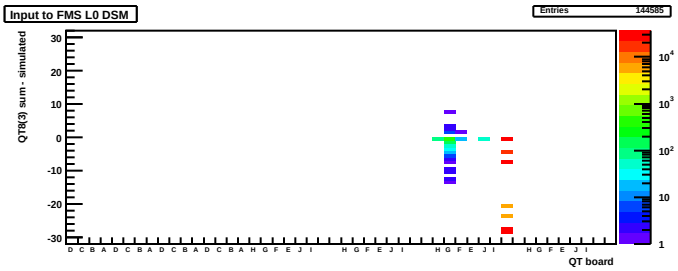

Input to FMS L0 DSM
Entries 7462464
QT8(1) sum
QT board

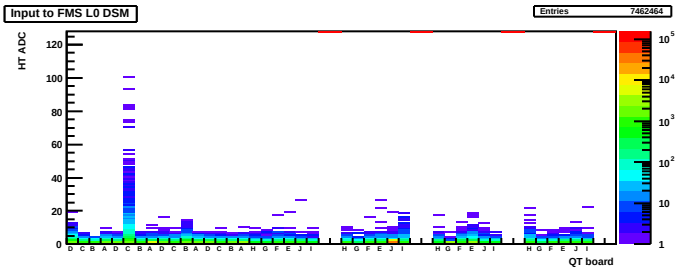

Input to FMS L0 DSM
Entries 513
QT8(1) sum - simulated
QT board

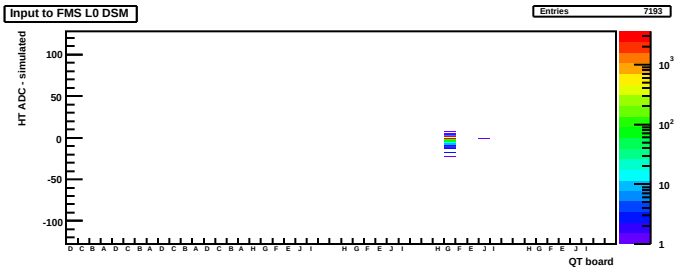

Input to FMS L0 DSM
Entries 7462464
QT8(2) sum
QT board

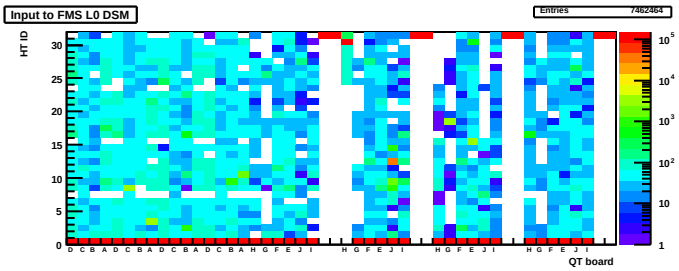

Input to FMS L0 DSM
Entries 116386
QT8(2) sum - simulated
QT board

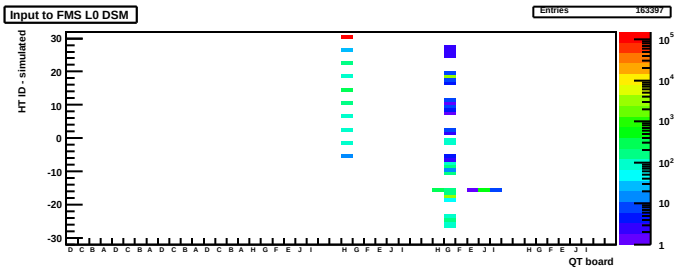

Input to FMS L0 DSM
Entries 7462464
QT8(3) sum
QT board

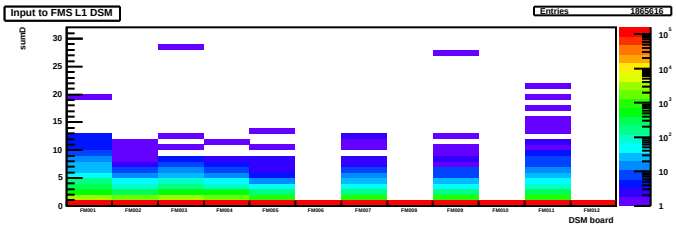

Input to FMS L0 DSM
Entries 144585
QT8(3) sum - simulated
QT board

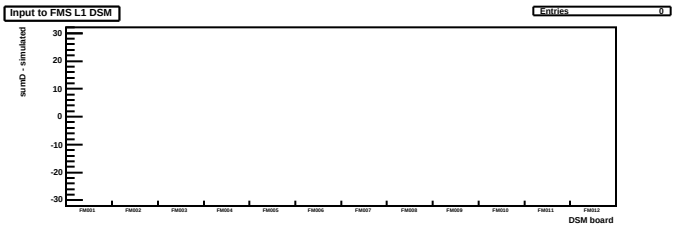

Input to FMS L0 DSM
Entries 7462464
HT ADC
QT board

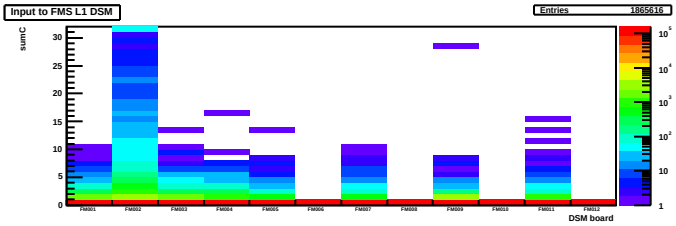

Input to FMS L0 DSM
Entries 7193
HT ADC - simulated
QT board

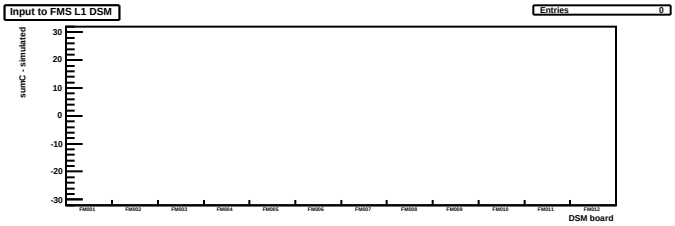

Input to FMS L0 DSM
Entries 7462464
HT ID
QT board

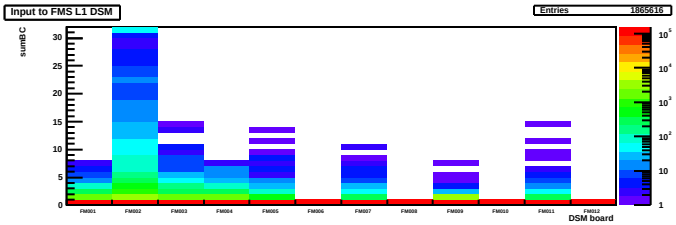

Input to FMS L0 DSM
Entries 163397
HT ID - simulated
QT board

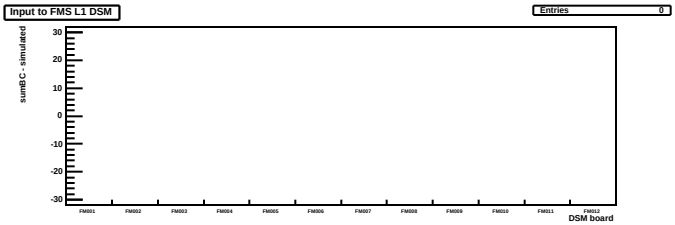

Input to FMS L1 DSM
Entries 1865616
sumD
DSM board

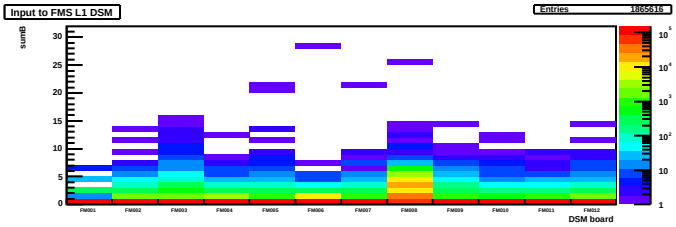

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

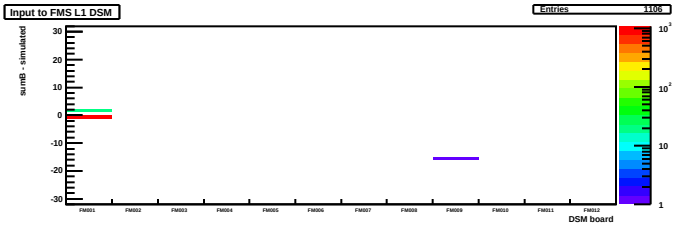

Input to FMS L1 DSM
Entries 1865616
sumC
DSM board

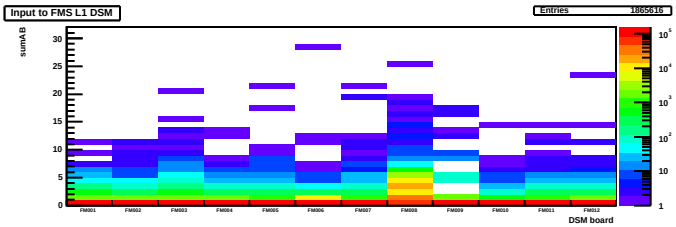

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

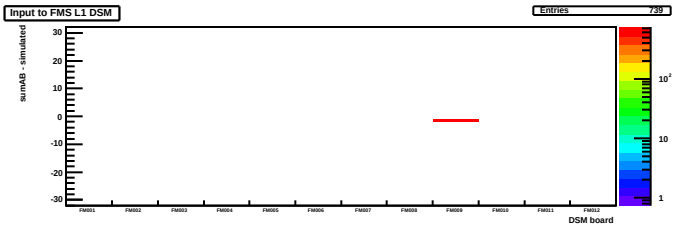

Input to FMS L1 DSM
Entries 1865616
sumBC
DSM board

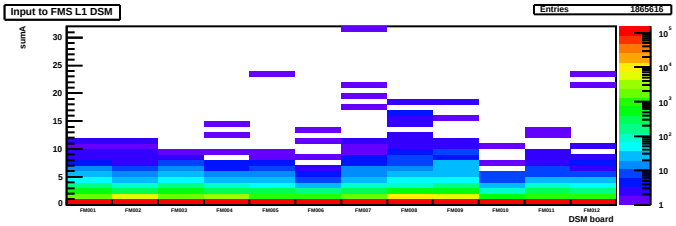

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

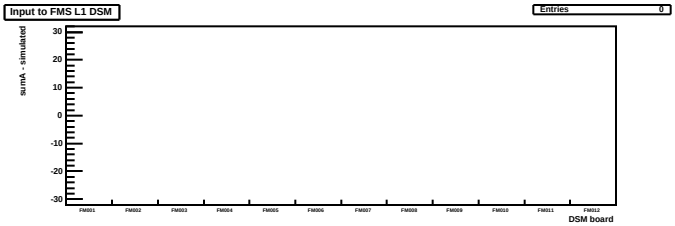

Input to FMS L1 DSM
Entries 1865616
sumB
DSM board

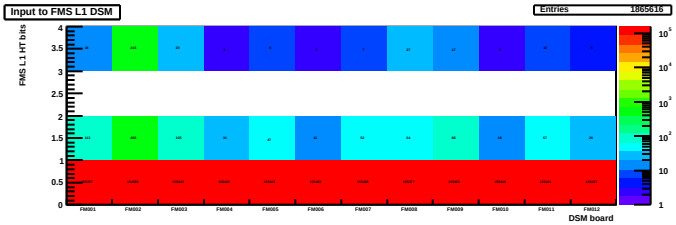

Input to FMS L1 DSM
Entries 1106
sumB - simulated
DSM board

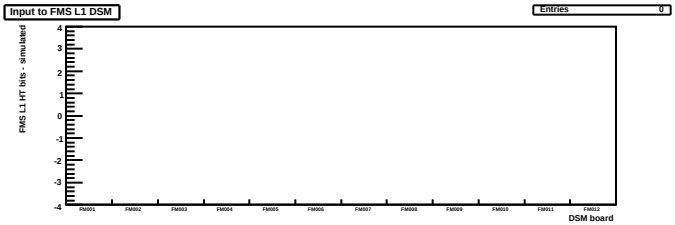

Input to FMS L1 DSM
Entries 1865616
sumAB
DSM board

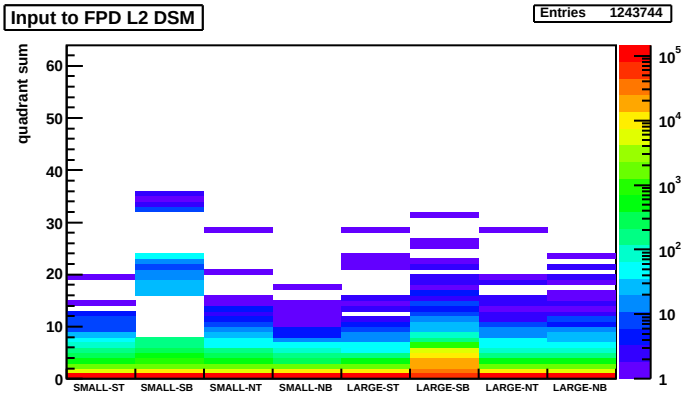

Input to FMS L1 DSM
Entries 739
sumAB - simulated
DSM board

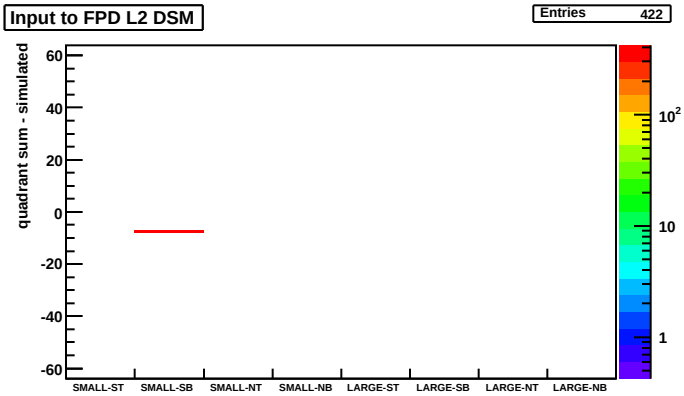

Input to FMS L1 DSM
Entries 1865616
sumA
DSM board

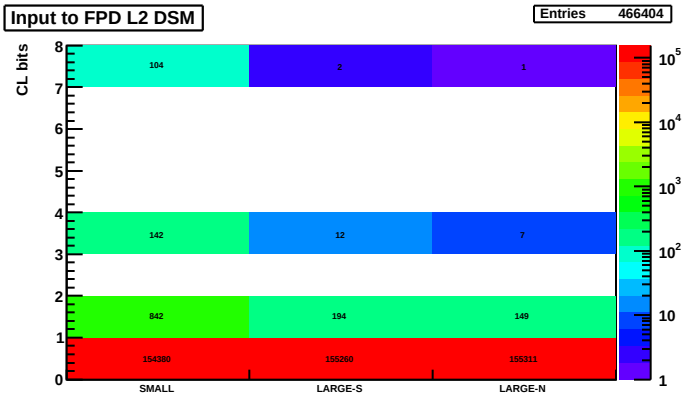

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

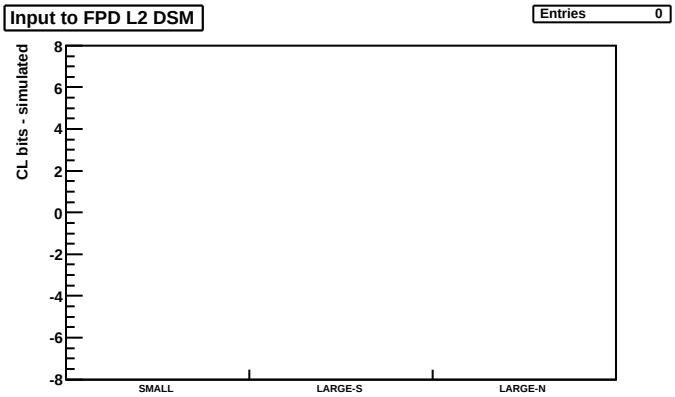

Input to FMS L1 DSM
Entries 1865616
FMS L1 HT bits
DSM board

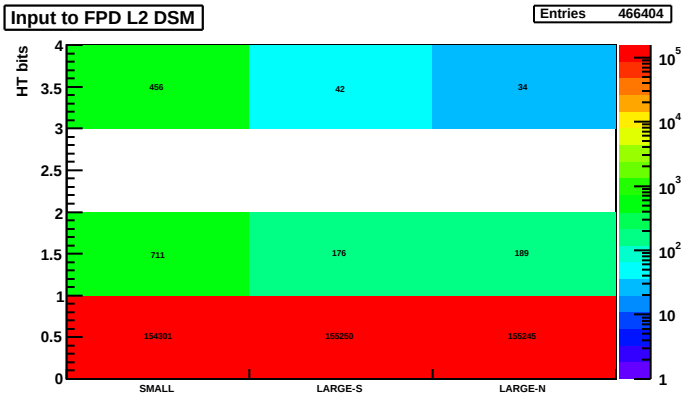

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

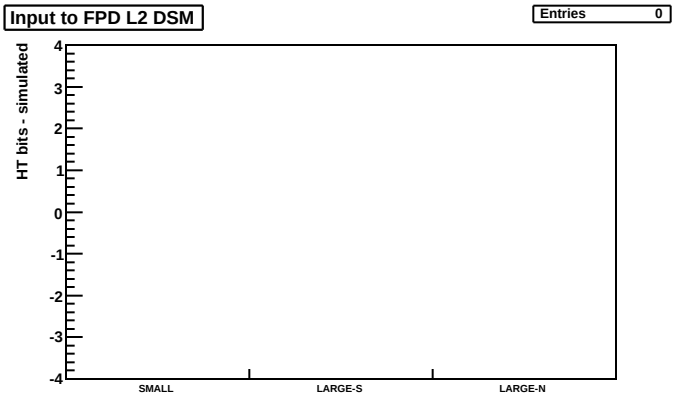

Input to FPD L2 DSM
Entries 1243744
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

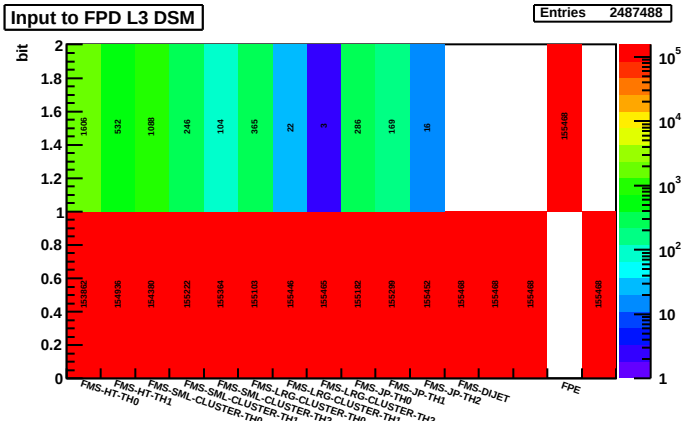

Input to FPD L2 DSM
Entries 422
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

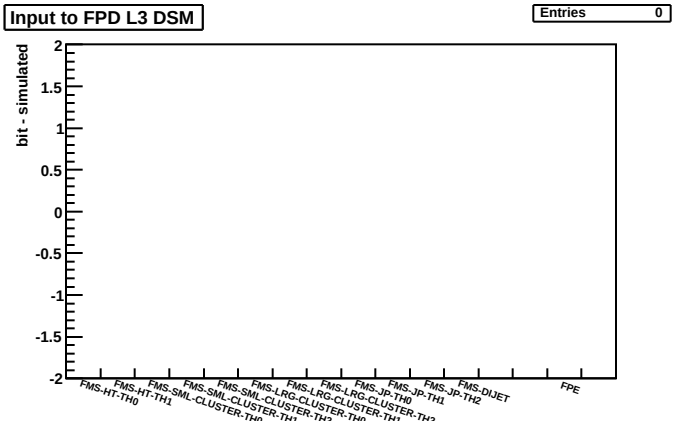

Input to FPD L2 DSM
Entries 466404
CL bits
104
2
1
142
12
7
842
194
149
154380
155260
155311
SMALL
LARGE-S
LARGE-N

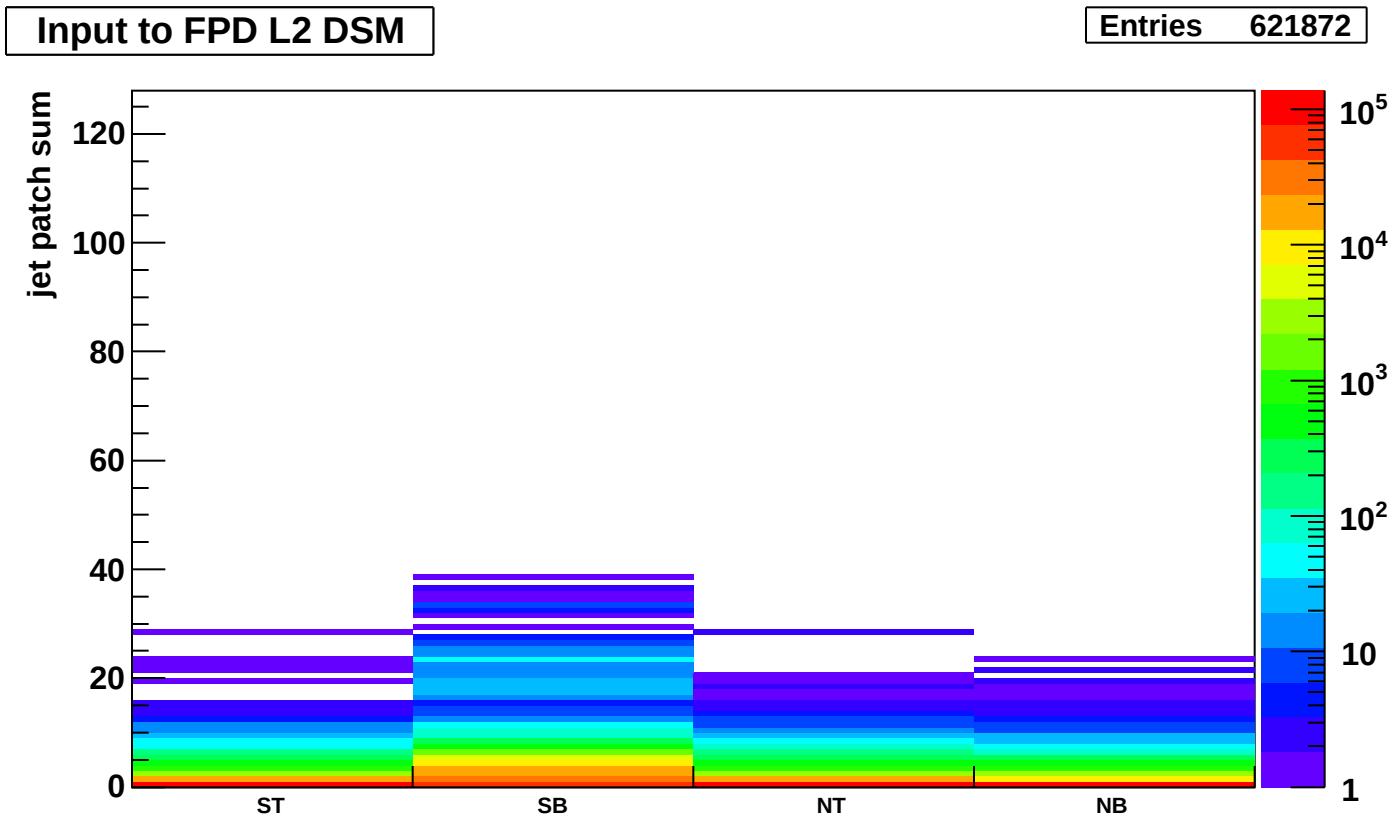

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

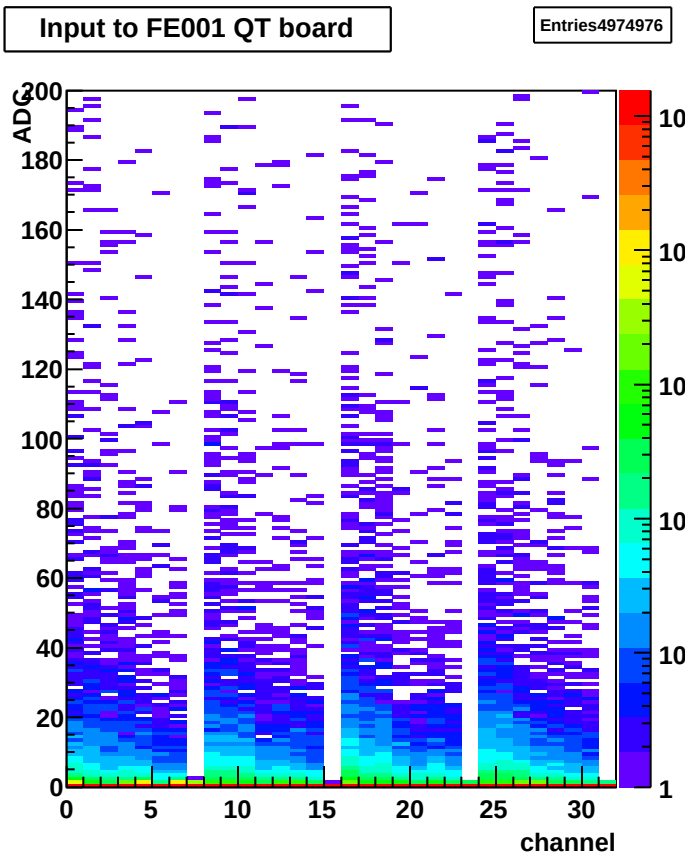

Input to FPD L2 DSM
Entries 466404
HT bits
456
42
34
711
176
189
154301
155250
155245
SMALL
LARGE-S
LARGE-N

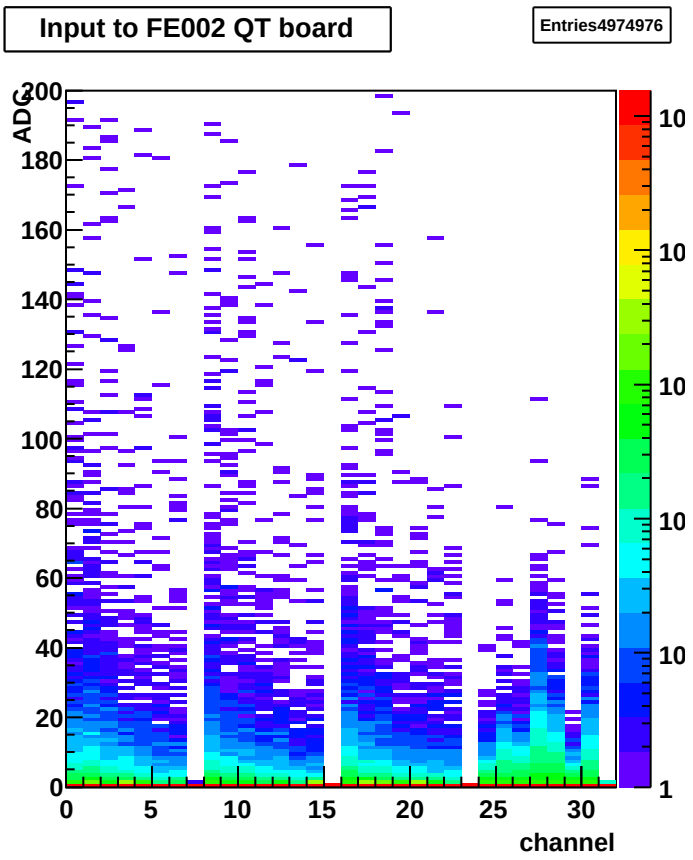

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

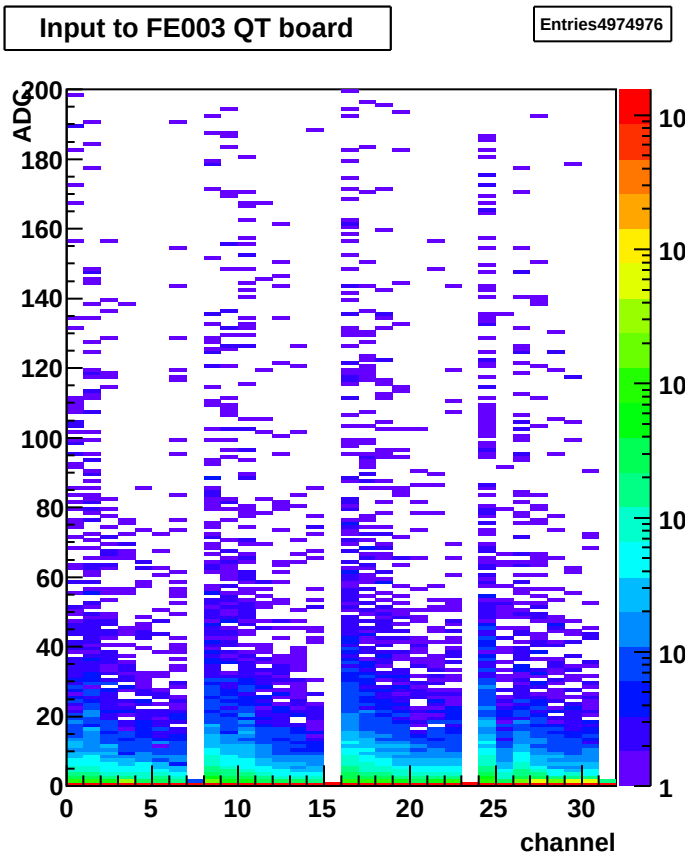

Input to FPD L3 DSM
Entries 2487488
bit
1606
532
1088
246
104
365
22
3
286
169
16
155468
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

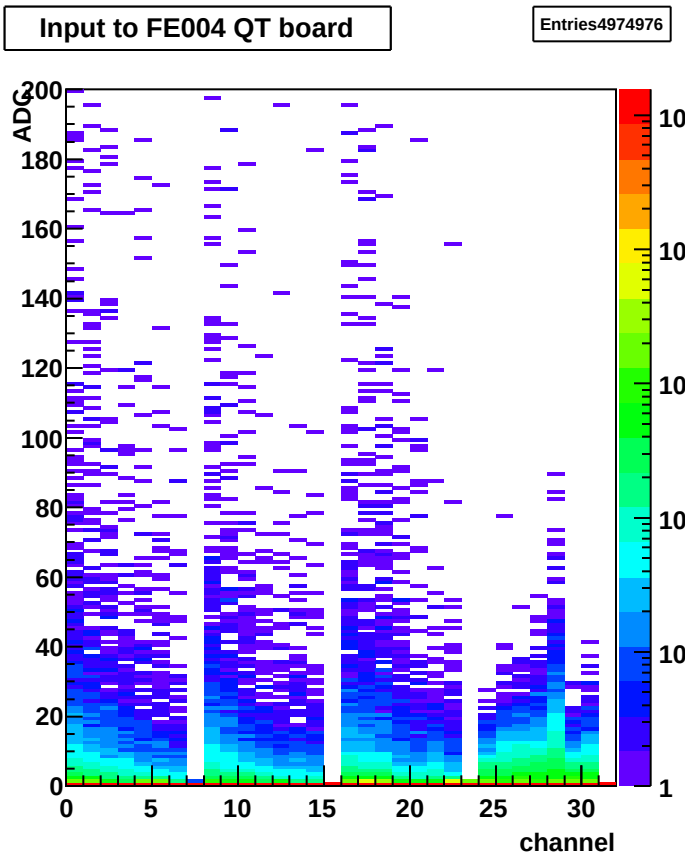

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

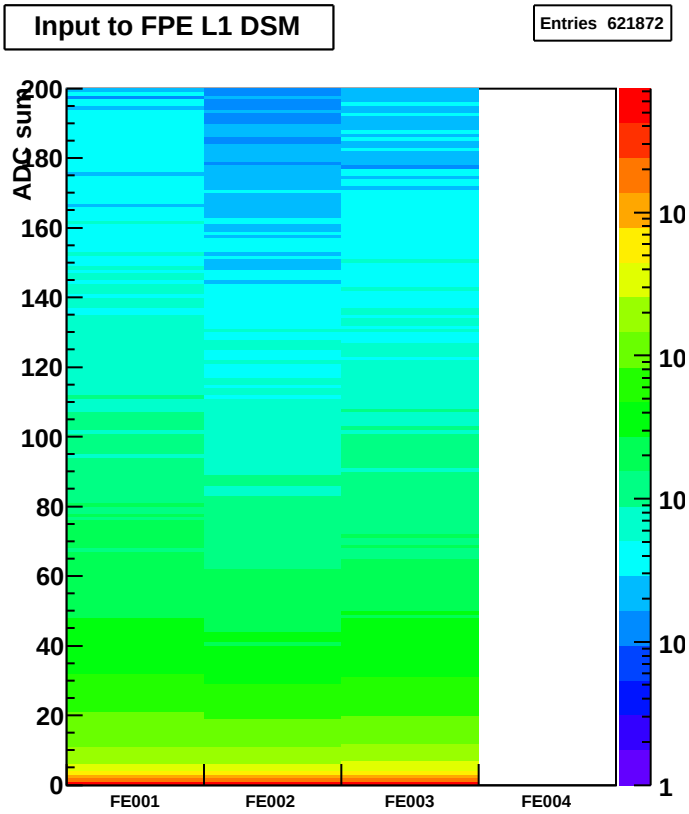

Input to FPD L2 DSM
Entries 621872
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

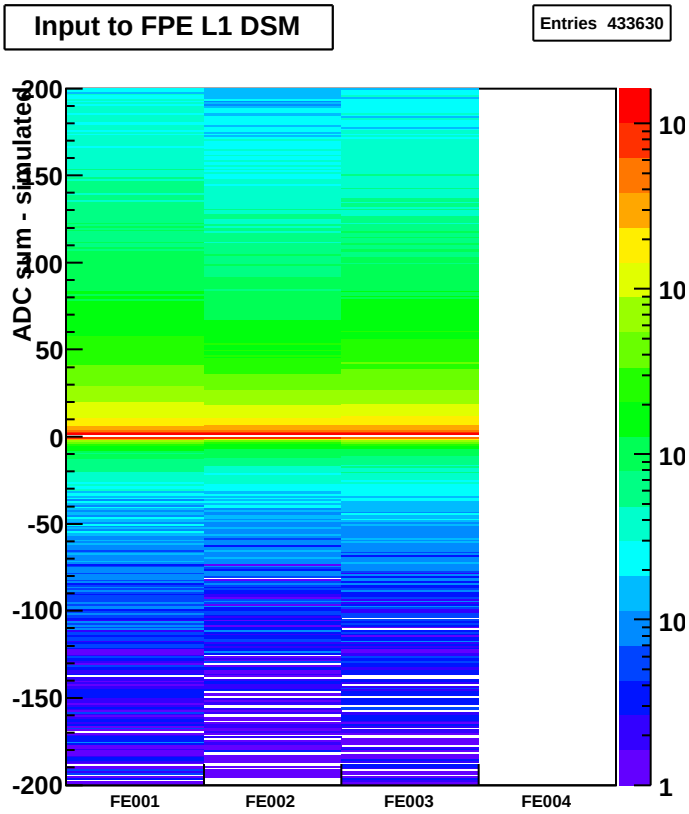

Input to FE001 QT board
Entries4974976
ADC
channel

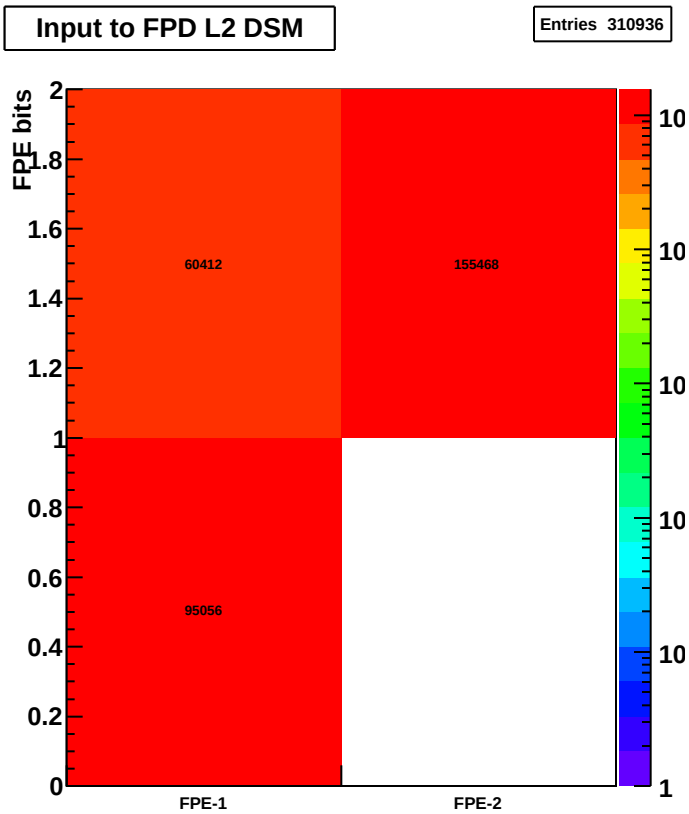

Input to FE002 QT board
Entries4974976
ADC
channel

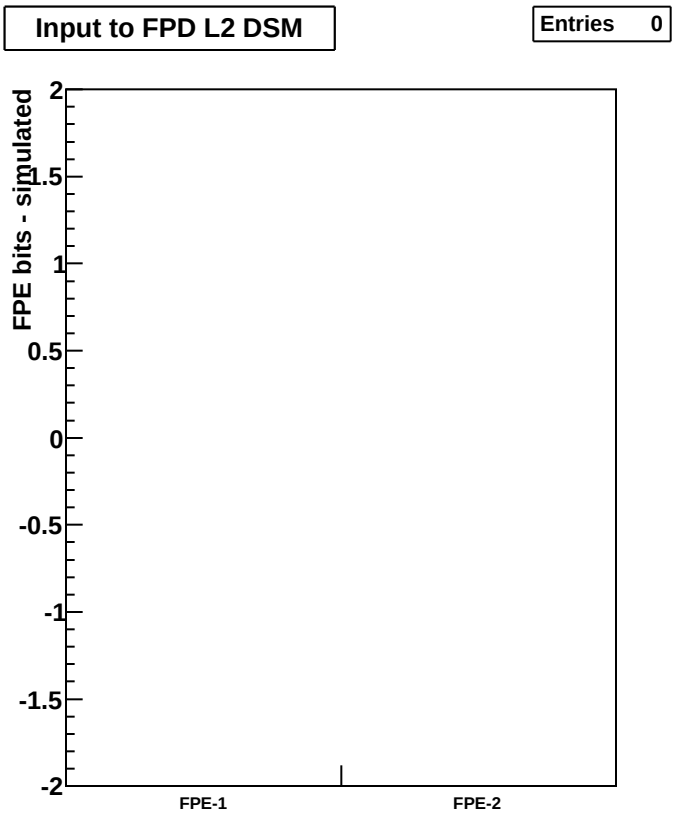

Input to FE003 QT board
Entries4974976
ADC
channel


Input to FE004 QT board
Entries4974976
ADC
channel


Input to FPE L1 DSM
Entries 621872
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 433630
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 310936
FPE bits
60412
155468
95056
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2