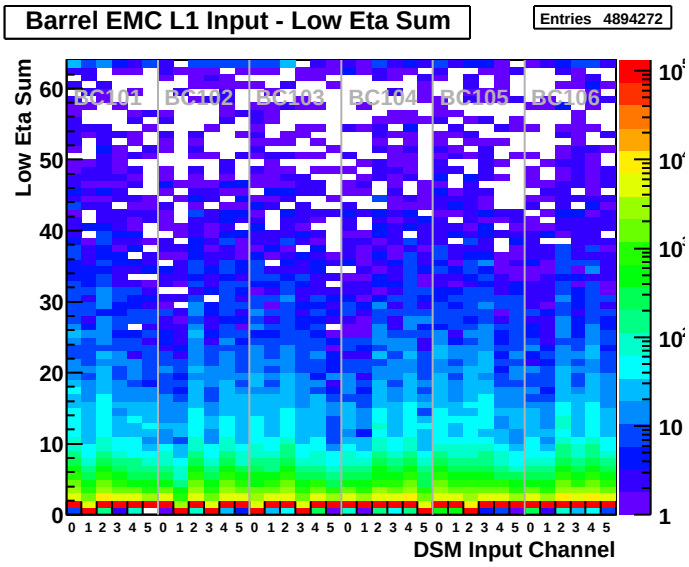
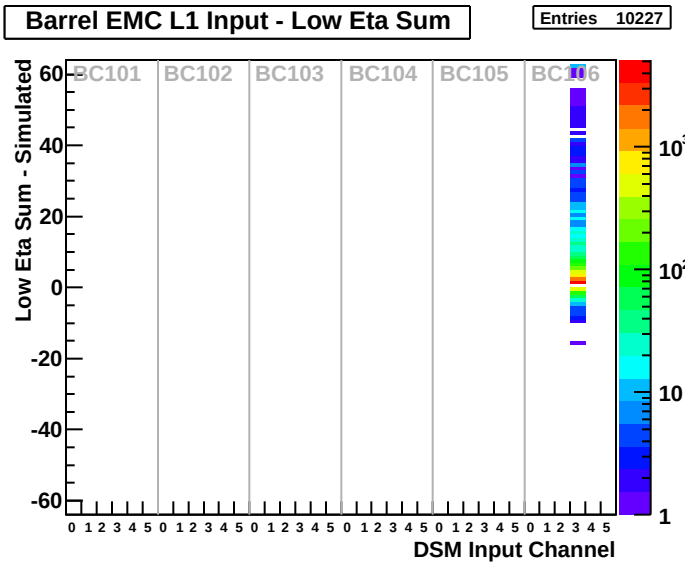
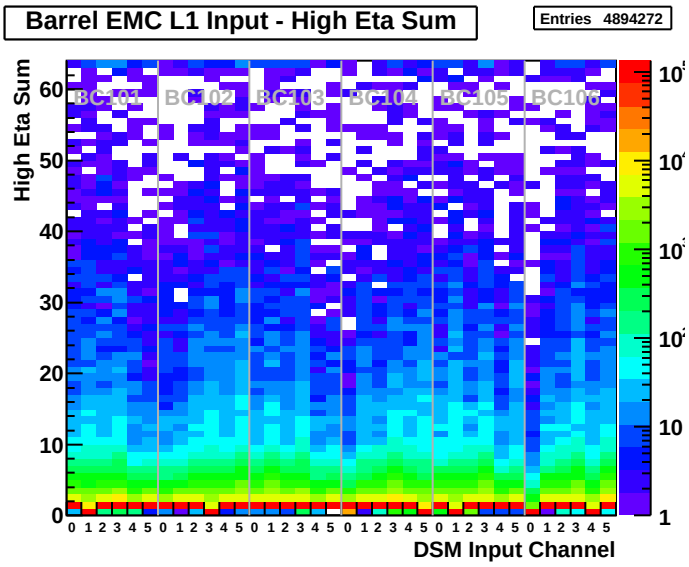
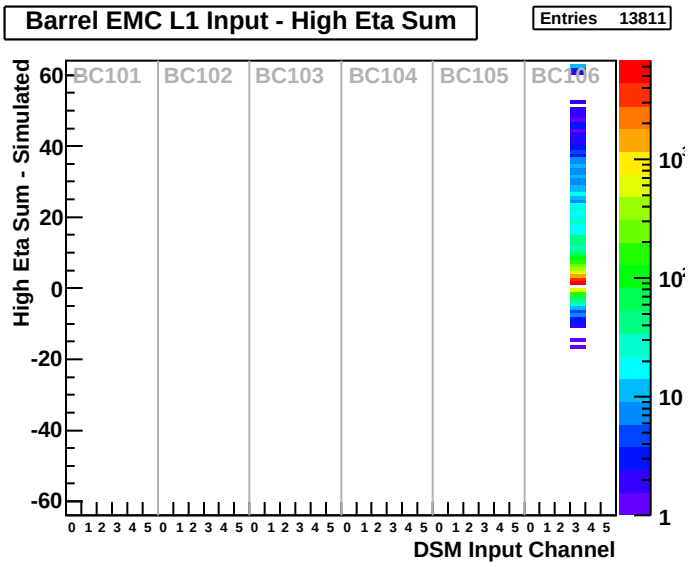
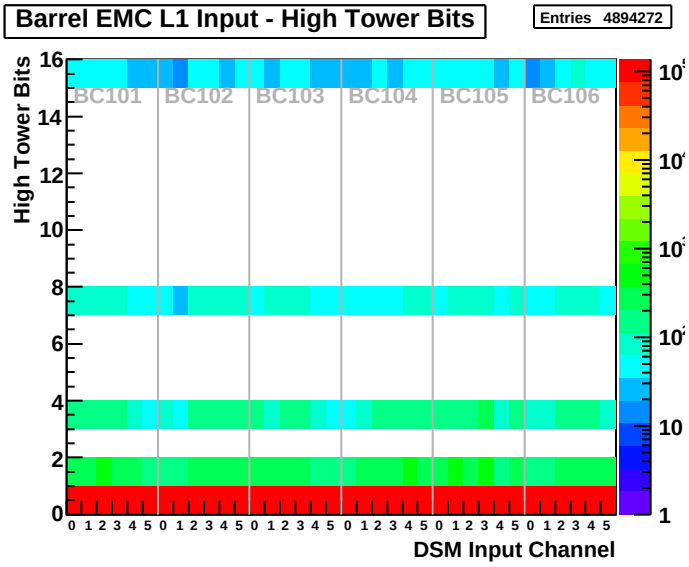
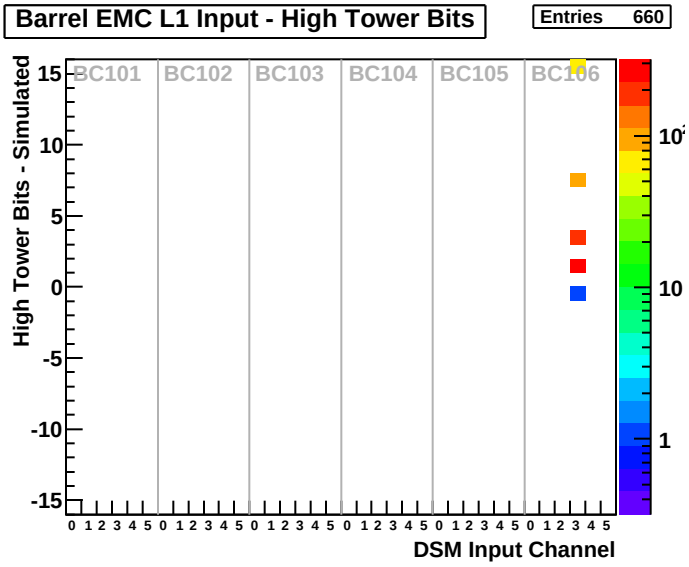

Barrel EMC L1 Input - Low Eta Sum
Entries 4894272
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

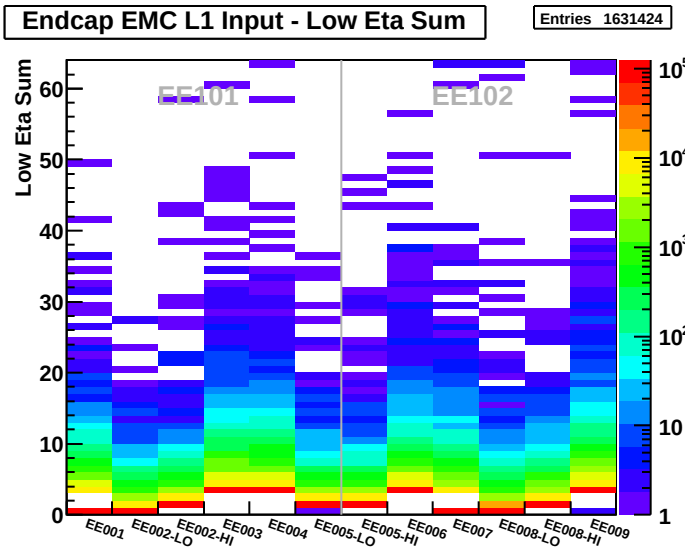

Barrel EMC L1 Input - Low Eta Sum
Entries 10227
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

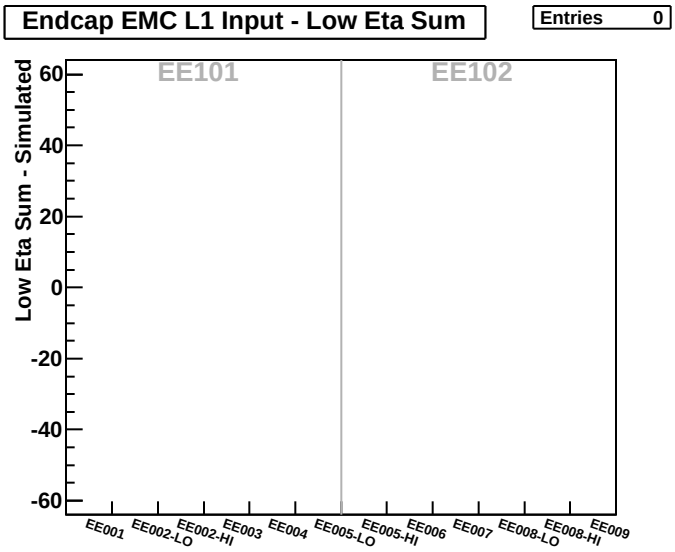

Barrel EMC L1 Input - High Eta Sum
Entries 4894272
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

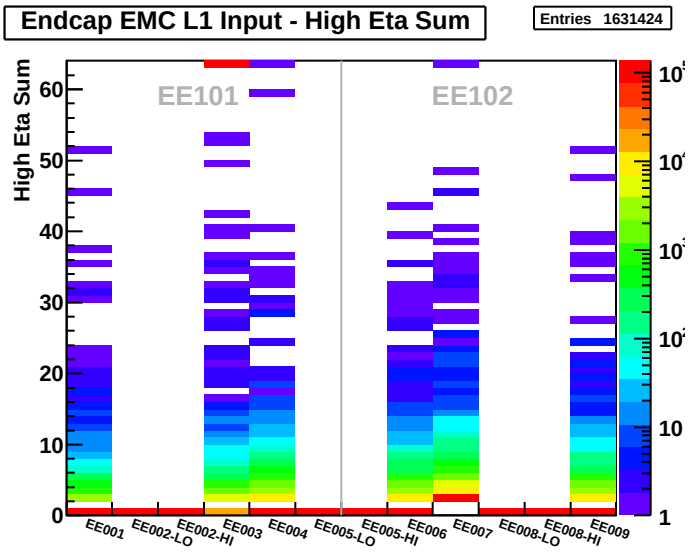

Barrel EMC L1 Input - High Eta Sum
Entries 13811
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

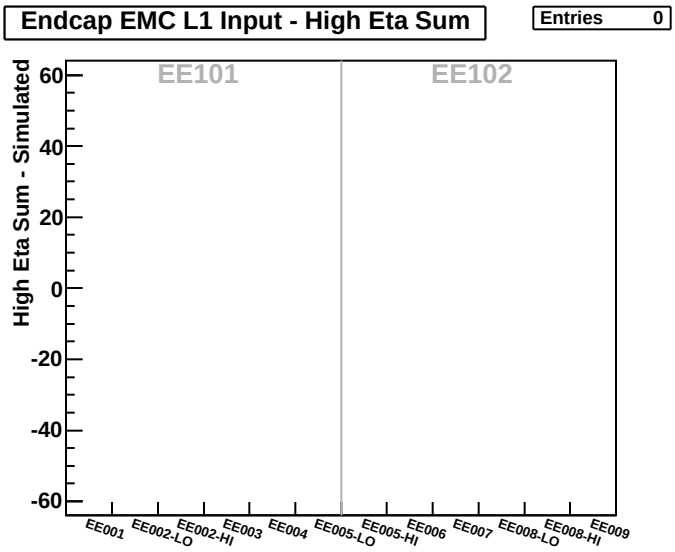

Barrel EMC L1 Input - High Tower Bits
Entries 4894272
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

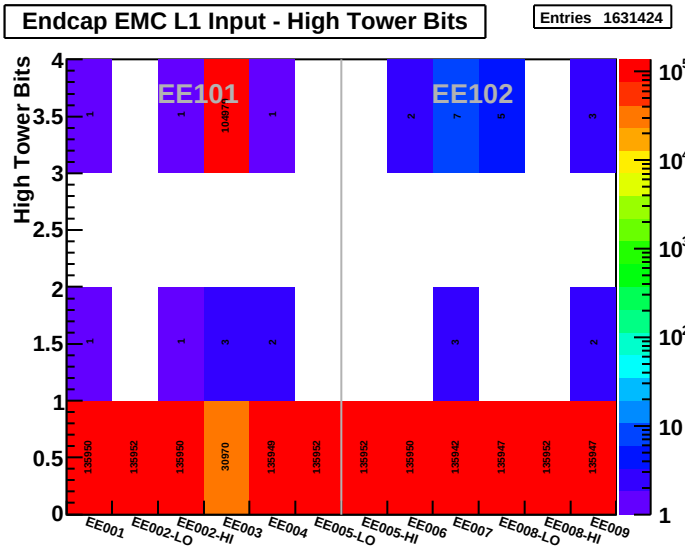

Barrel EMC L1 Input - High Tower Bits
Entries 660
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

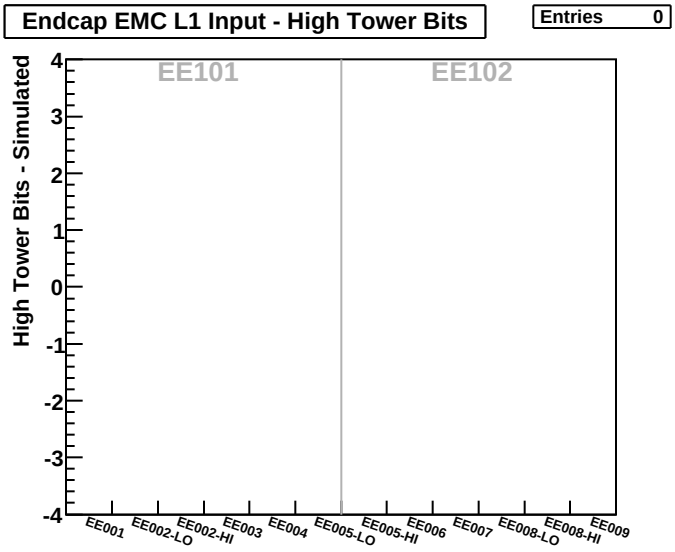

Endcap EMC L1 Input - Low Eta Sum
Entries 1631424
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

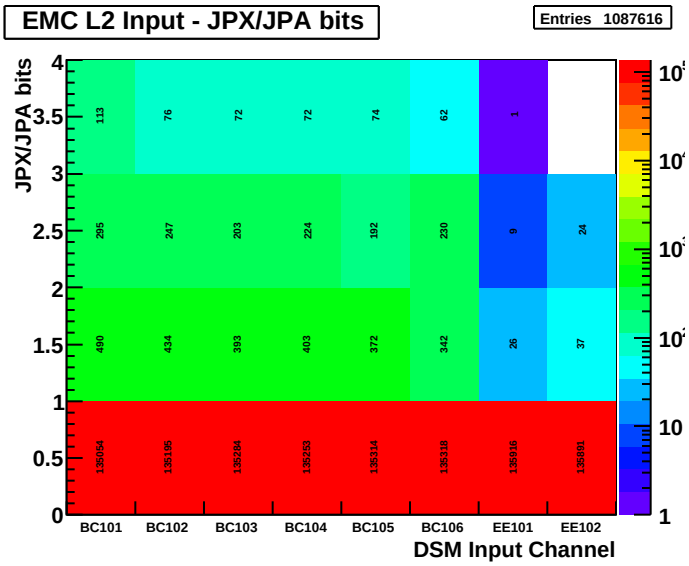

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

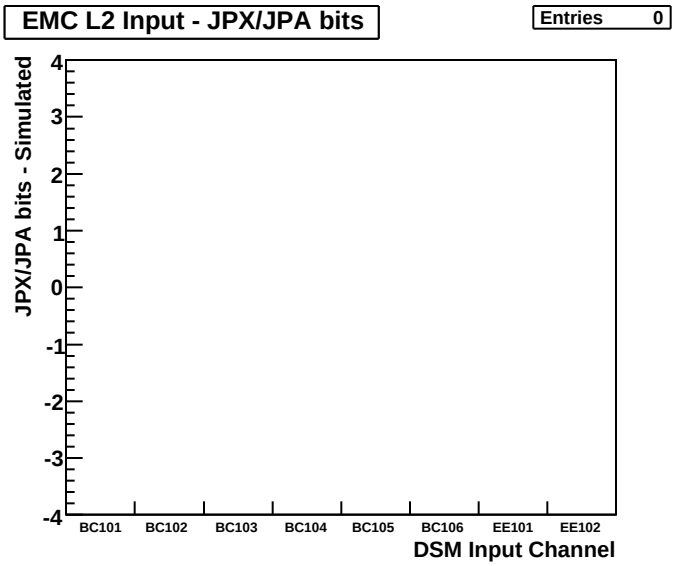

Endcap EMC L1 Input - High Eta Sum
Entries 1631424
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

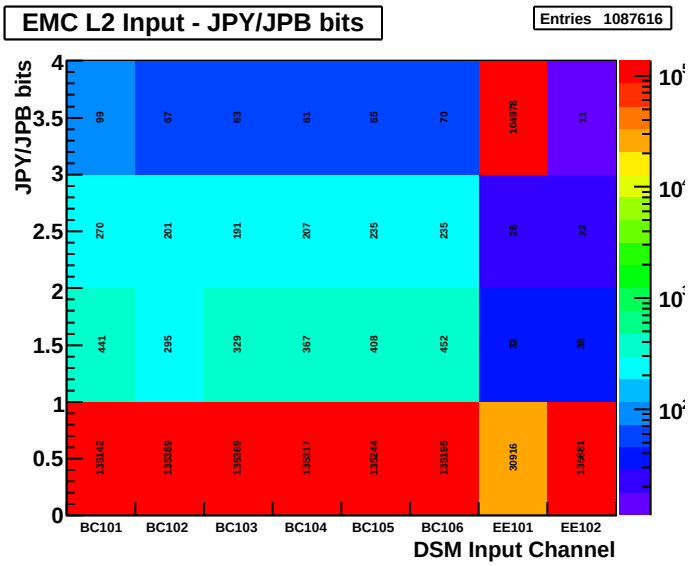

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

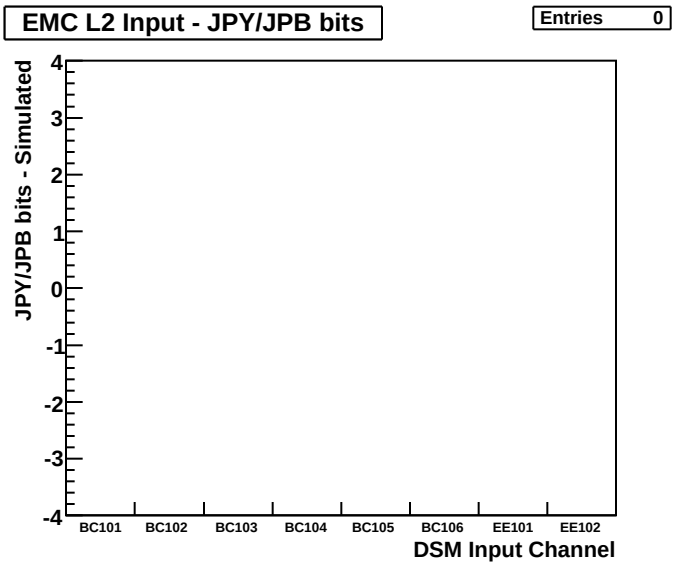

Endcap EMC L1 Input - High Tower Bits
Entries 1631424
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

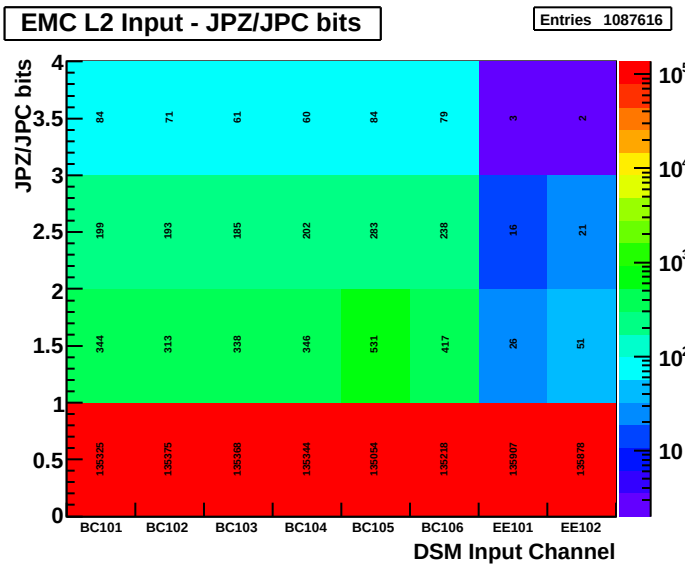

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

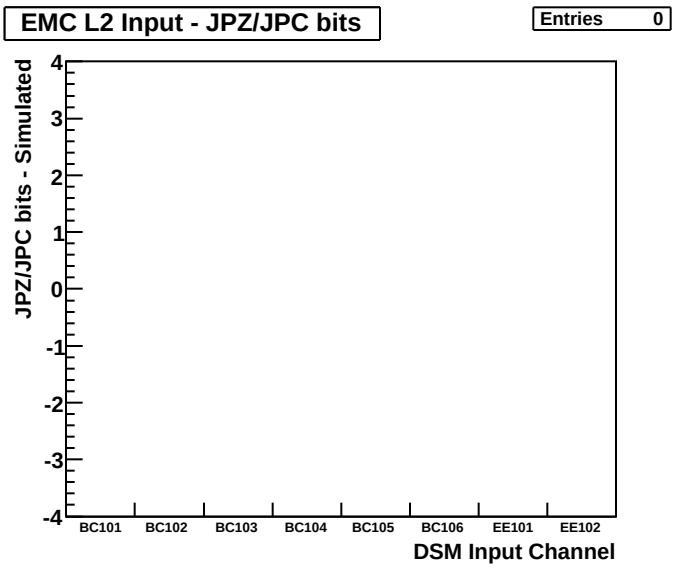

EMC L2 Input - JPX/JPA bits
Entries 1087616
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
113 76 72 72 74 62 1
295 247 203 224 192 230 9 24
490 434 393 403 372 342 26 37
135054 135185 135284 135253 135314 135318 135916 135891

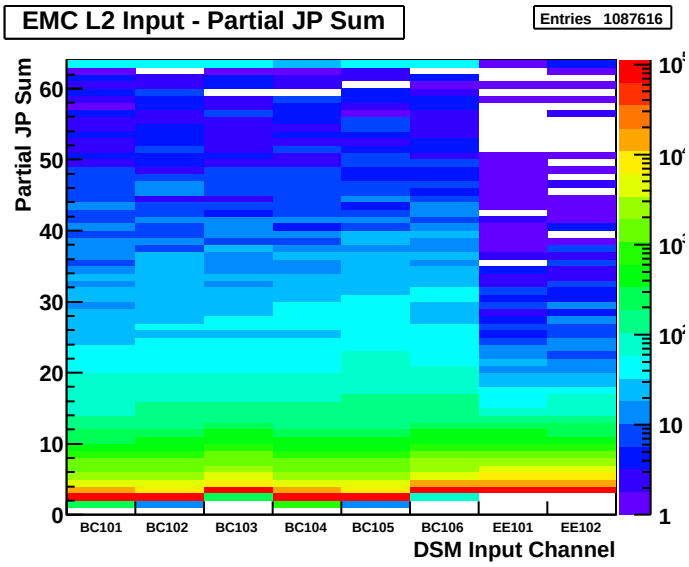

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

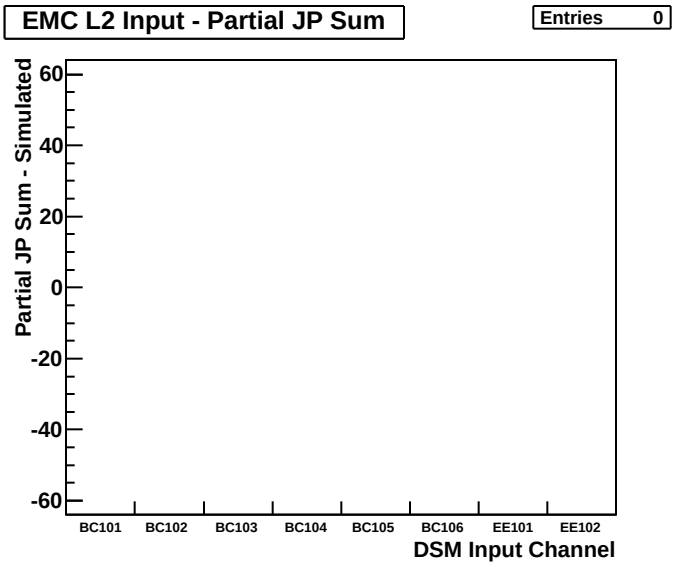

EMC L2 Input - JPY/JPB bits
Entries 1087616
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
99 67 63 61 65 70 104978 11
270 201 191 207 235 235 26 22
441 295 329 367 408 452 32 38
135142 135389 135369 135317 135244 135195 30916 135881

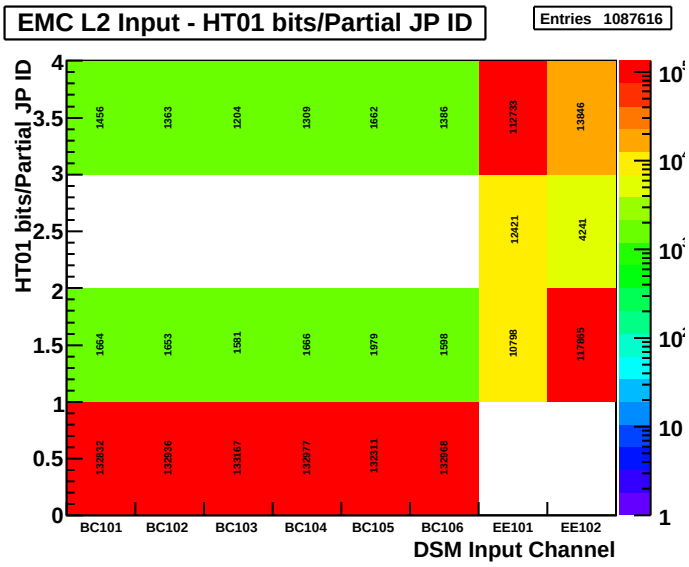

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

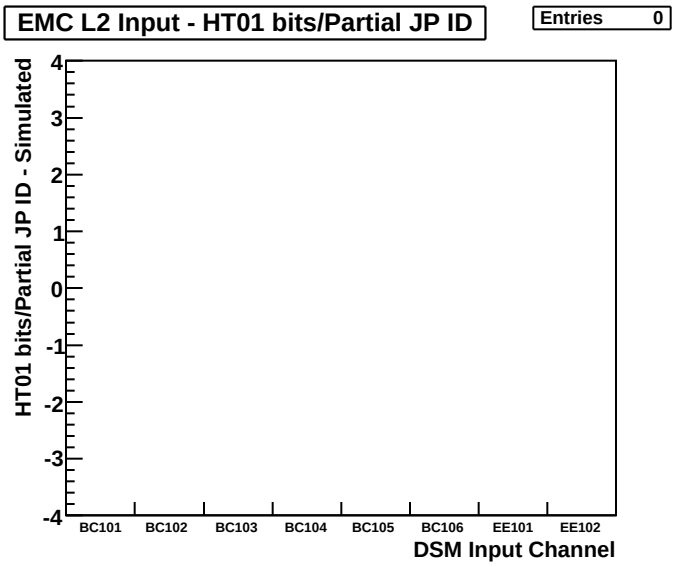

EMC L2 Input - JPZ/JPC bits
Entries 1087616
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
84 71 61 60 84 79 3 2
199 193 185 202 283 238 16 21
344 313 338 346 531 417 26 51
135325 135375 135369 135344 135054 135218 135907 135878

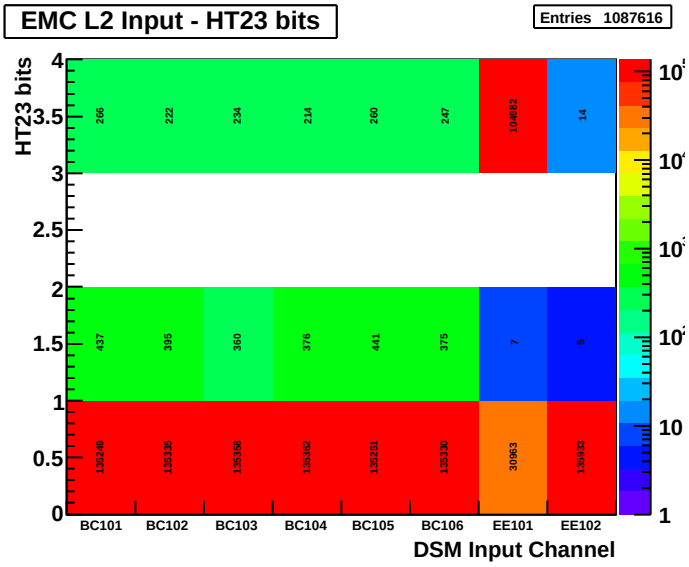

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

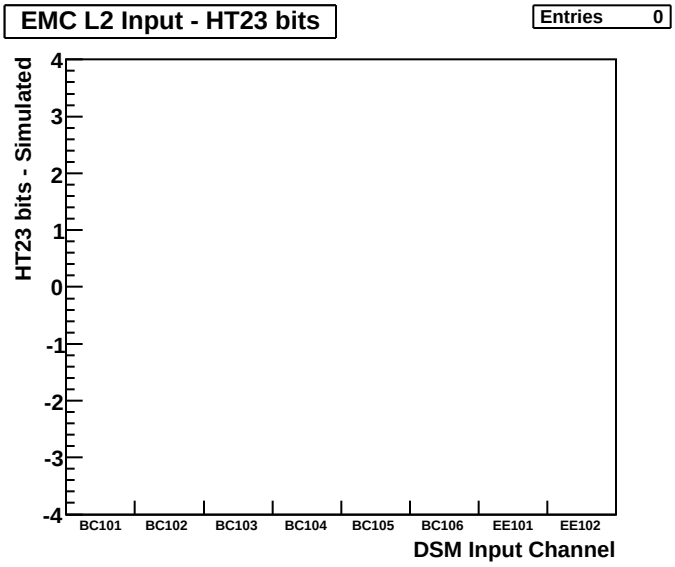

EMC L2 Input - Partial JP Sum
Entries 1087616
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

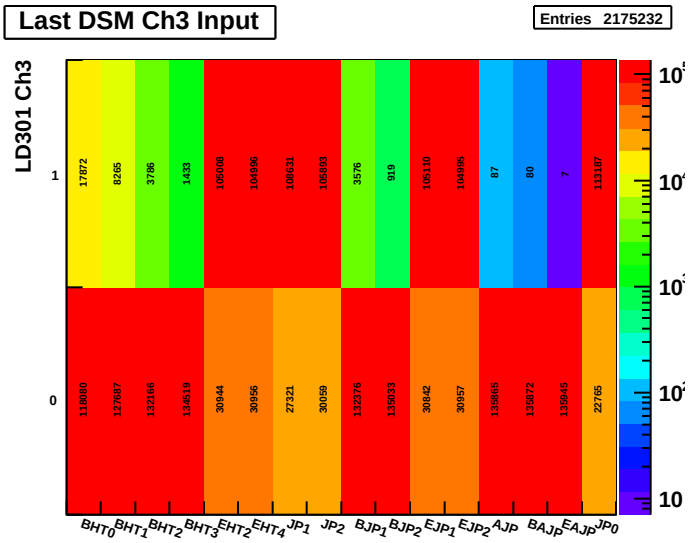

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

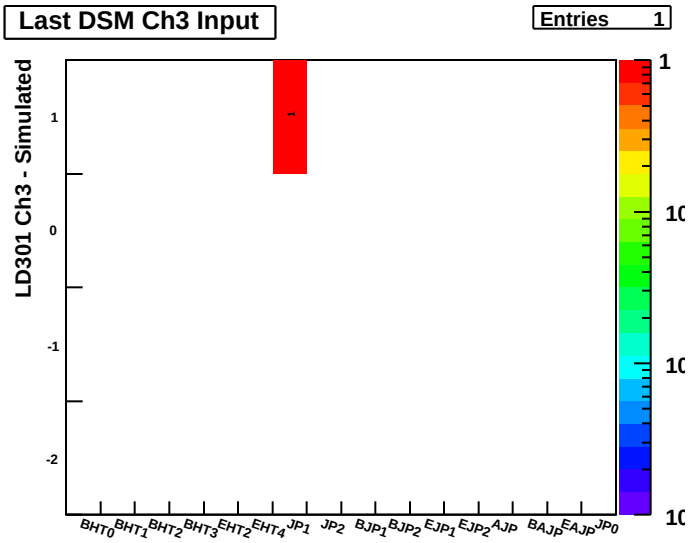

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1087616
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

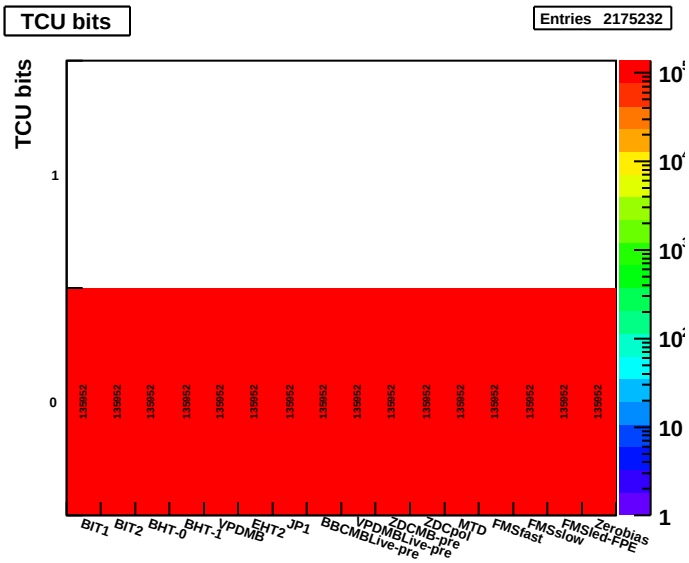

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

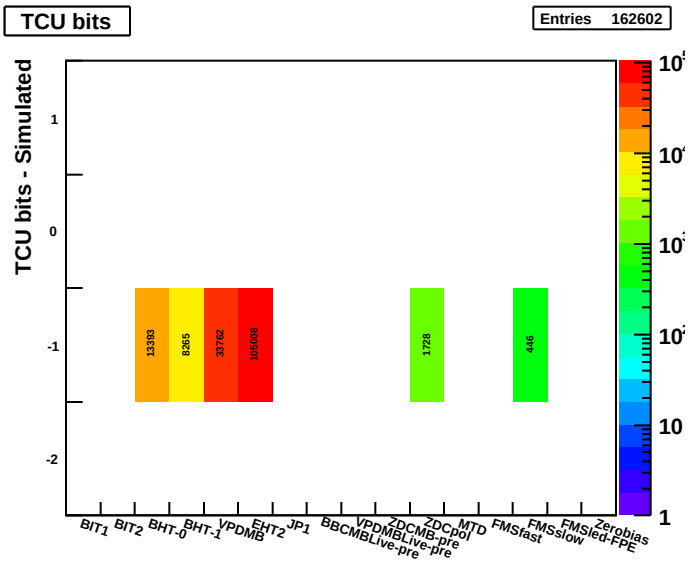

EMC L2 Input - HT23 bits
Entries 1087616
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

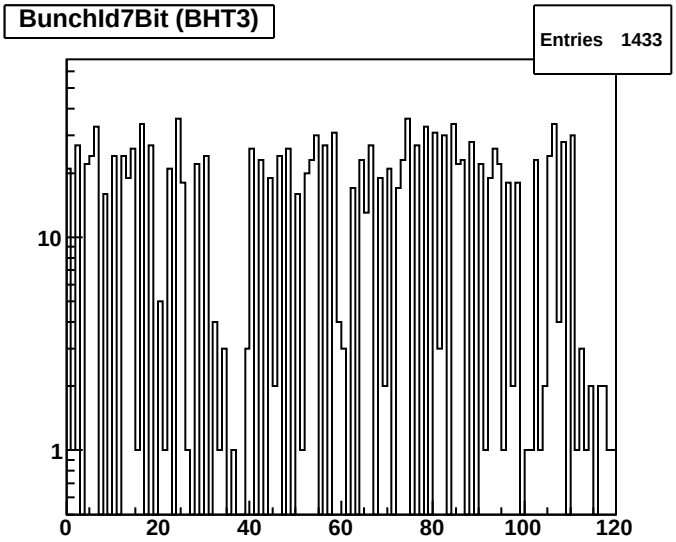

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

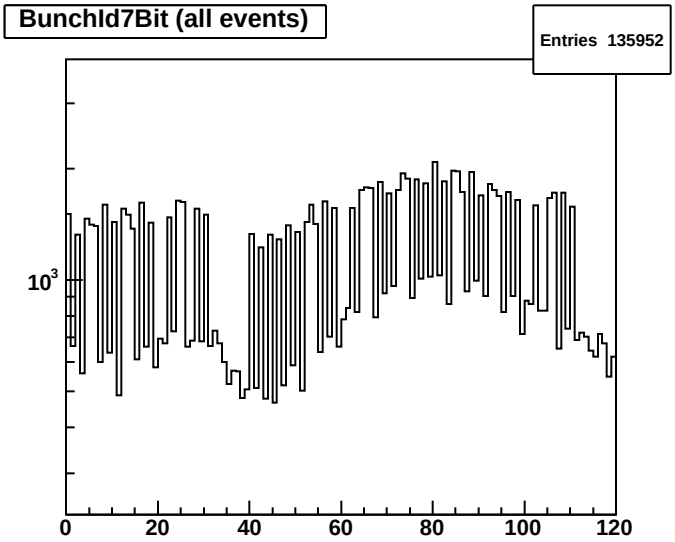

Last DSM Ch3 Input
Entries 2175232
LD301 Ch3
17872
8265
3786
1433
105008
104996
108631
105893
3576
919
105110
104995
87
80
7
113187
118080
127687
132166
134519
30944
30956
27321
30059
132376
135033
30842
30957
135865
135872
135945
22765
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

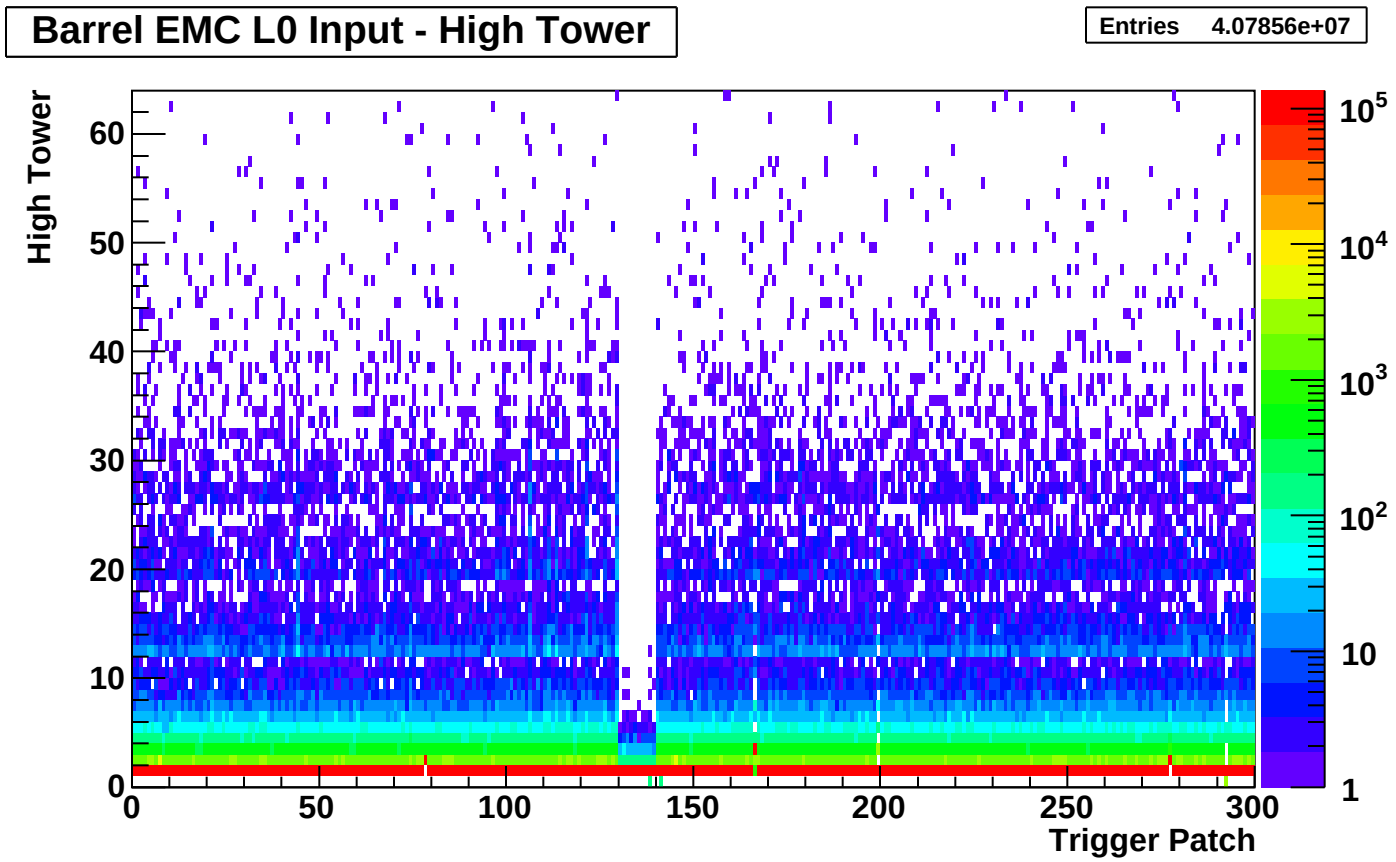

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

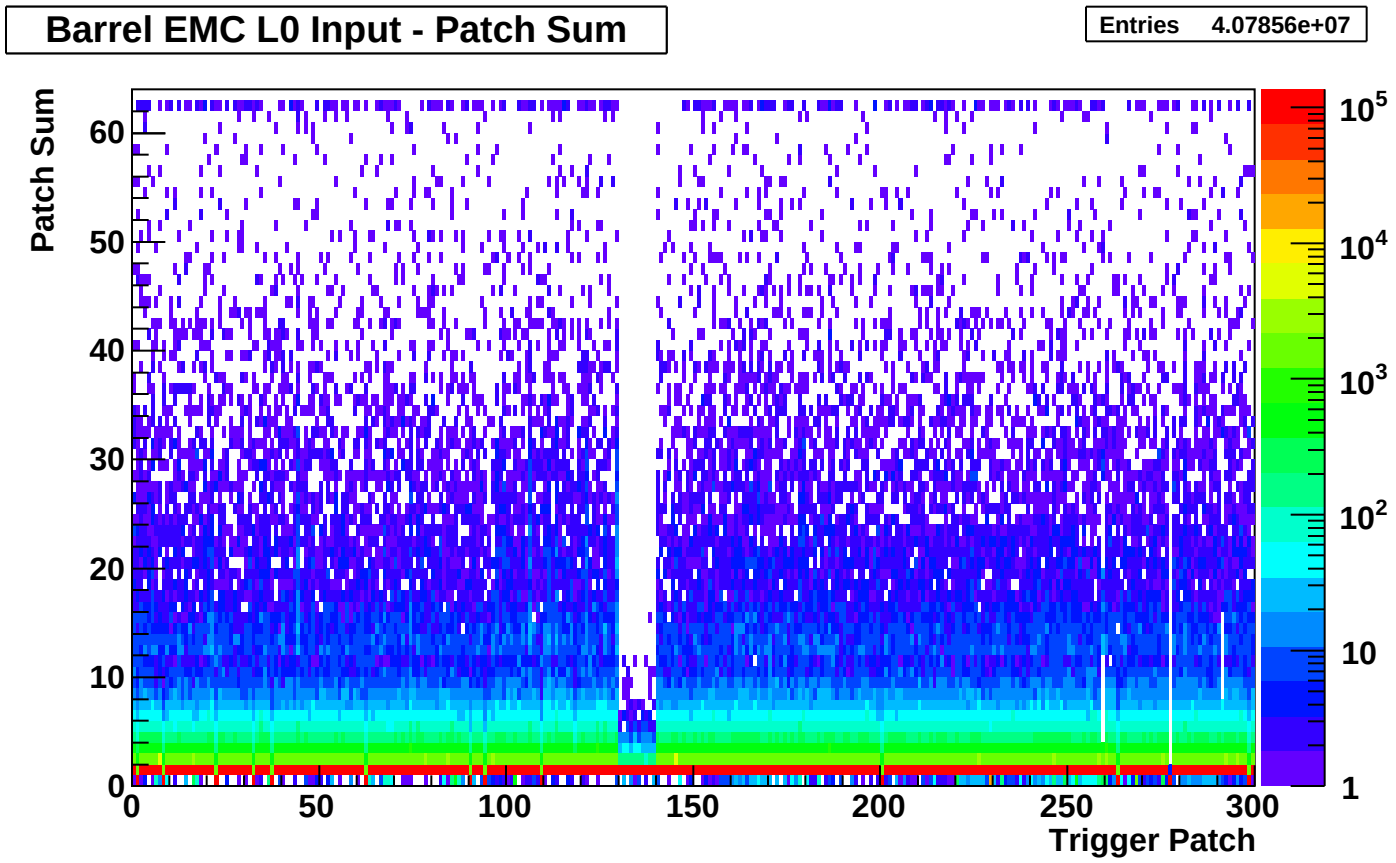

TCU bits
Entries 2175232
TCU bits
135952
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

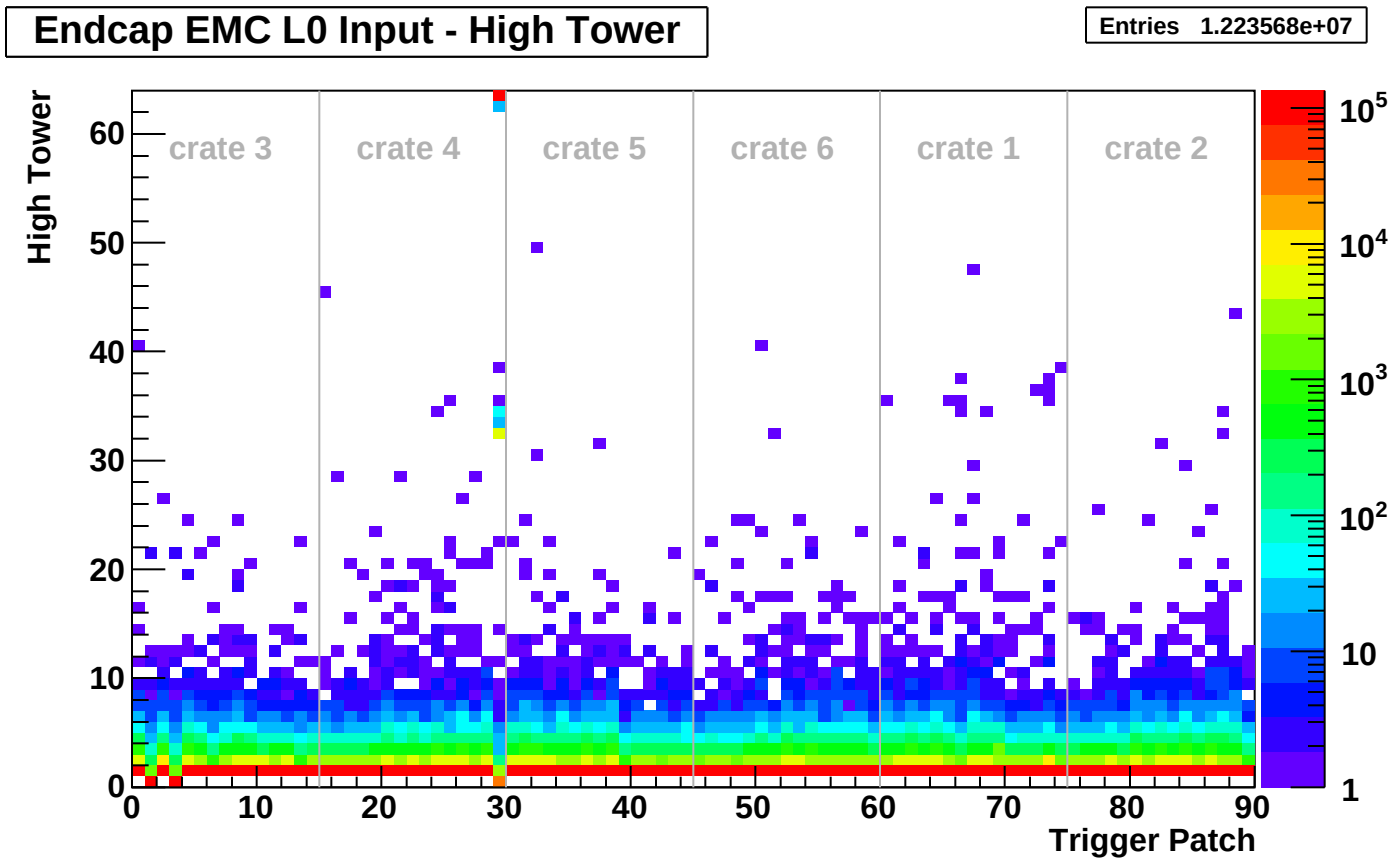

TCU bits
Entries 162602
TCU bits - Simulated
13393
8265
33762
105008
1728
446
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

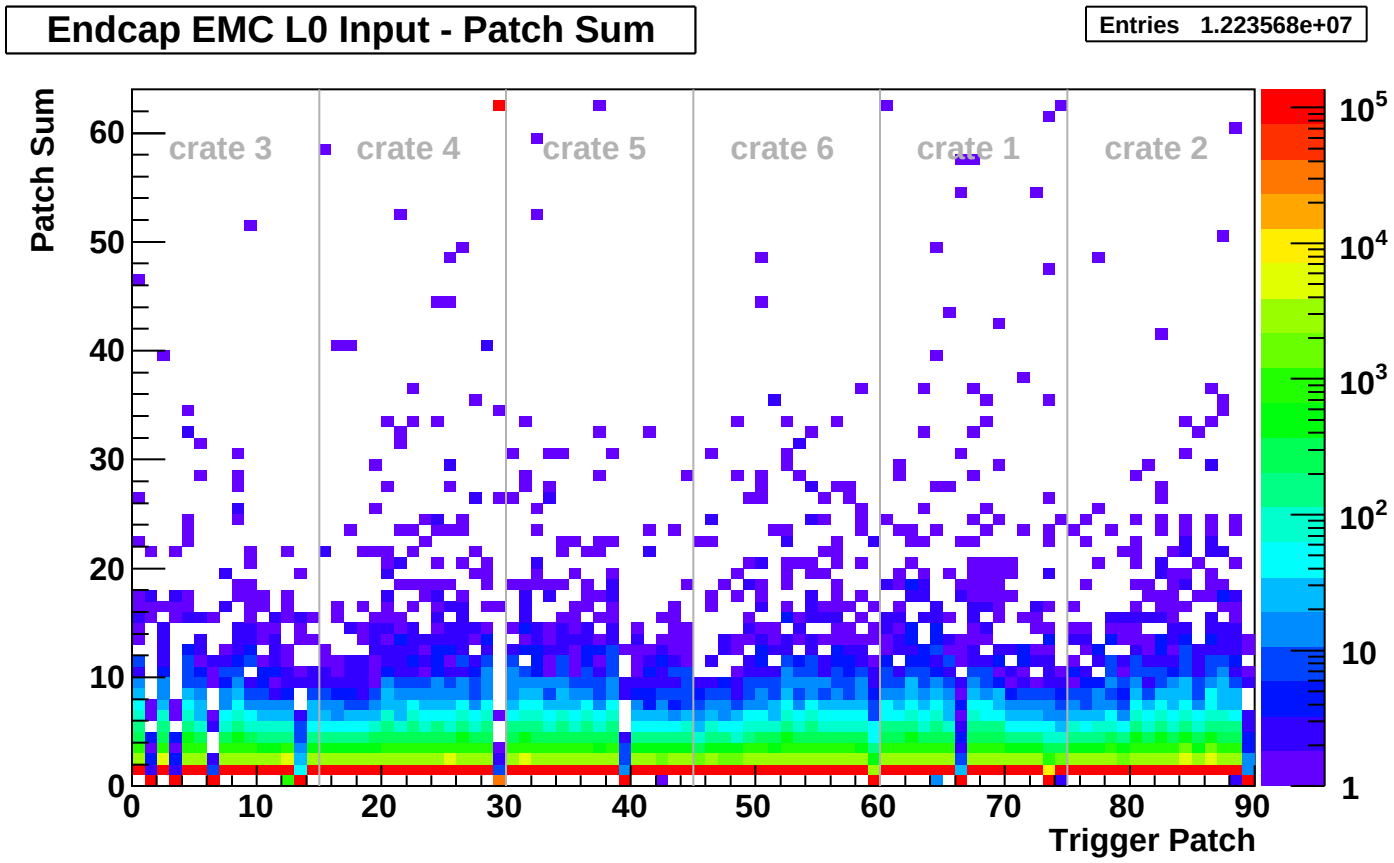

BunchId7Bit (BHT3)
Entries 1433

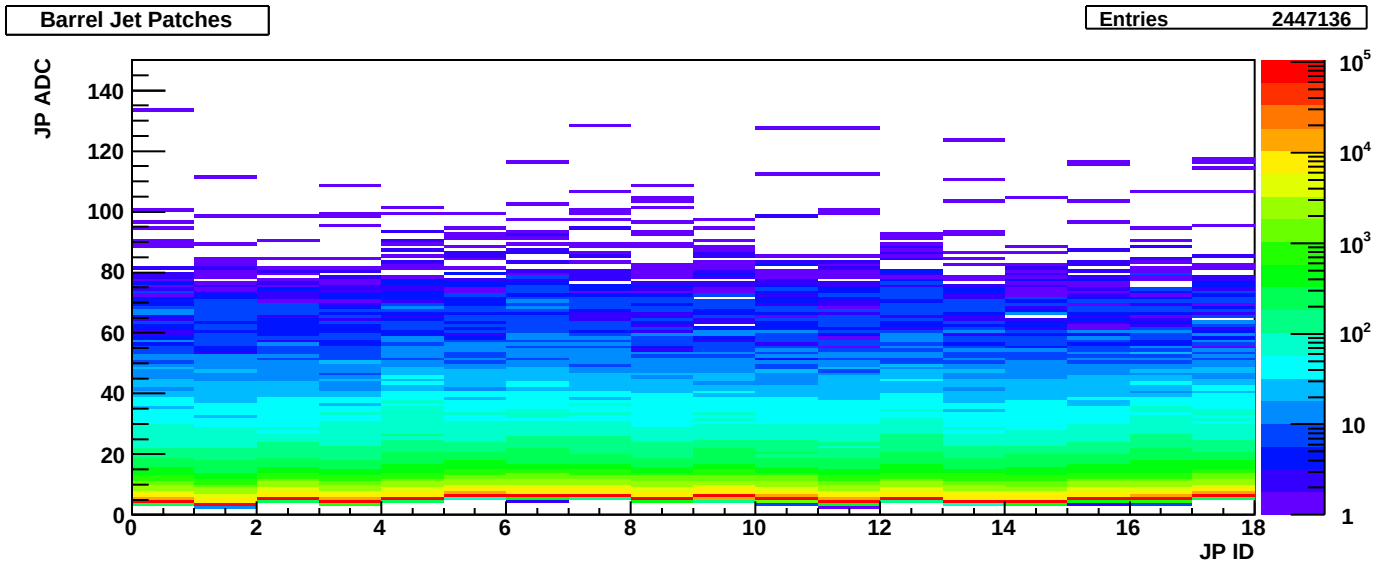

BunchId7Bit (all events)
Entries 135952

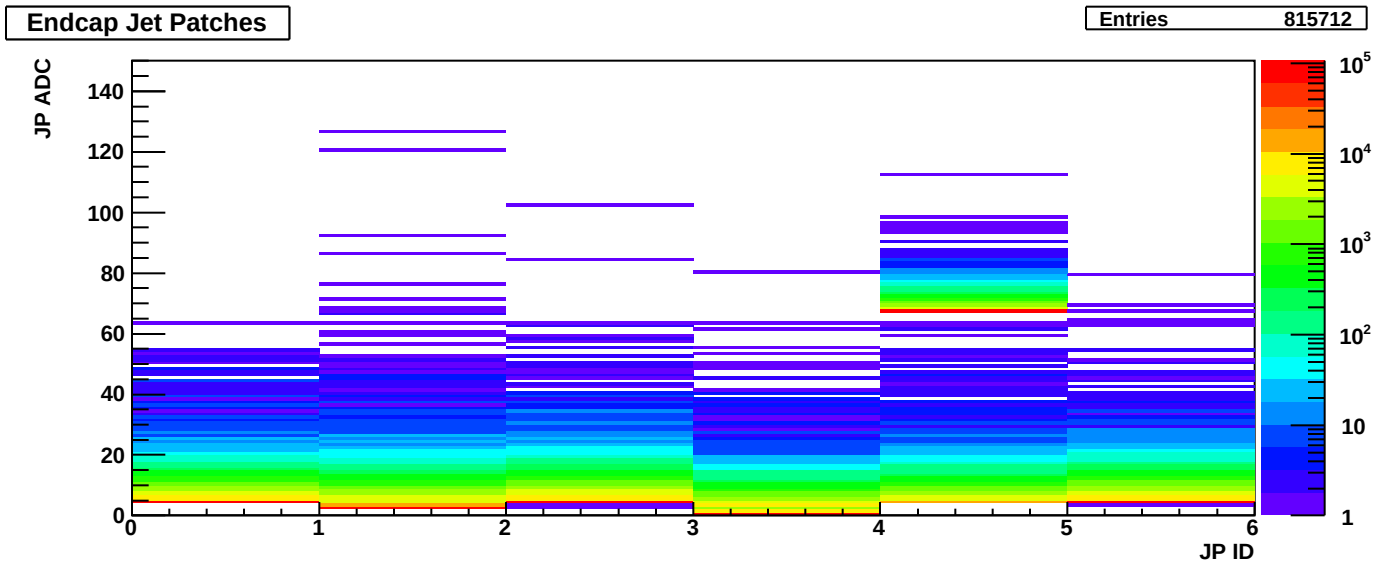

Barrel EMC L0 Input - High Tower
Entries 4.07856e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

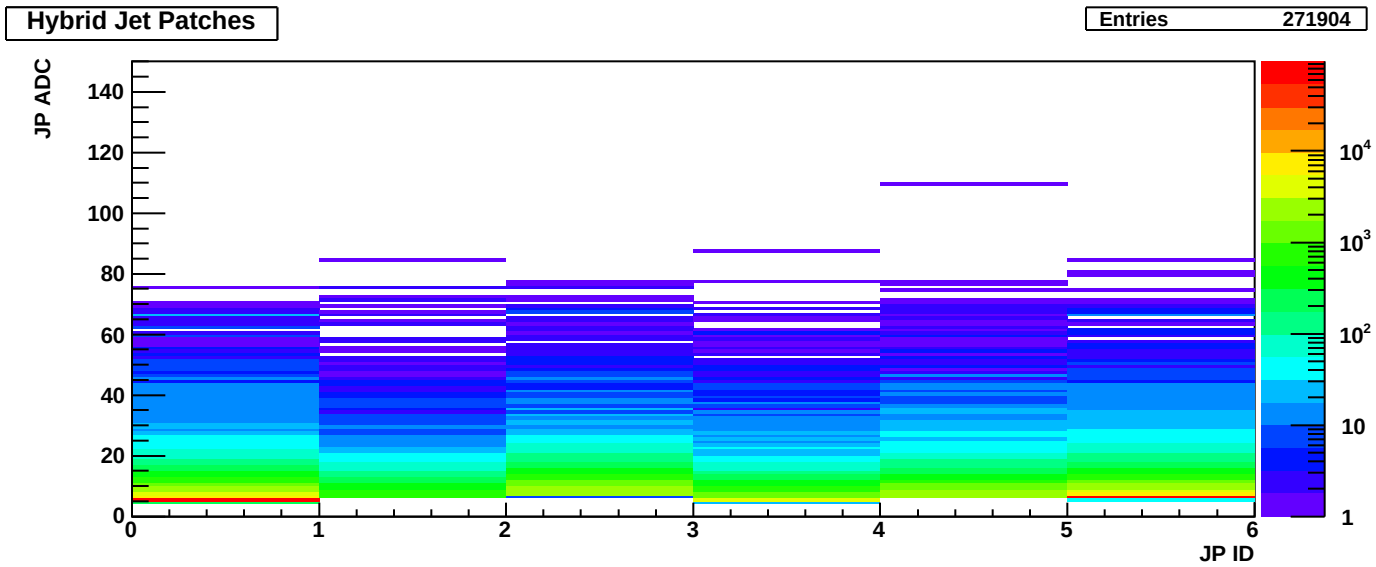

Barrel EMC L0 Input - Patch Sum
Entries 4.07856e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

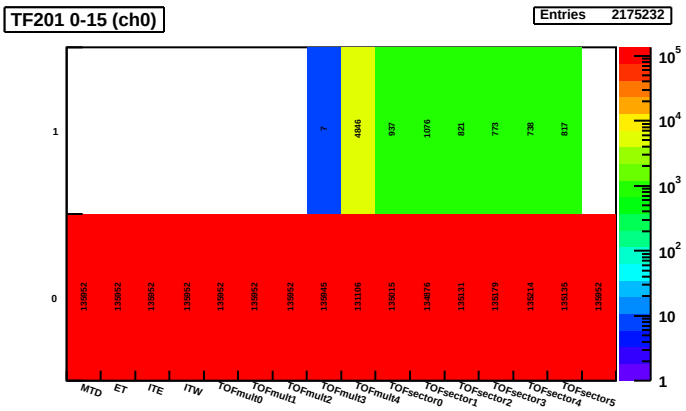

Endcap EMC L0 Input - High Tower
Entries 1.223568e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

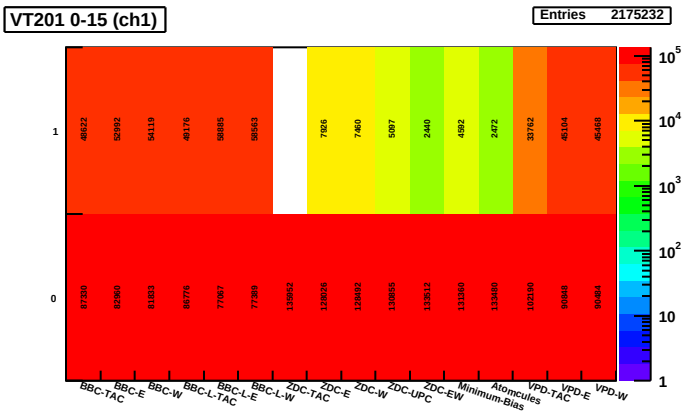

Endcap EMC L0 Input - Patch Sum
Entries 1.223568e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

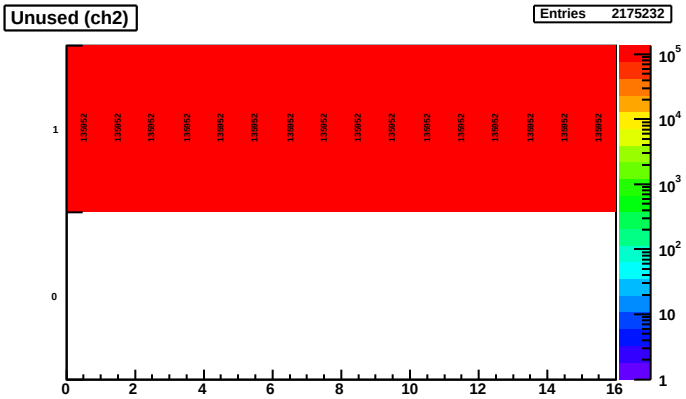

Barrel Jet Patches
Entries 2447136
JP ADC
JP ID

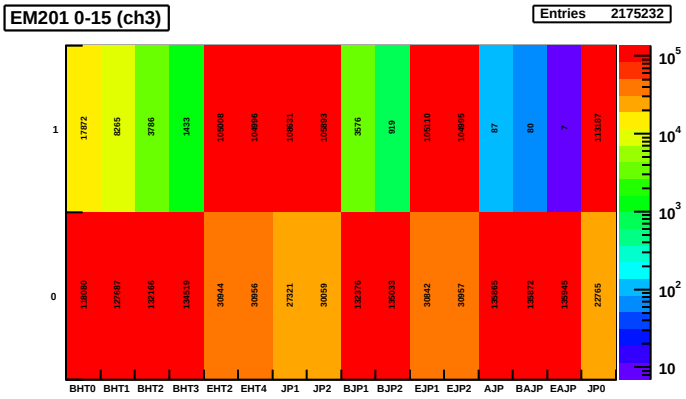

Endcap Jet Patches
Entries 815712
JP ADC
JP ID

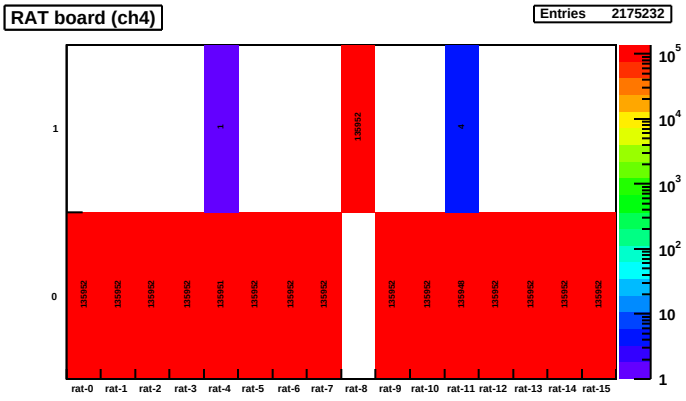

Hybrid Jet Patches
Entries 271904
JP ADC
JP ID

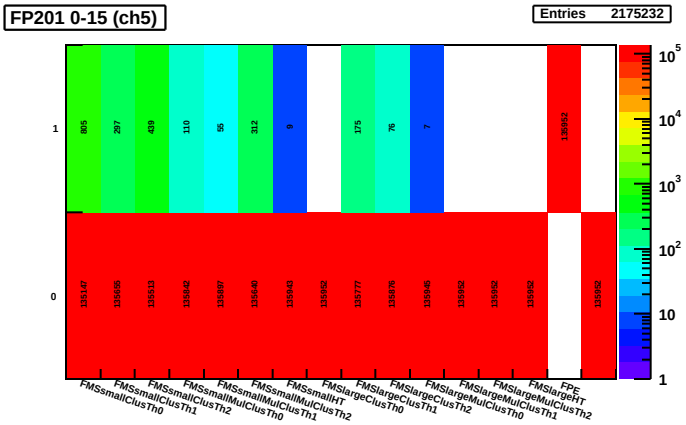

TF201 0-15 (ch0)
Entries 2175232
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

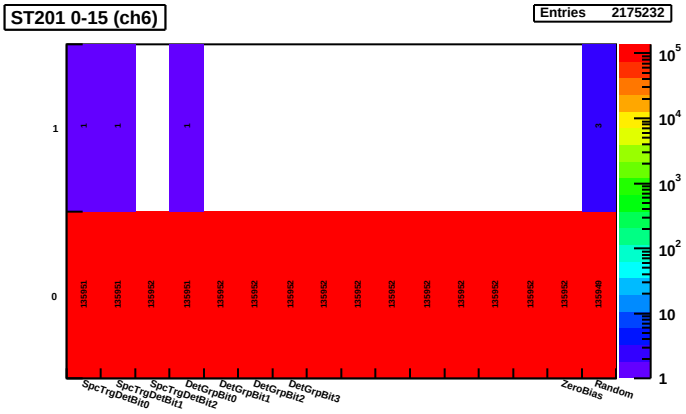

VT201 0-15 (ch1)
Entries 2175232
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

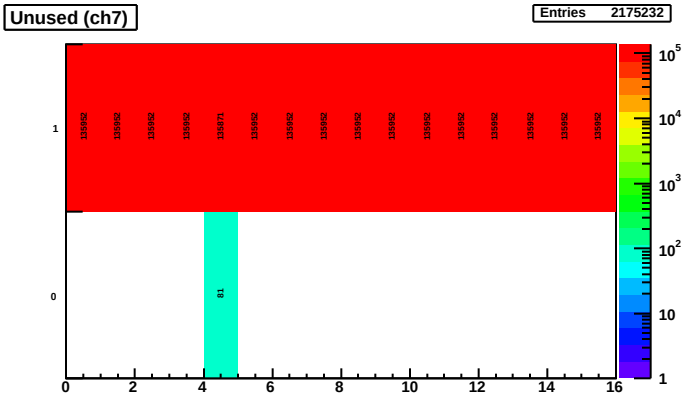

Unused (ch2)
Entries 2175232

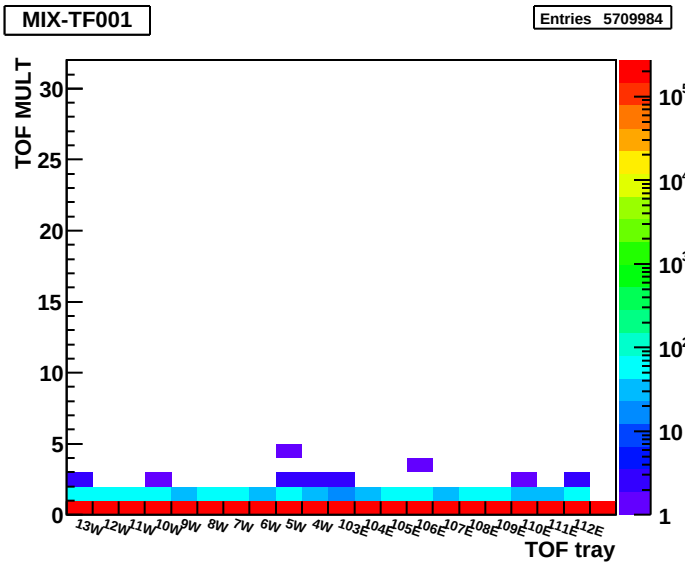

EM201 0-15 (ch3)
Entries 2175232
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

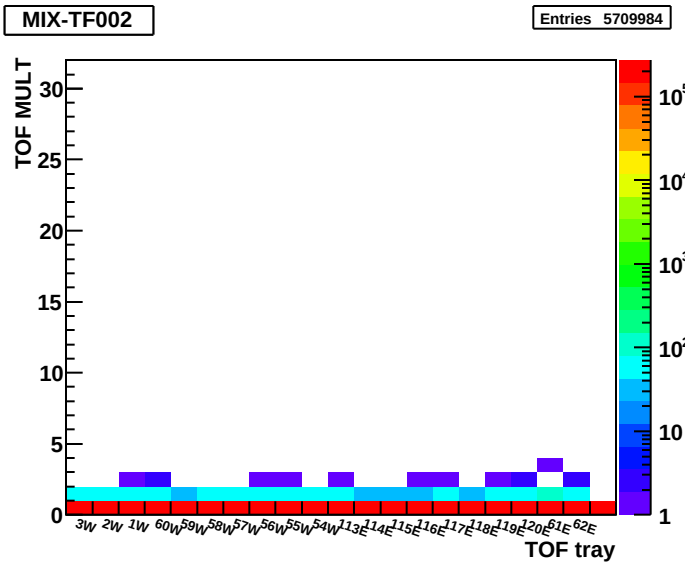

RAT board (ch4)
Entries 2175232
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

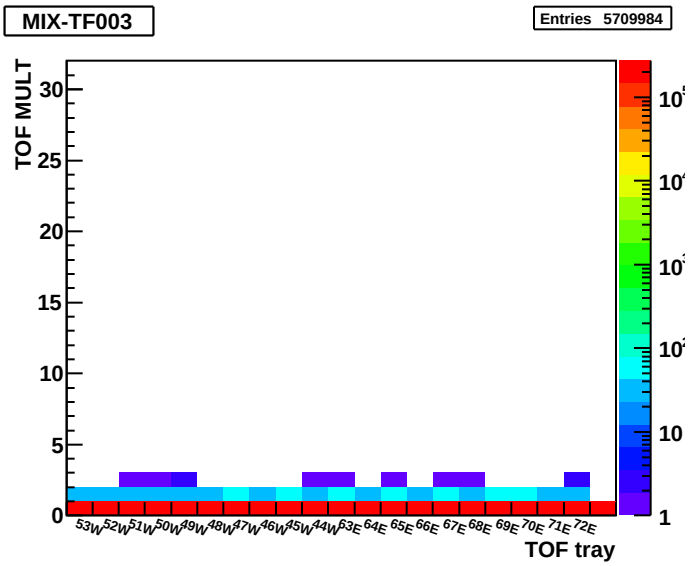

FP201 0-15 (ch5)
Entries 2175232
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

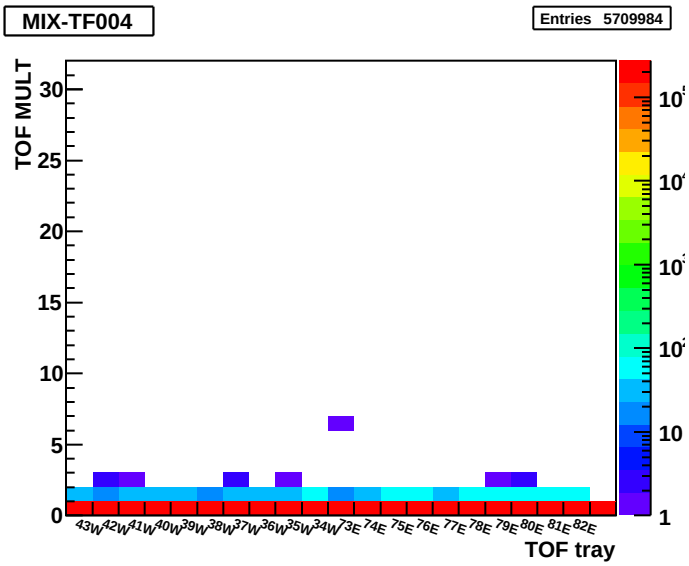

ST201 0-15 (ch6)
Entries 2175232
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

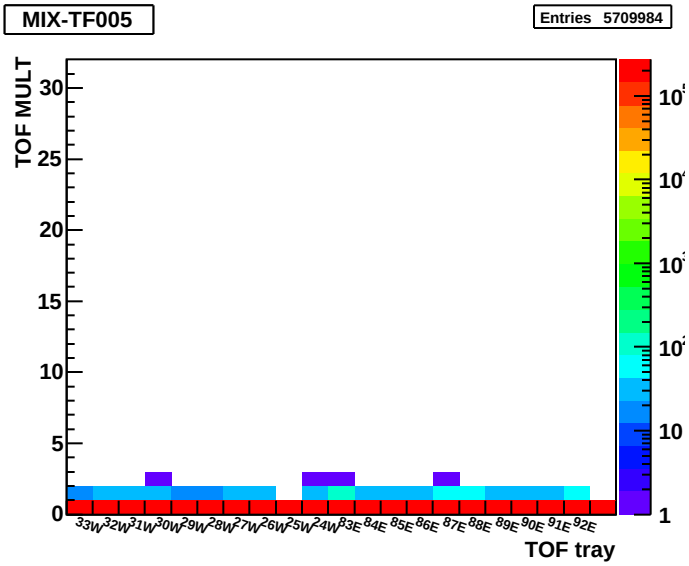

Unused (ch7)
Entries 2175232

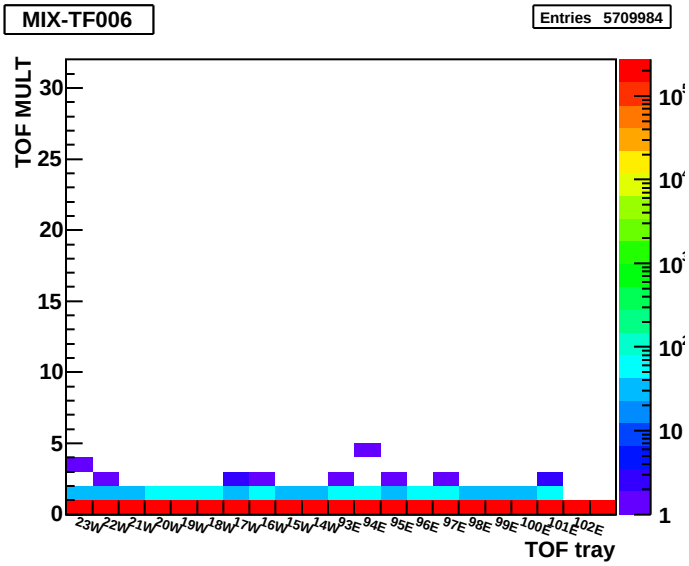

MIX-TF001
Entries 5709984
TOF MULT
TOF tray

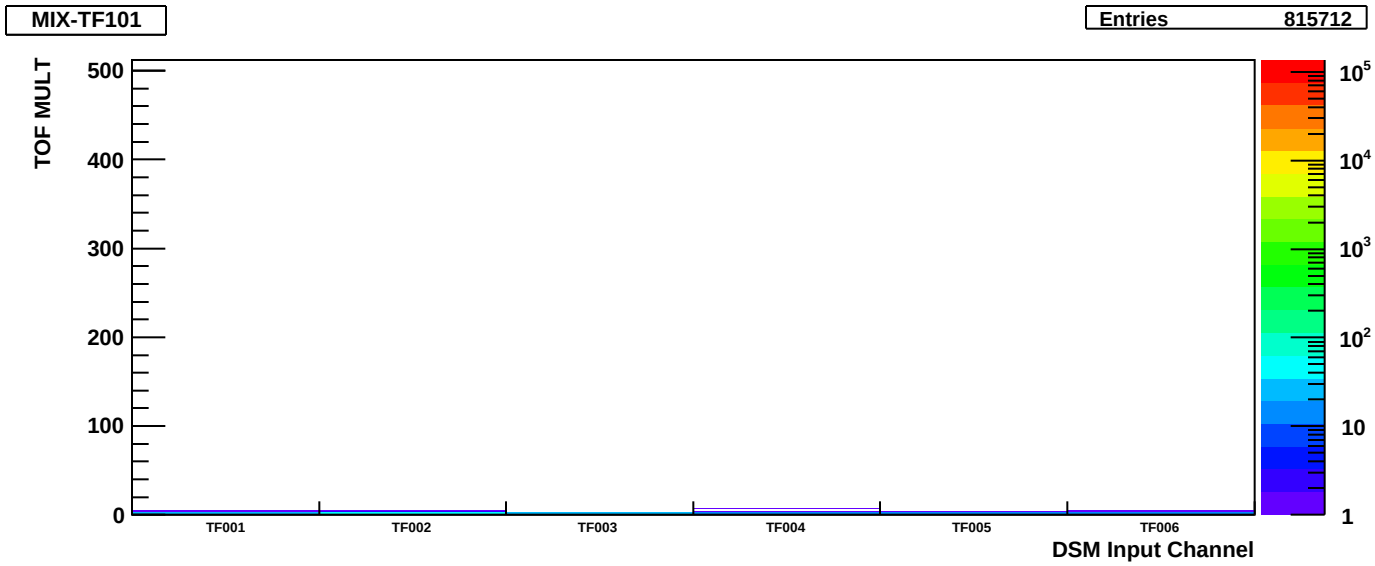

MIX-TF002
Entries 5709984
TOF MULT
TOF tray

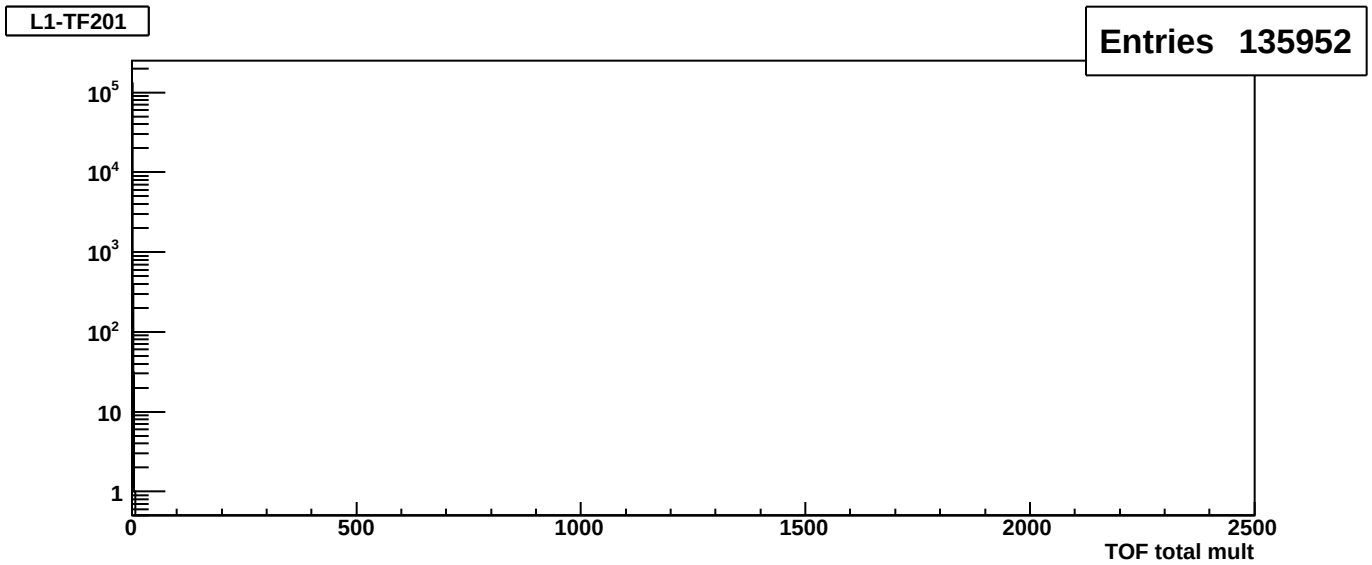

MIX-TF003
Entries 5709984
TOF MULT
TOF tray

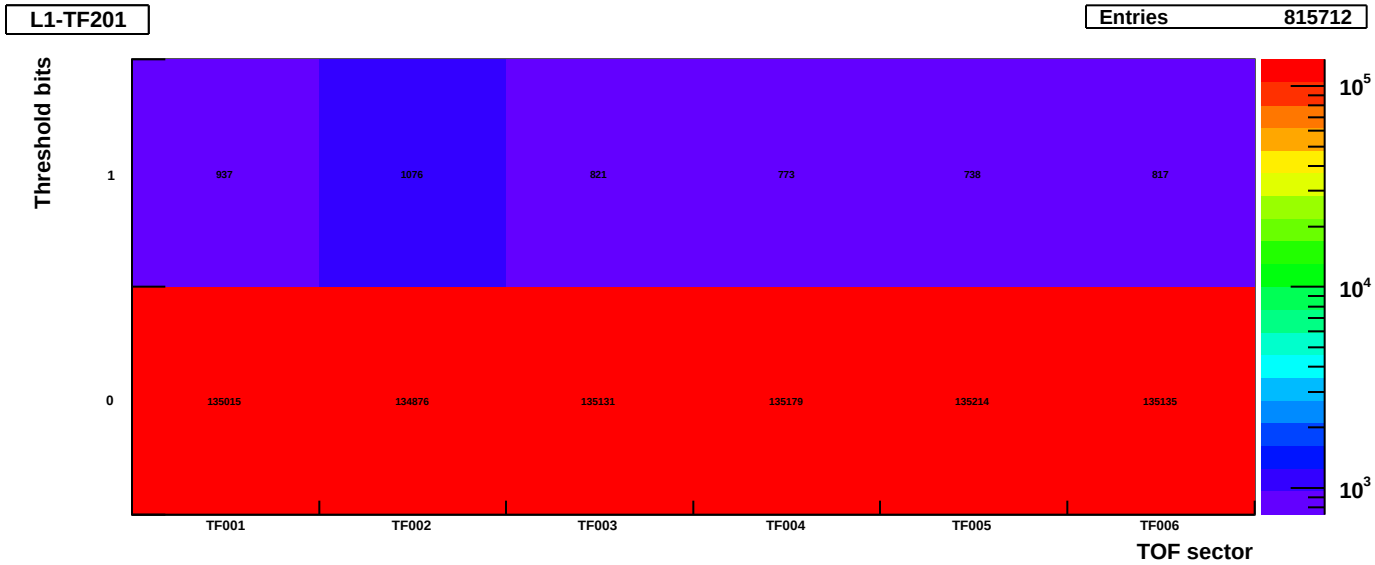

MIX-TF004
Entries 5709984
TOF MULT
TOF tray

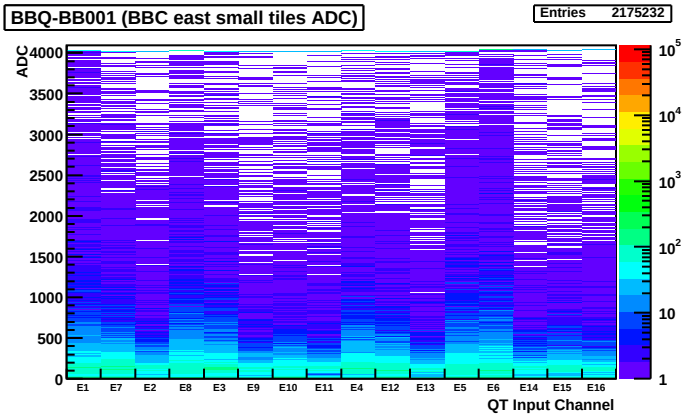

MIX-TF005
Entries 5709984
TOF MULT
TOF tray

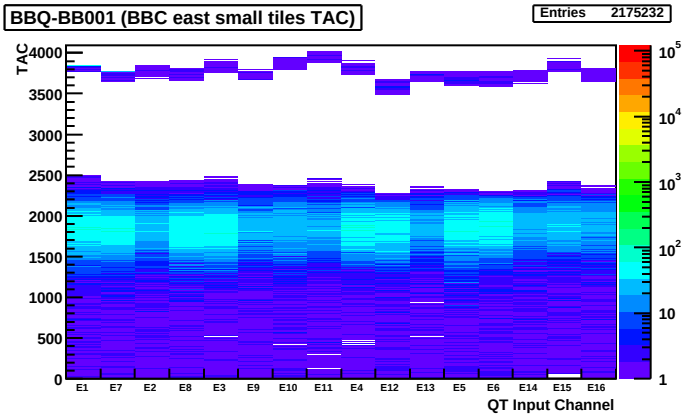

MIX-TF006
Entries 5709984
TOF MULT
TOF tray

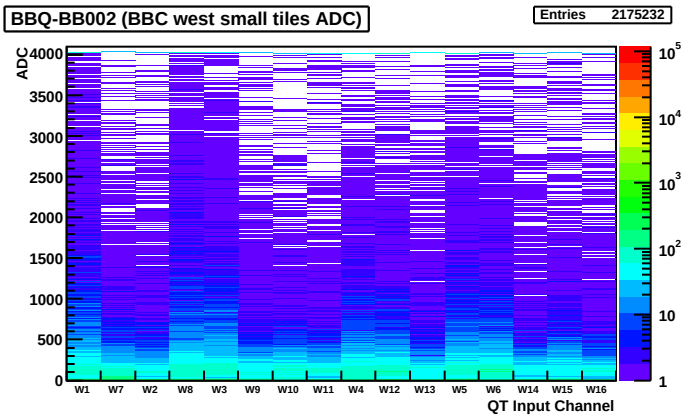

MIX-TF101
Entries 815712
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

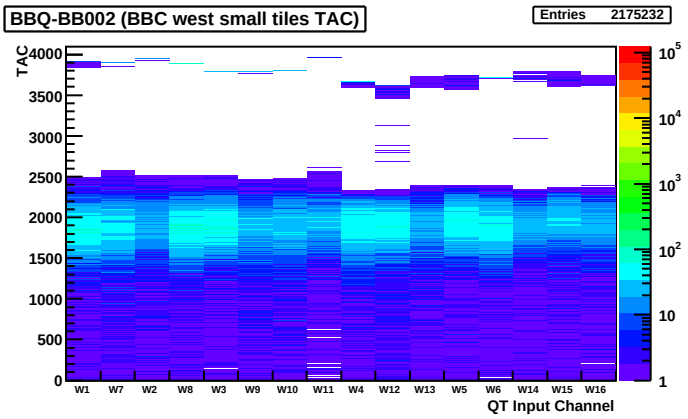

L1-TF201
Entries 135952
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

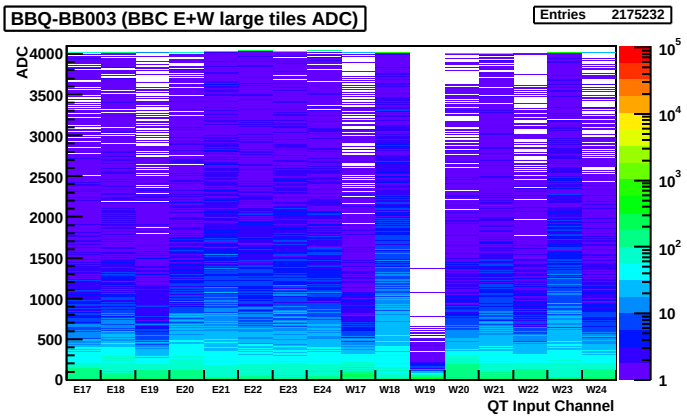

L1-TF201
Entries 815712
Threshold bits
1
0
937
1076
821
773
738
817
135015
134876
135131
135179
135214
135135
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3

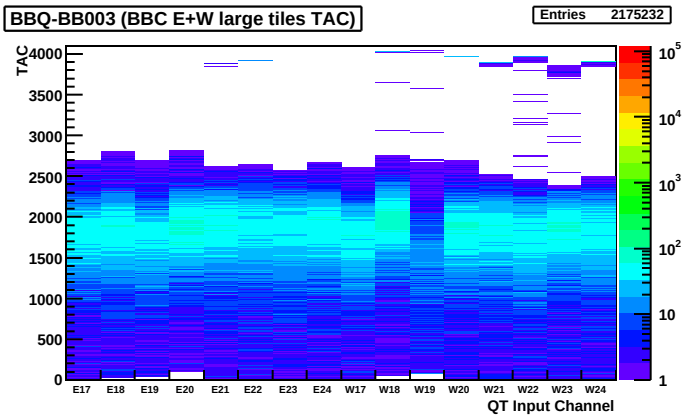

BBQ-BB001 (BBC east small tiles ADC)
Entries 2175232
ADC
QT Input Channel

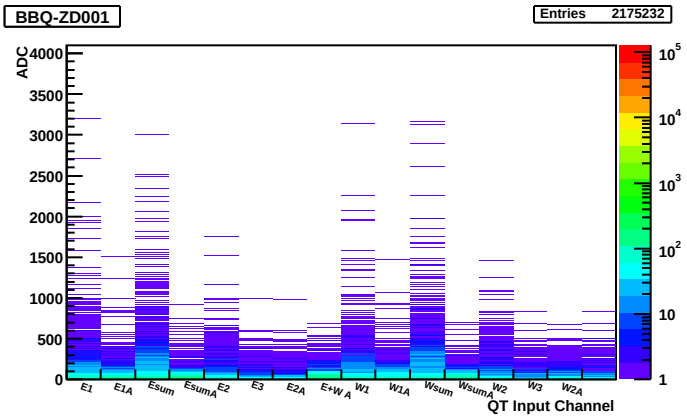

BBQ-BB001 (BBC east small tiles TAC)
Entries 2175232
TAC
QT Input Channel

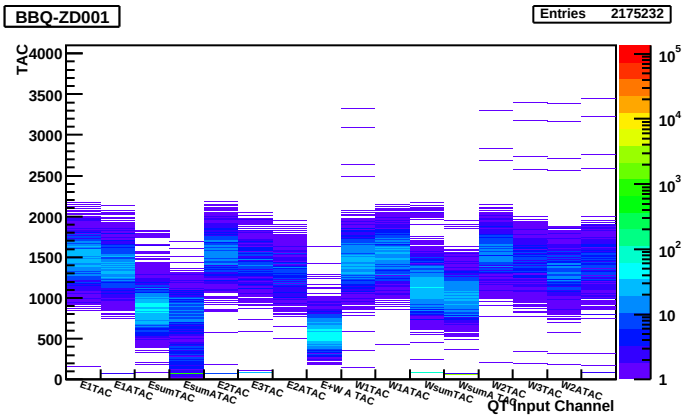

BBQ-BB002 (BBC west small tiles ADC)
Entries 2175232
ADC
QT Input Channel

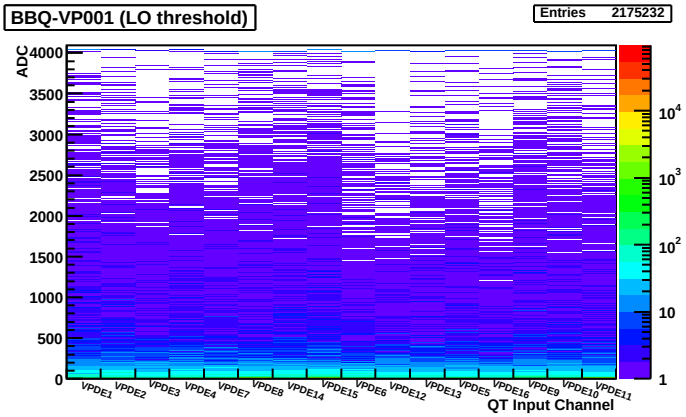

BBQ-BB002 (BBC west small tiles TAC)
Entries 2175232
TAC
QT Input Channel

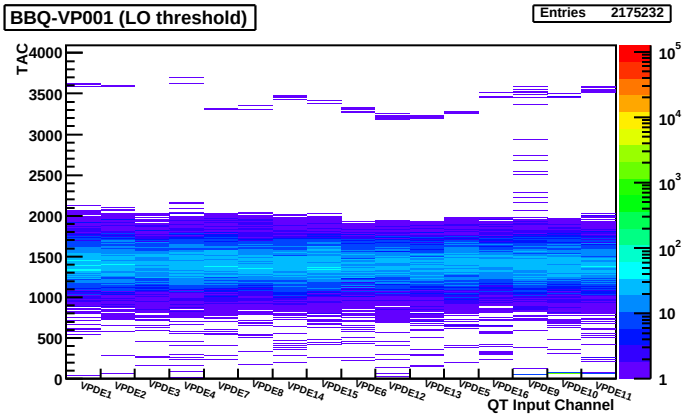

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2175232
ADC
QT Input Channel

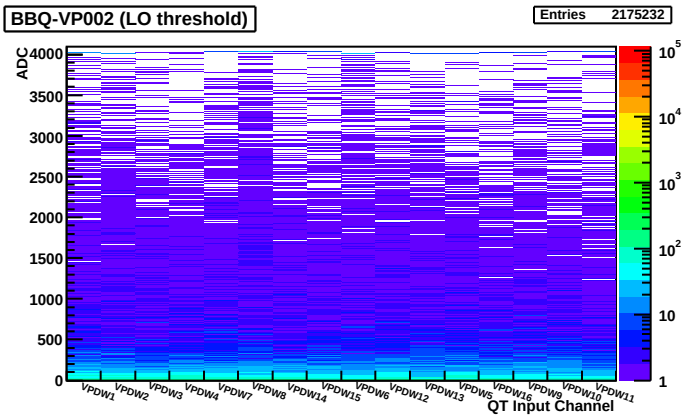

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2175232
TAC
QT Input Channel

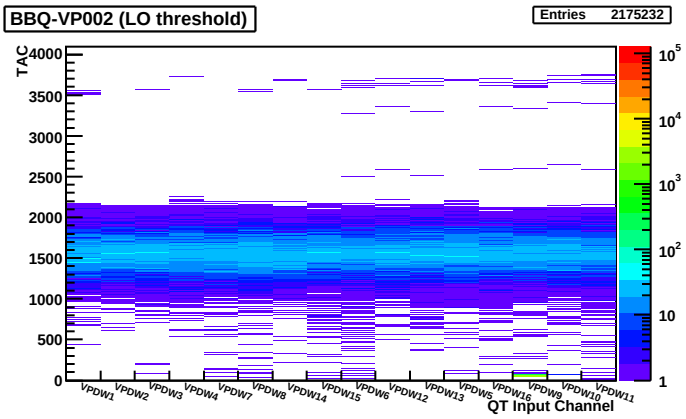

BBQ-ZD001
Entries 2175232
ADC
QT Input Channel

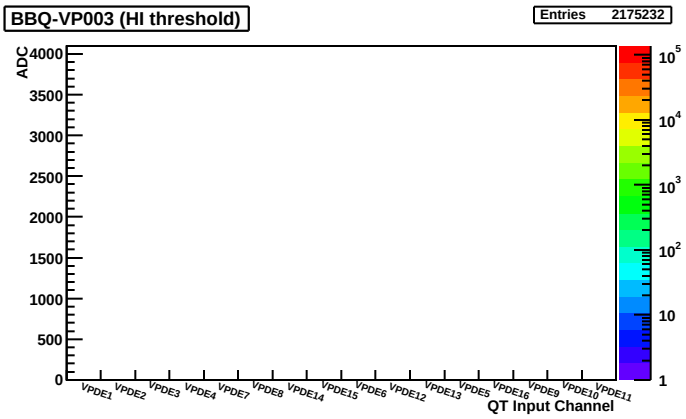

BBQ-ZD001
Entries 2175232
TAC
QT Input Channel

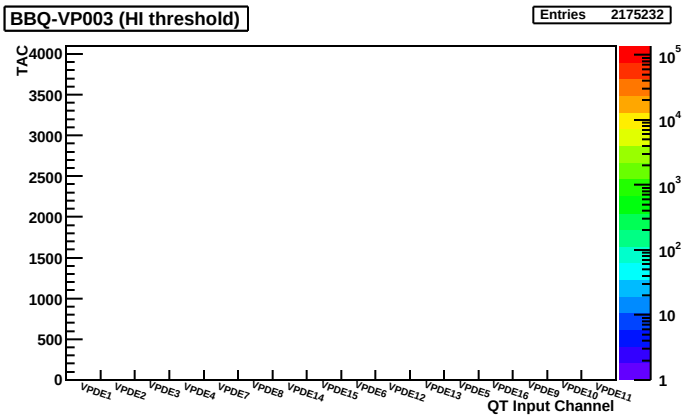

BBQ-VP001 (LO threshold)
Entries 2175232
ADC
QT Input Channel

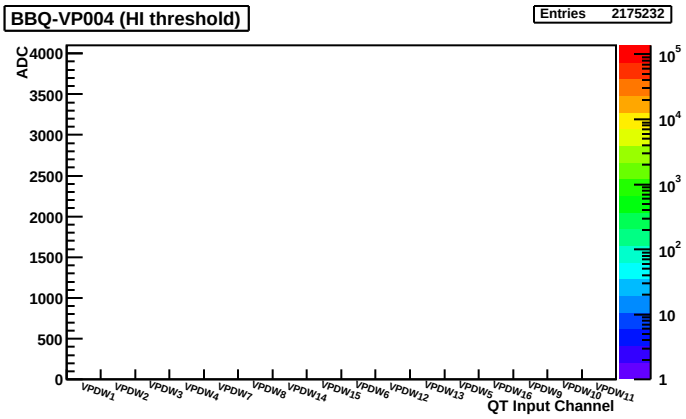

BBQ-VP001 (LO threshold)
Entries 2175232
TAC
QT Input Channel

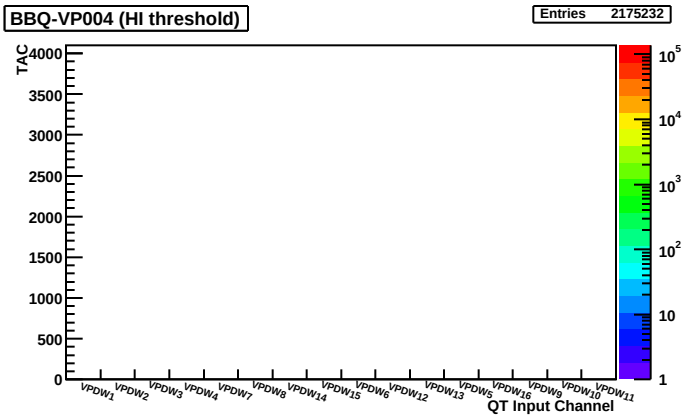

BBQ-VP002 (LO threshold)
Entries 2175232
ADC
QT Input Channel

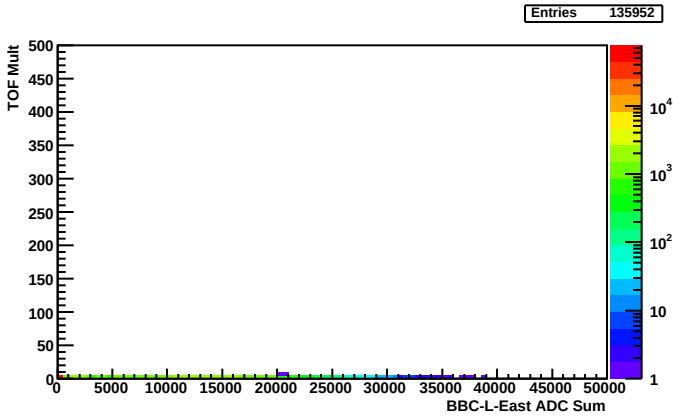

BBQ-VP002 (LO threshold)
Entries 2175232
TAC
QT Input Channel

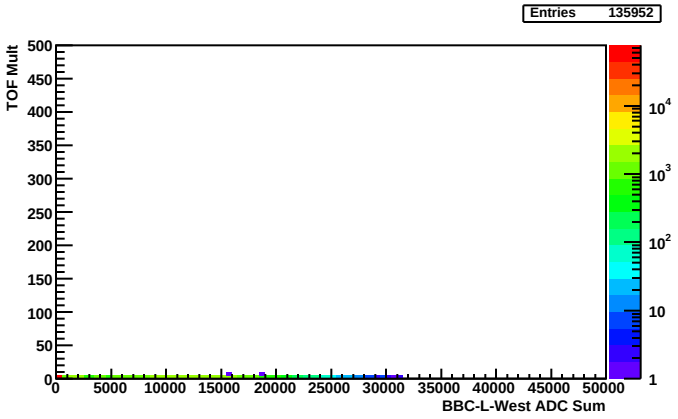

BBQ-VP003 (HI threshold)
Entries 2175232
ADC
QT Input Channel

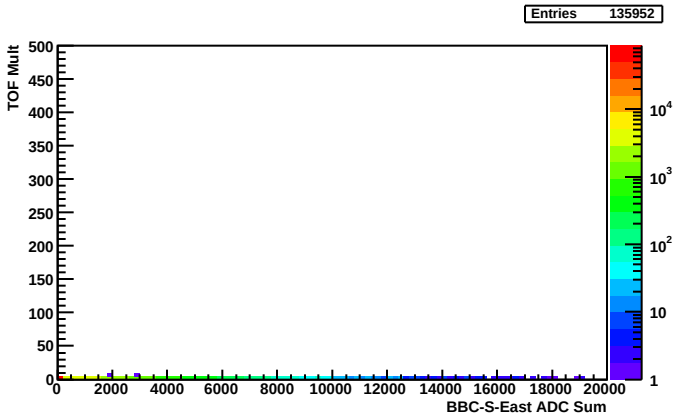

BBQ-VP003 (HI threshold)
Entries 2175232
TAC
QT Input Channel

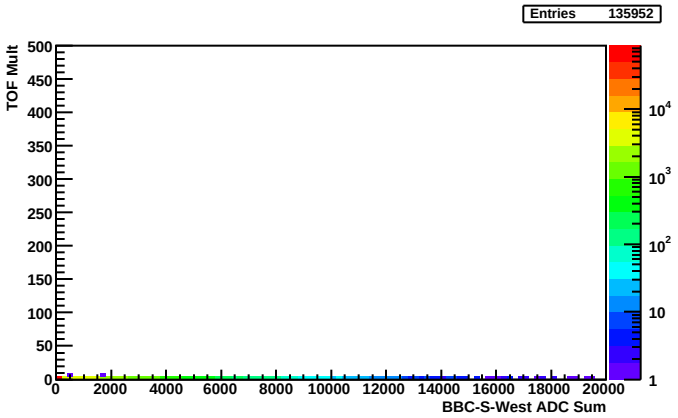

BBQ-VP004 (HI threshold)
Entries 2175232
ADC
QT Input Channel

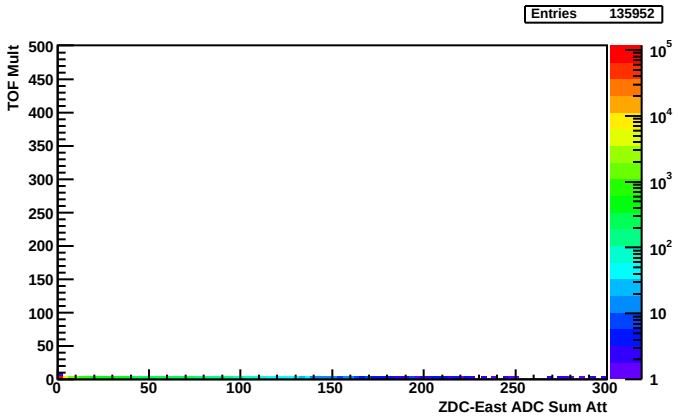

BBQ-VP004 (HI threshold)
Entries 2175232
TAC
QT Input Channel

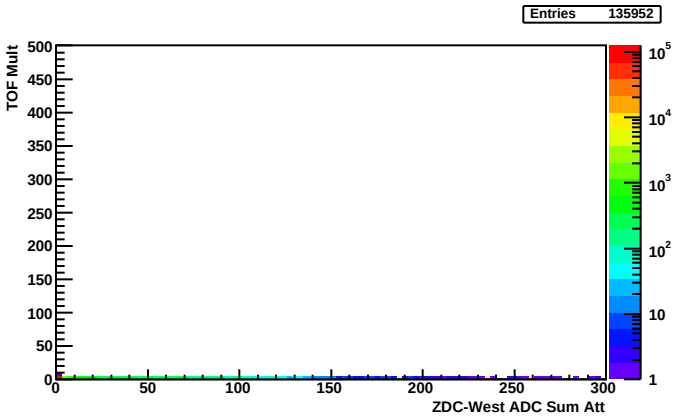

Entries 135952
TOF Mult
BBC-L-East ADC Sum

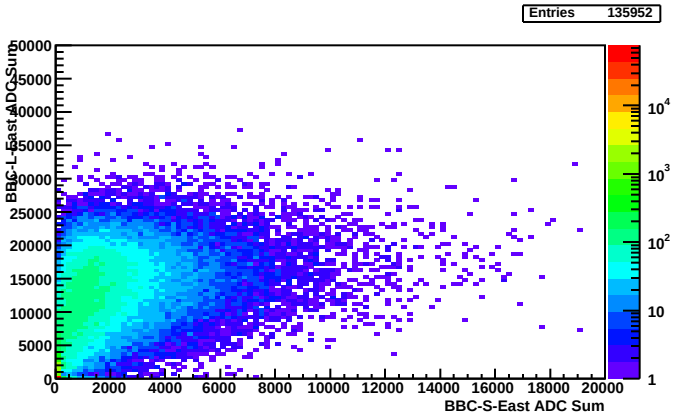

Entries 135952
TOF Mult
BBC-L-West ADC Sum

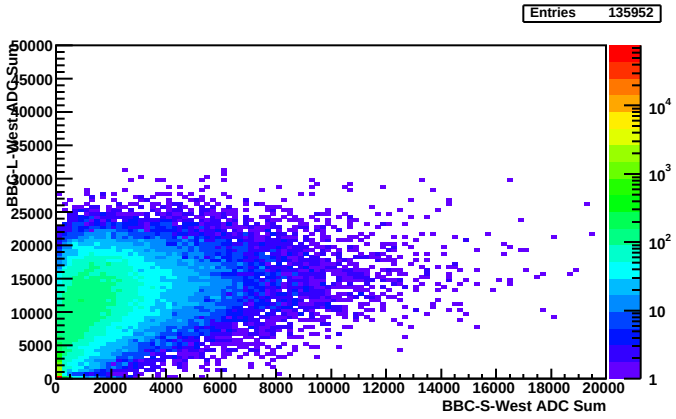

Entries 135952
TOF Mult
BBC-S-East ADC Sum

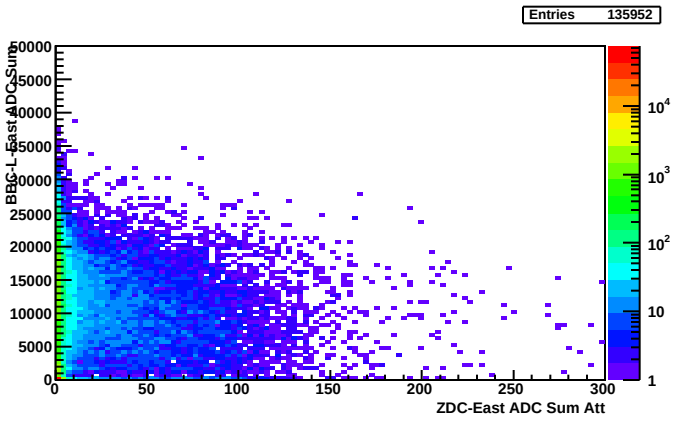

Entries 135952
TOF Mult
BBC-S-West ADC Sum

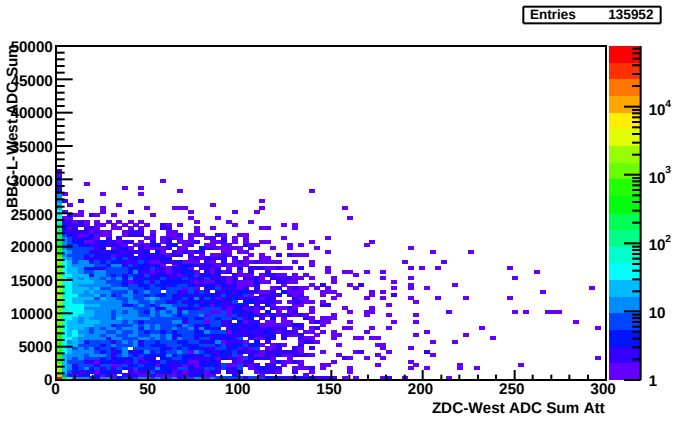

Entries 135952
TOF Mult
ZDC-East ADC Sum Att

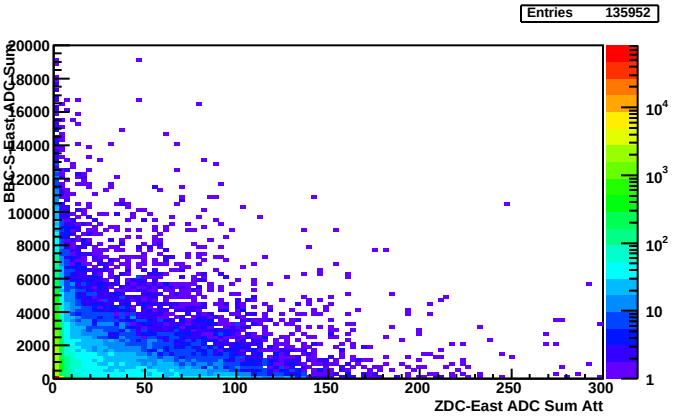

Entries 135952
TOF Mult
ZDC-West ADC Sum Att

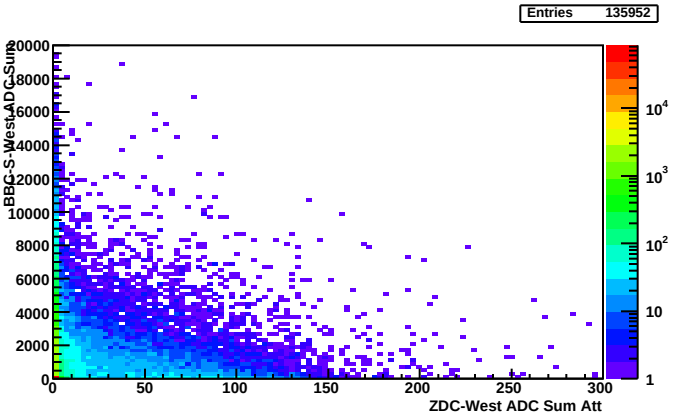

Entries 135952
BBC-L-East ADC Sum
BBC-S-East ADC Sum

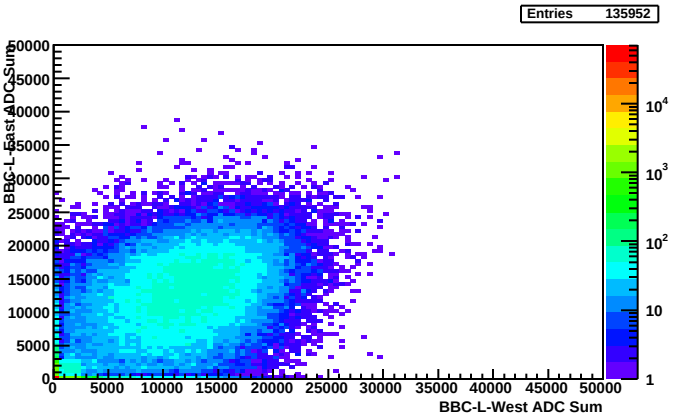

Entries 135952
BBC-L-West ADC Sum
BBC-S-West ADC Sum

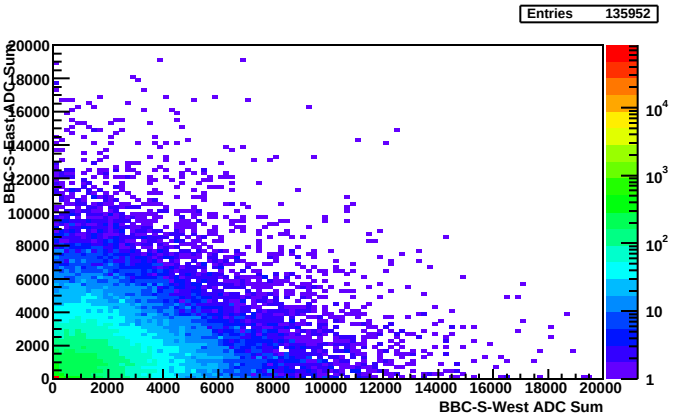

Entries 135952
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

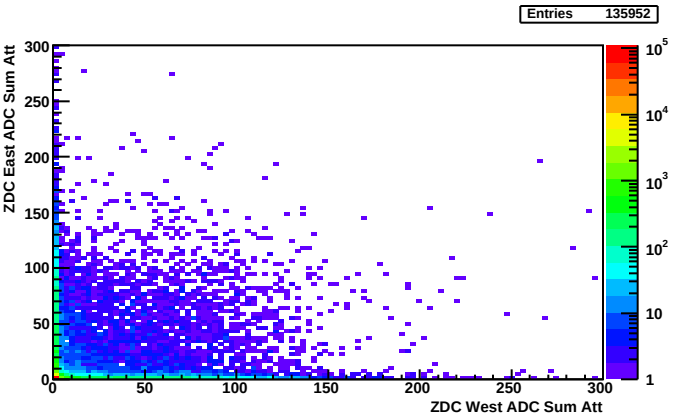

Entries 135952
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

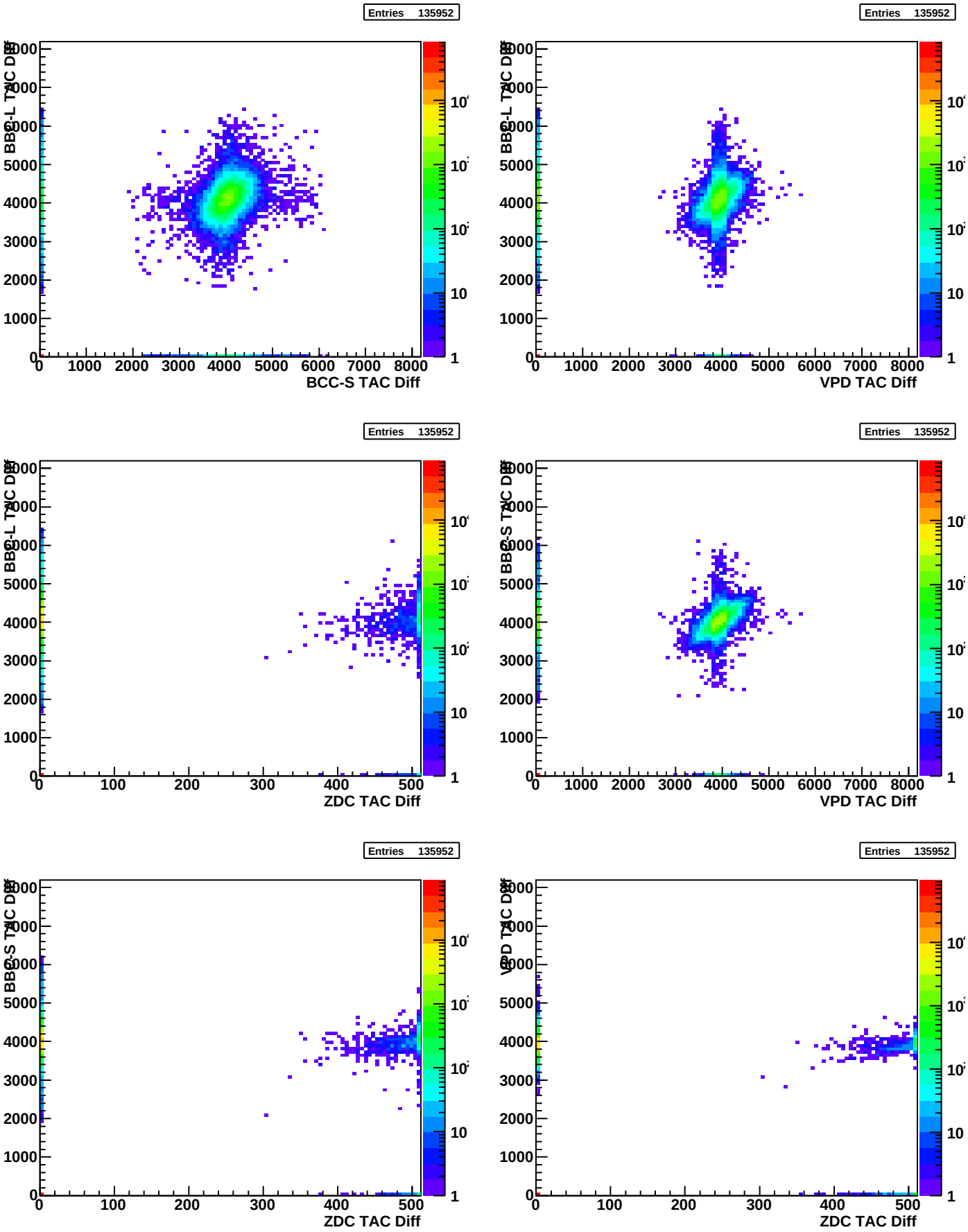

Entries 135952
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

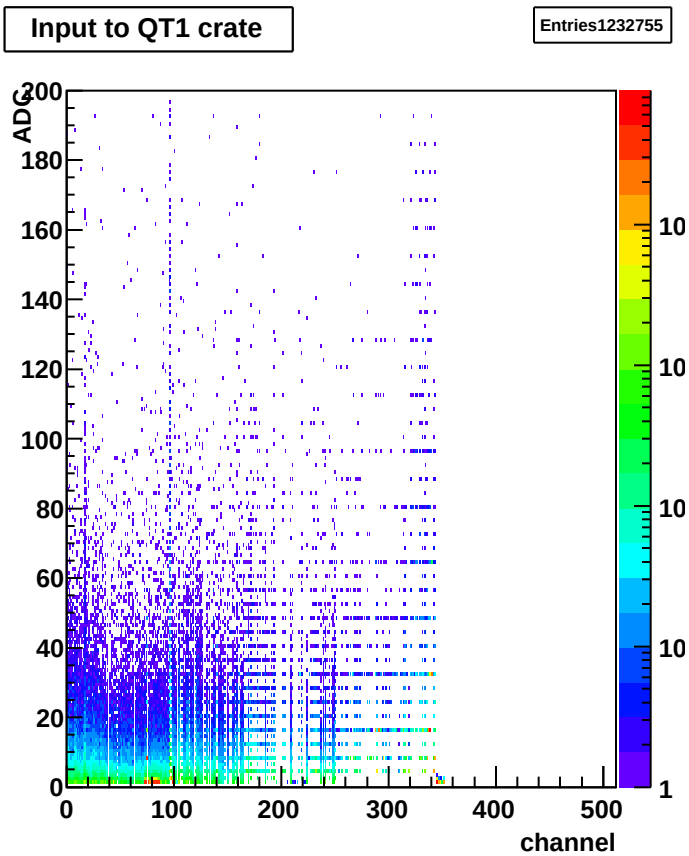

Entries 135952
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

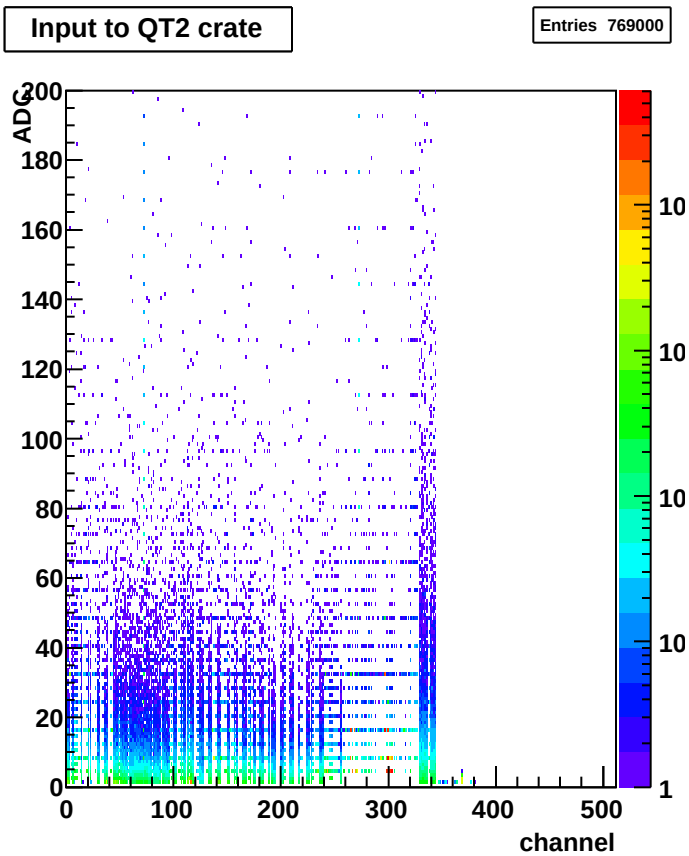

Entries 135952
BBC-L-East ADC Sum
BBC-L-West ADC Sum

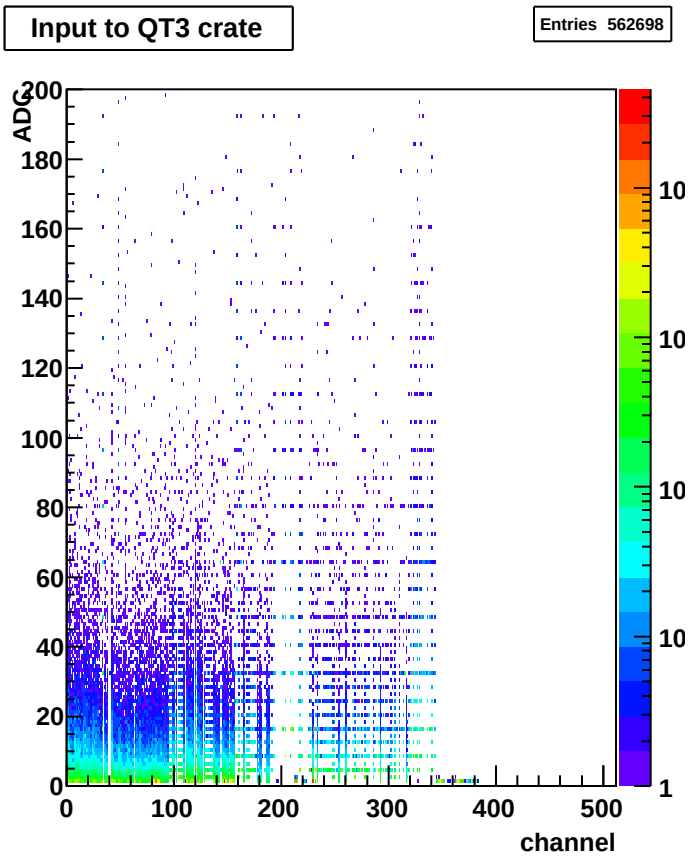

Entries 135952
BBC-S-East ADC Sum
BBC-S-West ADC Sum

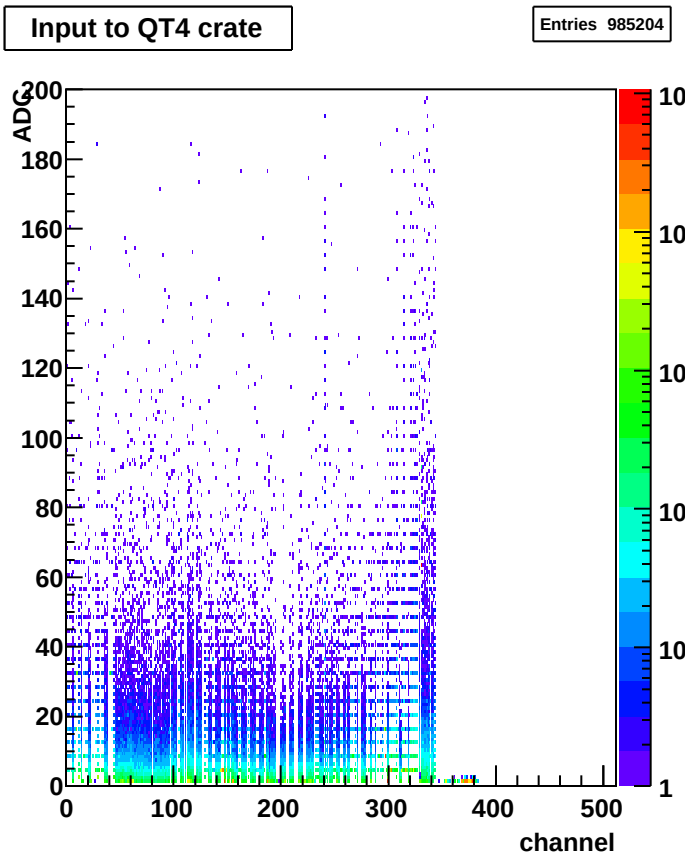

Entries 135952
ZDC East ADC Sum Att
ZDC West ADC Sum Att

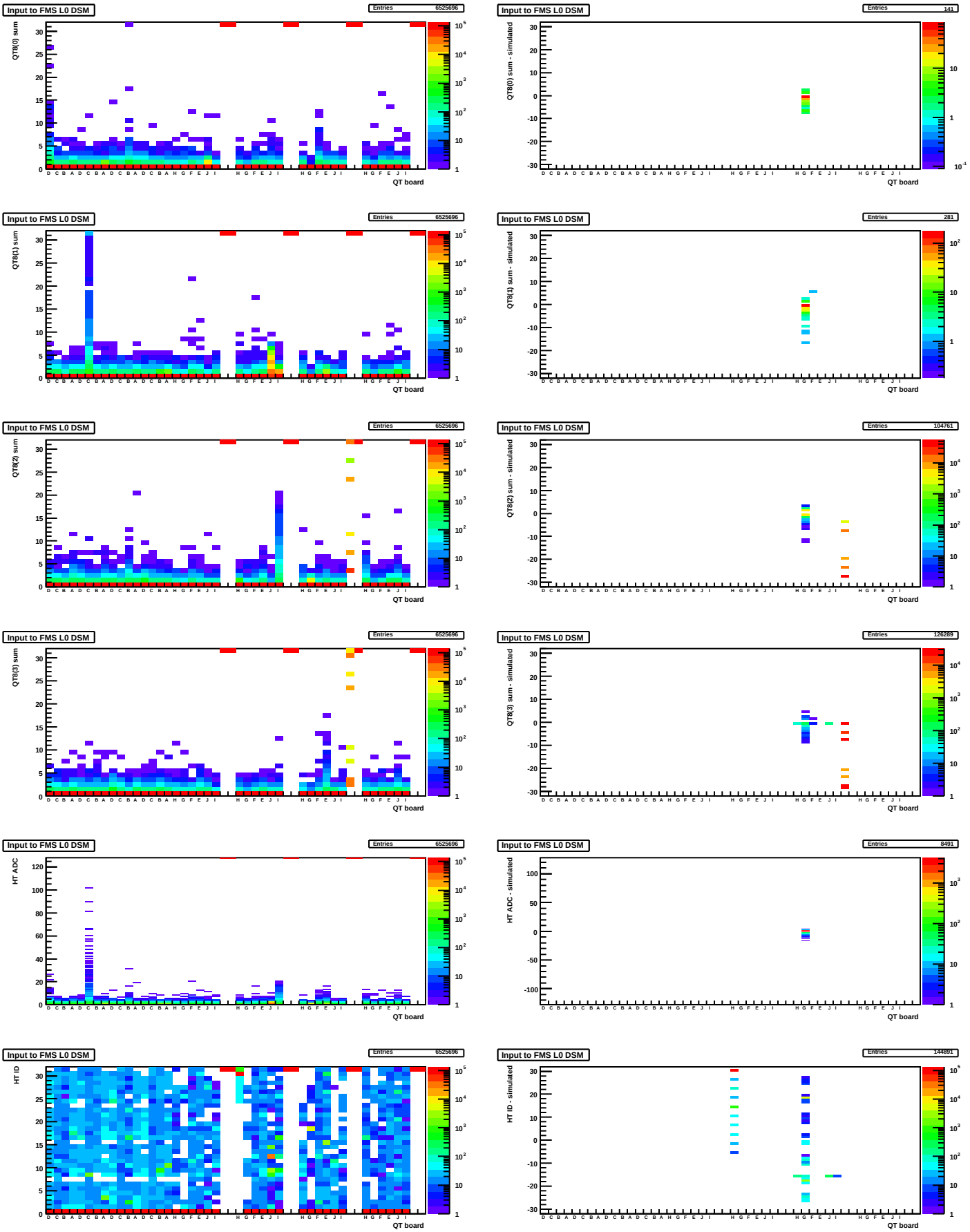

Entries 135952
BBC-L TAC Diff
BCC-S TAC Diff
Entries 135952
BBC-L TAC Diff
VPD TAC Diff
Entries 135952
BBC-L TAC Diff
ZDC TAC Diff
Entries 135952
BBC-S TAC Diff
VPD TAC Diff
Entries 135952
BBC-S TAC Diff
ZDC TAC Diff
Entries 135952
VPD TAC Diff
ZDC TAC Diff

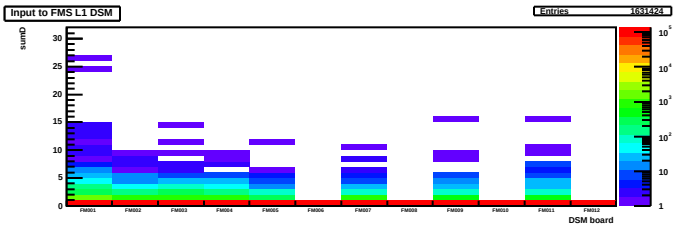

Input to QT1 crate
Entries1232755
ADC
channel

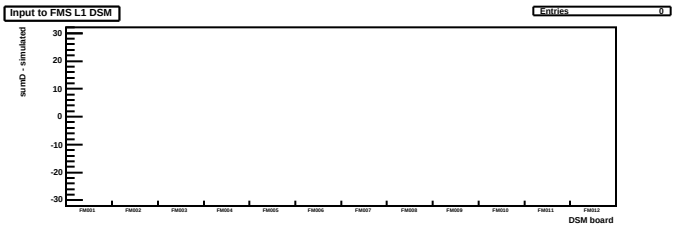

Input to QT2 crate
Entries 769000
ADC
channel

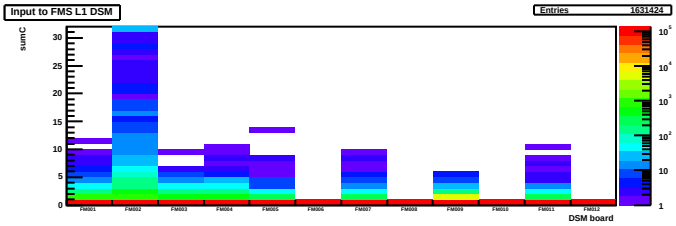

Input to QT3 crate
Entries 562698
ADC
channel

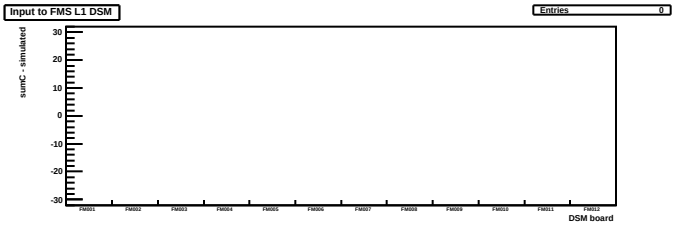

Input to QT4 crate
Entries 985204
ADC
channel

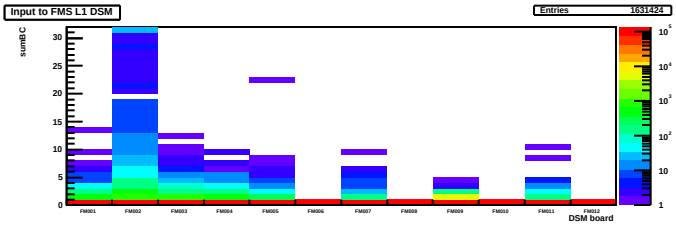

Input to FMS L0 DSM
Entries 6525696
QT8(0) sum
QT8(1) sum
QT8(2) sum
QT8(3) sum
HT ADC
HT ID
Entries 141
Entries 281
Entries 104761
Entries 126289
Entries 8491
Entries 144891
QT8(0) sum - simulated
QT8(1) sum - simulated
QT8(2) sum - simulated
QT8(3) sum - simulated
HT ADC - simulated
HT ID - simulated
QT board

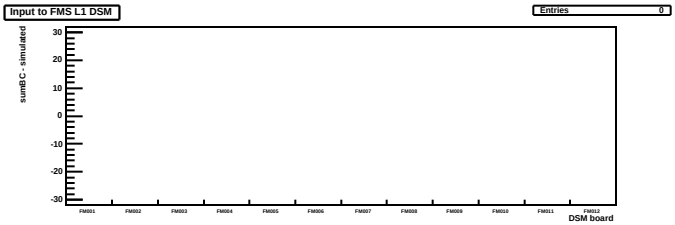

Input to FMS L1 DSM
Entries 1631424
sumD
DSM board

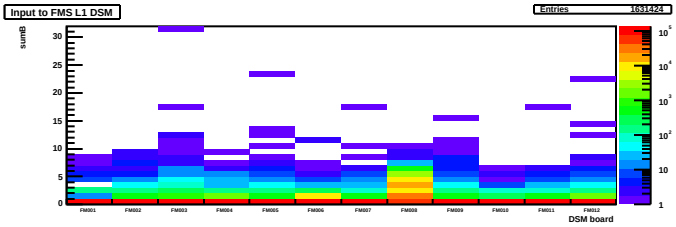

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

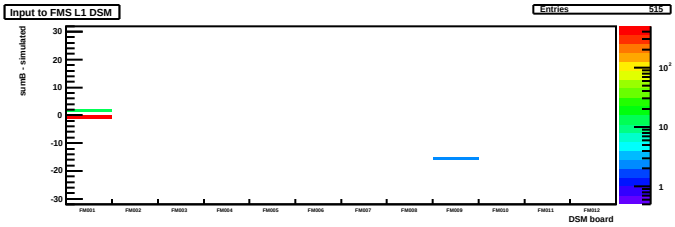

Input to FMS L1 DSM
Entries 1631424
sumC
DSM board

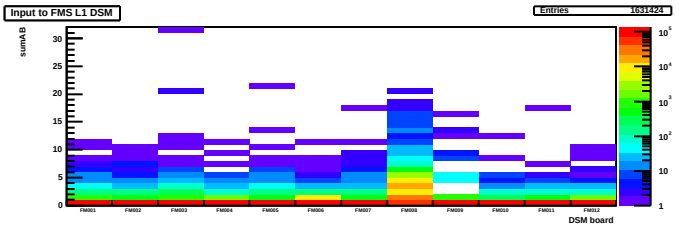

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

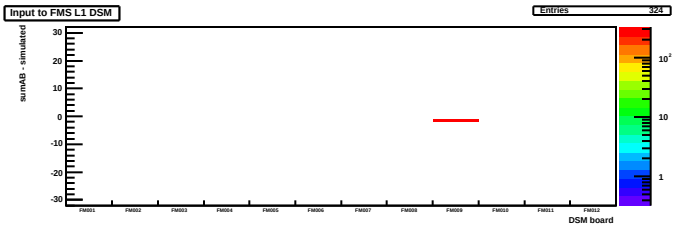

Input to FMS L1 DSM
Entries 1631424
sumBC
DSM board

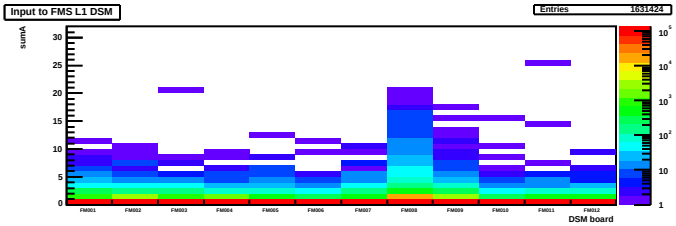

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

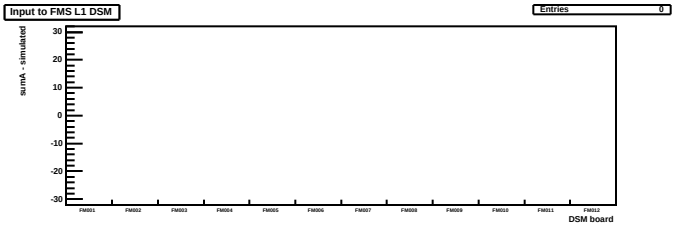

Input to FMS L1 DSM
Entries 1631424
sumB
DSM board

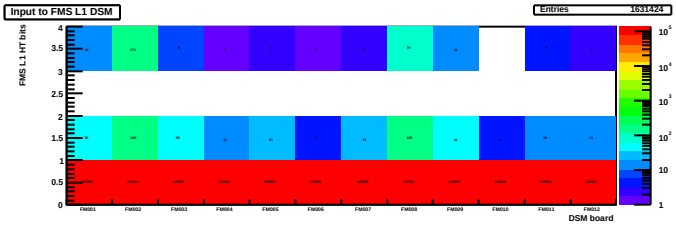

Input to FMS L1 DSM
Entries 515
sumB - simulated
DSM board

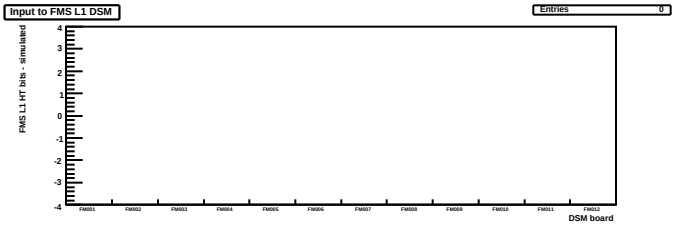

Input to FMS L1 DSM
Entries 1631424
sumAB
DSM board

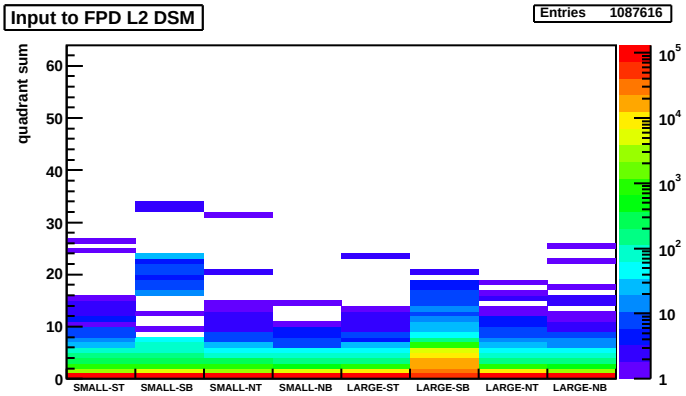

Input to FMS L1 DSM
Entries 324
sumAB - simulated
DSM board

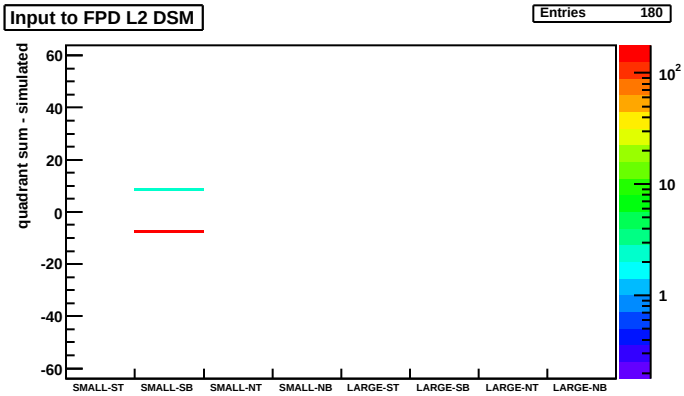

Input to FMS L1 DSM
Entries 1631424
sumA
DSM board

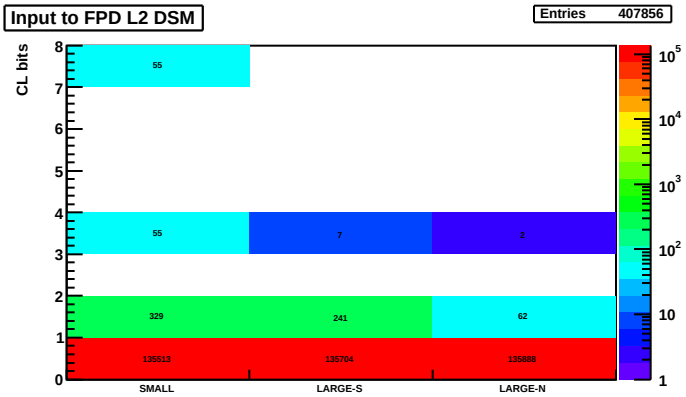

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

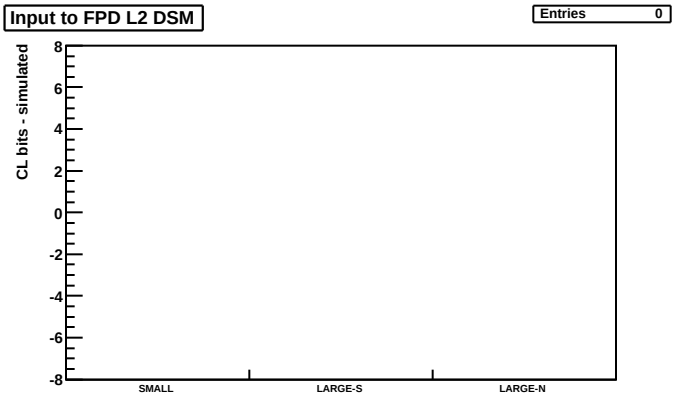

Input to FMS L1 DSM
Entries 1631424
FMS L1 HT bits
DSM board

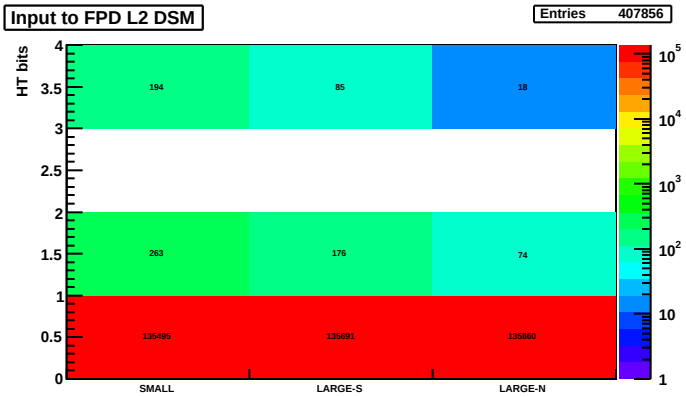

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

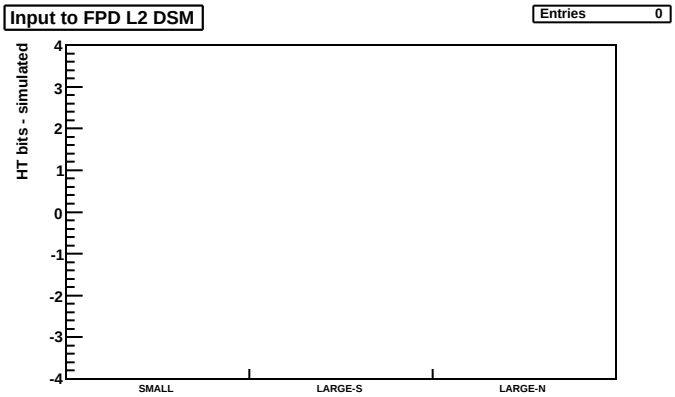

Input to FPD L2 DSM
Entries 1087616
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

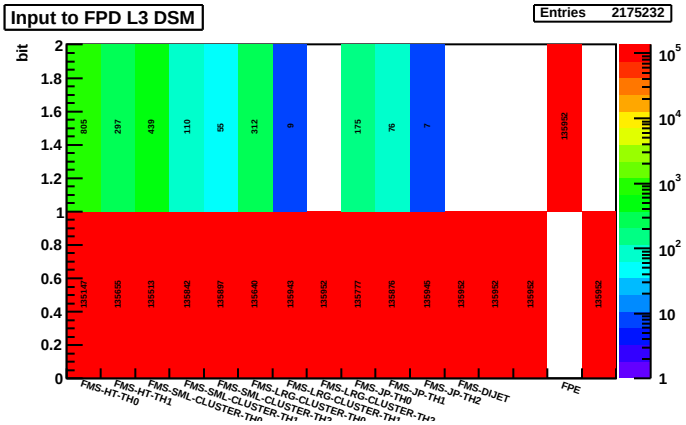

Input to FPD L2 DSM
Entries 180
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

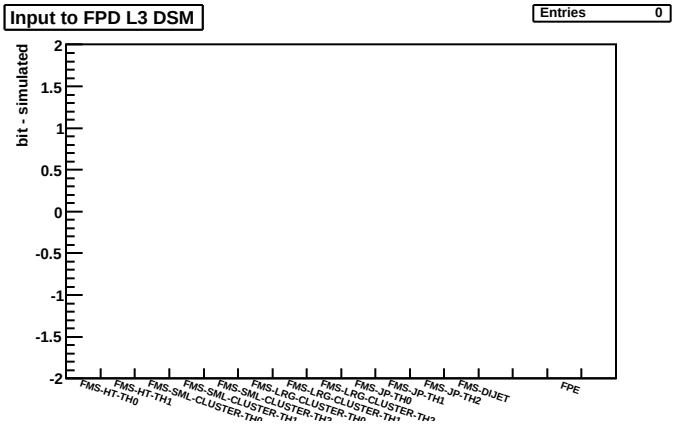

Input to FPD L2 DSM
Entries 407856
CL bits
55
55
7
2
329
241
62
135513
135704
135888
SMALL
LARGE-S
LARGE-N

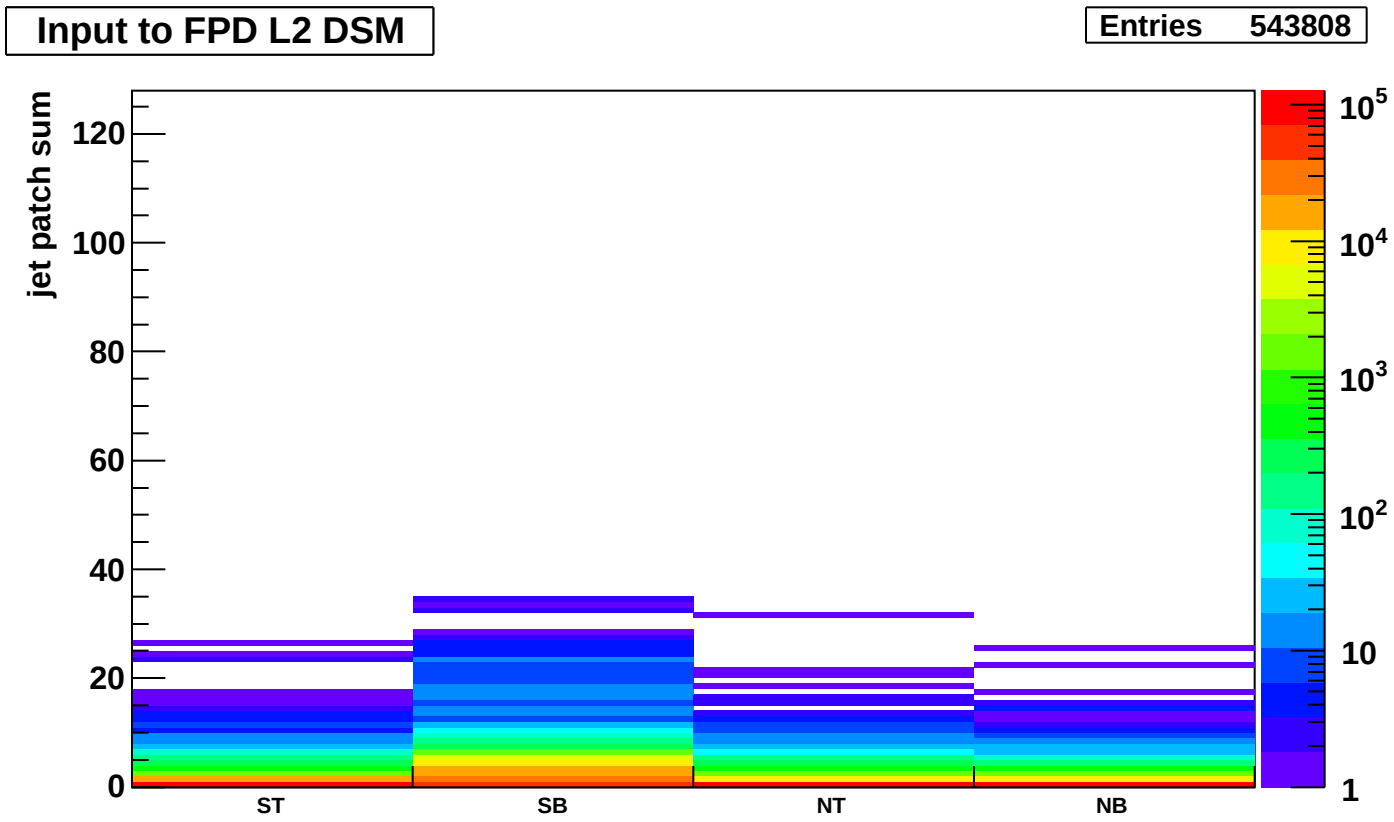

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

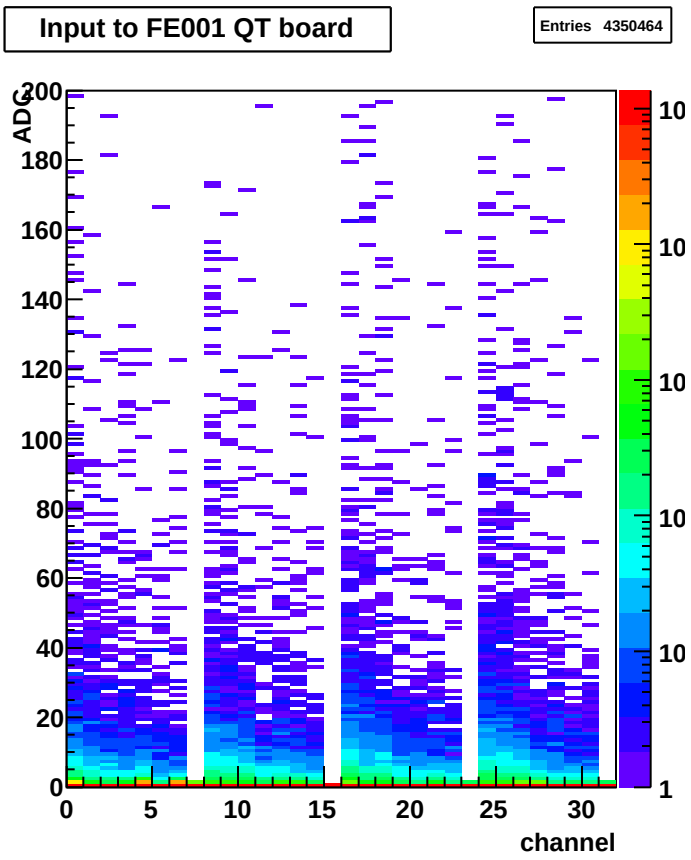

Input to FPD L2 DSM
Entries 407856
HT bits
194
85
18
263
176
74
135495
135691
135860
SMALL
LARGE-S
LARGE-N

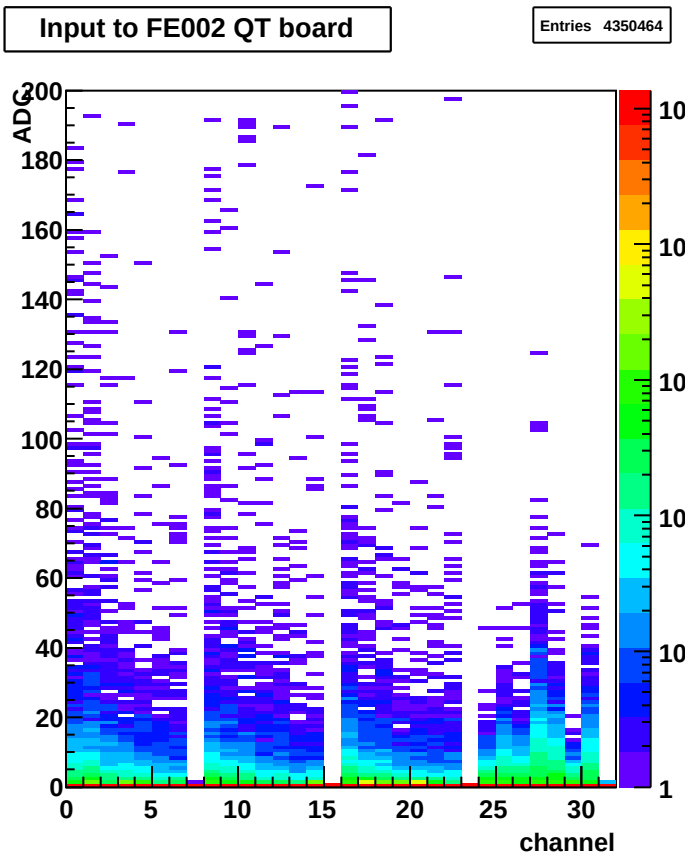

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

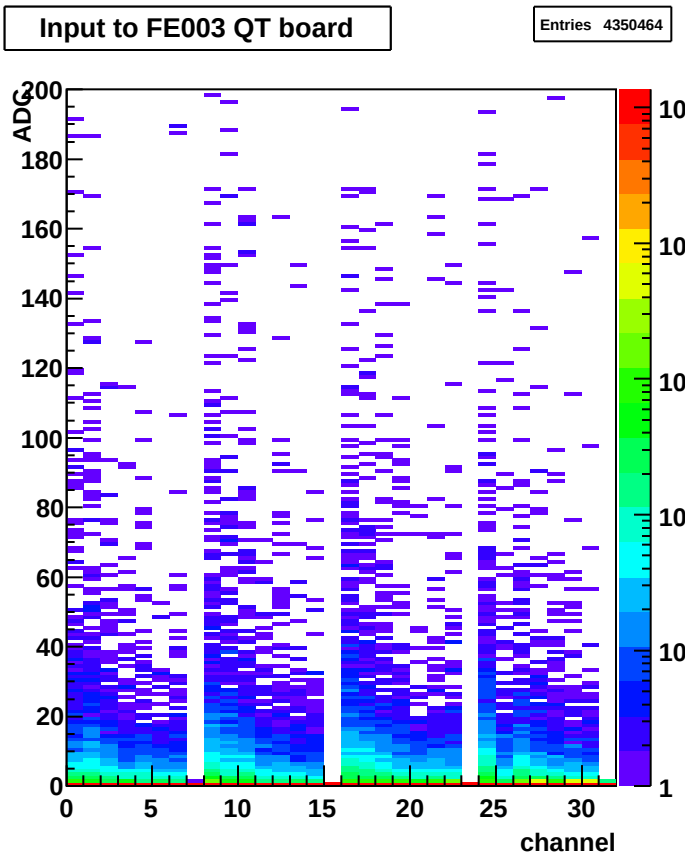

Input to FPD L3 DSM
Entries 2175232
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

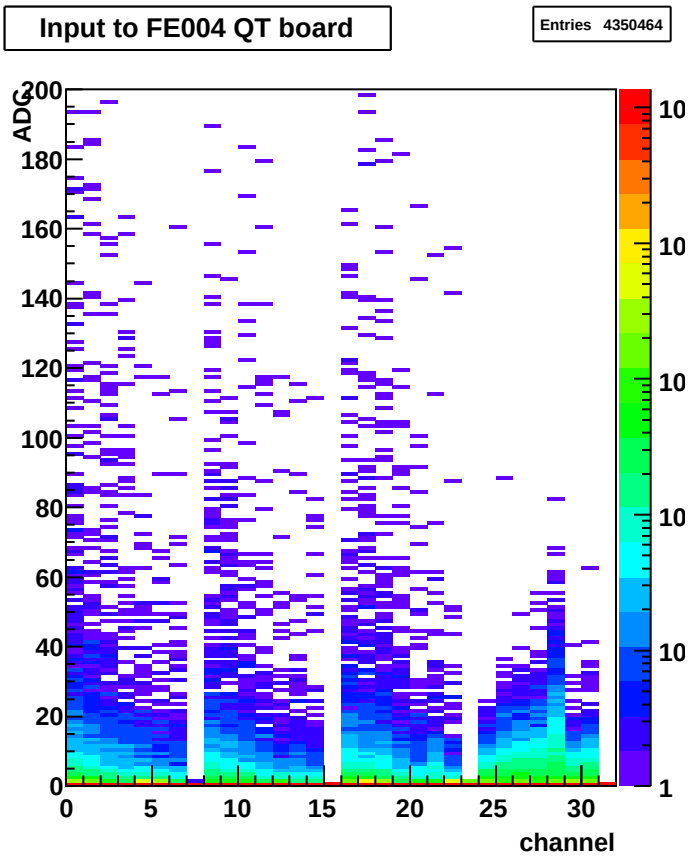

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

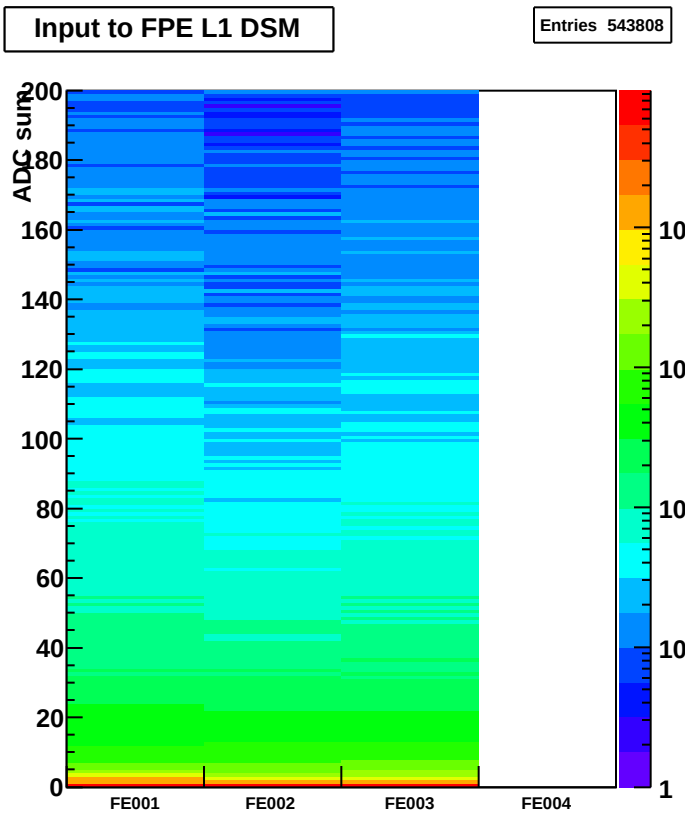

Input to FPD L2 DSM
Entries 543808
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

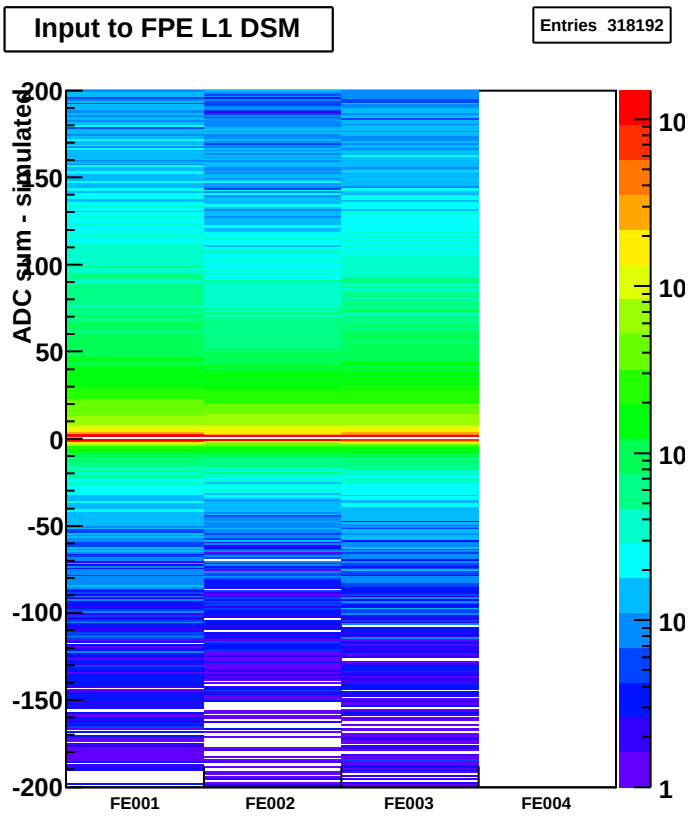

Input to FE001 QT board
Entries 4350464
ADC
channel

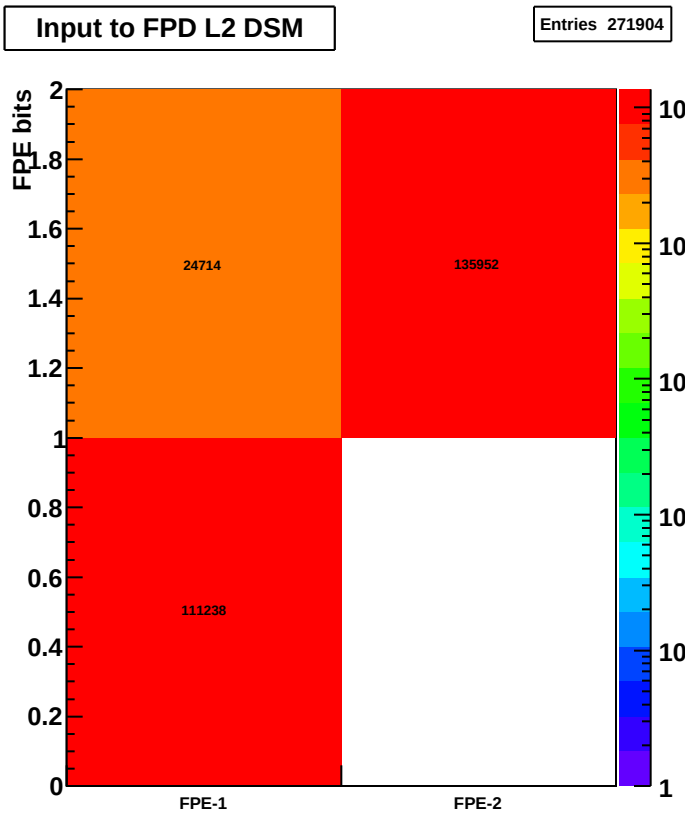

Input to FE002 QT board
Entries 4350464
ADC
channel

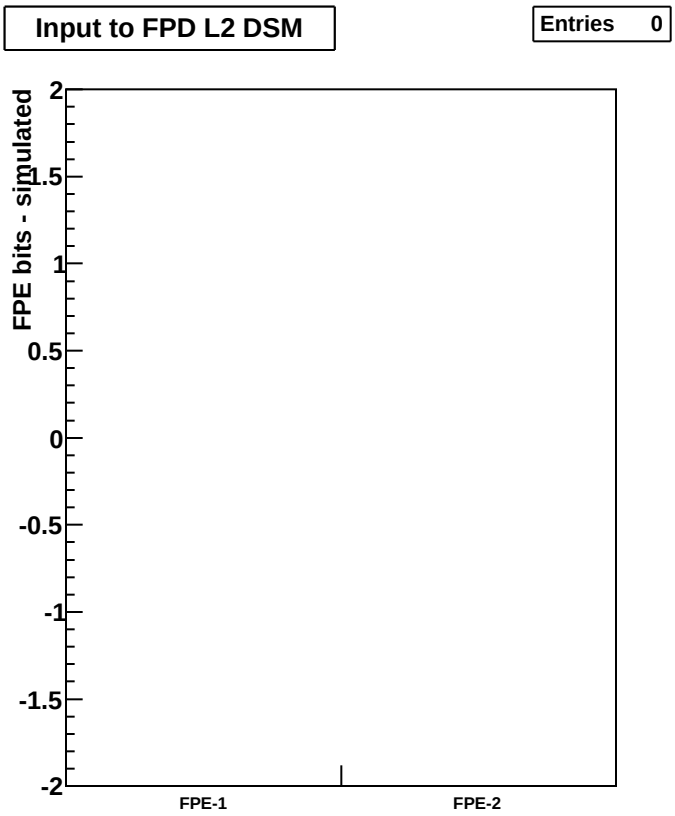

Input to FE003 QT board
Entries 4350464
ADC
channel


Input to FE004 QT board
Entries 4350464
ADC
channel


Input to FPE L1 DSM
Entries 543808
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 318192
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 271904
FPE bits
24714
135952
111238
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2