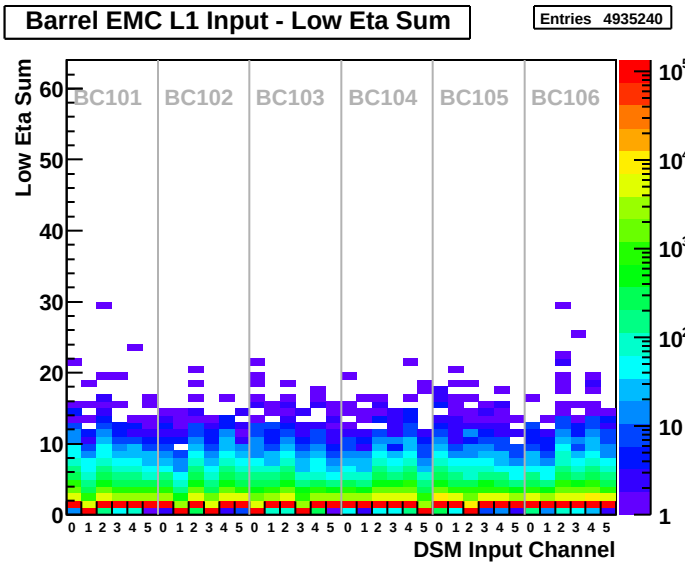
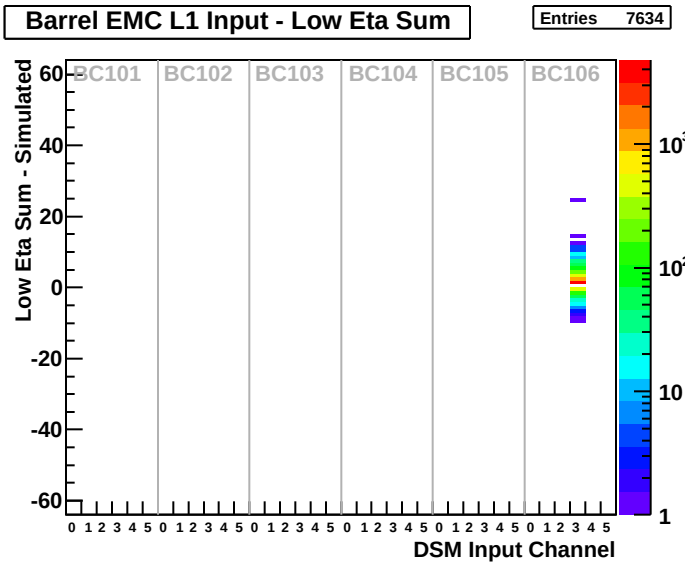
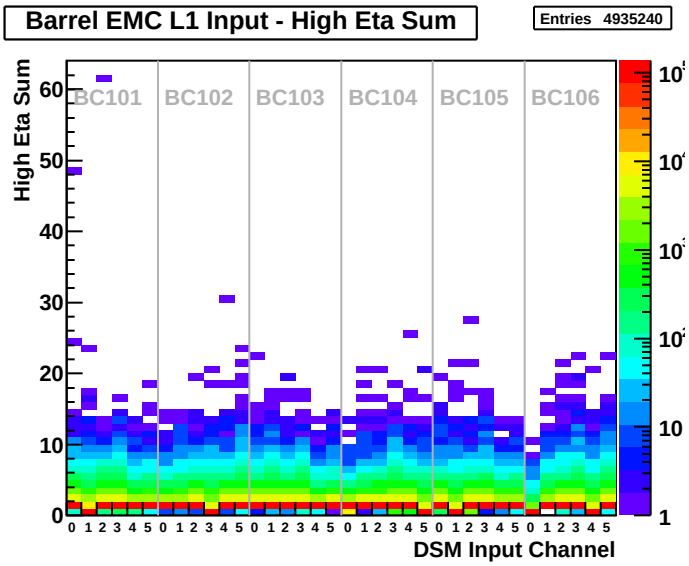
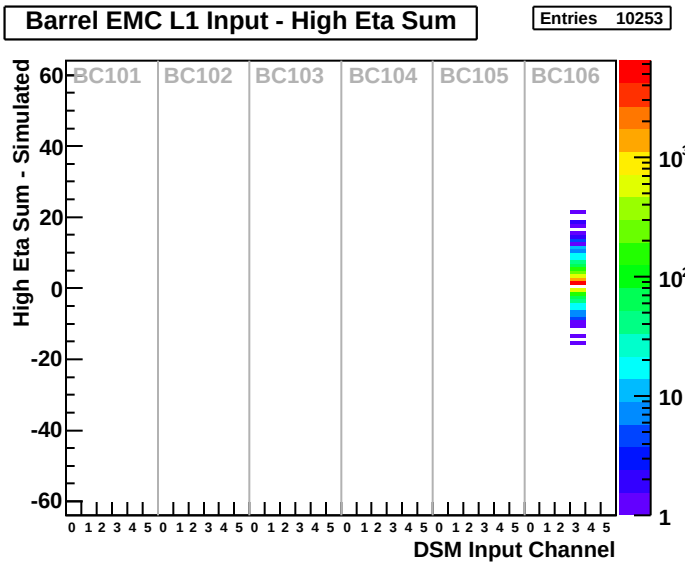
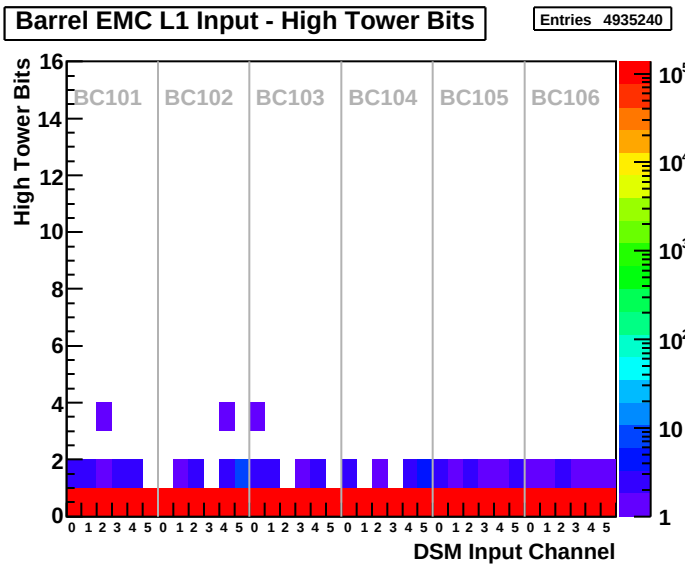
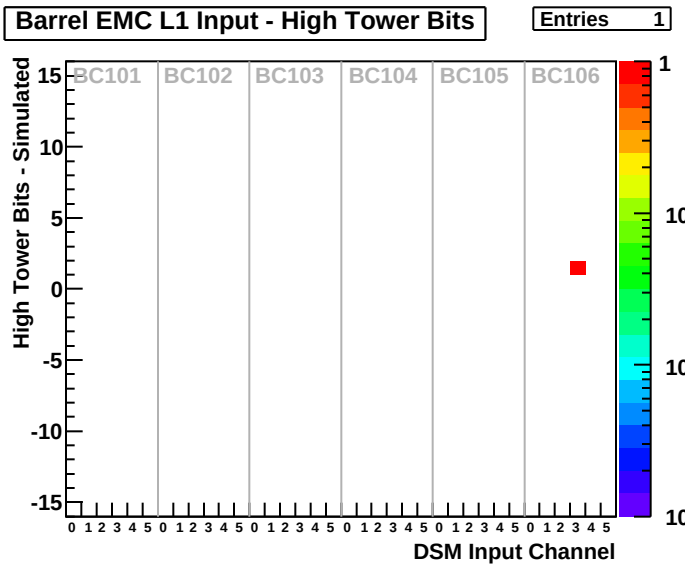

Barrel EMC L1 Input - Low Eta Sum
Entries 4935240
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

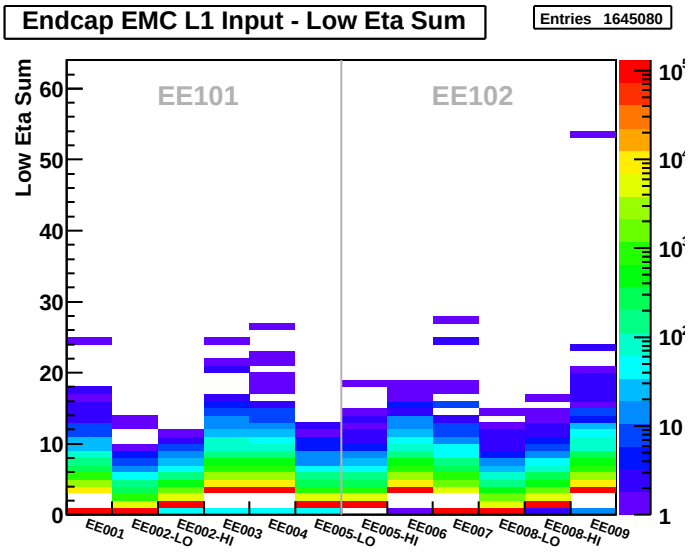

Barrel EMC L1 Input - Low Eta Sum
Entries 7634
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

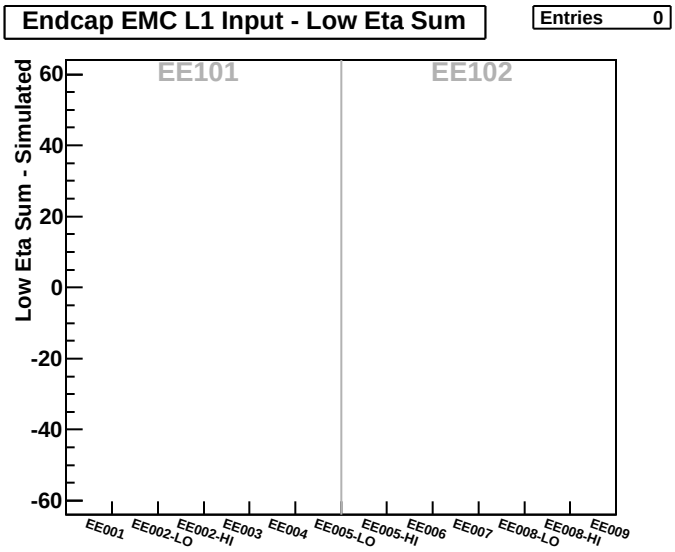

Barrel EMC L1 Input - High Eta Sum
Entries 4935240
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

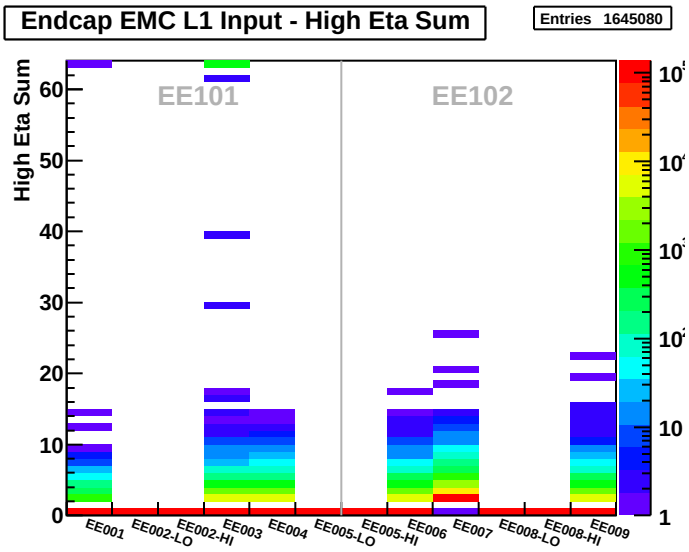

Barrel EMC L1 Input - High Eta Sum
Entries 10253
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

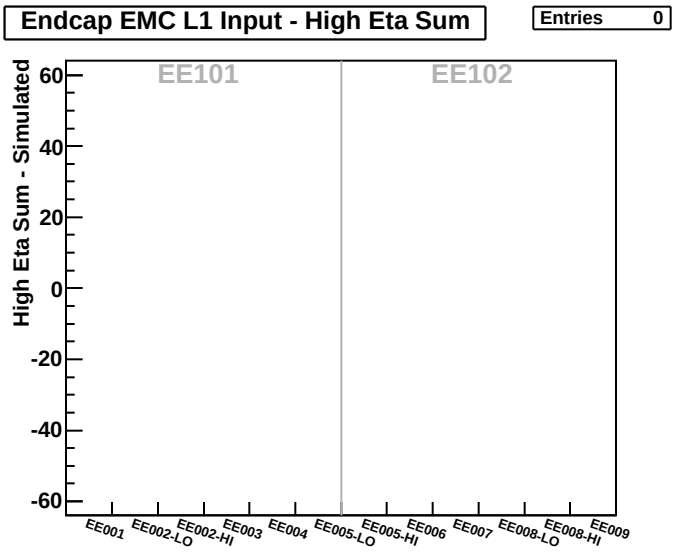

Barrel EMC L1 Input - High Tower Bits
Entries 4935240
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

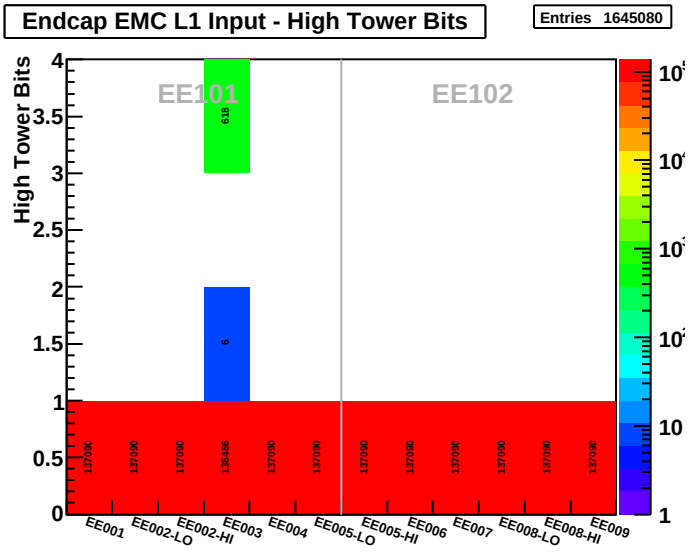

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

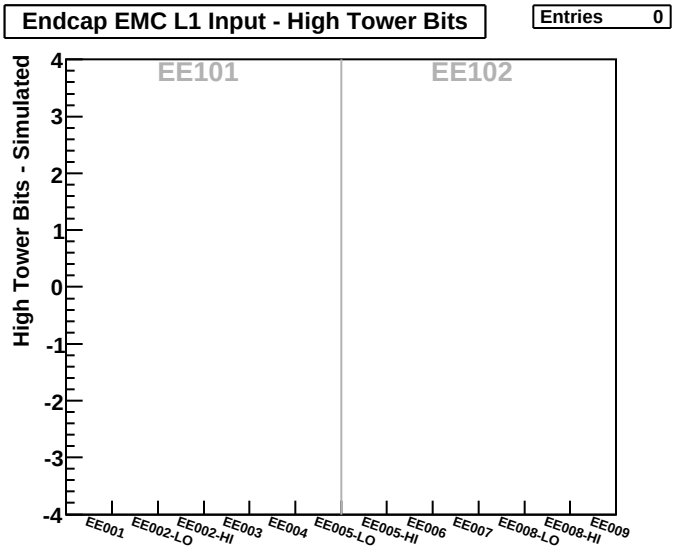

Endcap EMC L1 Input - Low Eta Sum
Entries 1645080
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

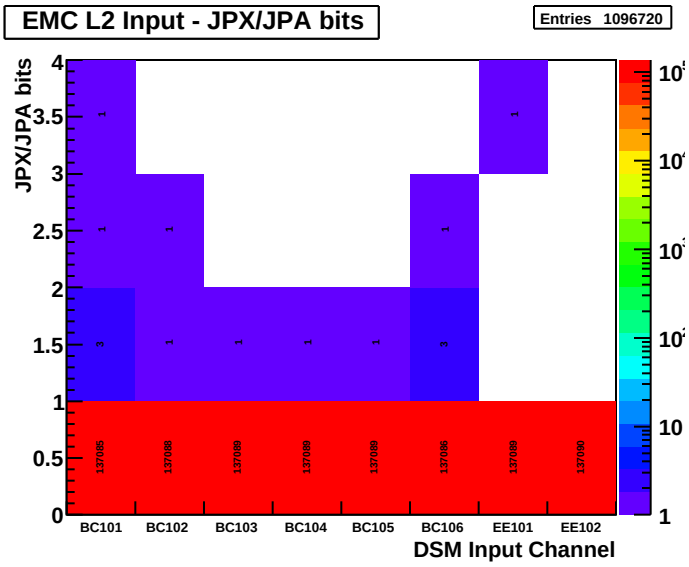

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

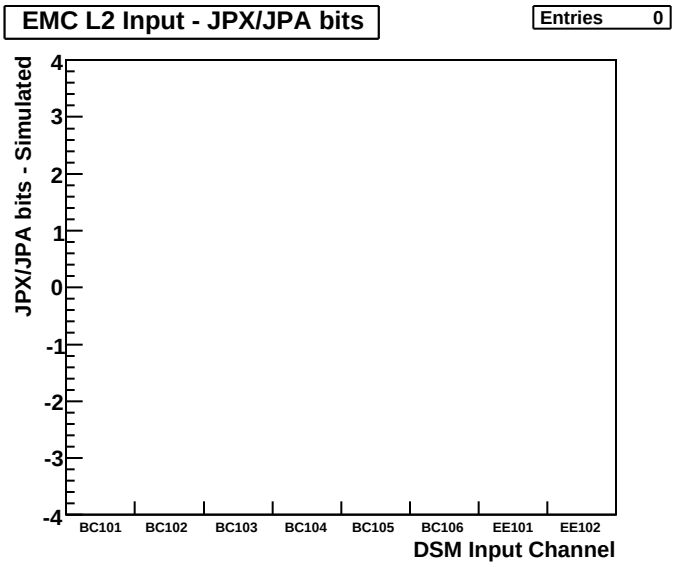

Endcap EMC L1 Input - High Eta Sum
Entries 1645080
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

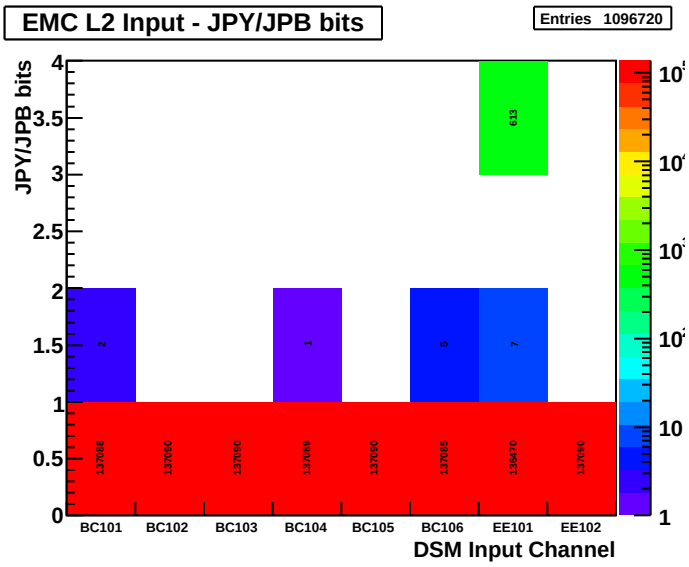

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

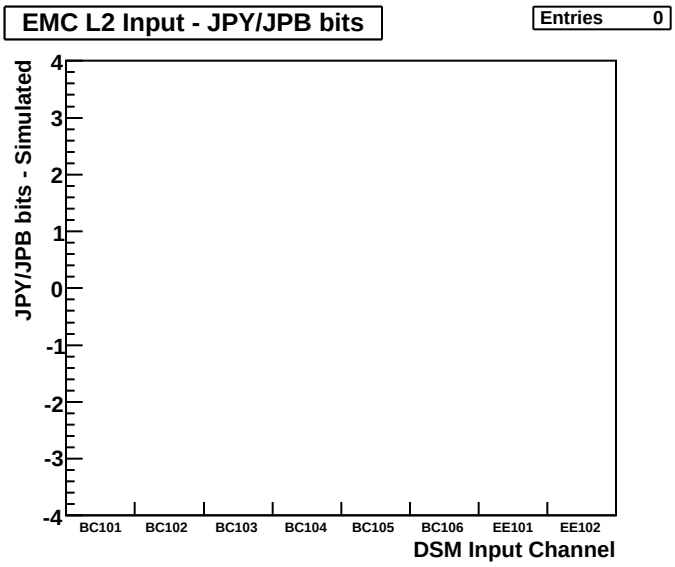

Endcap EMC L1 Input - High Tower Bits
Entries 1645080
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

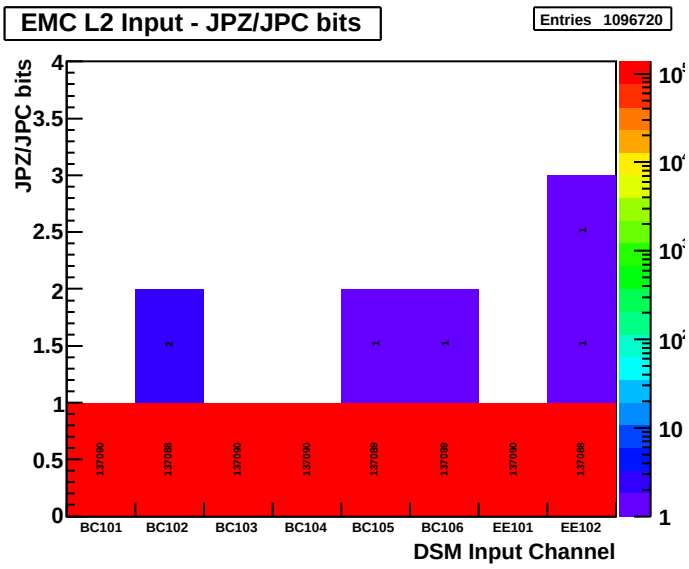

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

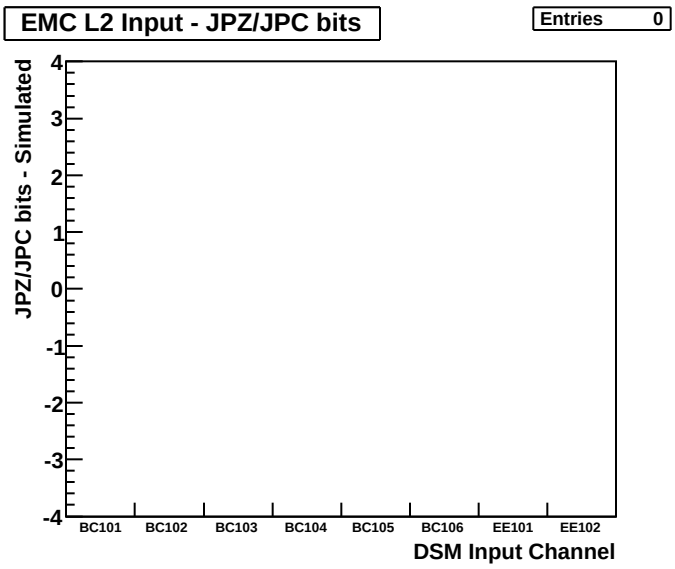

EMC L2 Input - JPX/JPA bits
Entries 1096720
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

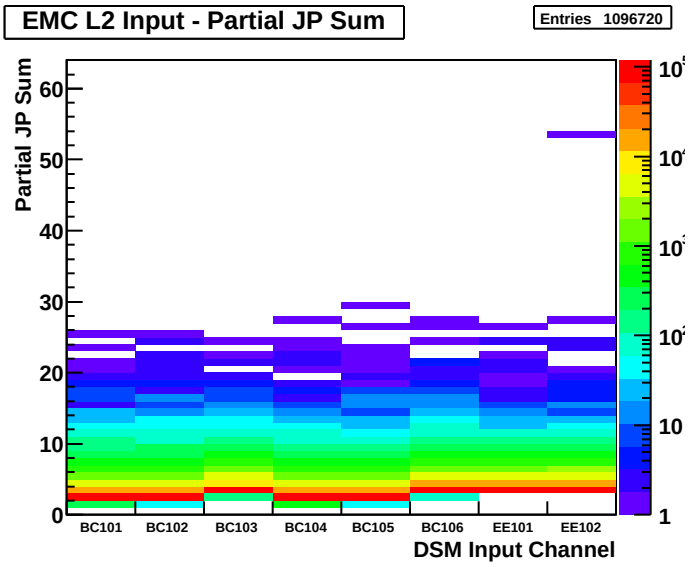

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

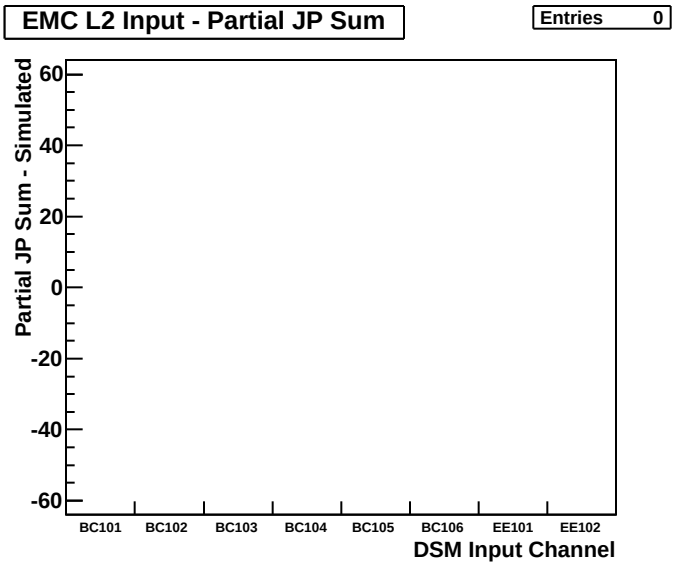

EMC L2 Input - JPY/JPB bits
Entries 1096720
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

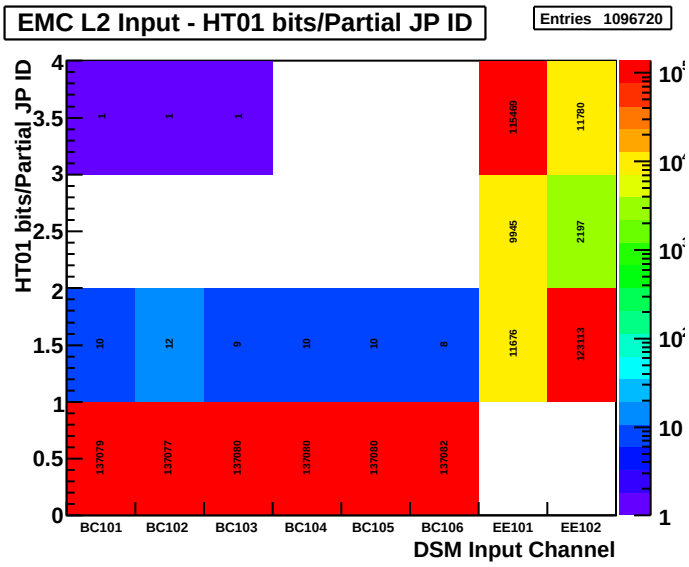

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

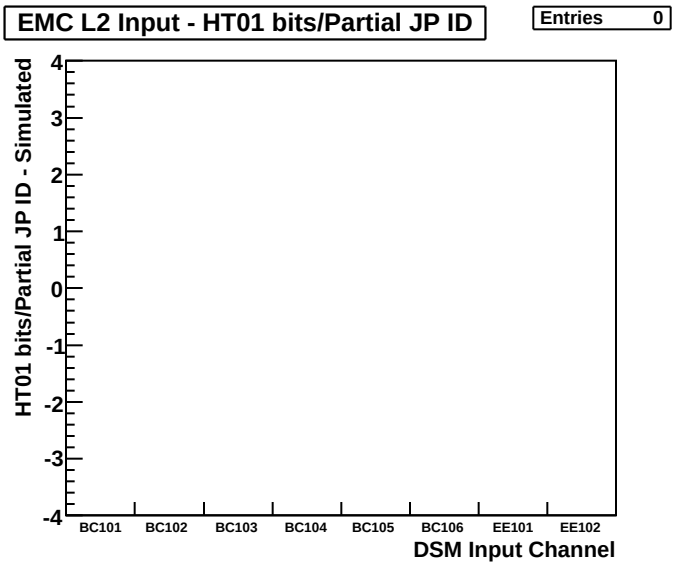

EMC L2 Input - JPZ/JPC bits
Entries 1096720
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

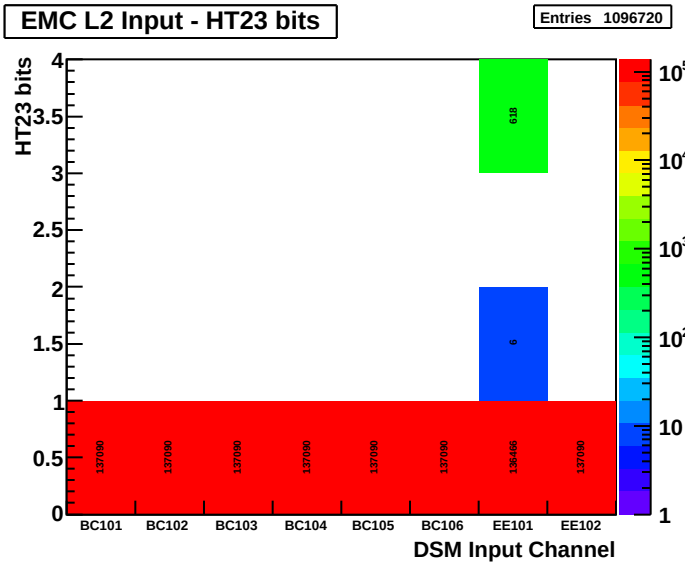

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

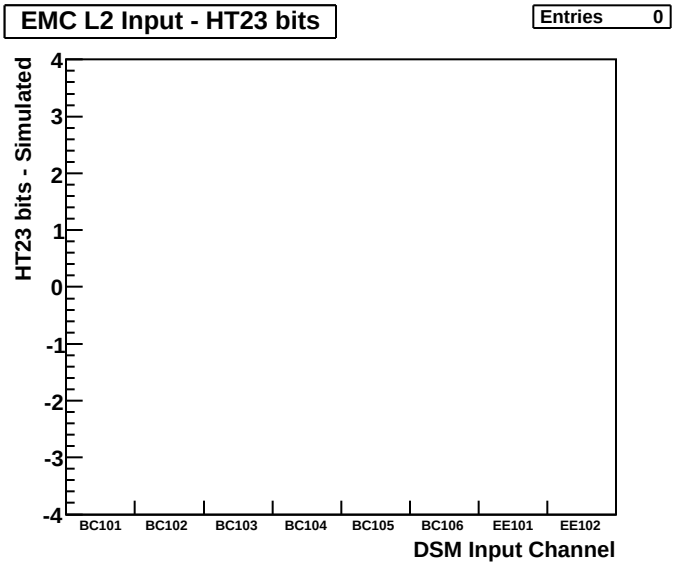

EMC L2 Input - Partial JP Sum
Entries 1096720
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

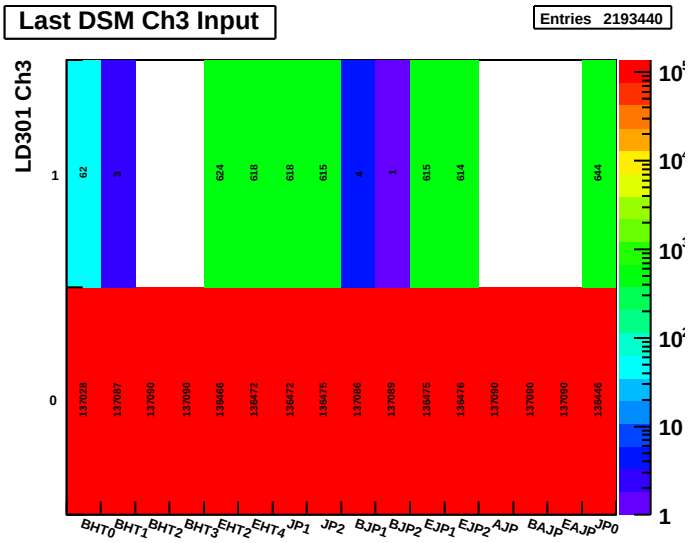

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

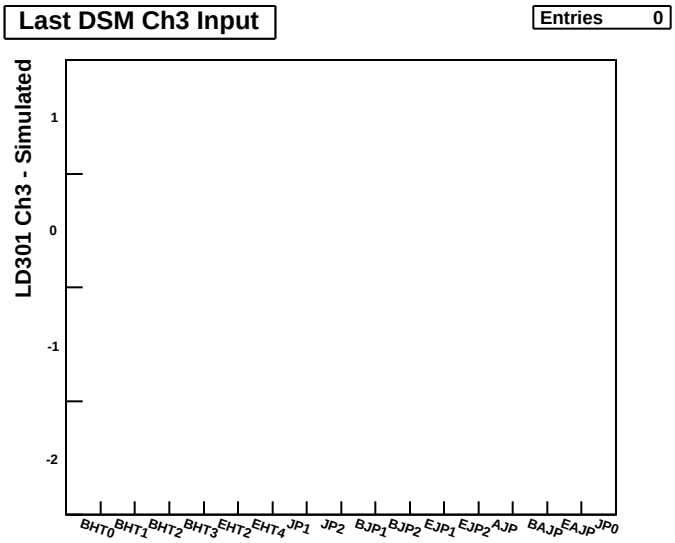

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1096720
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

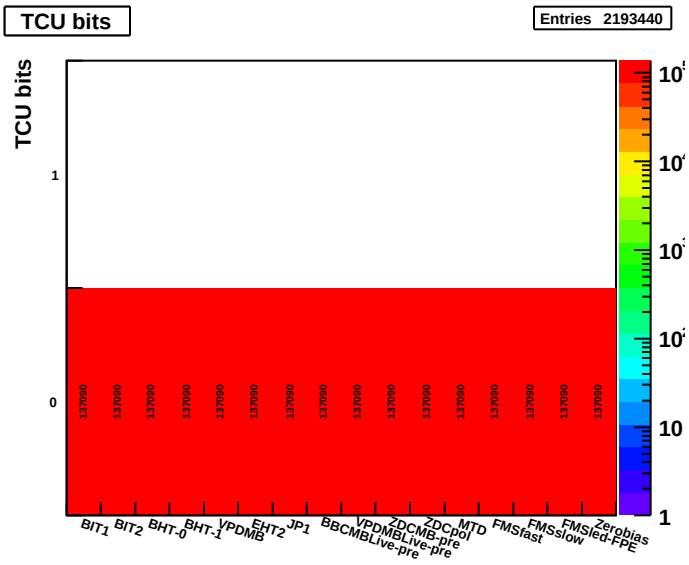

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

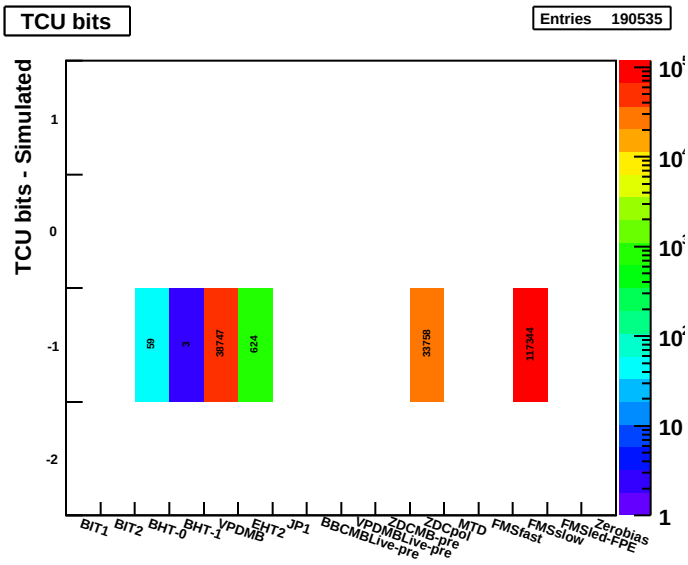

EMC L2 Input - HT23 bits
Entries 1096720
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

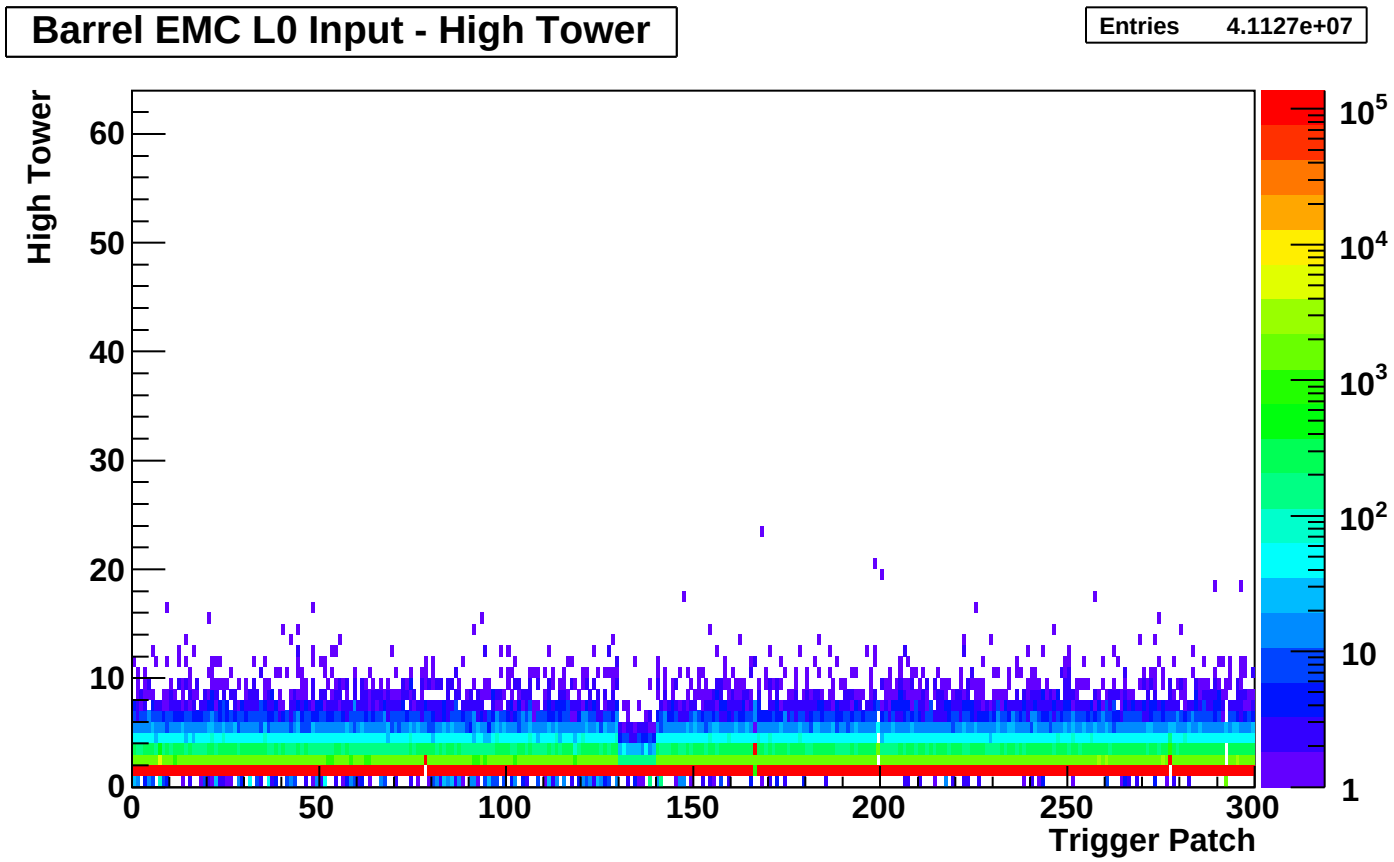

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

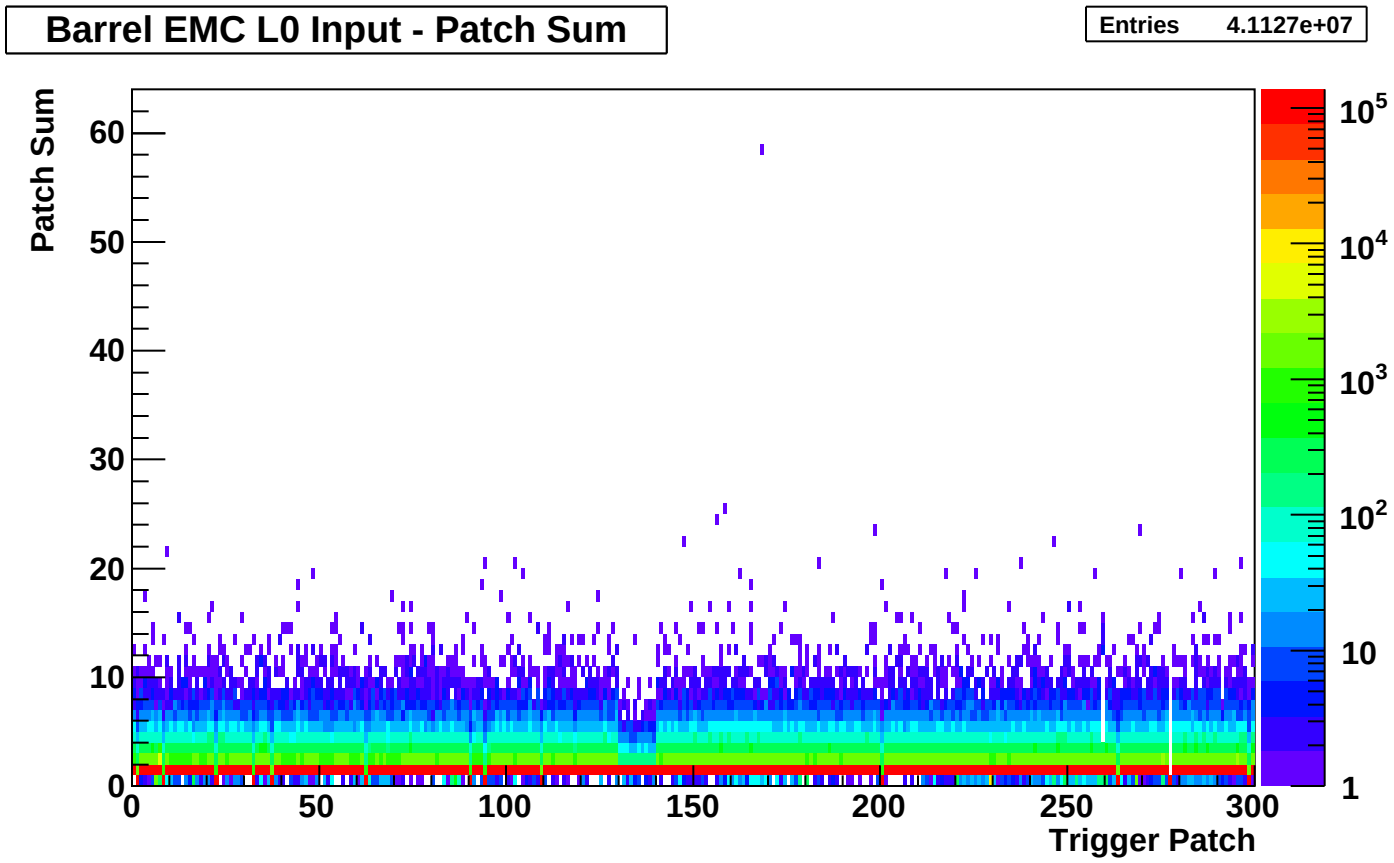

Last DSM Ch3 Input
Entries 2193440
LD301 Ch3
1
0
62
3
624
618
618
615
4
1
615
614
644
137028
137087
137090
137090
136466
136472
136472
136475
137086
137089
136475
136476
137090
137090
137090
136446
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0
1

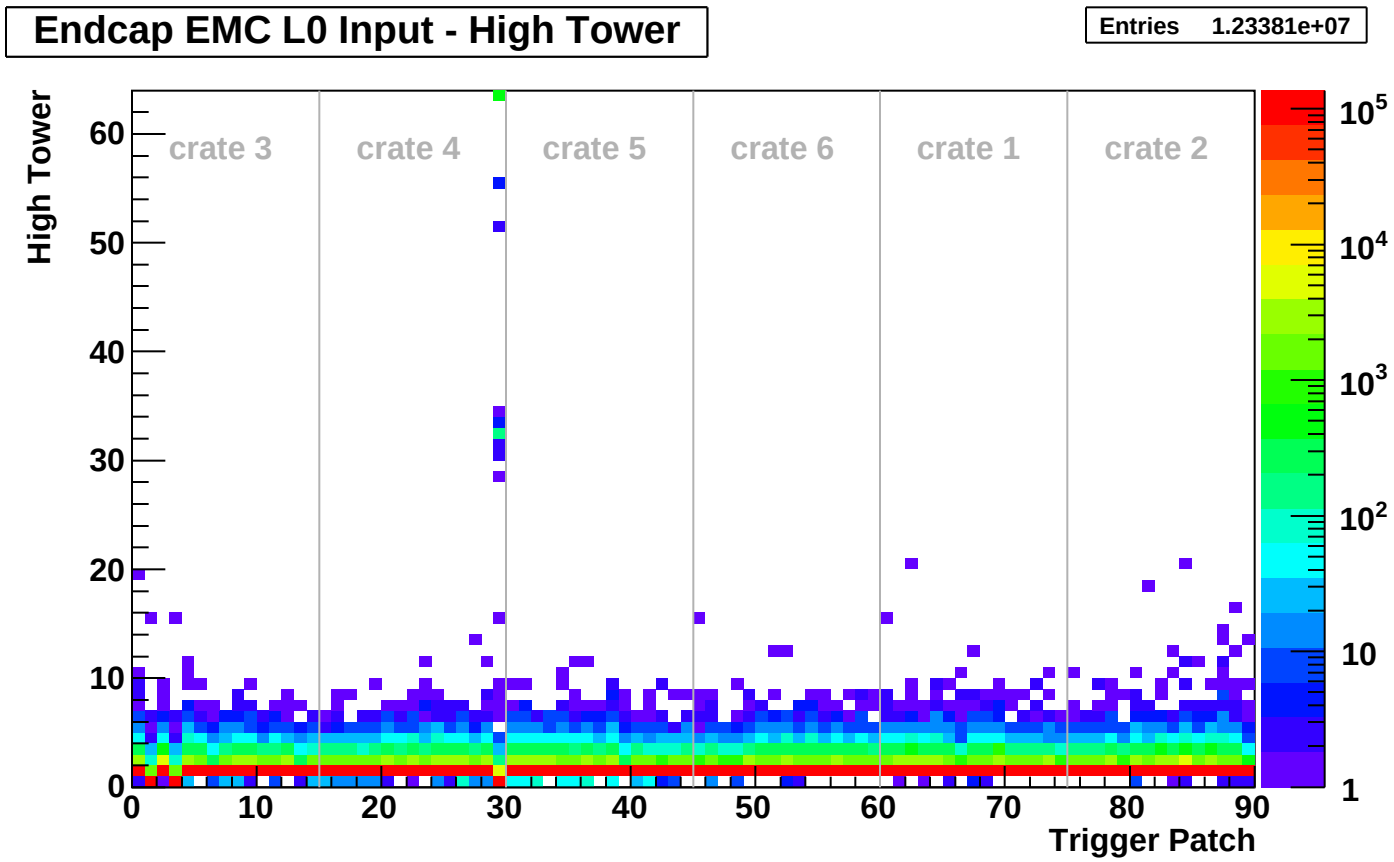

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

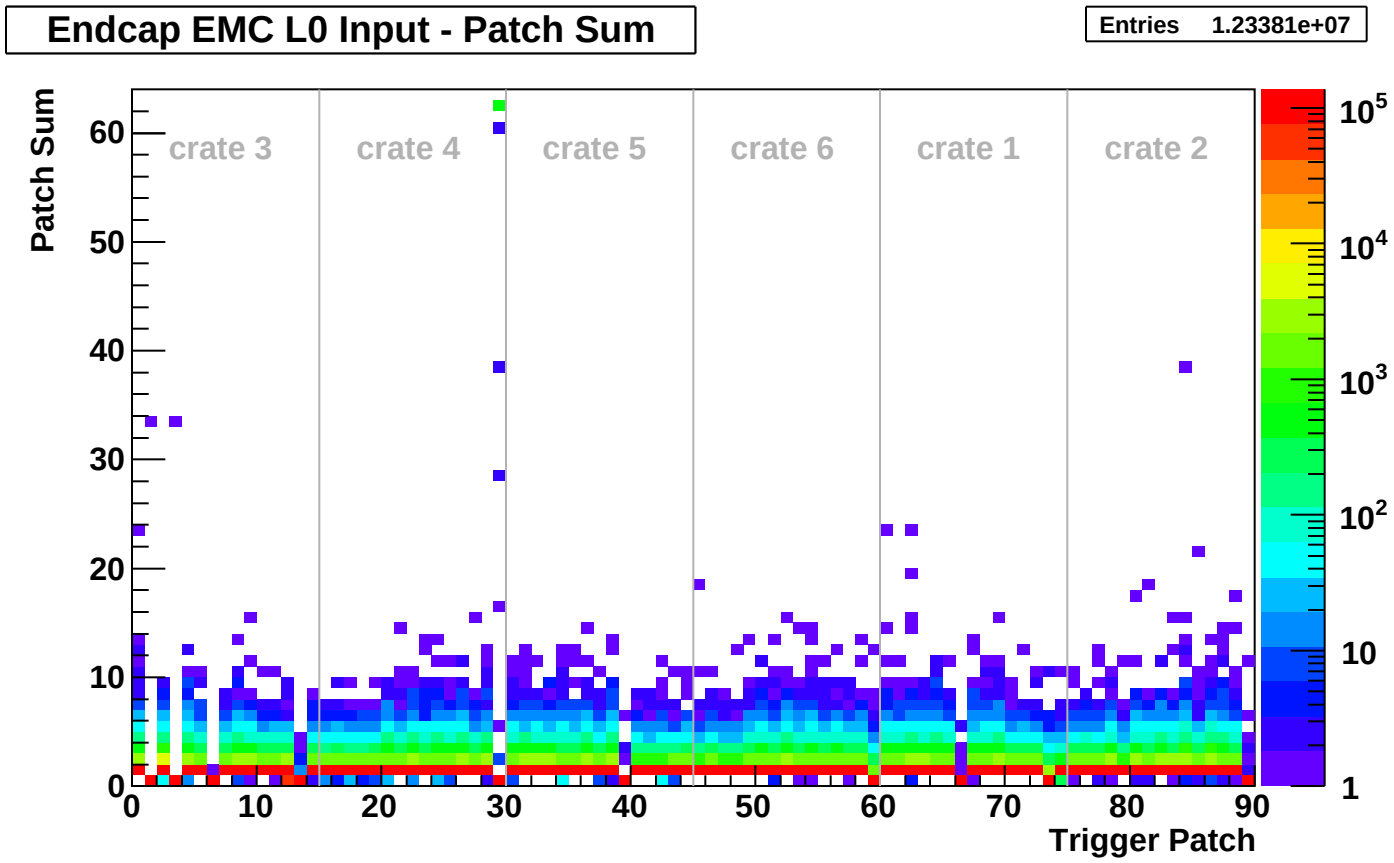

TCU bits
Entries 2193440
TCU bits
1
0
137090
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

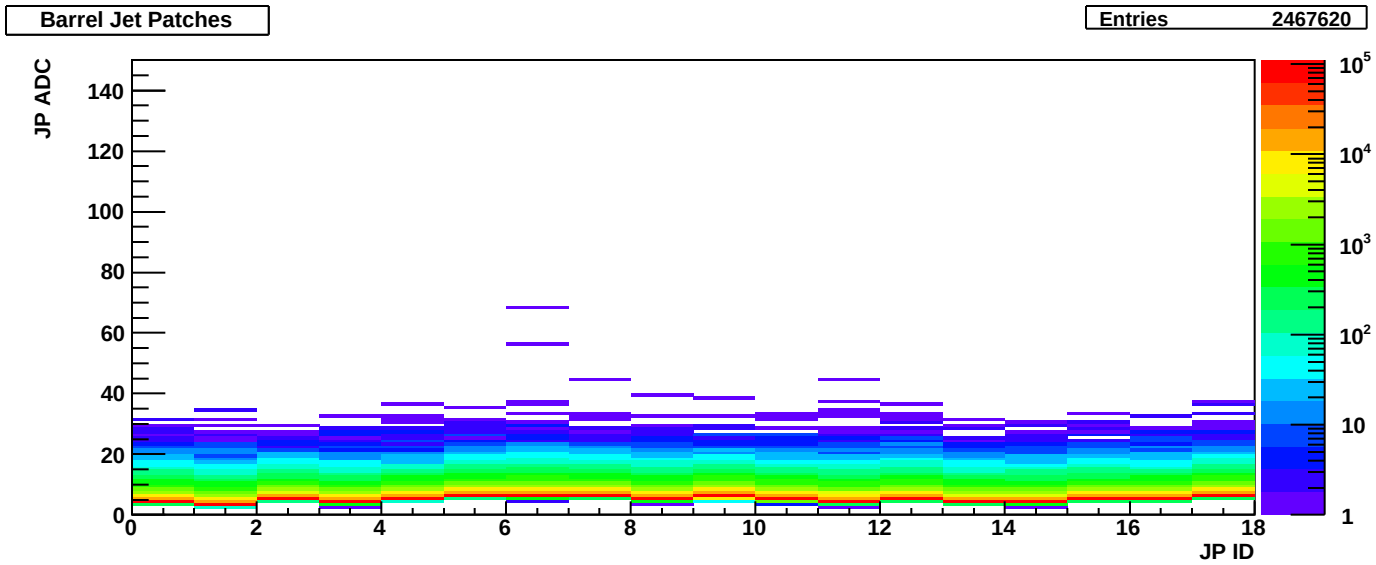

TCU bits
Entries 190535
TCU bits - Simulated
1
0
-1
-2
59
3
38747
624
33758
117344
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

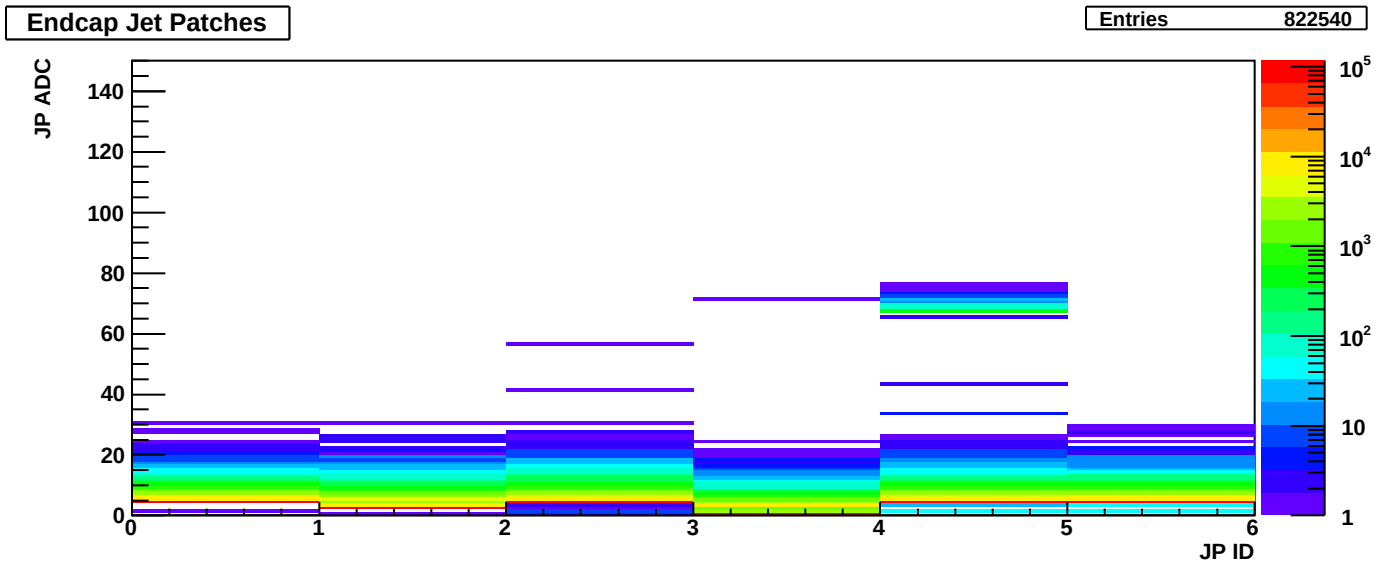

Barrel EMC L0 Input - High Tower
Entries 4.1127e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

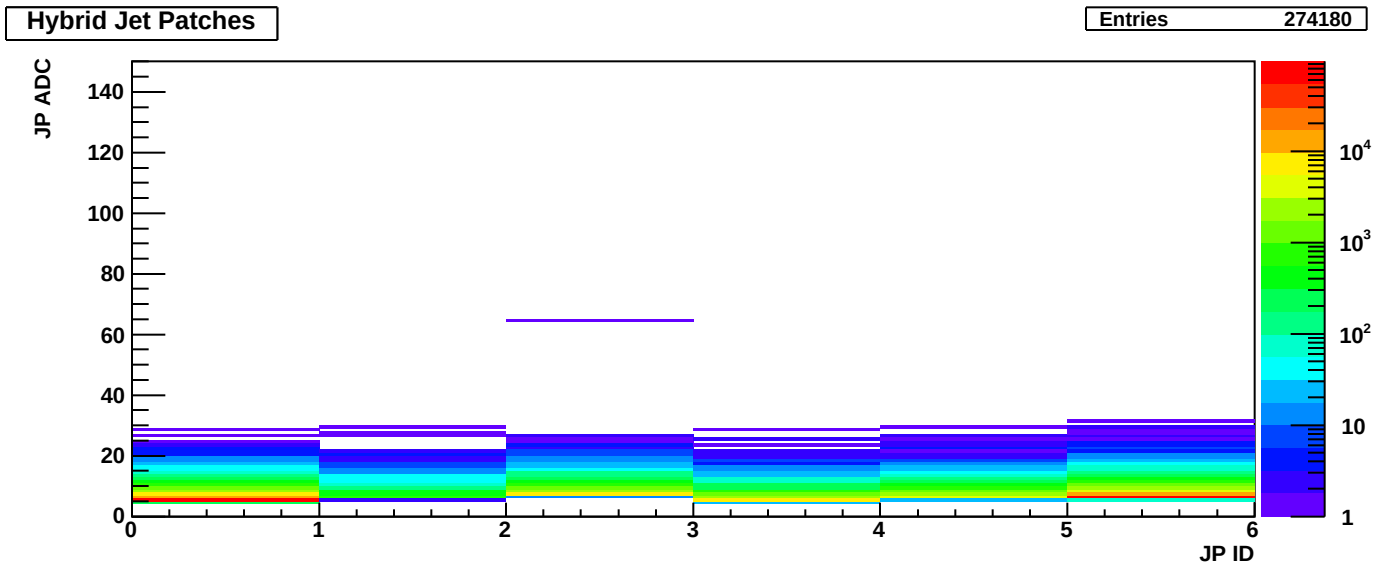

Barrel EMC L0 Input - Patch Sum
Entries 4.1127e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

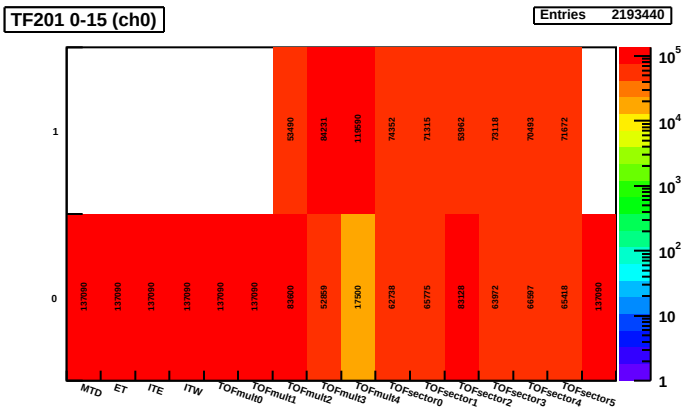

Endcap EMC L0 Input - High Tower
Entries 1.23381e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

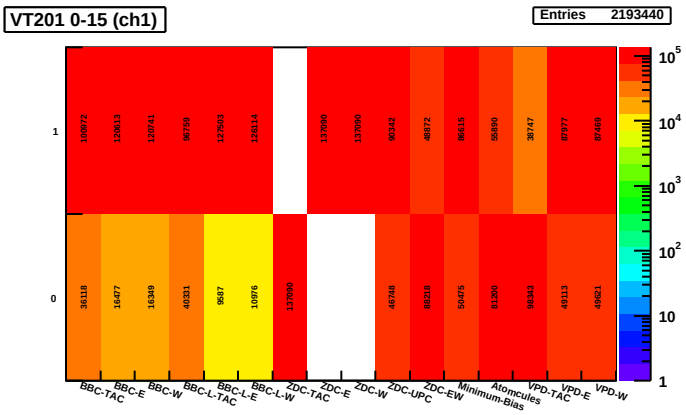

Endcap EMC L0 Input - Patch Sum
Entries 1.23381e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

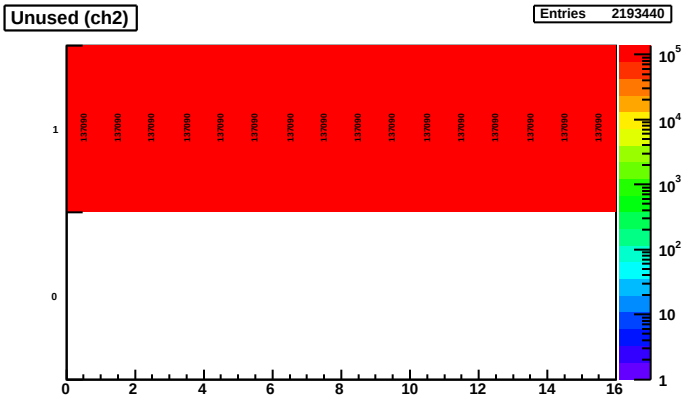

Barrel Jet Patches
Entries 2467620
JP ADC
JP ID

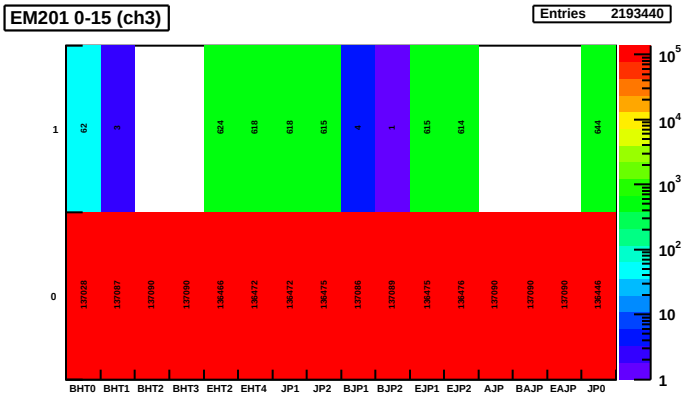

Endcap Jet Patches
Entries 822540
JP ADC
JP ID

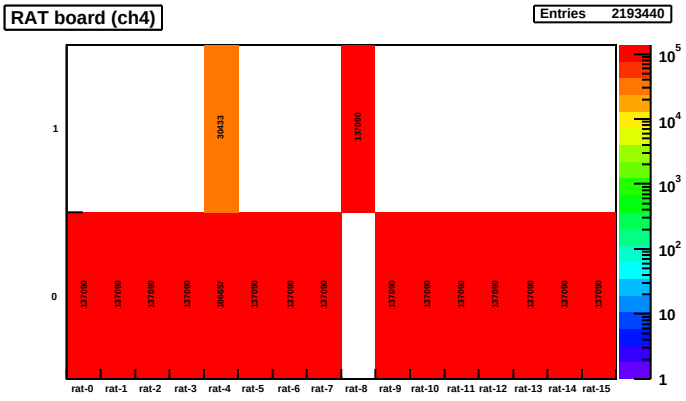

Hybrid Jet Patches
Entries 274180
JP ADC
JP ID

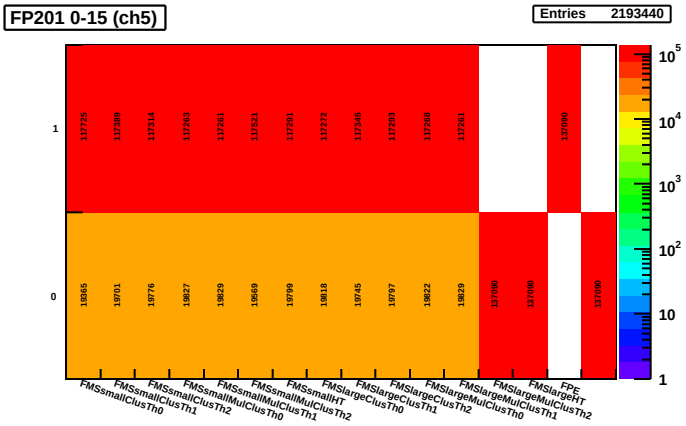

TF201 0-15 (ch0)
Entries 2193440
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

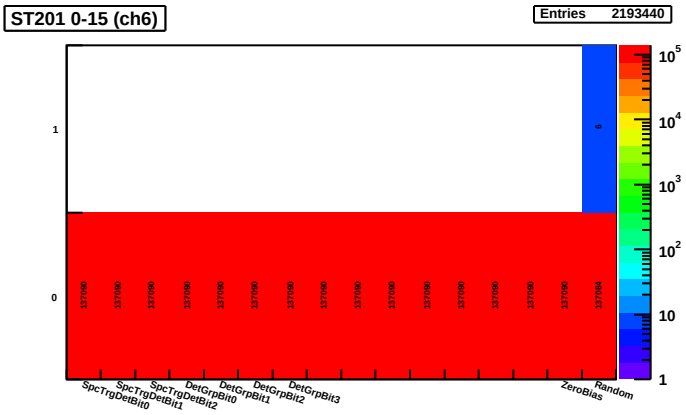

VT201 0-15 (ch1)
Entries 2193440
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

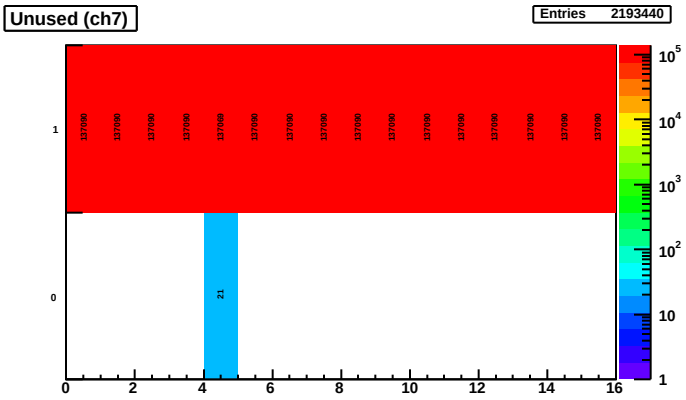

Unused (ch2)
Entries 2193440

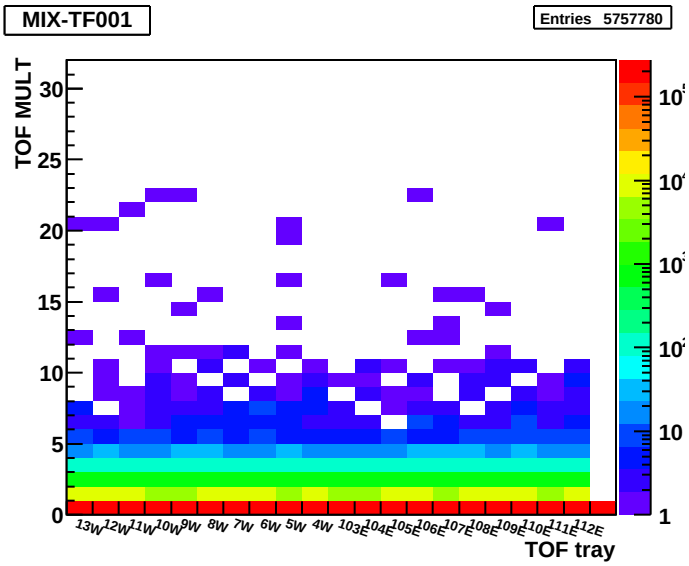

EM201 0-15 (ch3)
Entries 2193440
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

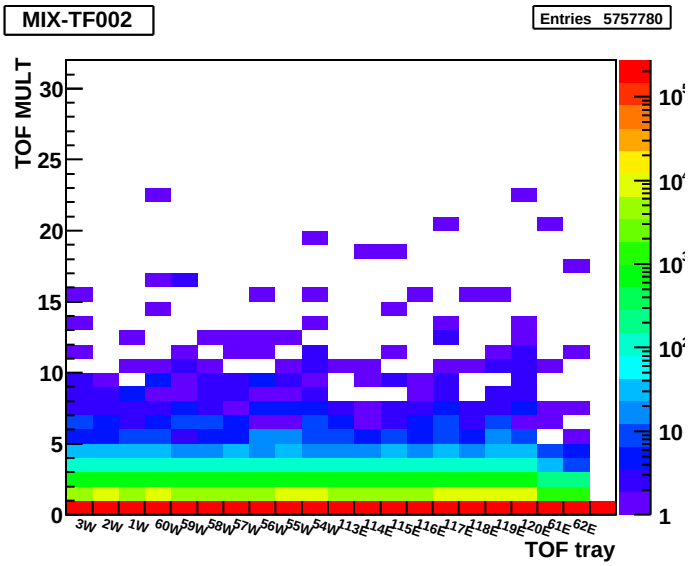

RAT board (ch4)
Entries 2193440
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

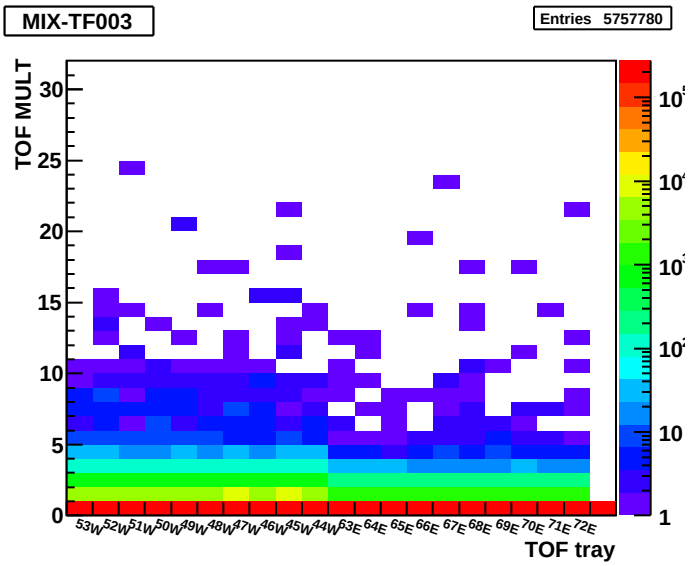

FP201 0-15 (ch5)
Entries 2193440
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

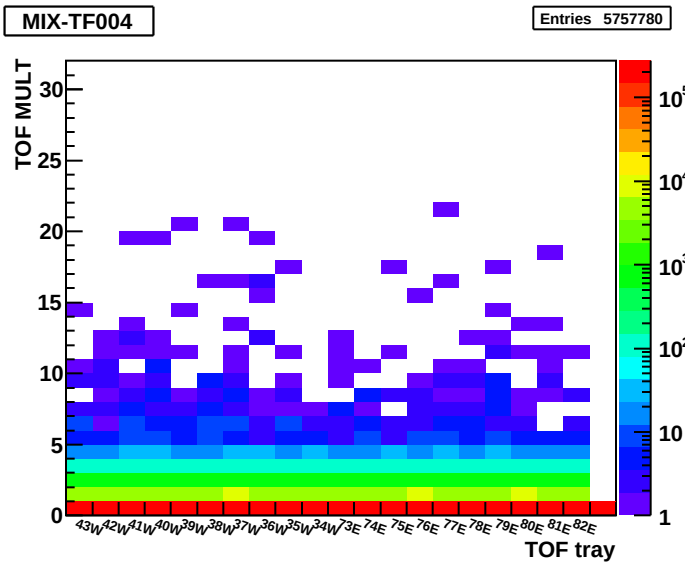

ST201 0-15 (ch6)
Entries 2193440
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

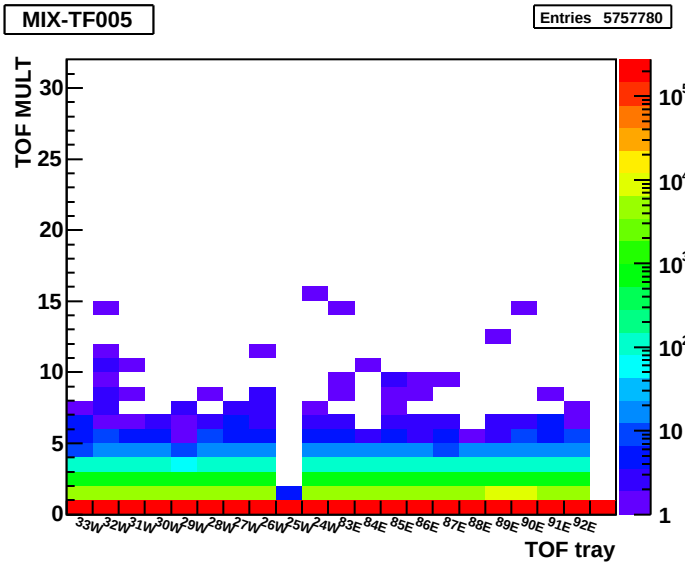

Unused (ch7)
Entries 2193440

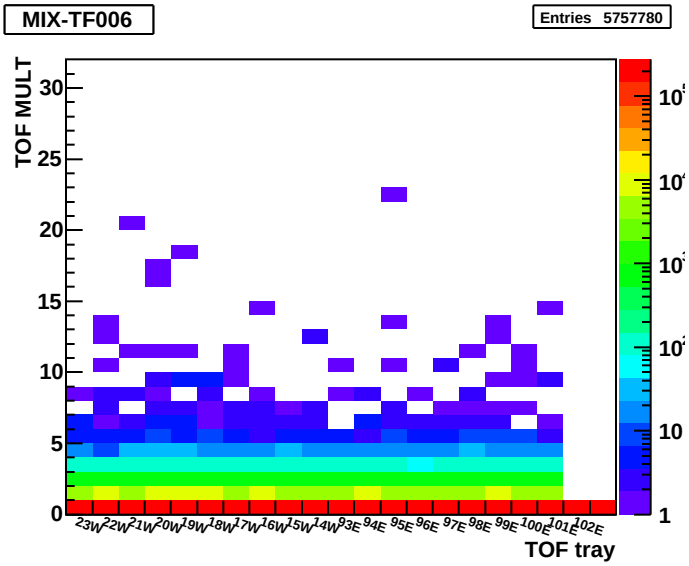

MIX-TF001
Entries 5757780
TOF MULT
TOF tray

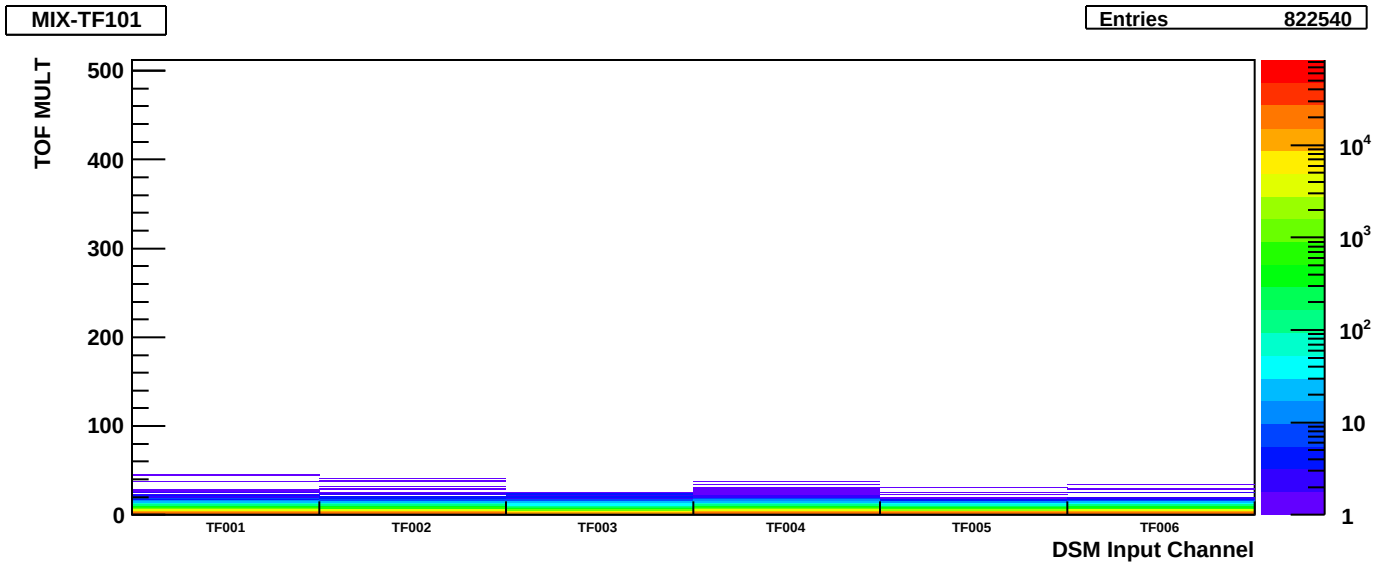

MIX-TF002
Entries 5757780
TOF MULT
TOF tray

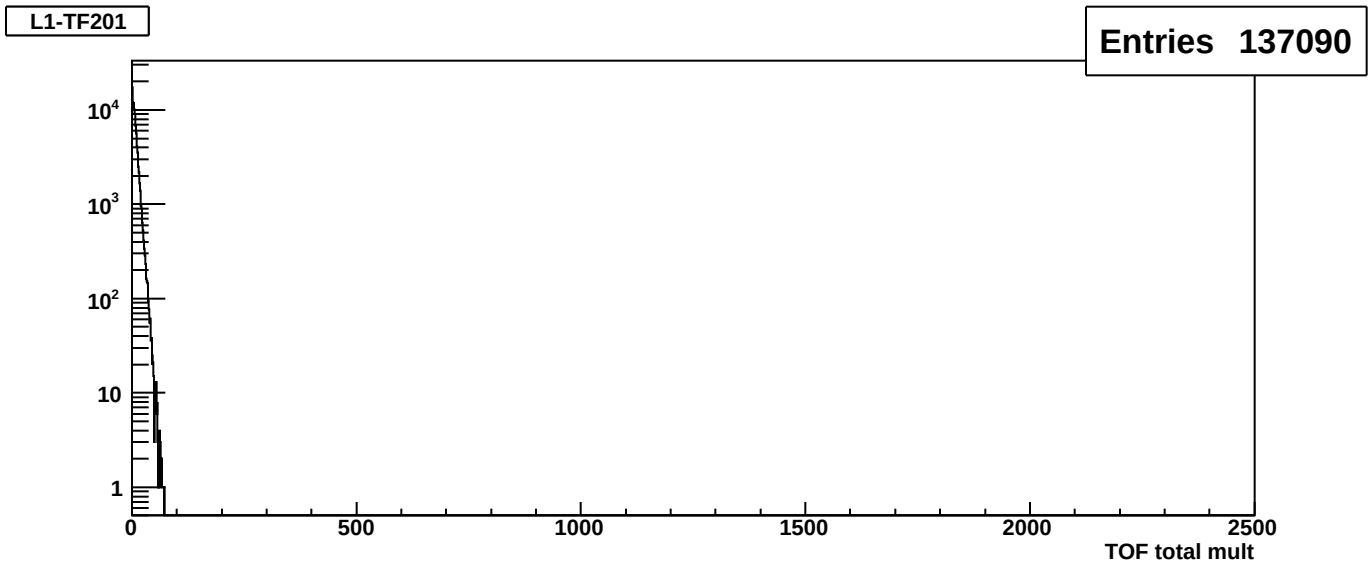

MIX-TF003
Entries 5757780
TOF MULT
TOF tray

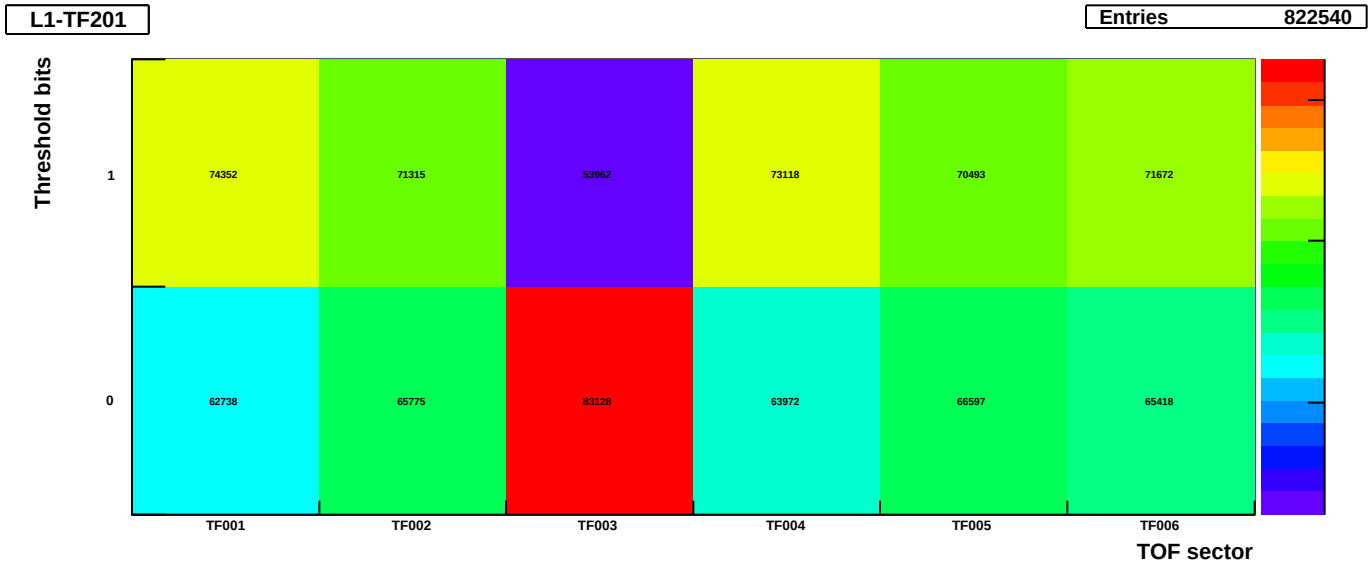

MIX-TF004
Entries 5757780
TOF MULT
TOF tray

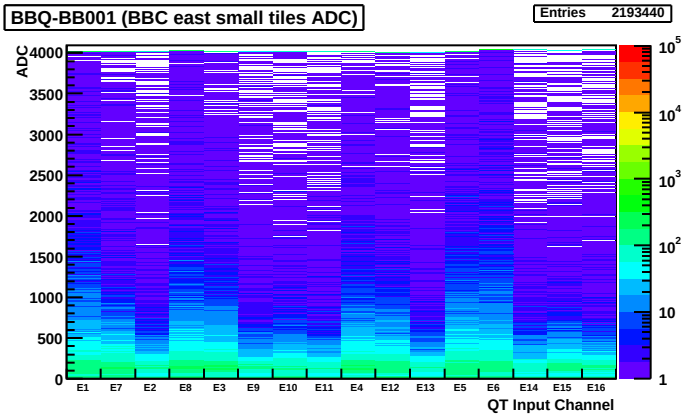

MIX-TF005
Entries 5757780
TOF MULT
TOF tray

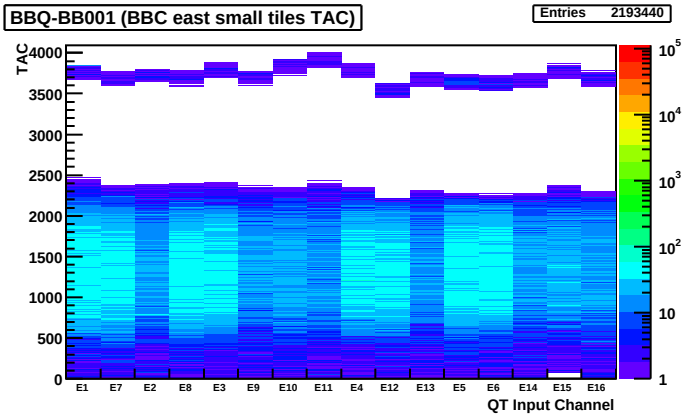

MIX-TF006
Entries 5757780
TOF MULT
TOF tray

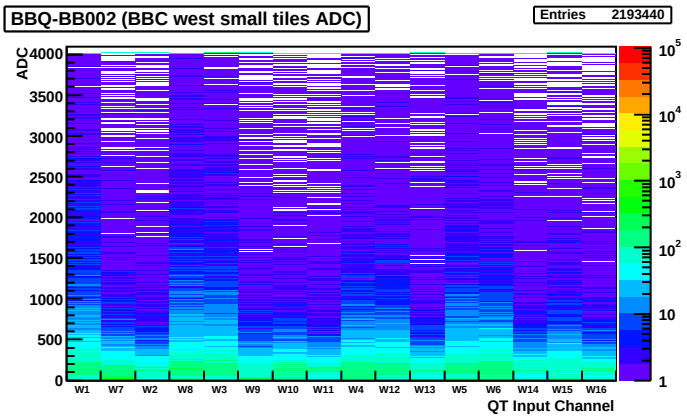

MIX-TF101
Entries 822540
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

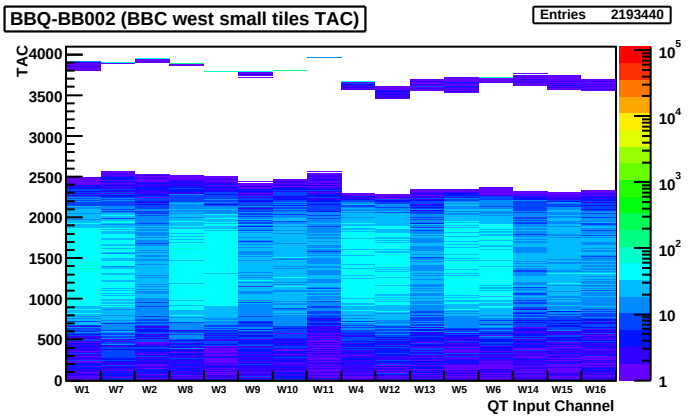

L1-TF201
Entries 137090
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

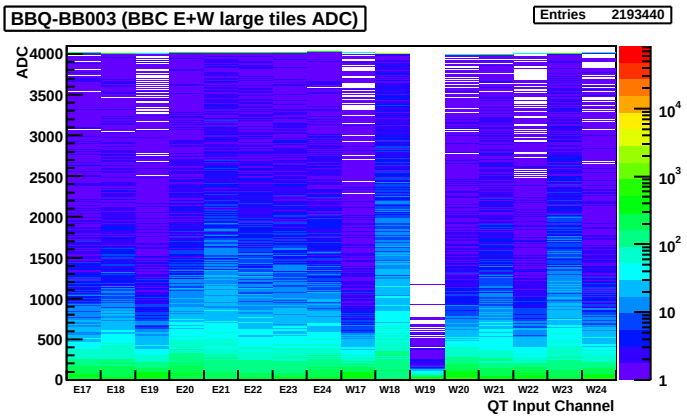

L1-TF201
Entries 822540
Threshold bits
1
0
74352
71315
53962
73118
70493
71672
62738
65775
83128
63972
66597
65418
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

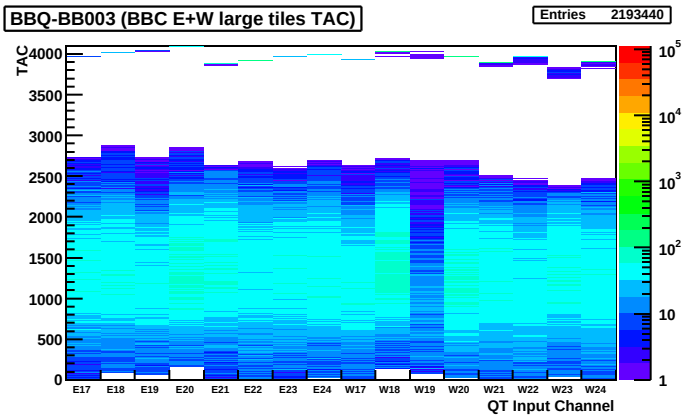

BBQ-BB001 (BBC east small tiles ADC)
Entries 2193440
ADC
QT Input Channel

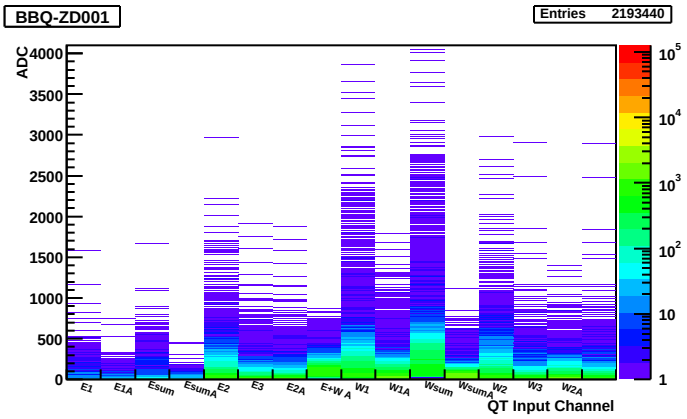

BBQ-BB001 (BBC east small tiles TAC)
Entries 2193440
TAC
QT Input Channel

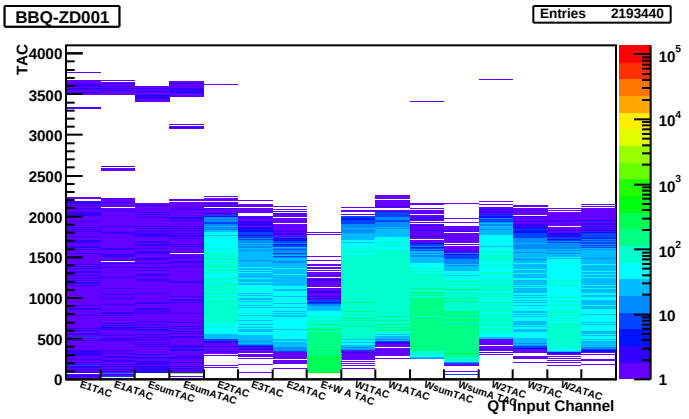

BBQ-BB002 (BBC west small tiles ADC)
Entries 2193440
ADC
QT Input Channel

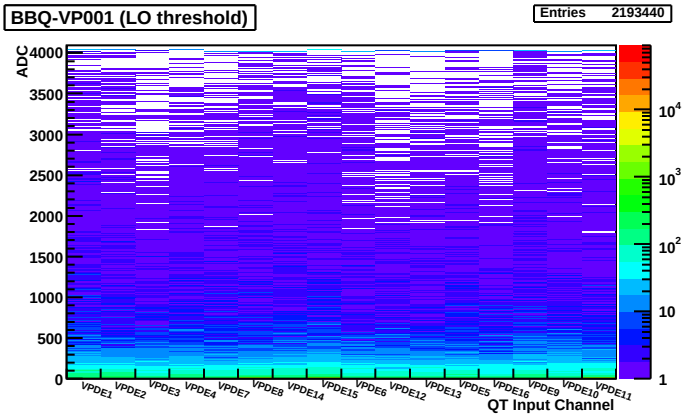

BBQ-BB002 (BBC west small tiles TAC)
Entries 2193440
TAC
QT Input Channel

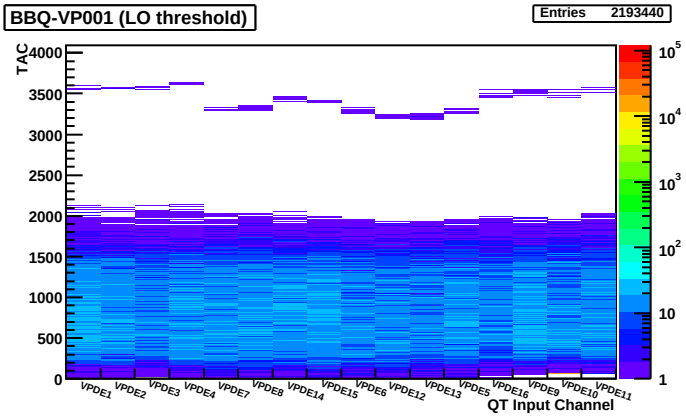

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2193440
ADC
QT Input Channel

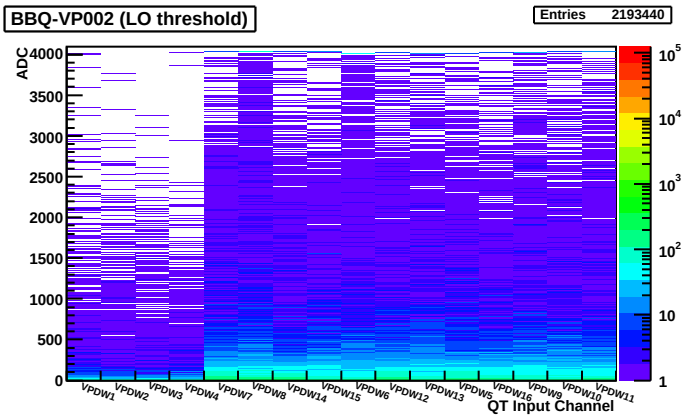

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2193440
TAC
QT Input Channel

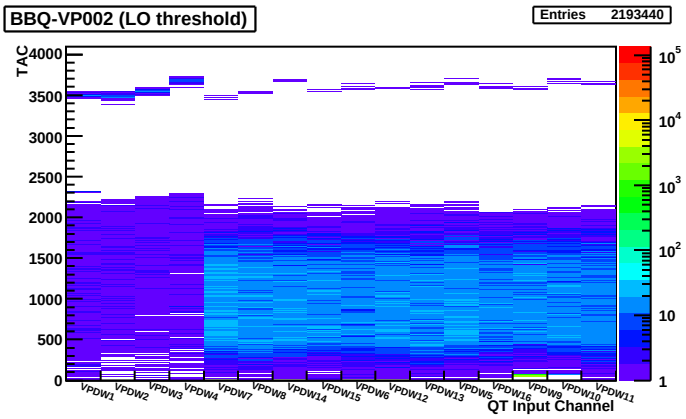

BBQ-ZD001
Entries 2193440
ADC
QT Input Channel

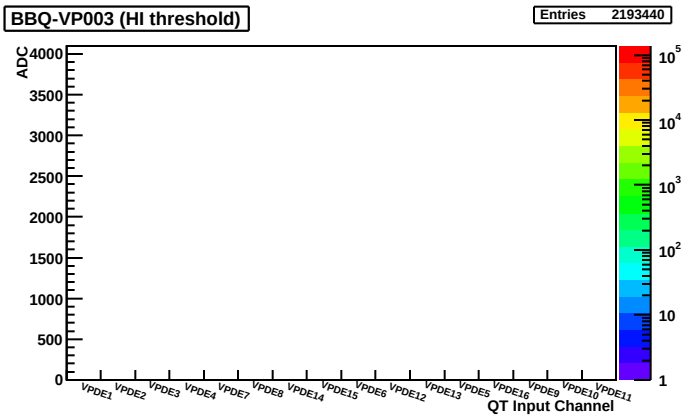

BBQ-ZD001
Entries 2193440
TAC
QT Input Channel

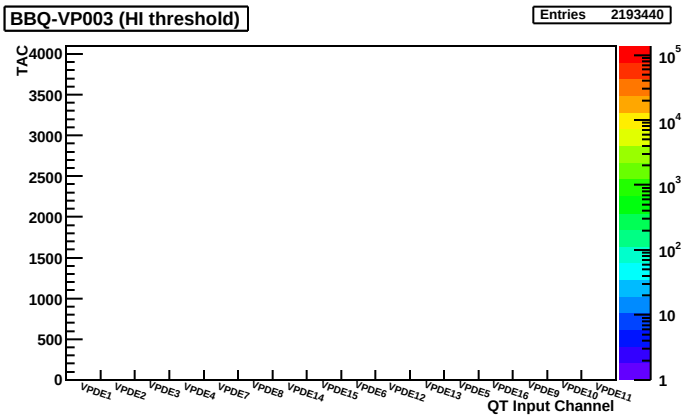

BBQ-VP001 (LO threshold)
Entries 2193440
ADC
QT Input Channel

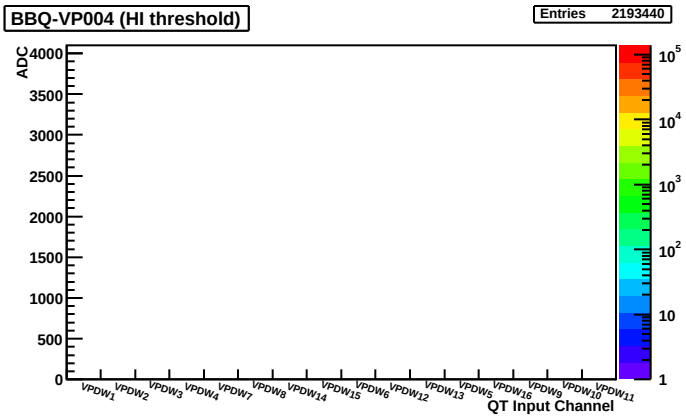

BBQ-VP001 (LO threshold)
Entries 2193440
TAC
QT Input Channel

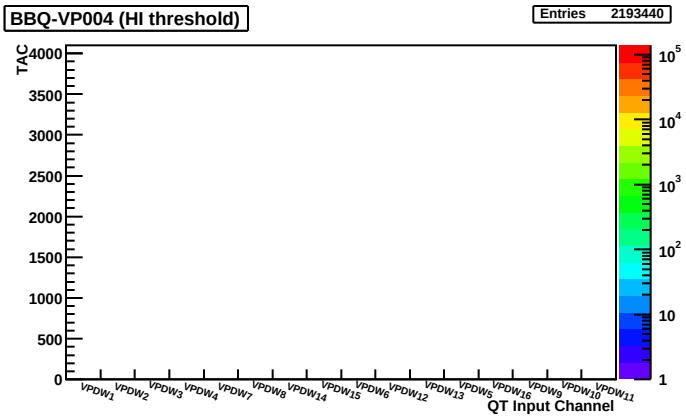

BBQ-VP002 (LO threshold)
Entries 2193440
ADC
QT Input Channel

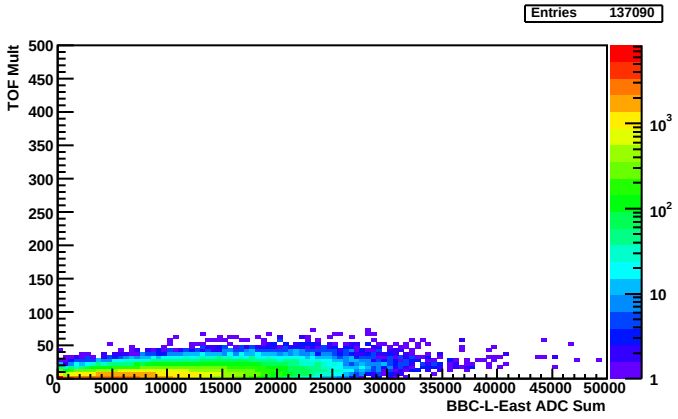

BBQ-VP002 (LO threshold)
Entries 2193440
TAC
QT Input Channel

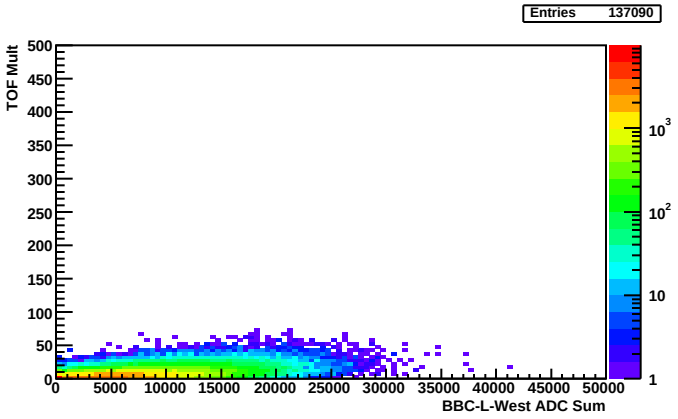

BBQ-VP003 (HI threshold)
Entries 2193440
ADC
QT Input Channel

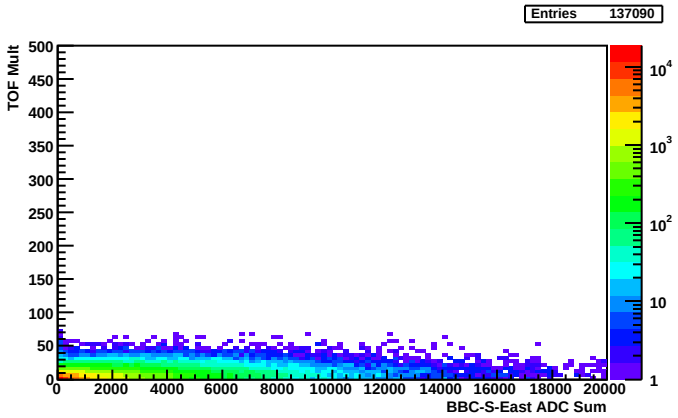

BBQ-VP003 (HI threshold)
Entries 2193440
TAC
QT Input Channel

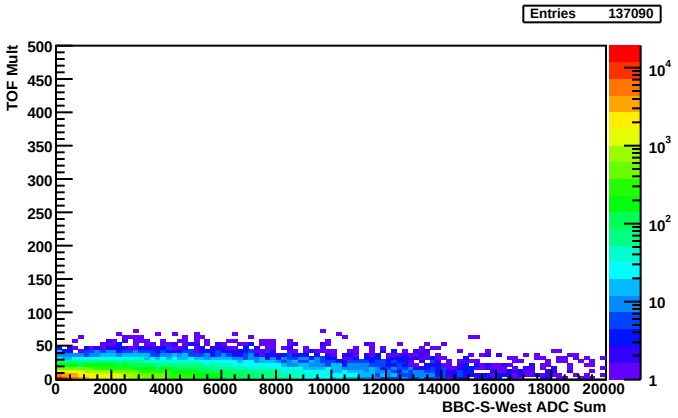

BBQ-VP004 (HI threshold)
Entries 2193440
ADC
QT Input Channel

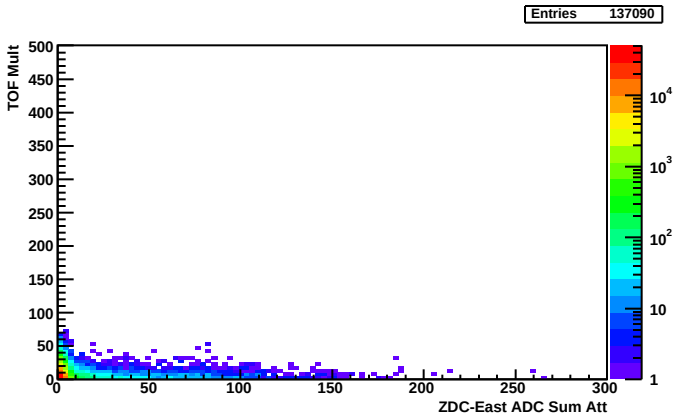

BBQ-VP004 (HI threshold)
Entries 2193440
TAC
QT Input Channel

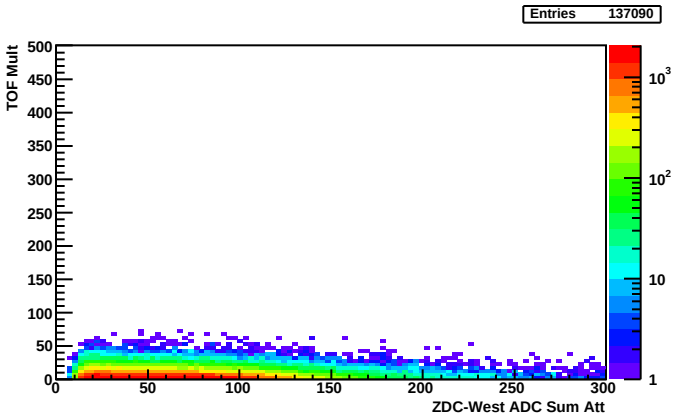

Entries 137090
TOF Mult
BBC-L-East ADC Sum

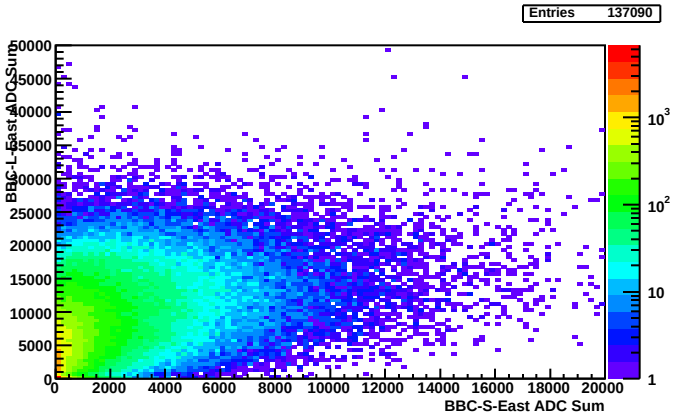

Entries 137090
TOF Mult
BBC-L-West ADC Sum

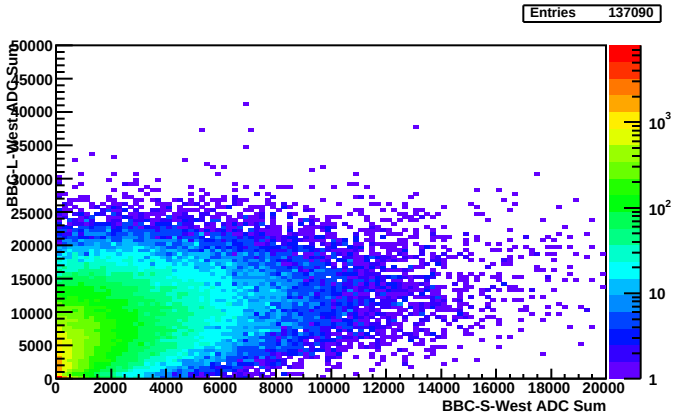

Entries 137090
TOF Mult
BBC-S-East ADC Sum

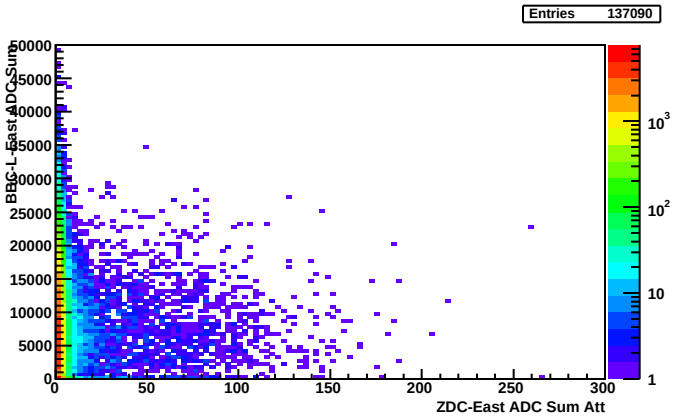

Entries 137090
TOF Mult
BBC-S-West ADC Sum

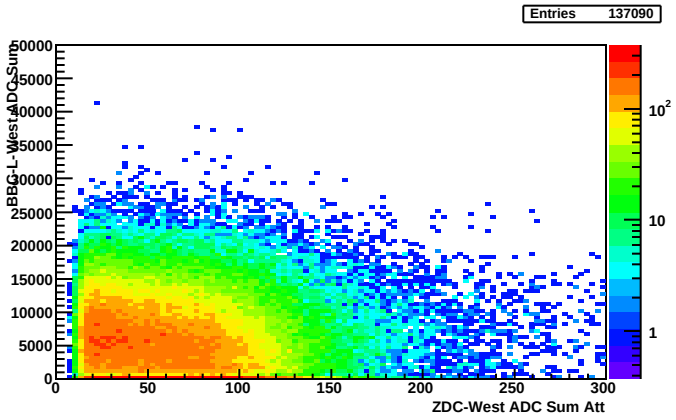

Entries 137090
TOF Mult
ZDC-East ADC Sum Att

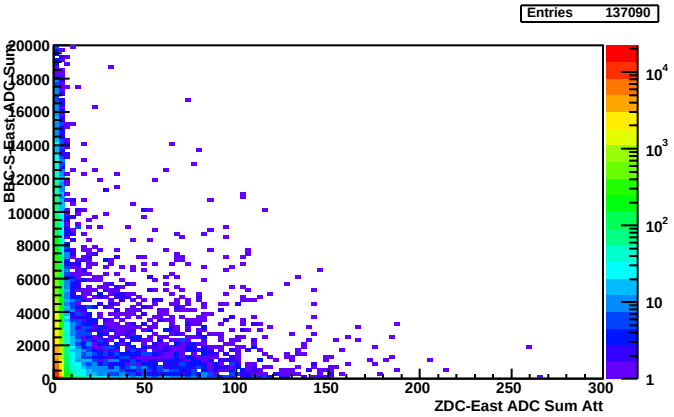

Entries 137090
TOF Mult
ZDC-West ADC Sum Att

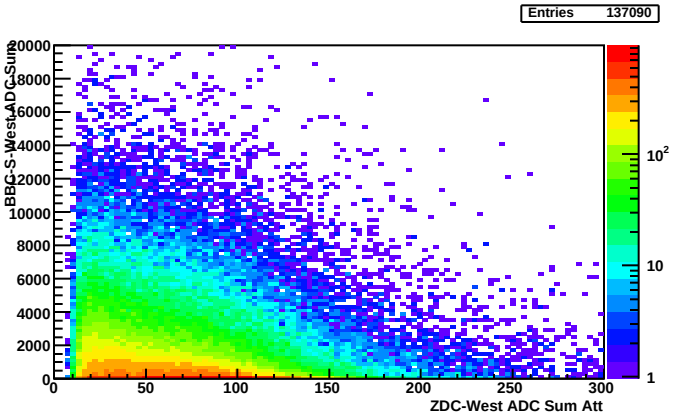

Entries 137090
BBC-L-East ADC Sum
BBC-S-East ADC Sum

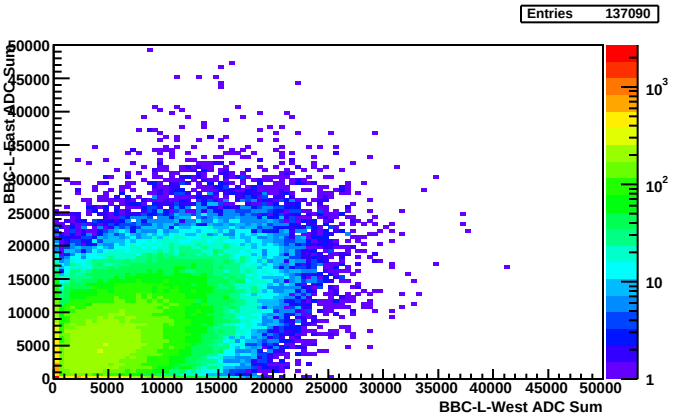

Entries 137090
BBC-L-West ADC Sum
BBC-S-West ADC Sum

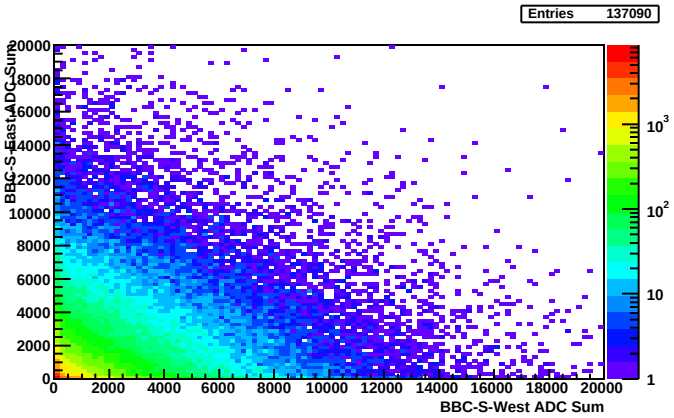

Entries 137090
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

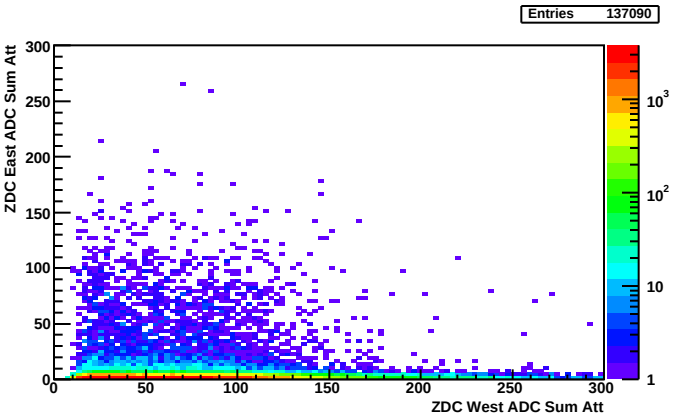

Entries 137090
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

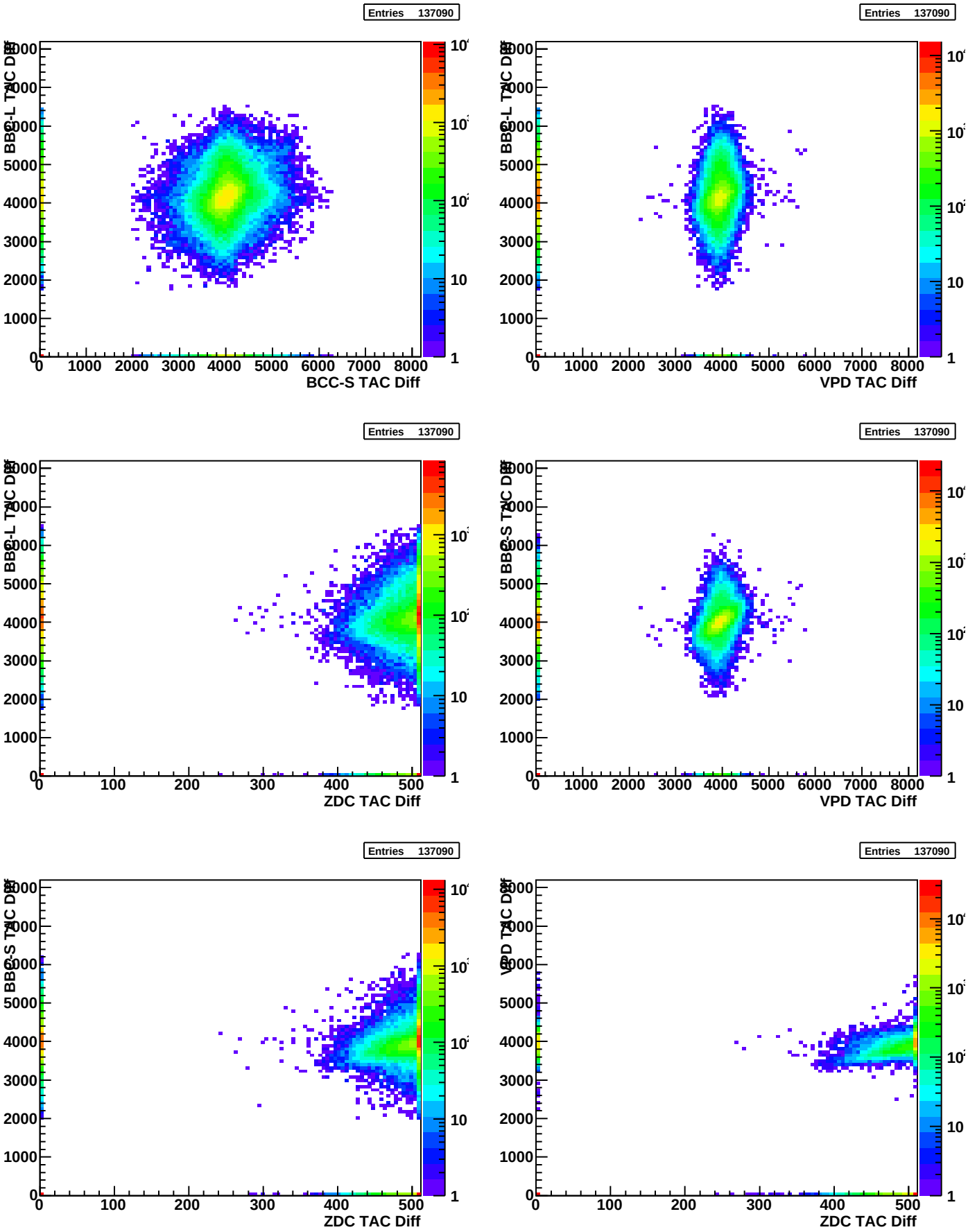

Entries 137090
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

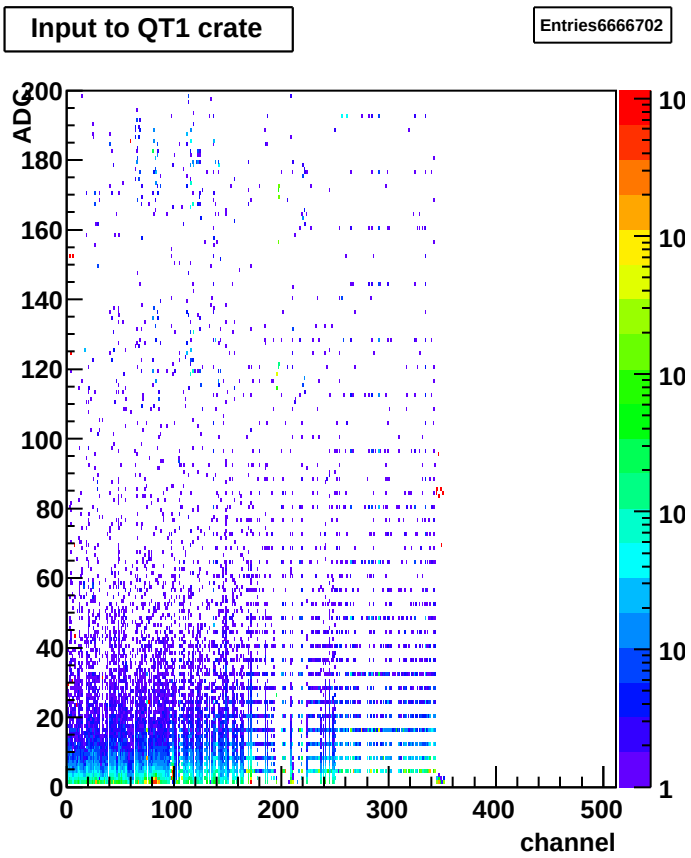

Entries 137090
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

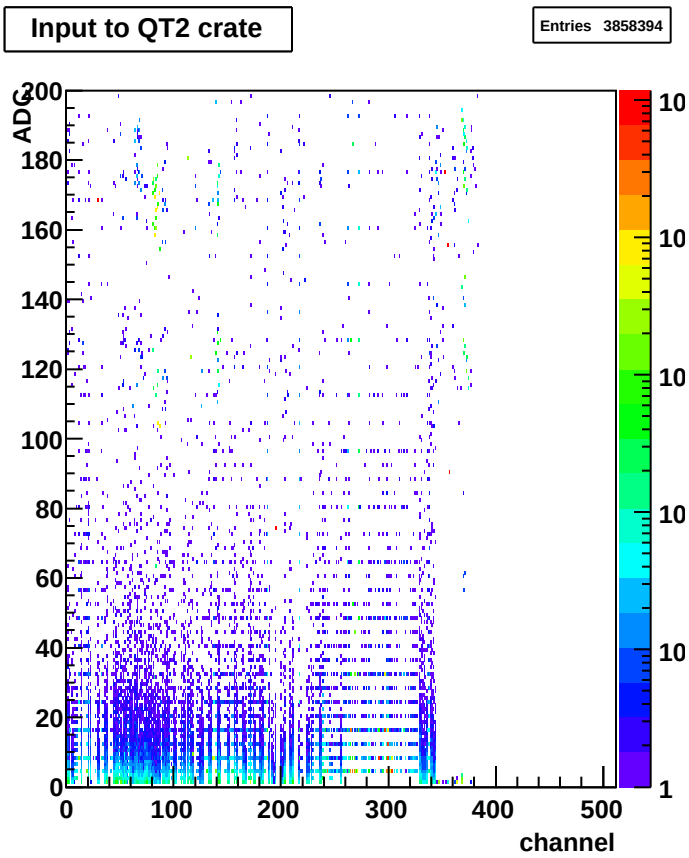

Entries 137090
BBC-L-East ADC Sum
BBC-L-West ADC Sum

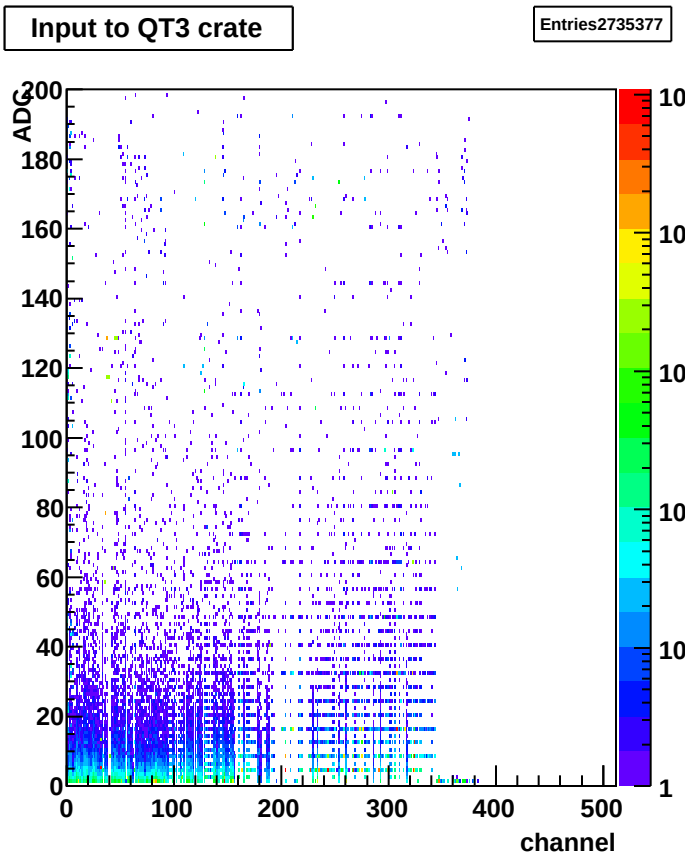

Entries 137090
BBC-S-East ADC Sum
BBC-S-West ADC Sum

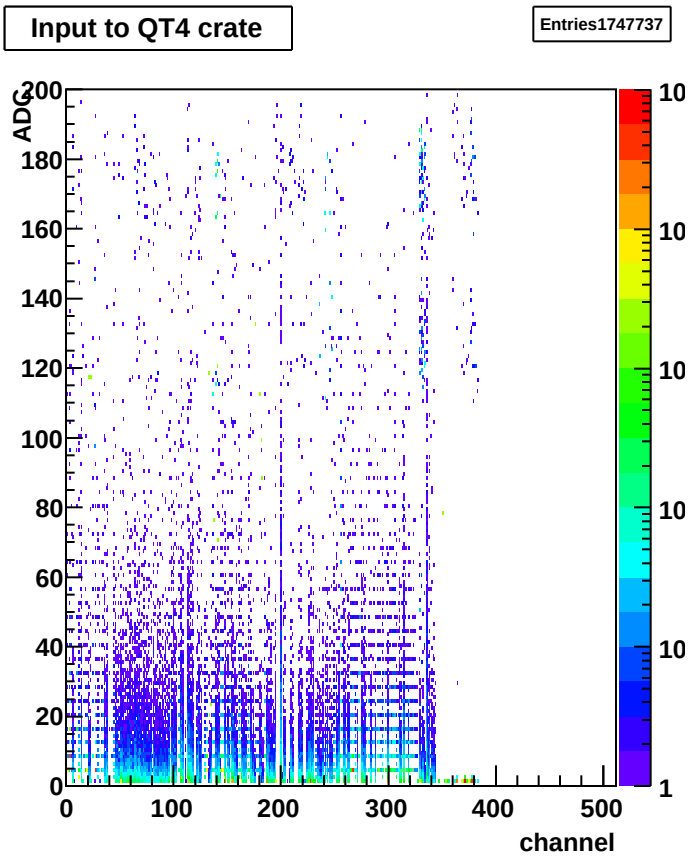

Entries 137090
ZDC East ADC Sum Att
ZDC West ADC Sum Att

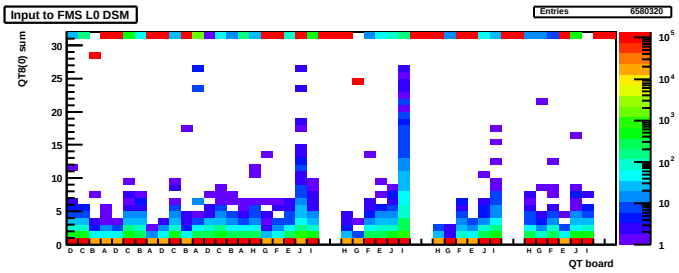

Entries 137090
BBC-L TAC Diff
BCC-S TAC Diff
Entries 137090
BBC-L TAC Diff
VPD TAC Diff
Entries 137090
BBC-L TAC Diff
ZDC TAC Diff
Entries 137090
BBC-S TAC Diff
VPD TAC Diff
Entries 137090
BBC-S TAC Diff
ZDC TAC Diff
Entries 137090
VPD TAC Diff
ZDC TAC Diff

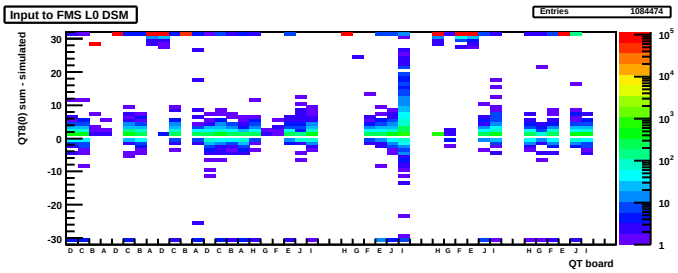

Input to QT1 crate
Entries6666702
ADC
channel

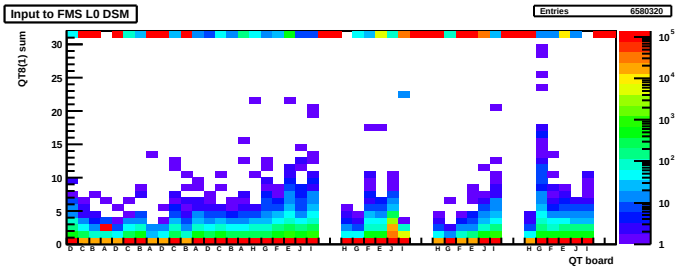

Input to QT2 crate
Entries 3858394
ADC
channel

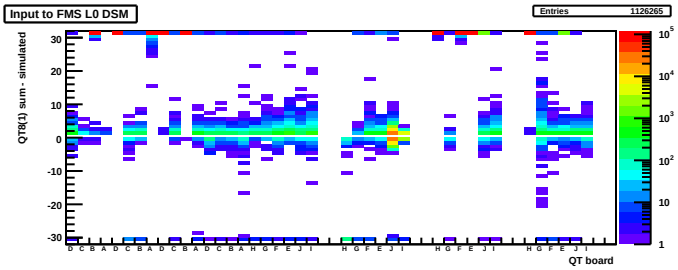

Input to QT3 crate
Entries2735377
ADC
channel

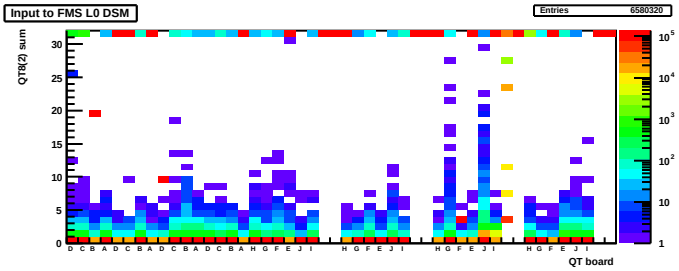

Input to QT4 crate
Entries1747737
ADC
channel

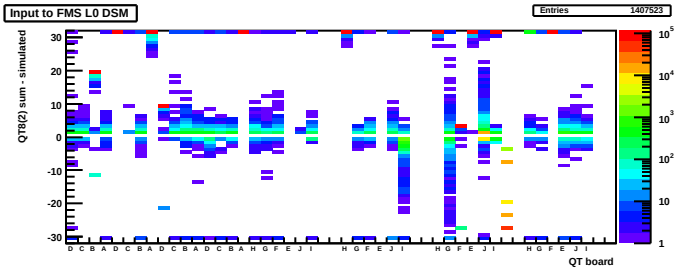

Input to FMS L0 DSM
Entries 6580320
QT8(0) sum
QT board

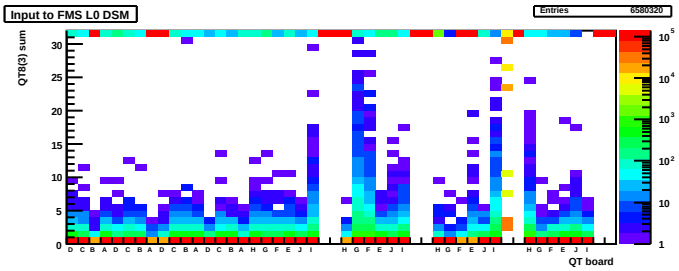

Input to FMS L0 DSM
Entries 1084474
QT8(0) sum - simulated
QT board

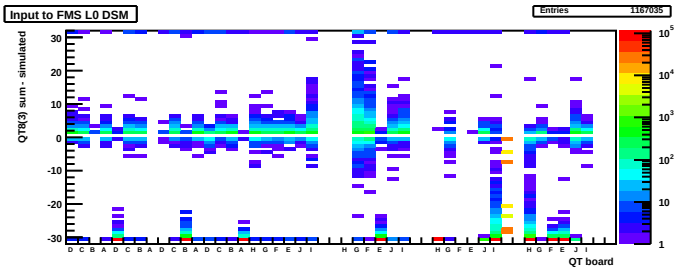

Input to FMS L0 DSM
Entries 6580320
QT8(1) sum
QT board

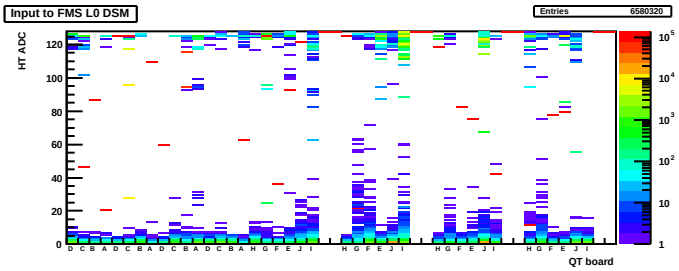

Input to FMS L0 DSM
Entries 1126265
QT8(1) sum - simulated
QT board

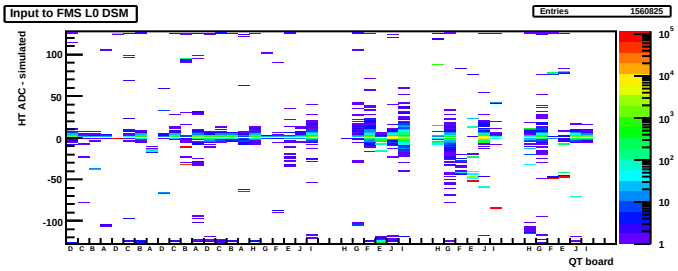

Input to FMS L0 DSM
Entries 6580320
QT8(2) sum
QT board

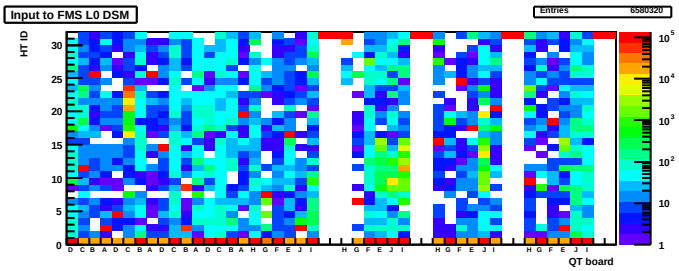

Input to FMS L0 DSM
Entries 1407523
QT8(2) sum - simulated
QT board

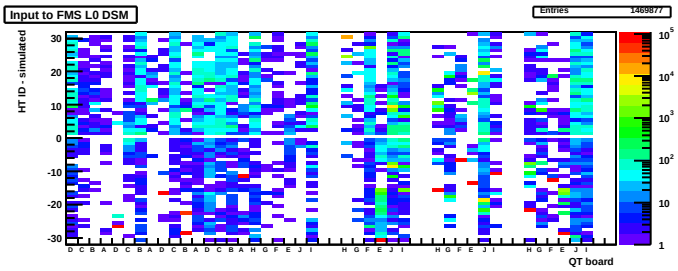

Input to FMS L0 DSM
Entries 6580320
QT8(3) sum
QT board

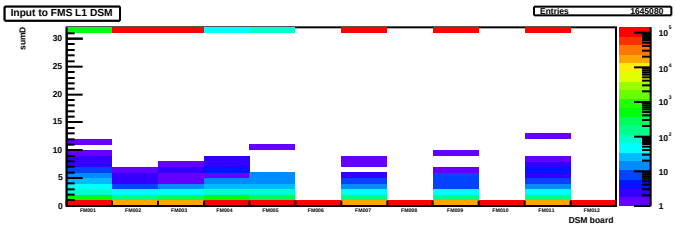

Input to FMS L0 DSM
Entries 1167035
QT8(3) sum - simulated
QT board

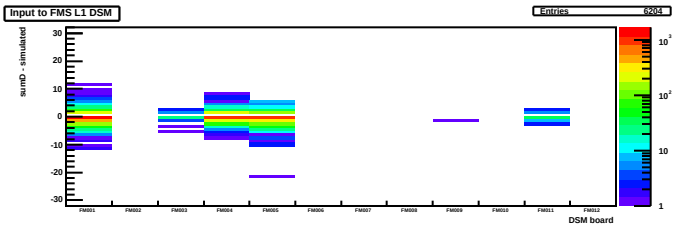

Input to FMS L0 DSM
Entries 6580320
HT ADC
QT board

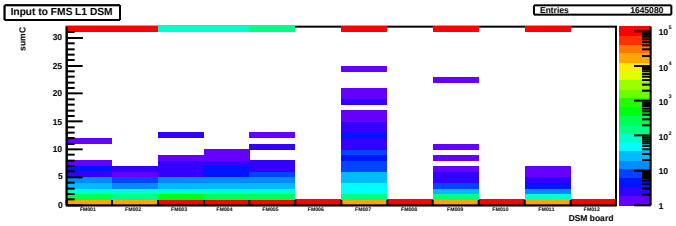

Input to FMS L0 DSM
Entries 1560825
HT ADC - simulated
QT board

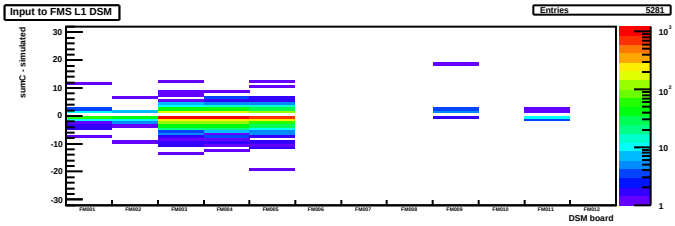

Input to FMS L0 DSM
Entries 6580320
HT ID
QT board

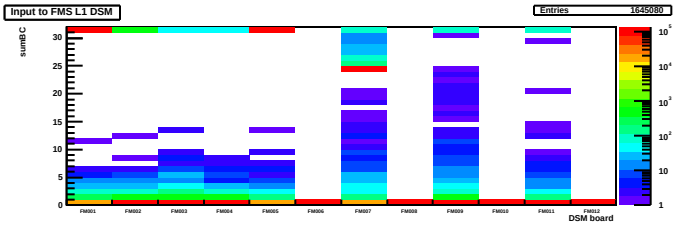

Input to FMS L0 DSM
Entries 1469877
HT ID - simulated
QT board

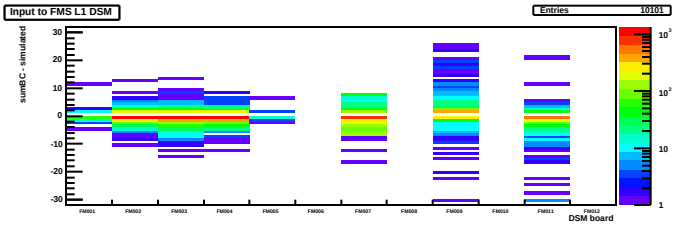

Input to FMS L1 DSM
Entries 1645080
sumD
DSM board

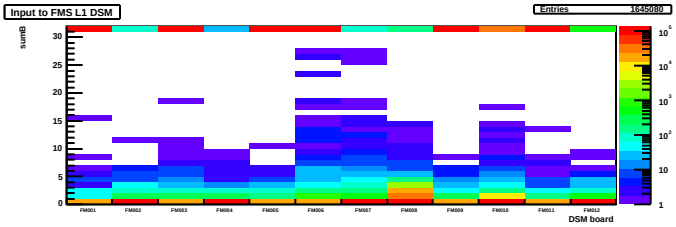

Input to FMS L1 DSM
Entries 6204
sumD - simulated
DSM board

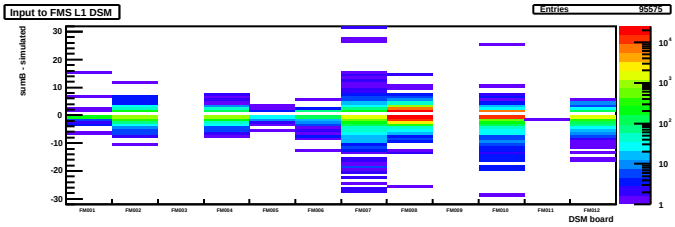

Input to FMS L1 DSM
Entries 1645080
sumC
DSM board

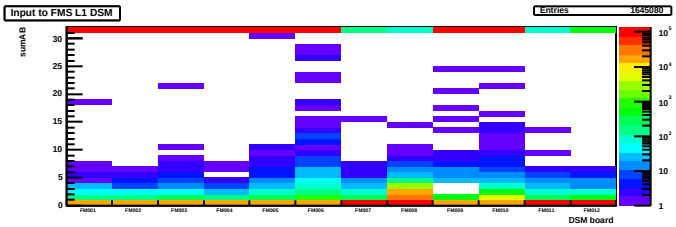

Input to FMS L1 DSM
Entries 5281
sumC - simulated
DSM board

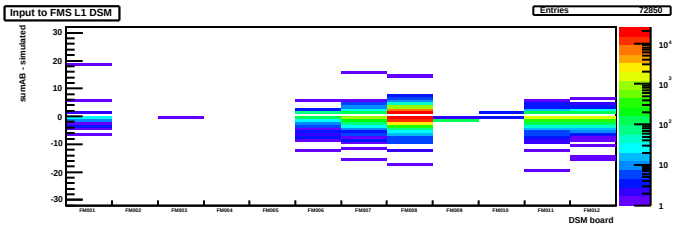

Input to FMS L1 DSM
Entries 1645080
sumBC
DSM board

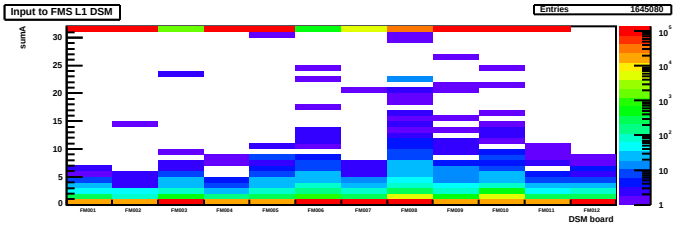

Input to FMS L1 DSM
Entries 10101
sumBC - simulated
DSM board

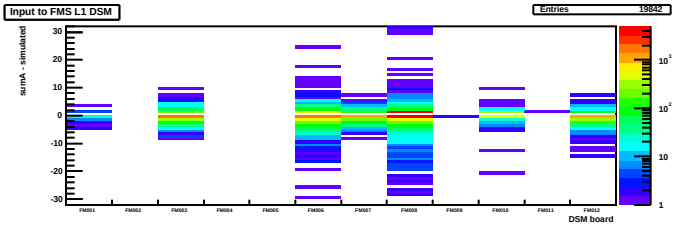

Input to FMS L1 DSM
Entries 1645080
sumB
DSM board

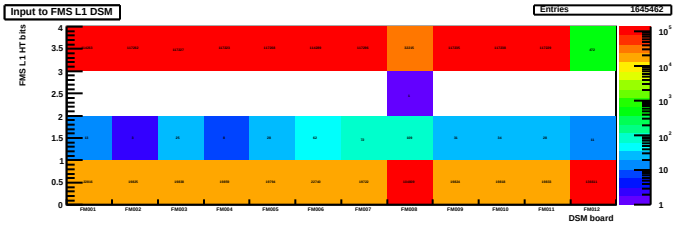

Input to FMS L1 DSM
Entries 95575
sumB - simulated
DSM board

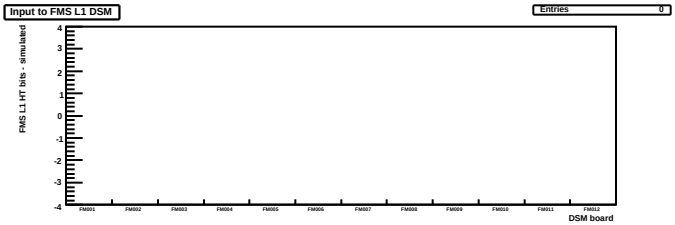

Input to FMS L1 DSM
Entries 1645080
sumAB
DSM board

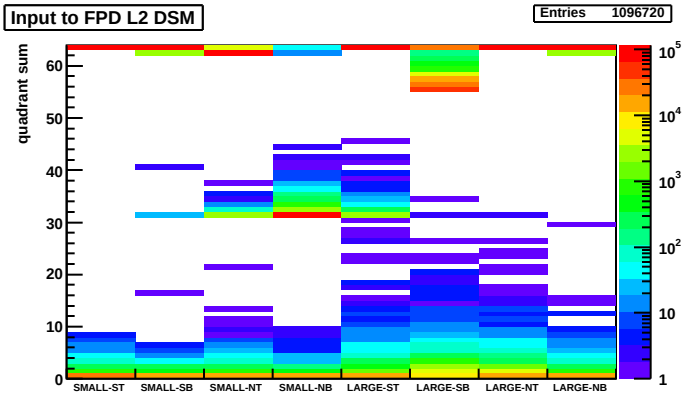

Input to FMS L1 DSM
Entries 72850
sumAB - simulated
DSM board

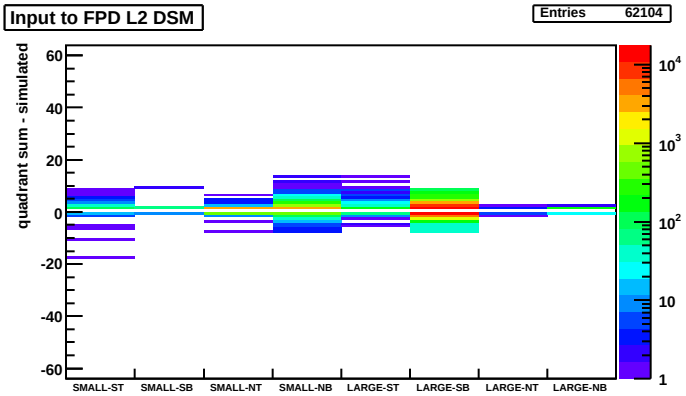

Input to FMS L1 DSM
Entries 1645080
sumA
DSM board

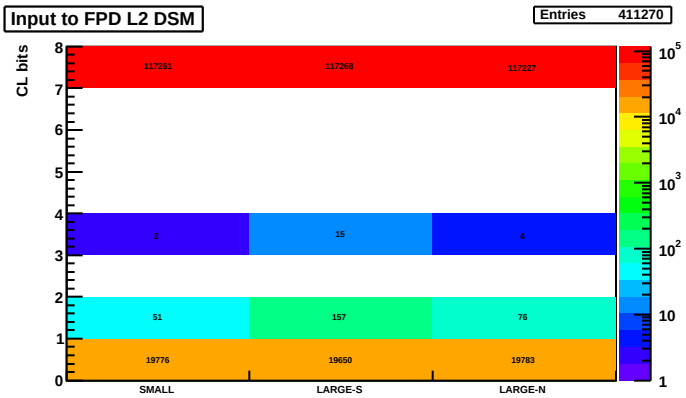

Input to FMS L1 DSM
Entries 19842
sumA - simulated
DSM board

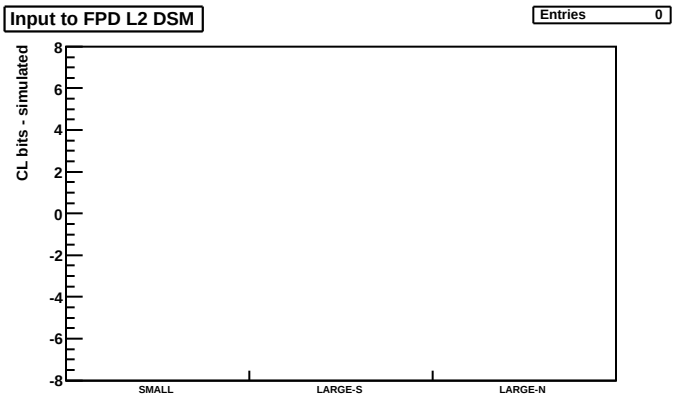

Input to FMS L1 DSM
Entries 1645462
FMS L1 HT bits
DSM board

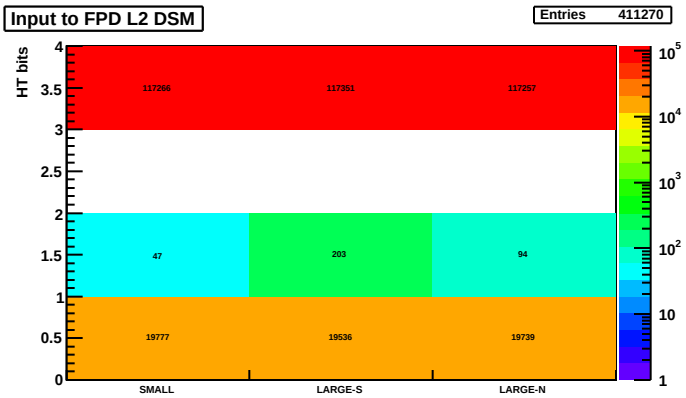

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

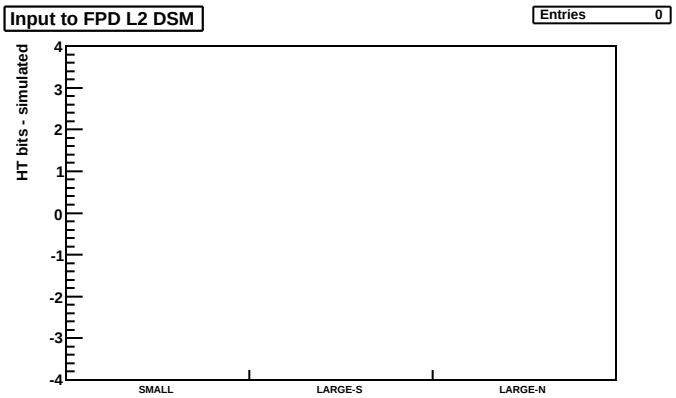

Input to FPD L2 DSM
Entries 1096720
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

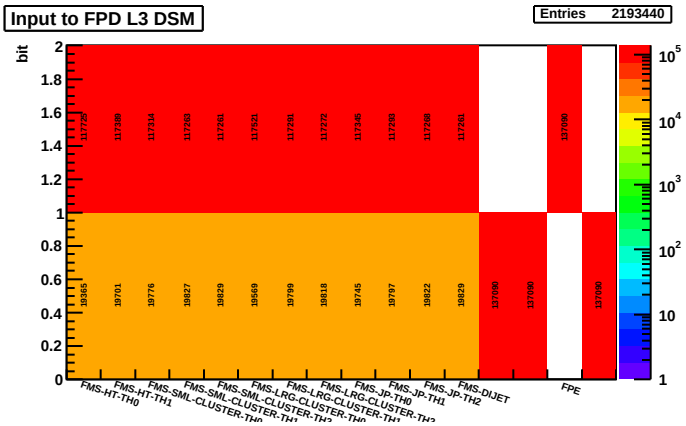

Input to FPD L2 DSM
Entries 62104
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

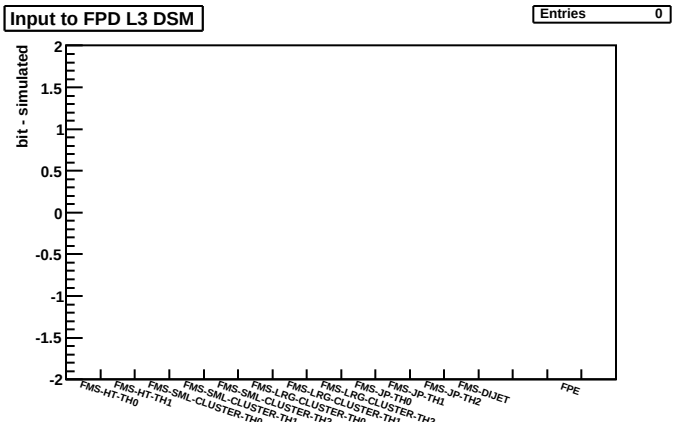

Input to FPD L2 DSM
Entries 411270
CL bits
117261
117268
117227
2
15
4
51
157
76
19776
19650
19783
SMALL
LARGE-S
LARGE-N

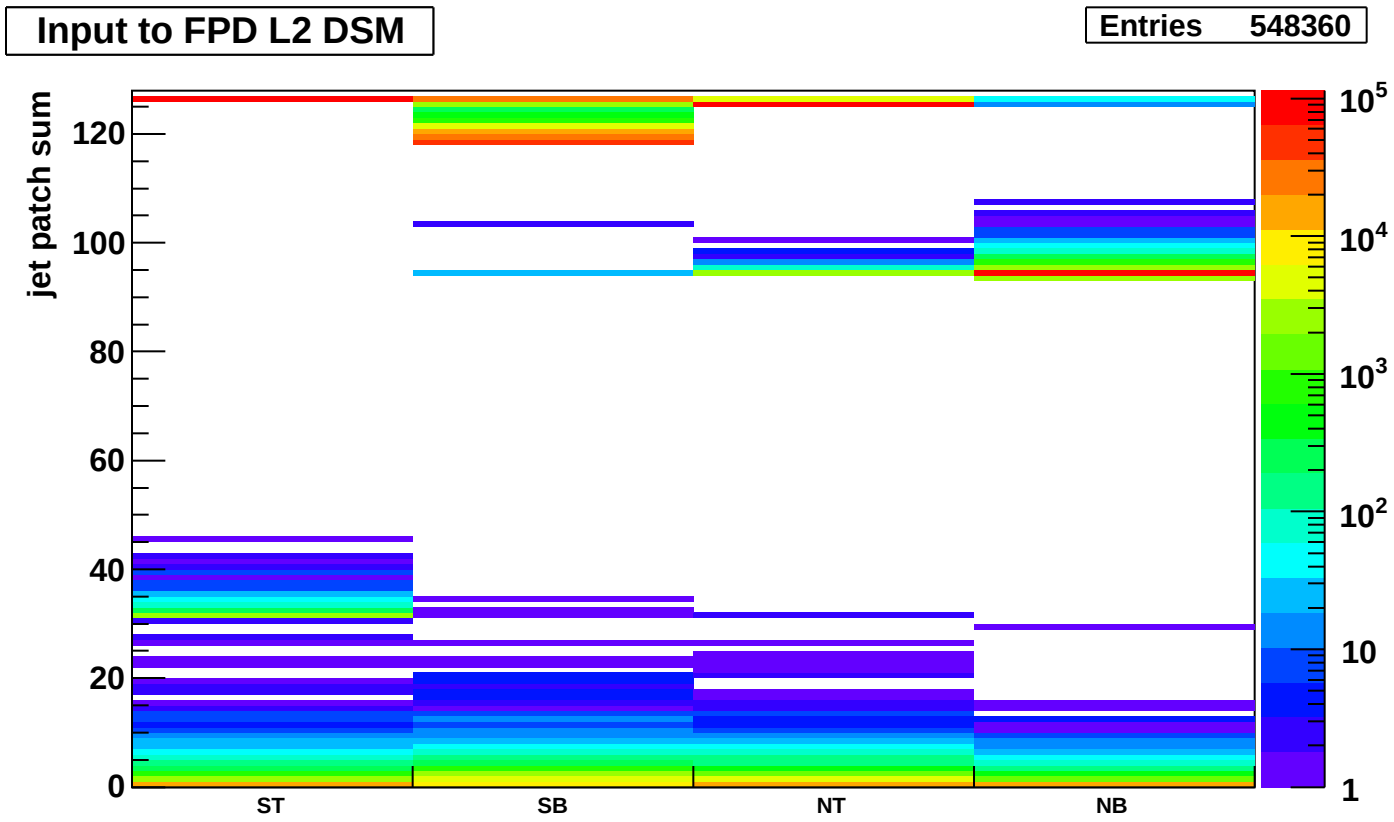

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

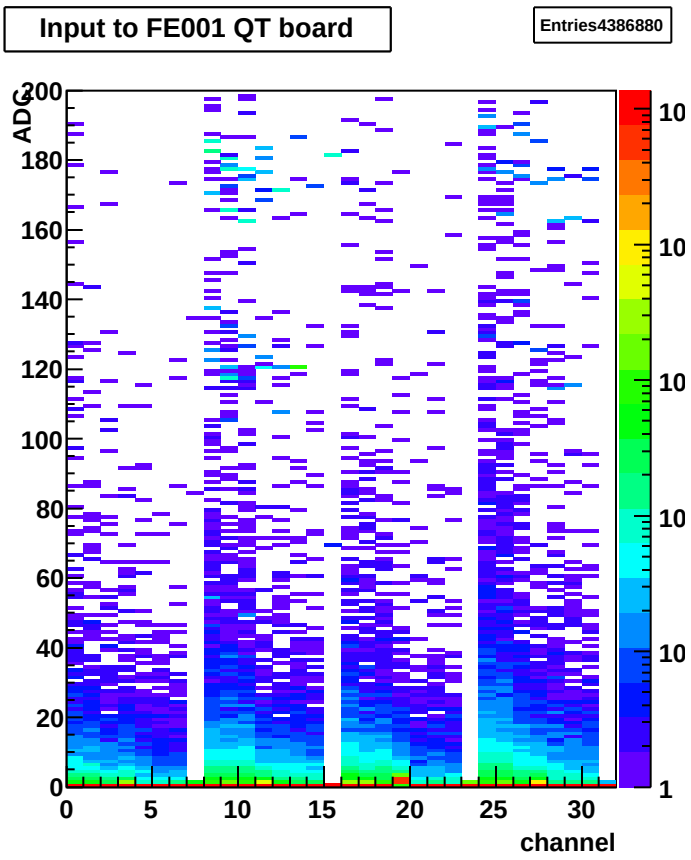

Input to FPD L2 DSM
Entries 411270
HT bits
117266
117351
117257
47
203
94
19777
19536
19739
SMALL
LARGE-S
LARGE-N

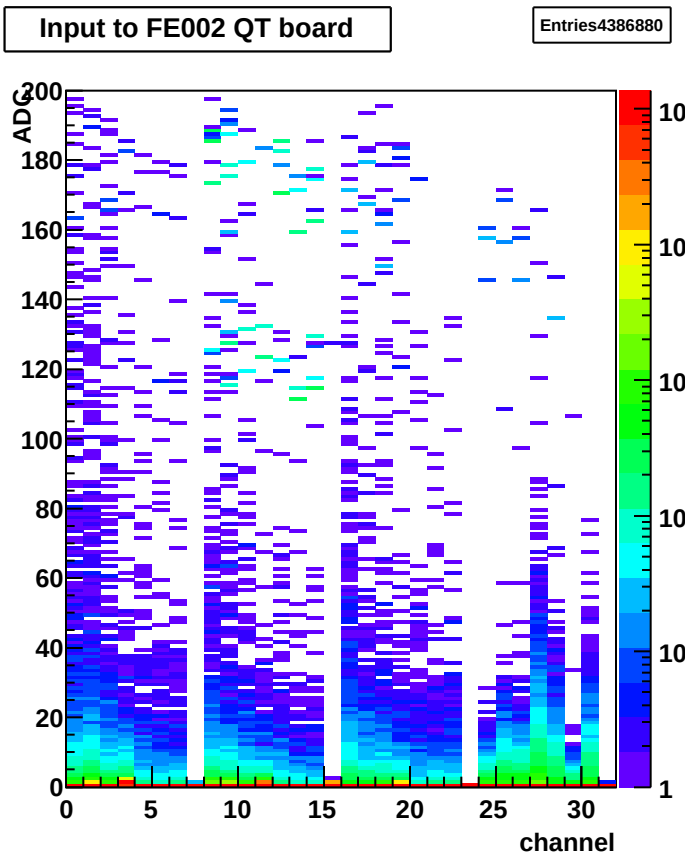

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

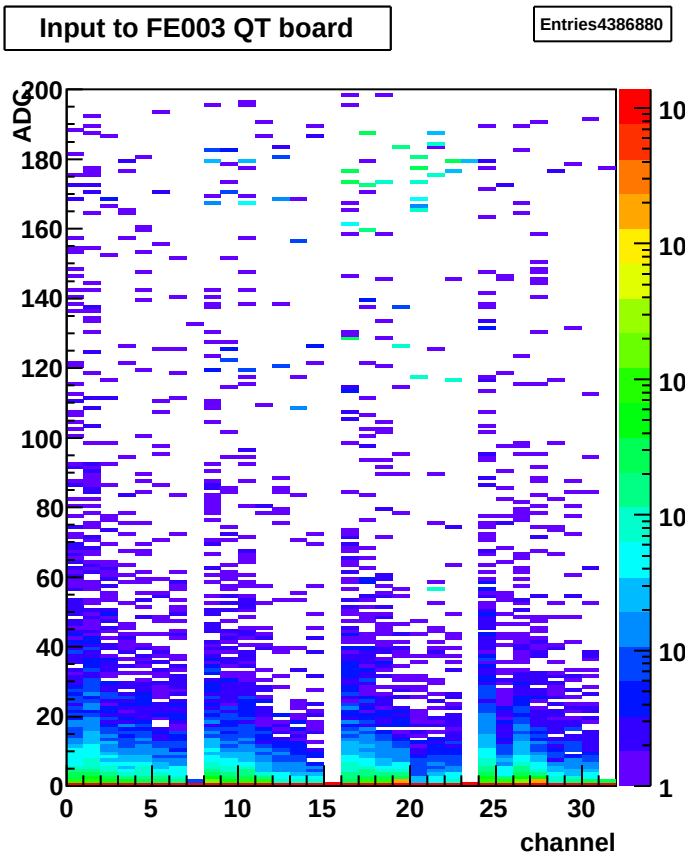

Input to FPD L3 DSM
Entries 2193440
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

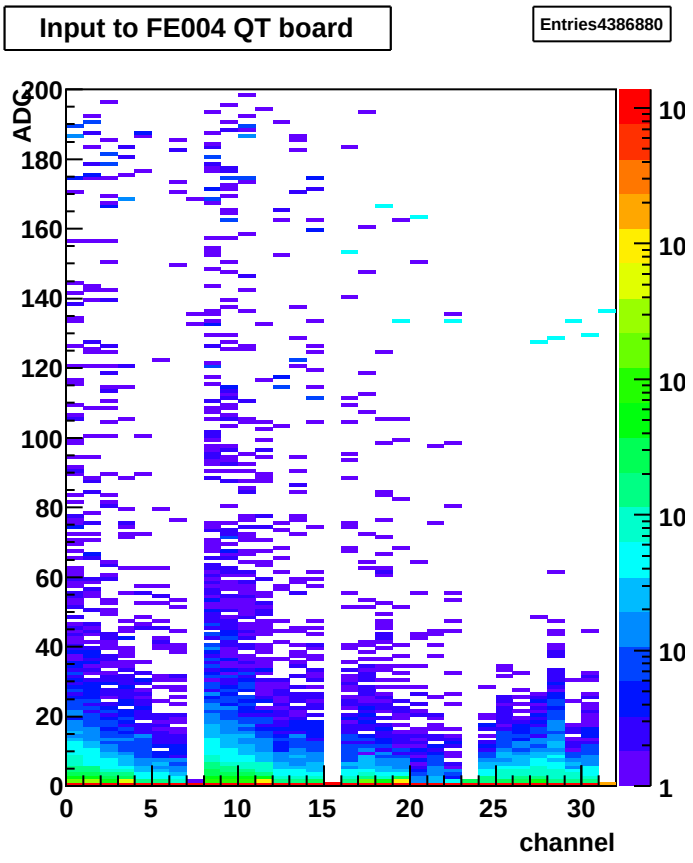

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

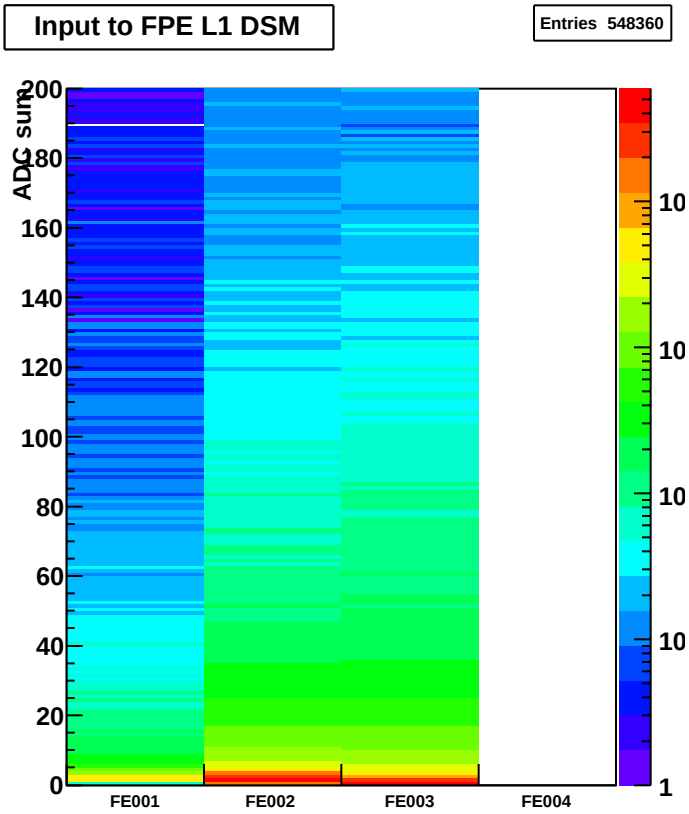

Input to FPD L2 DSM
Entries 548360
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

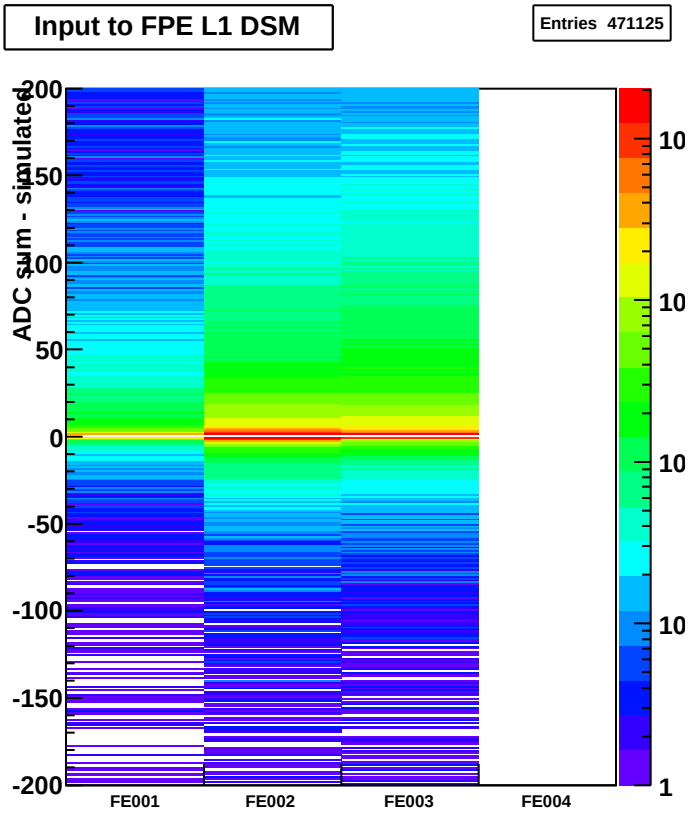

Input to FE001 QT board
Entries4386880
ADC
channel

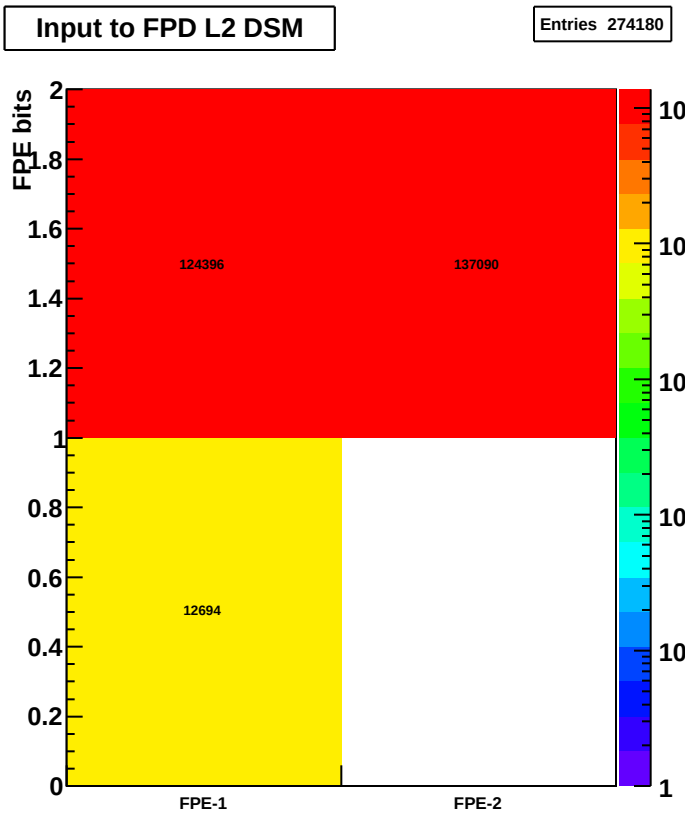

Input to FE002 QT board
Entries4386880
ADC
channel

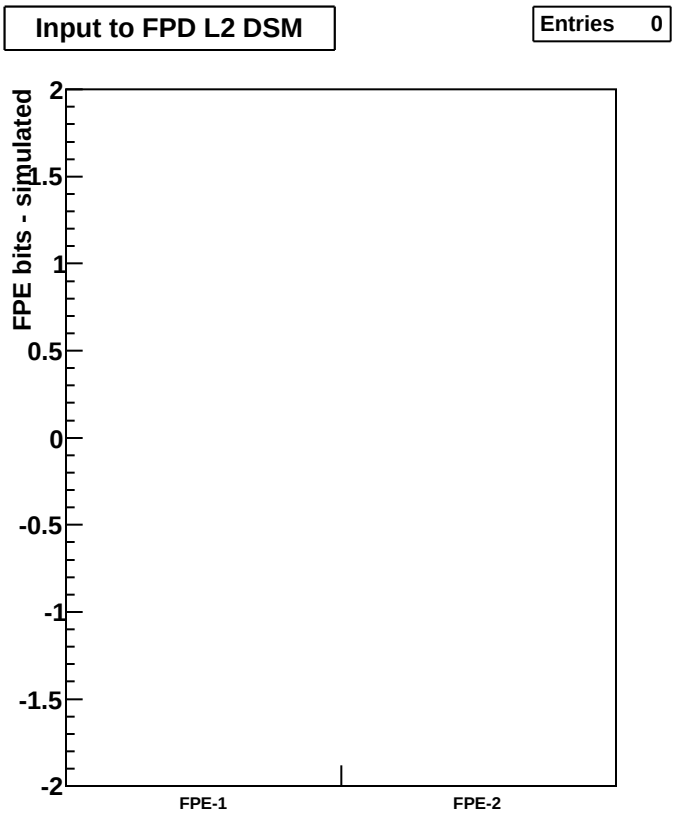

Input to FE003 QT board
Entries4386880
ADC
channel


Input to FE004 QT board
Entries4386880
ADC
channel


Input to FPE L1 DSM
Entries 548360
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 471125
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 274180
FPE bits
124396
137090
12694
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2