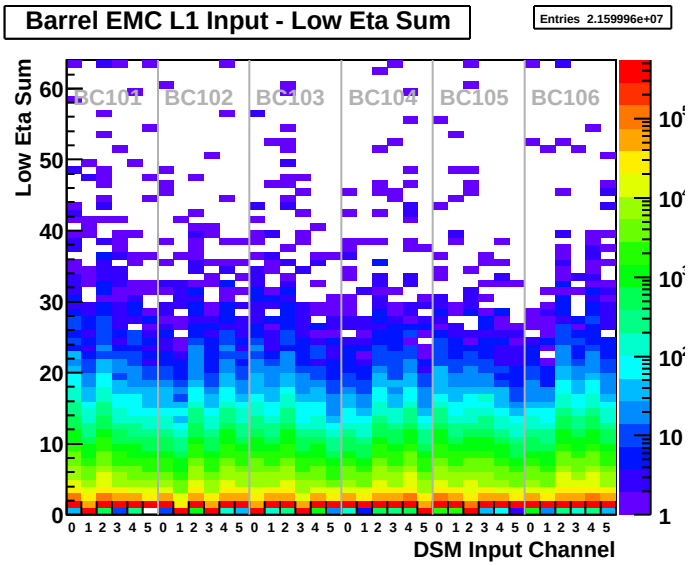
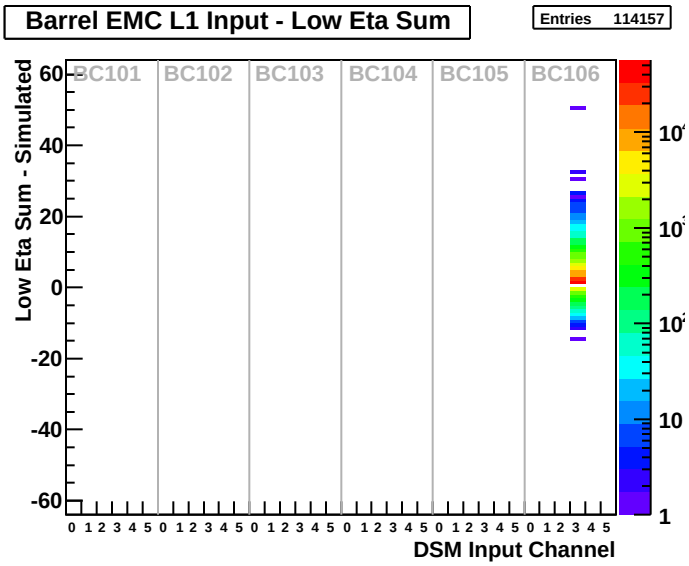
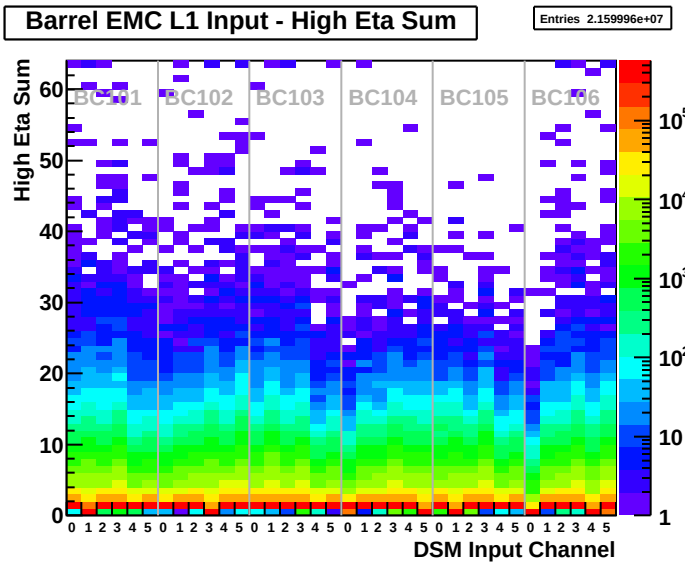
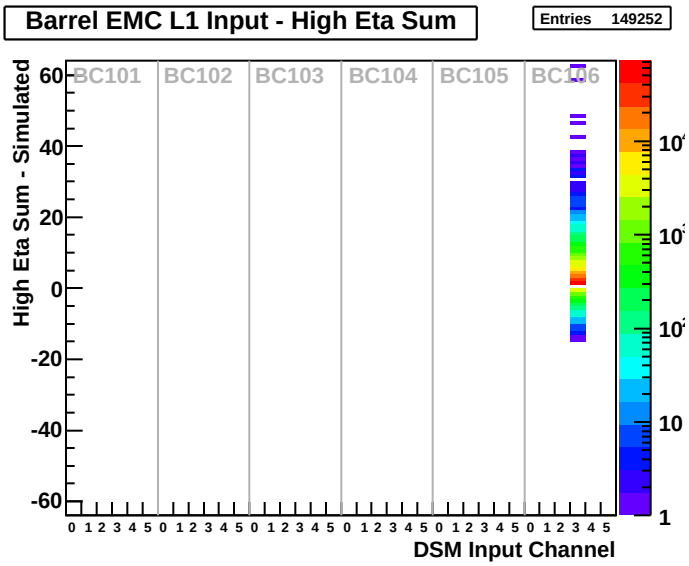
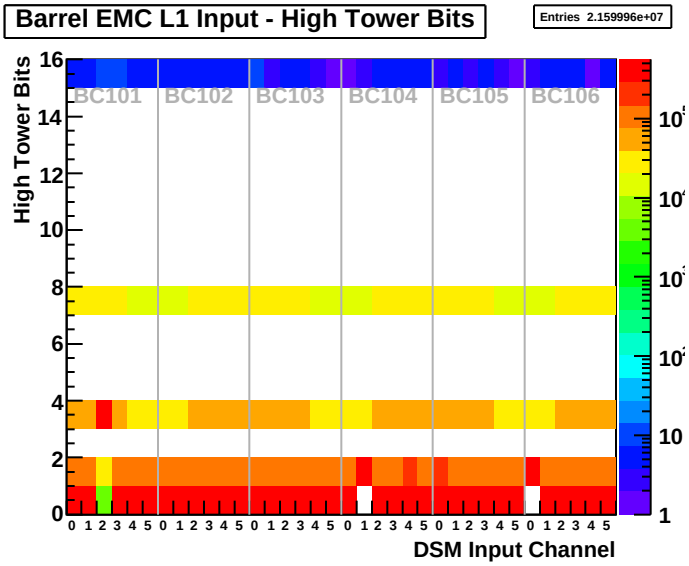
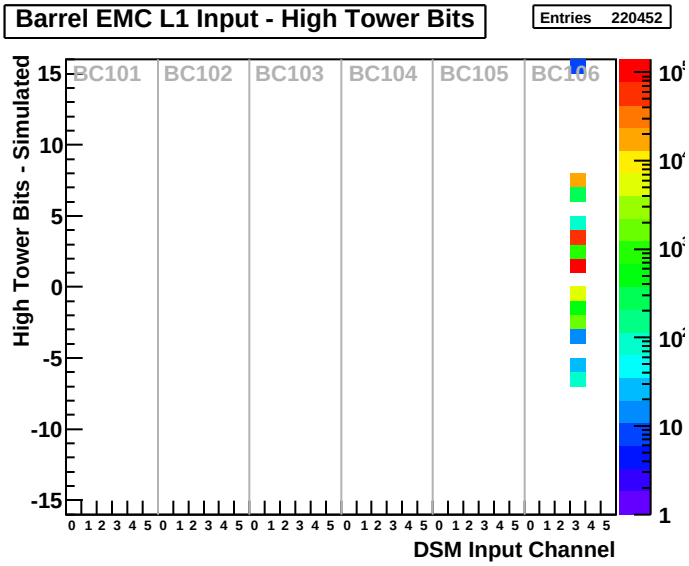

Barrel EMC L1 Input - Low Eta Sum
Entries 2.159996e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

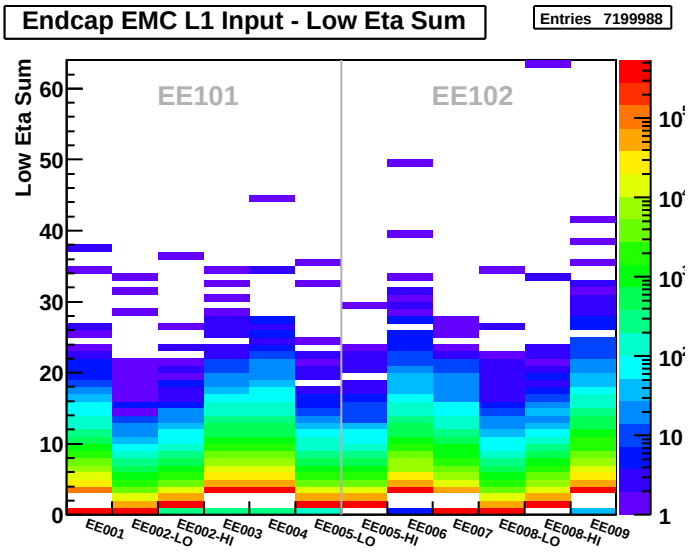

Barrel EMC L1 Input - Low Eta Sum
Entries 114157
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

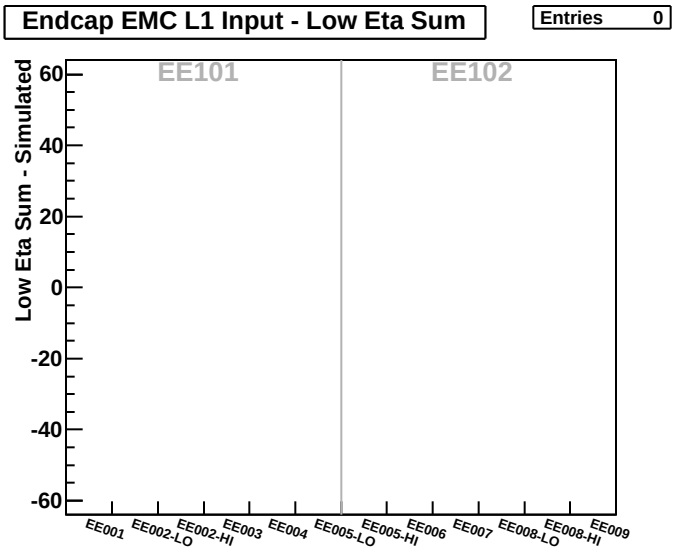

Barrel EMC L1 Input - High Eta Sum
Entries 2.159996e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

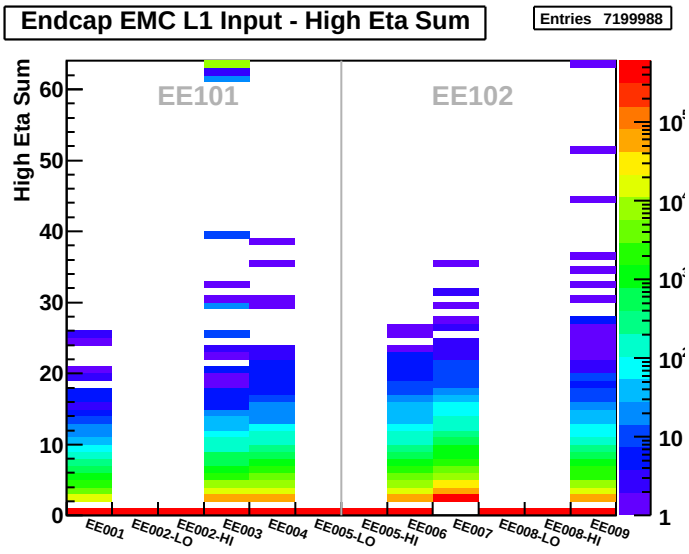

Barrel EMC L1 Input - High Eta Sum
Entries 149252
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

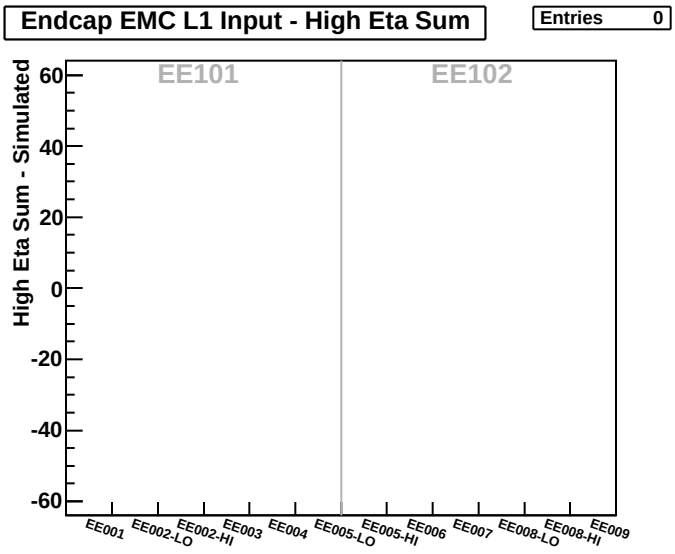

Barrel EMC L1 Input - High Tower Bits
Entries 2.159996e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

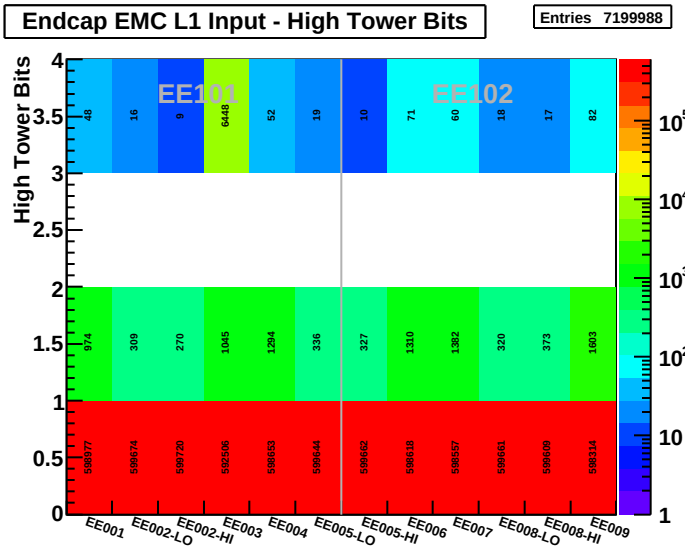

Barrel EMC L1 Input - High Tower Bits
Entries 220452
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

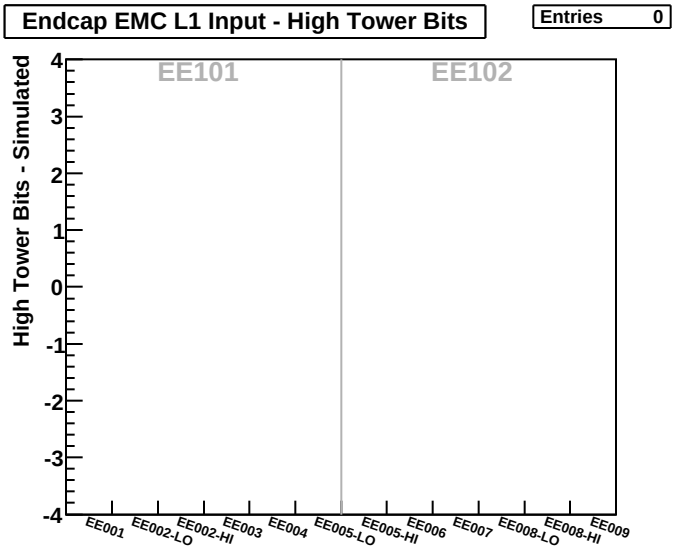

Endcap EMC L1 Input - Low Eta Sum
Entries 7199988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

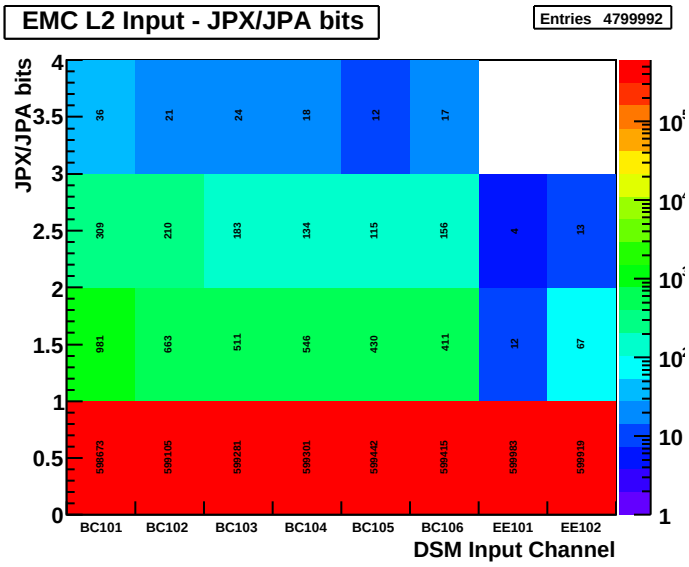

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

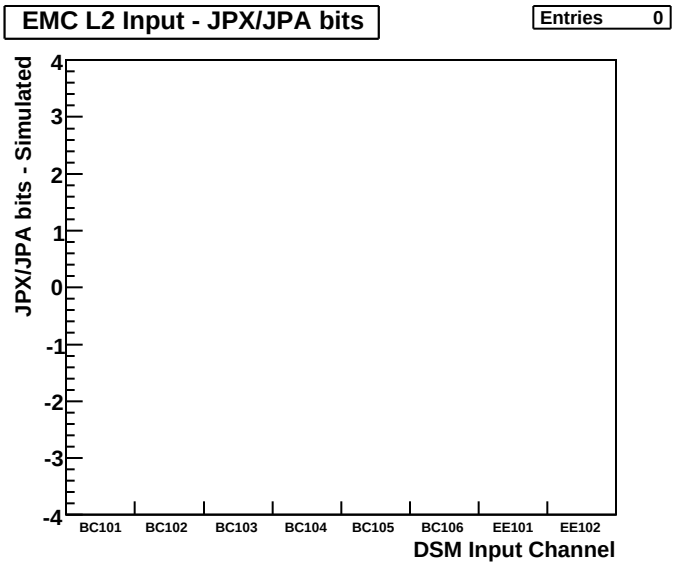

Endcap EMC L1 Input - High Eta Sum
Entries 7199988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

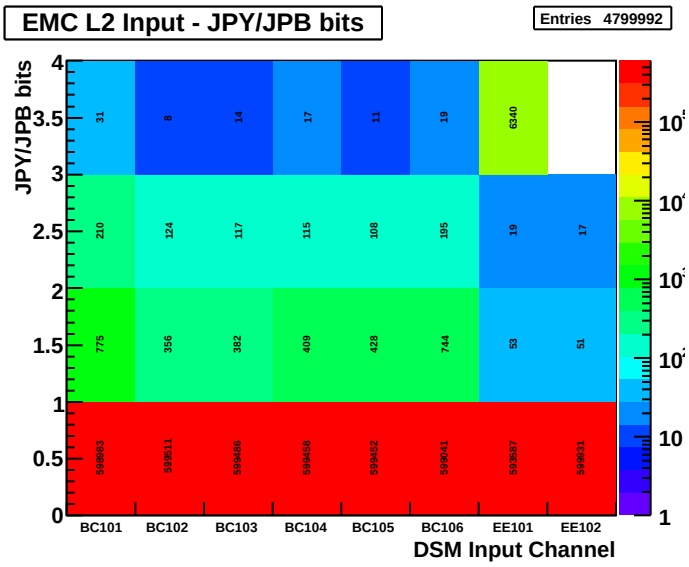

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

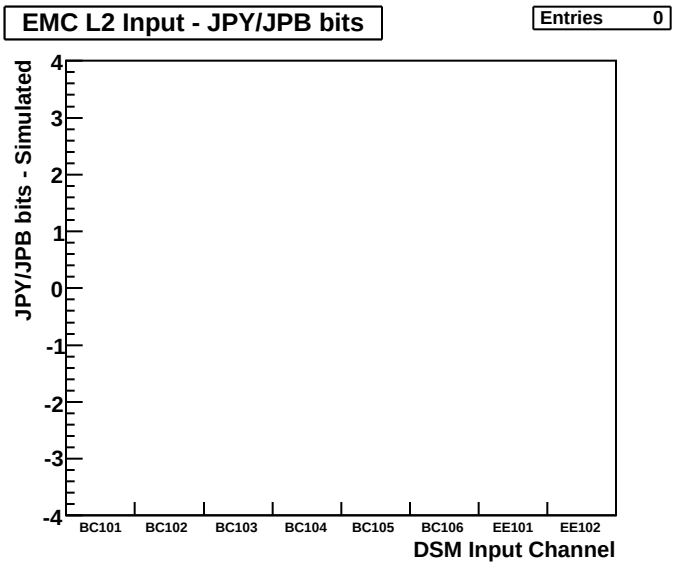

Endcap EMC L1 Input - High Tower Bits
Entries 7199988
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

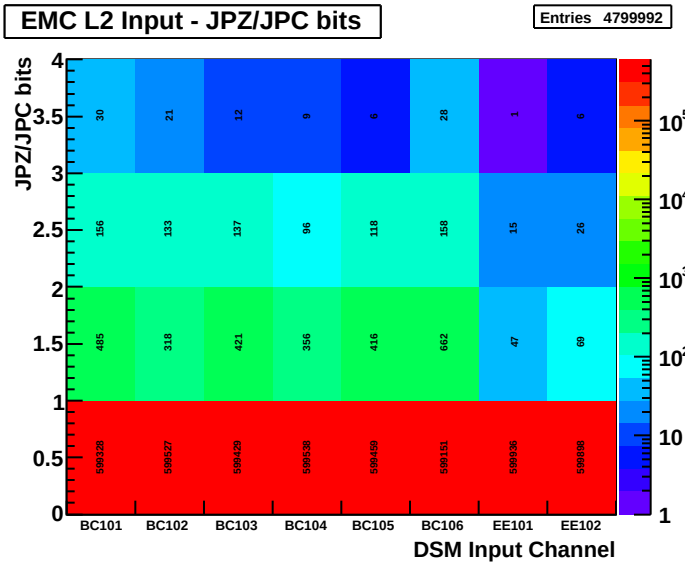

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

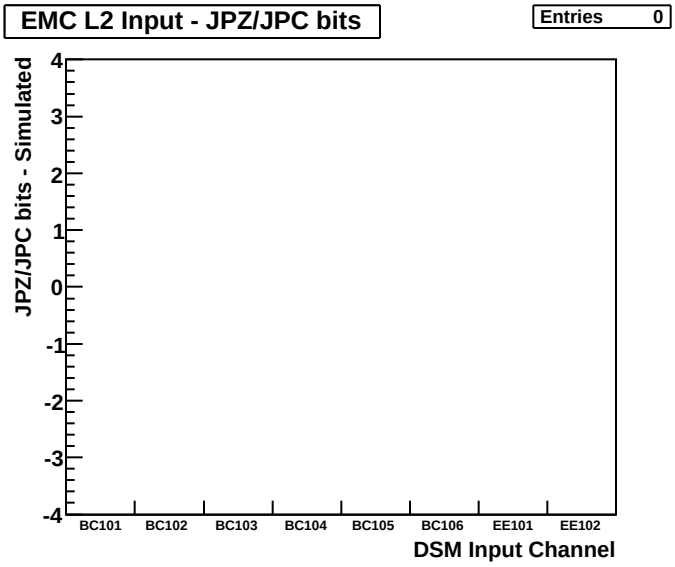

EMC L2 Input - JPX/JPA bits
Entries 4799992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36 21 24 18 12 17
309 210 183 134 115 156 4 13
981 663 511 546 430 411 12 67
598673 599105 599281 599301 599442 599415 599983 599919

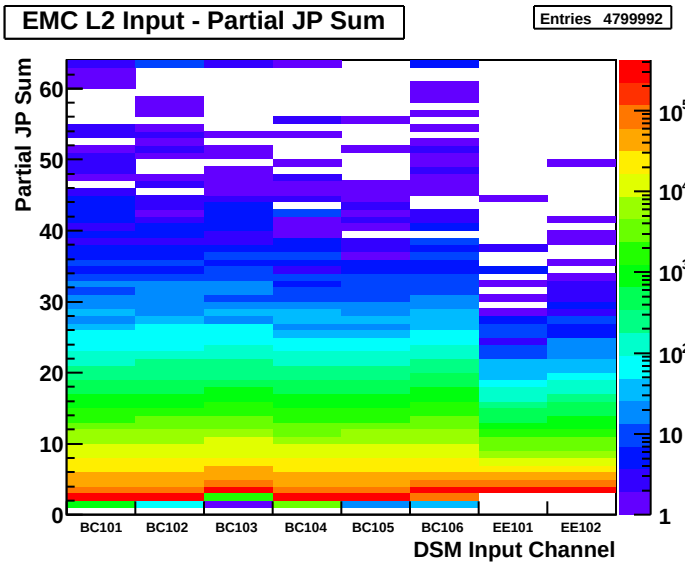

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

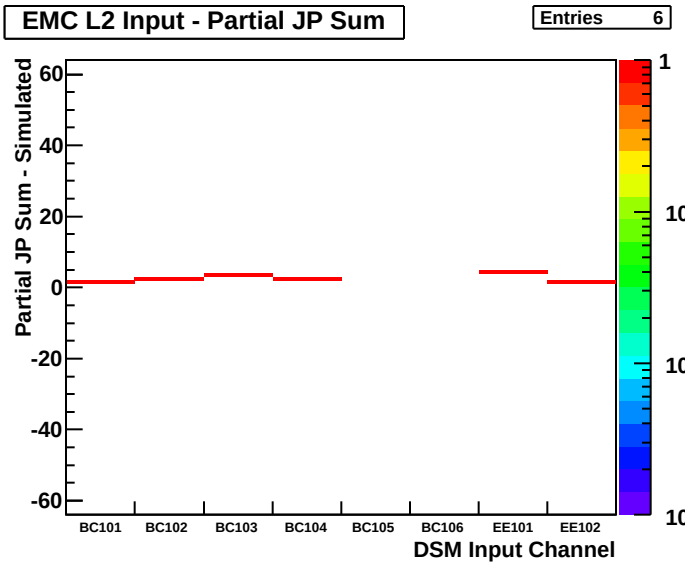

EMC L2 Input - JPY/JPB bits
Entries 4799992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
31 8 14 17 11 19 6340
210 124 117 115 108 195 19 17
775 356 382 409 428 744 53 51
599863 599511 599486 599458 599452 599041 593587 599931

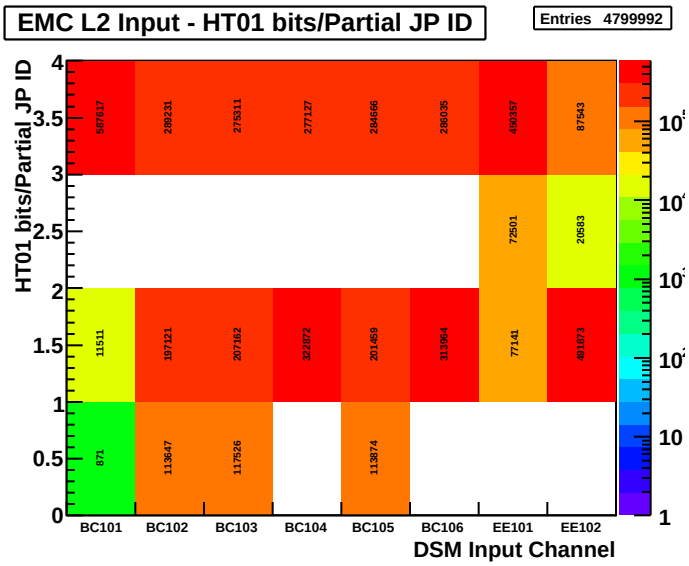

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

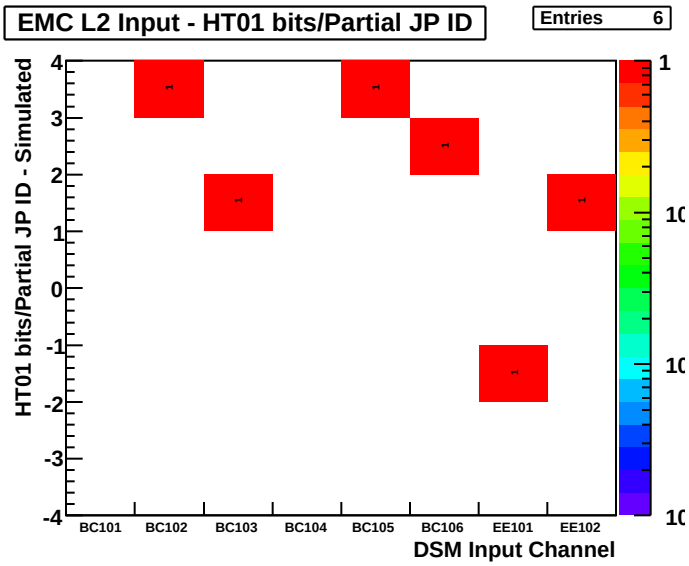

EMC L2 Input - JPZ/JPC bits
Entries 4799992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
30 21 12 9 6 28 1 6
156 133 137 96 118 158 15 26
485 318 421 356 416 662 47 69
599328 599527 599429 599538 599459 599151 599936 599886

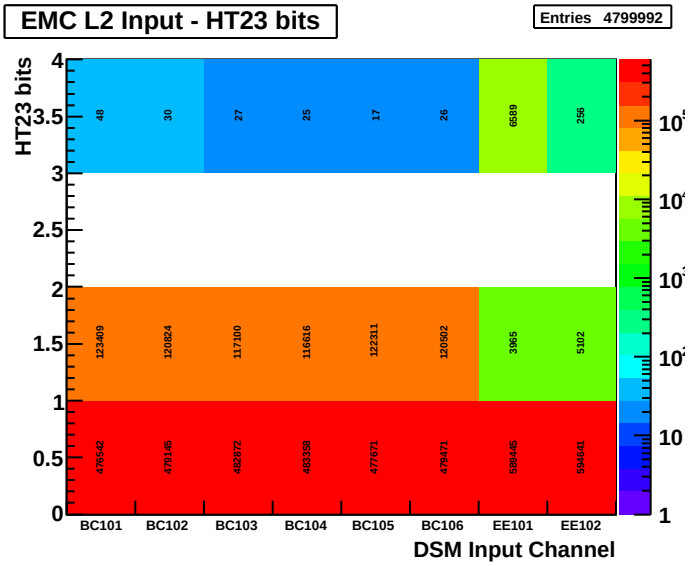

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

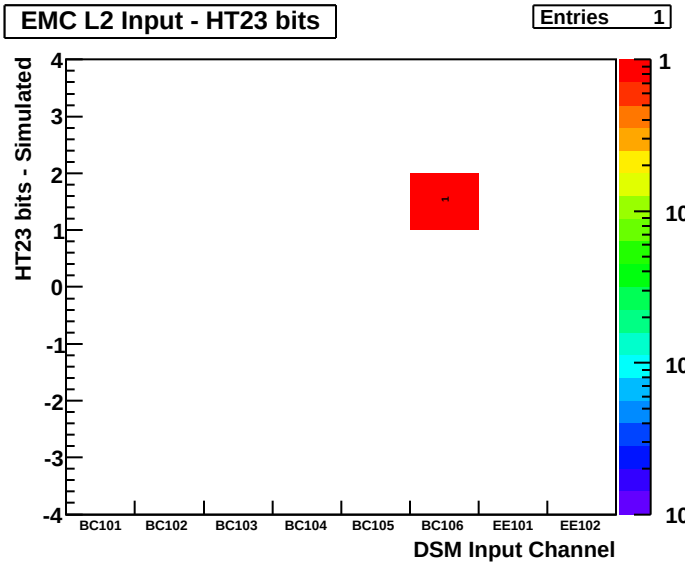

EMC L2 Input - Partial JP Sum
Entries 4799992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

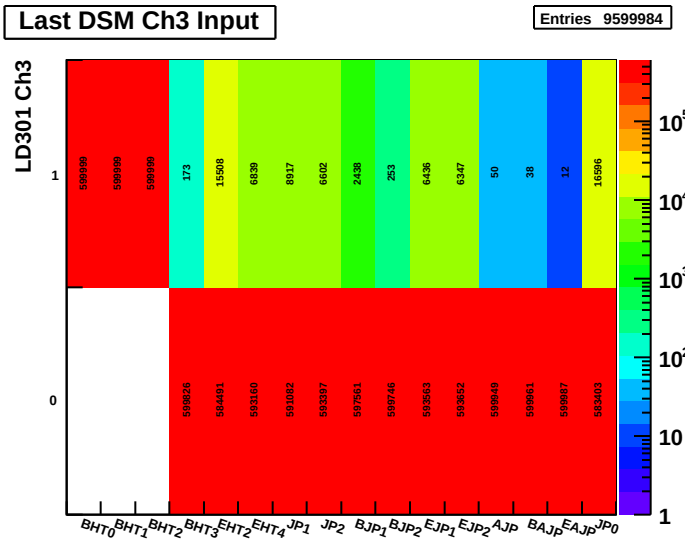

EMC L2 Input - Partial JP Sum
Entries 6
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

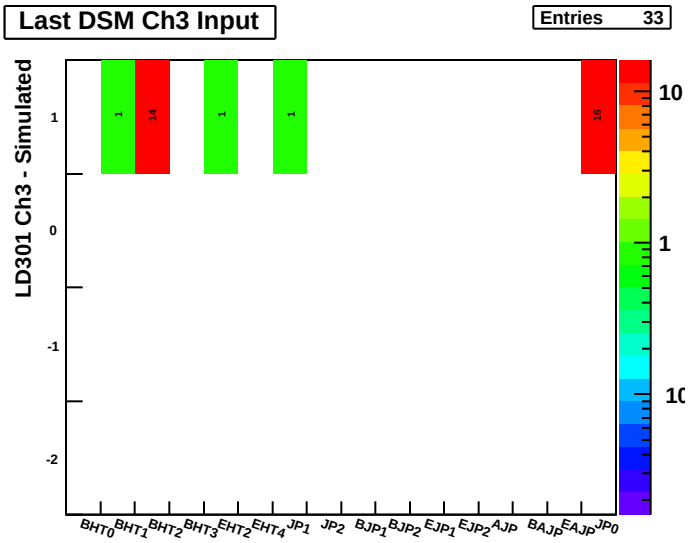

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4799992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

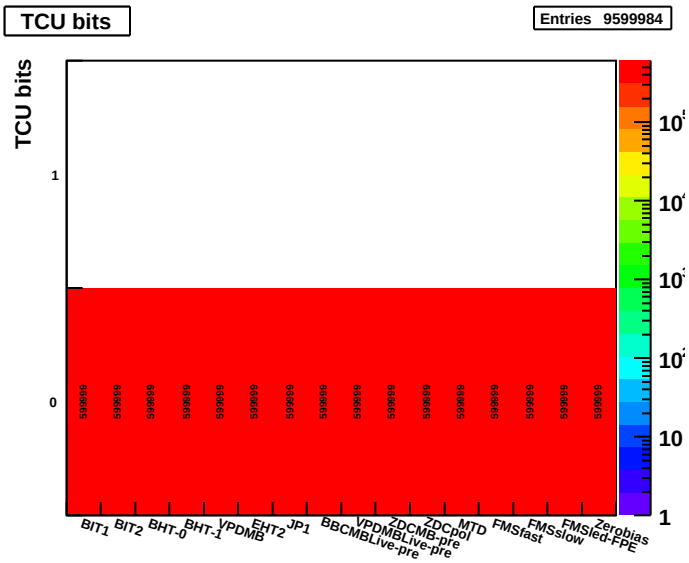

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

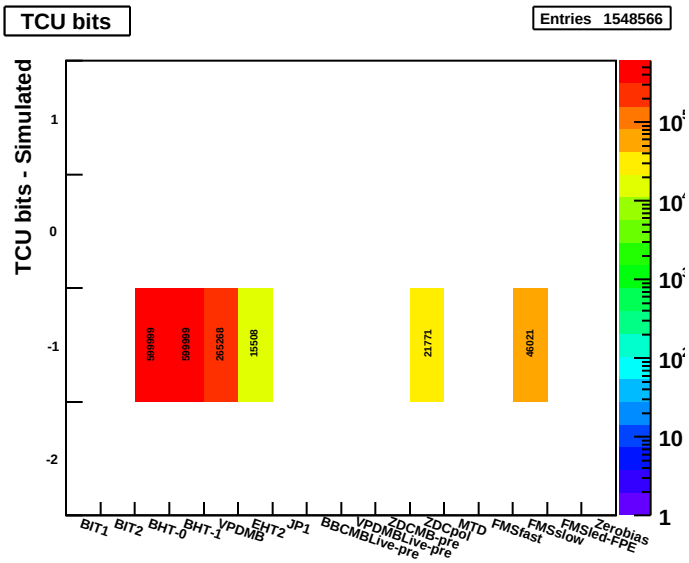

EMC L2 Input - HT23 bits
Entries 4799992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

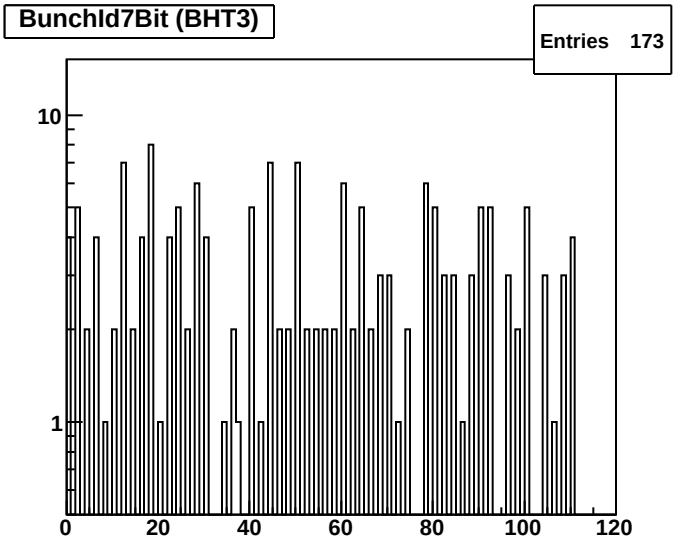

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

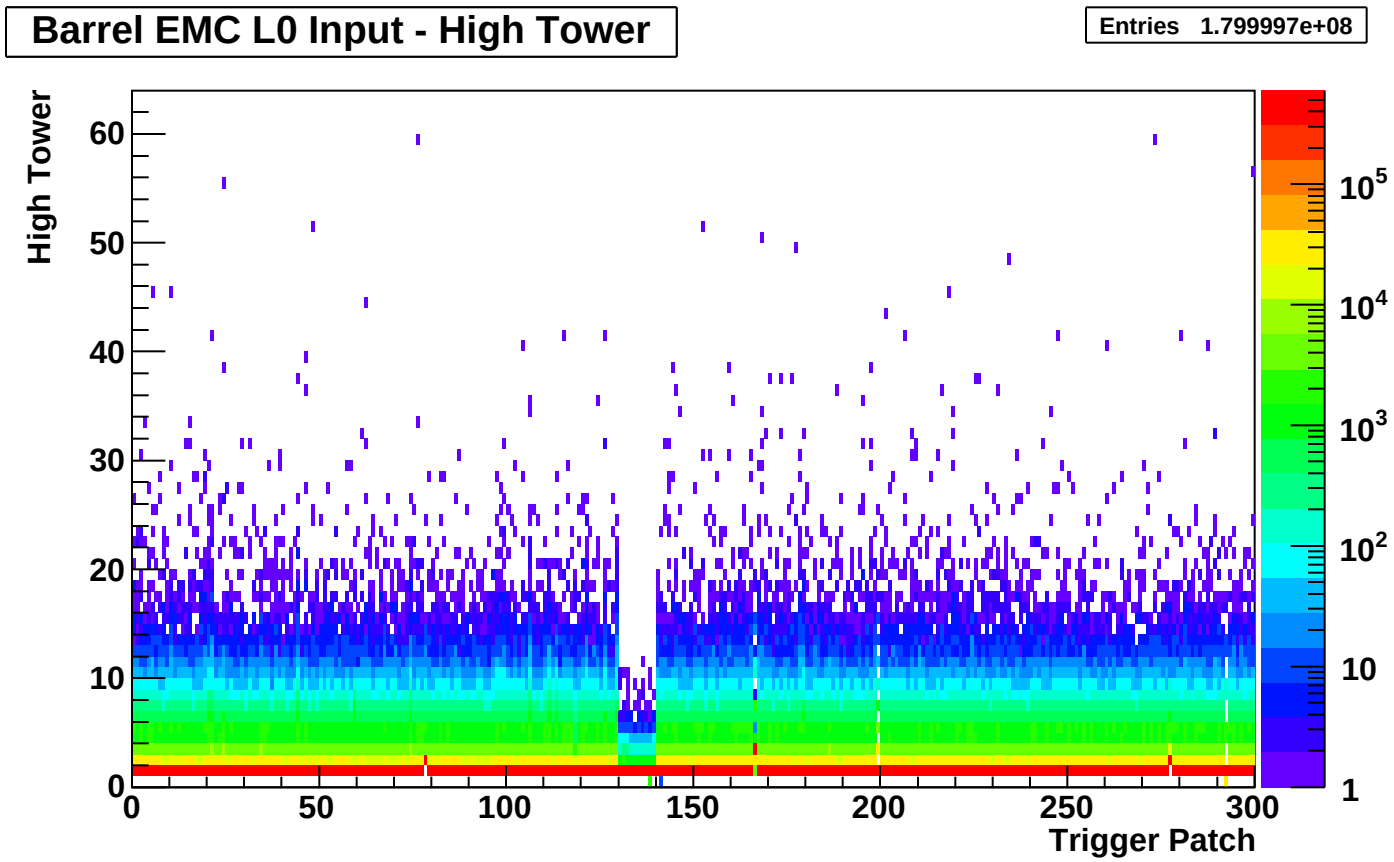

Last DSM Ch3 Input
Entries 9599984
LD301 Ch3
1
0
599999 599999 599999 173 15508 6839 8917 6602 2438 253 6436 6347 50 38 12 16596
598826 594491 593160 591082 593397 597561 599746 593563 593652 599949 599961 599987 583403
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0
10
1

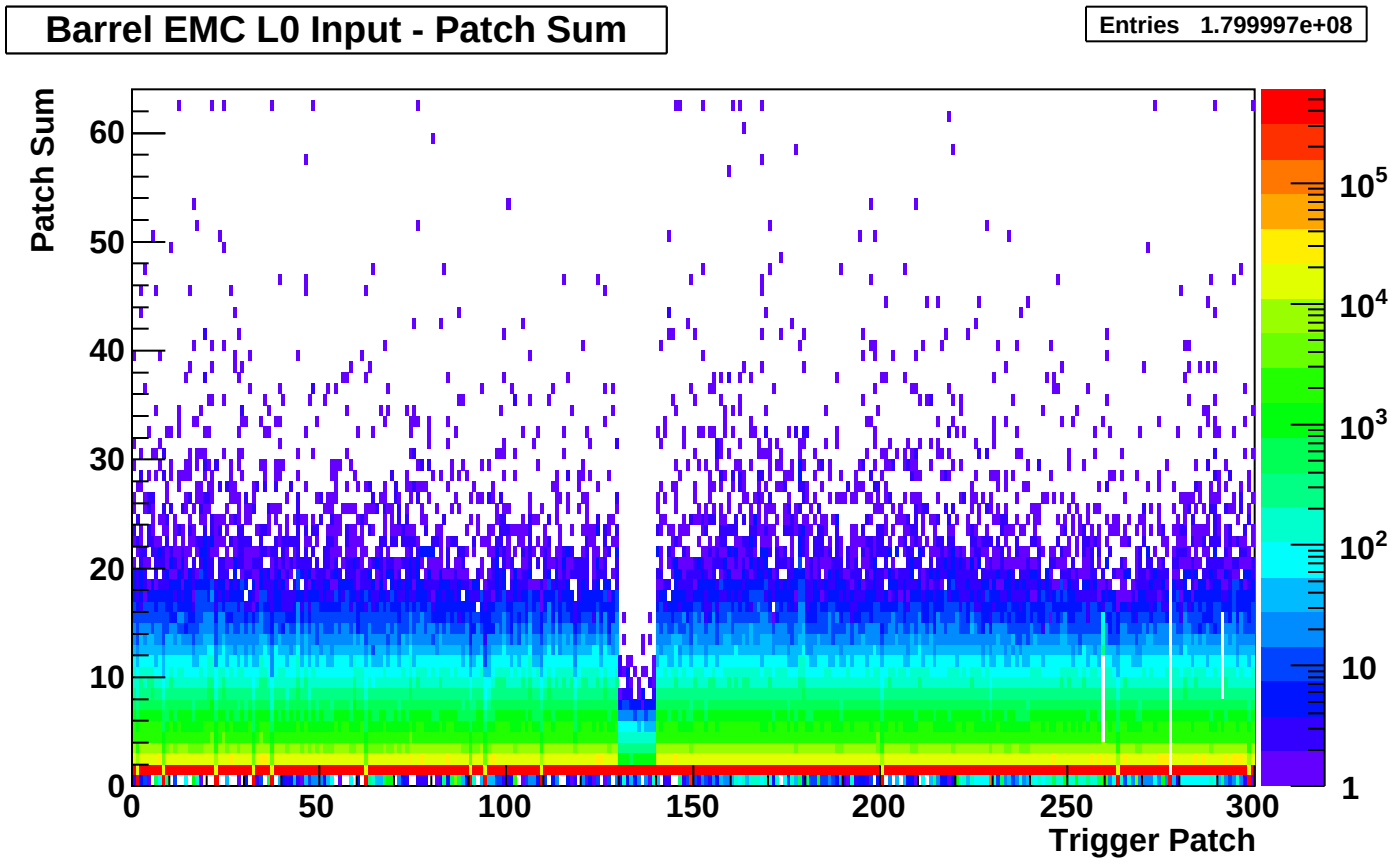

Last DSM Ch3 Input
Entries 33
LD301 Ch3 - Simulated
1
0
-1
-2
1 14 1 1 16
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0
10
1

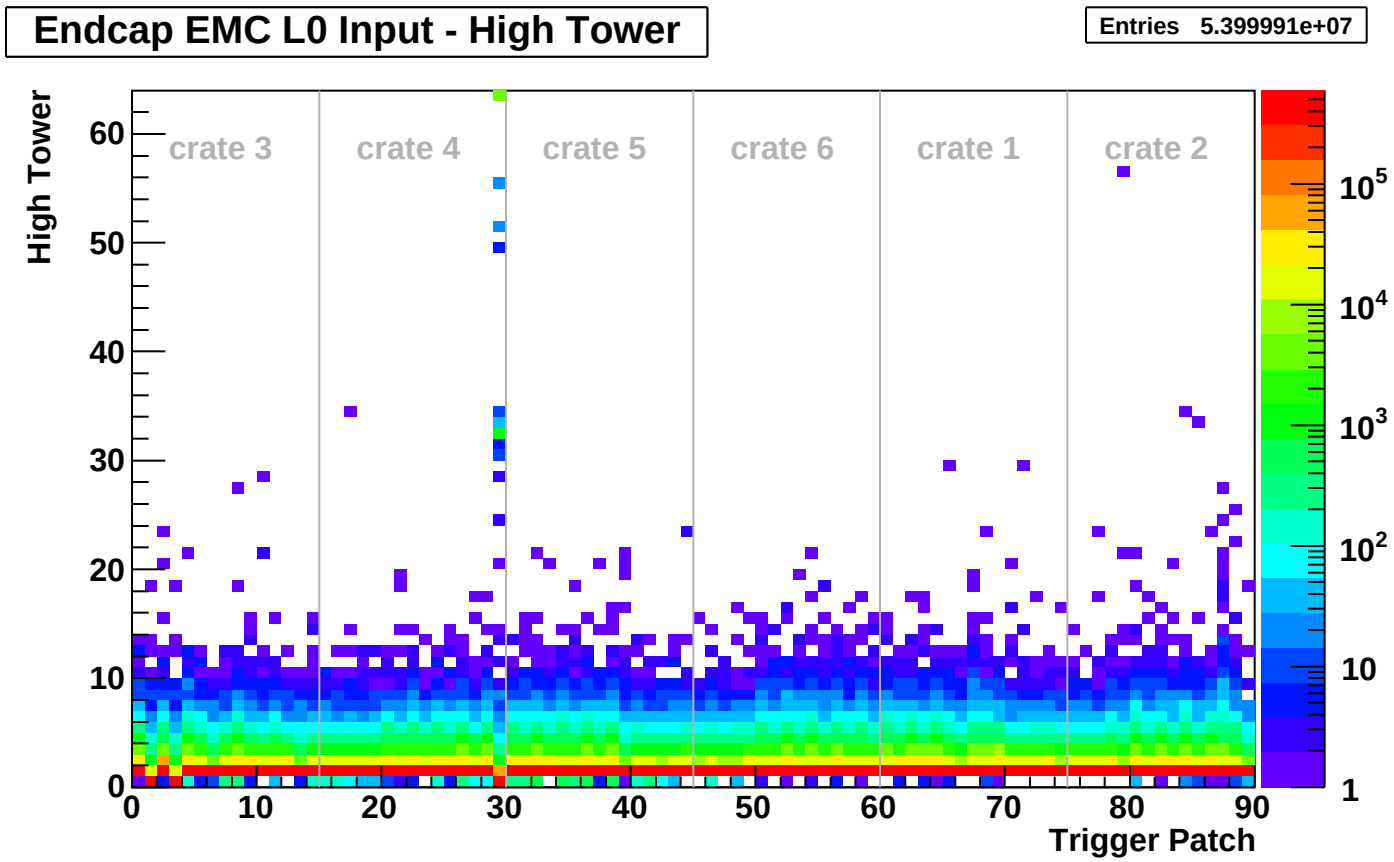

TCU bits
Entries 9599984
TCU bits
1
0
599999 599999 599999 599999 599999 599999 599999 599999 599999 599999 599999 599999 599999 599999 599999
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1

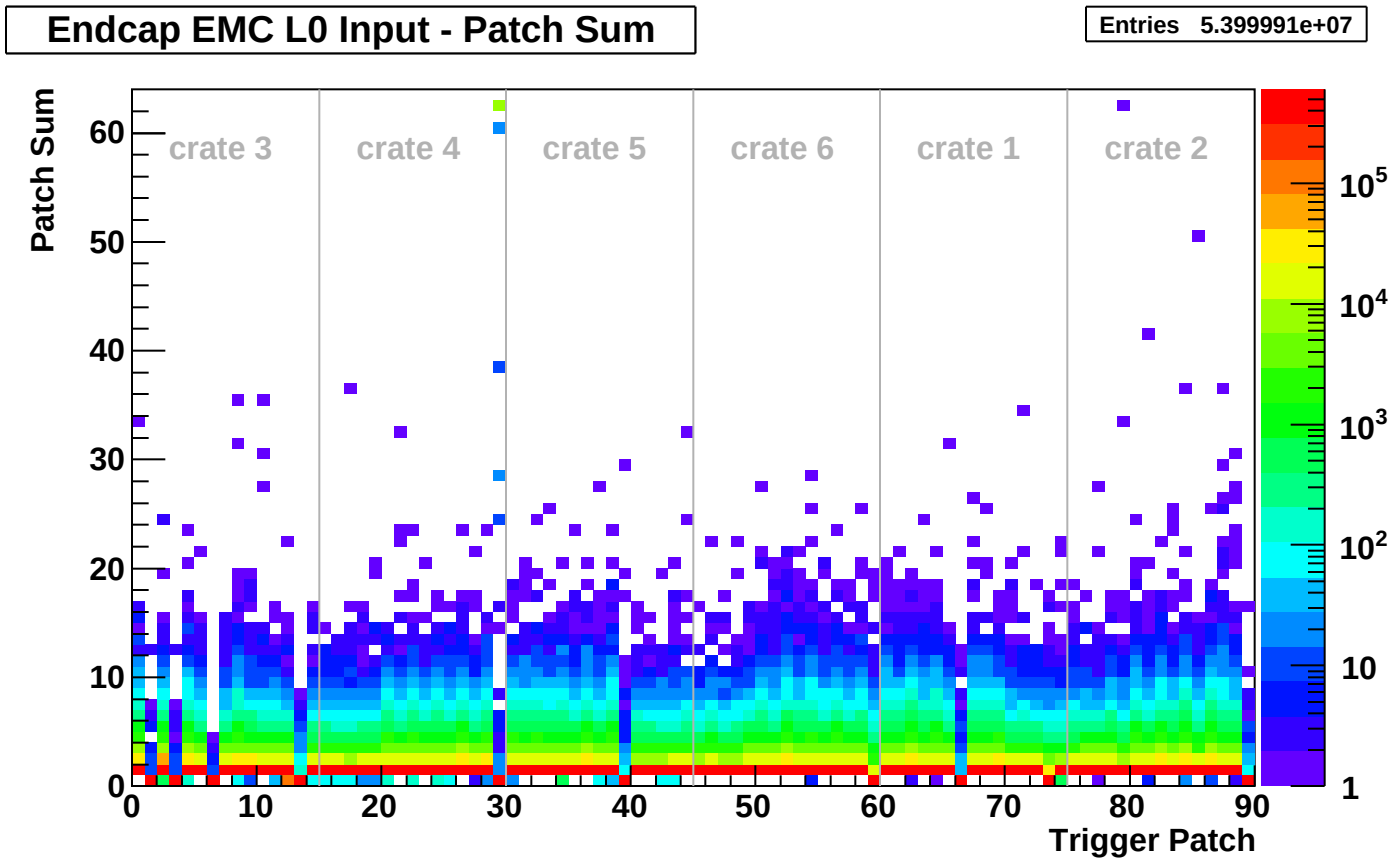

TCU bits
Entries 1548566
TCU bits - Simulated
1
0
-1
-2
599999 599999 265268 15508 21771 46021
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1

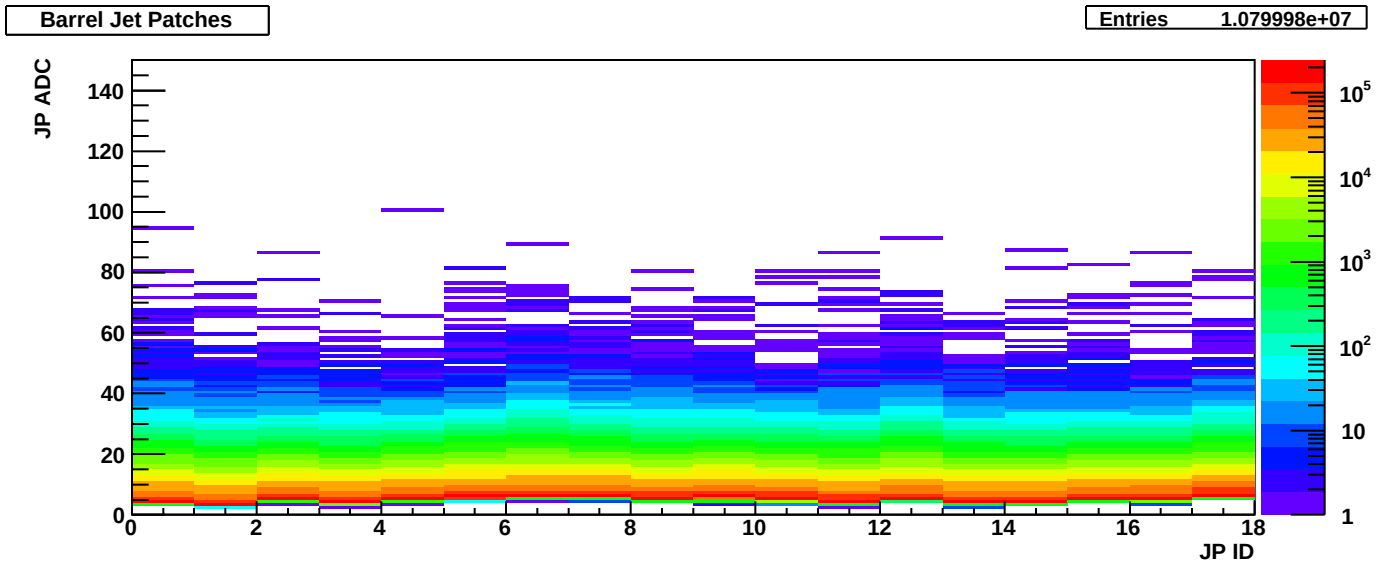

BunchId7Bit (BHT3)
Entries 173
10
1
0 20 40 60 80 100 120

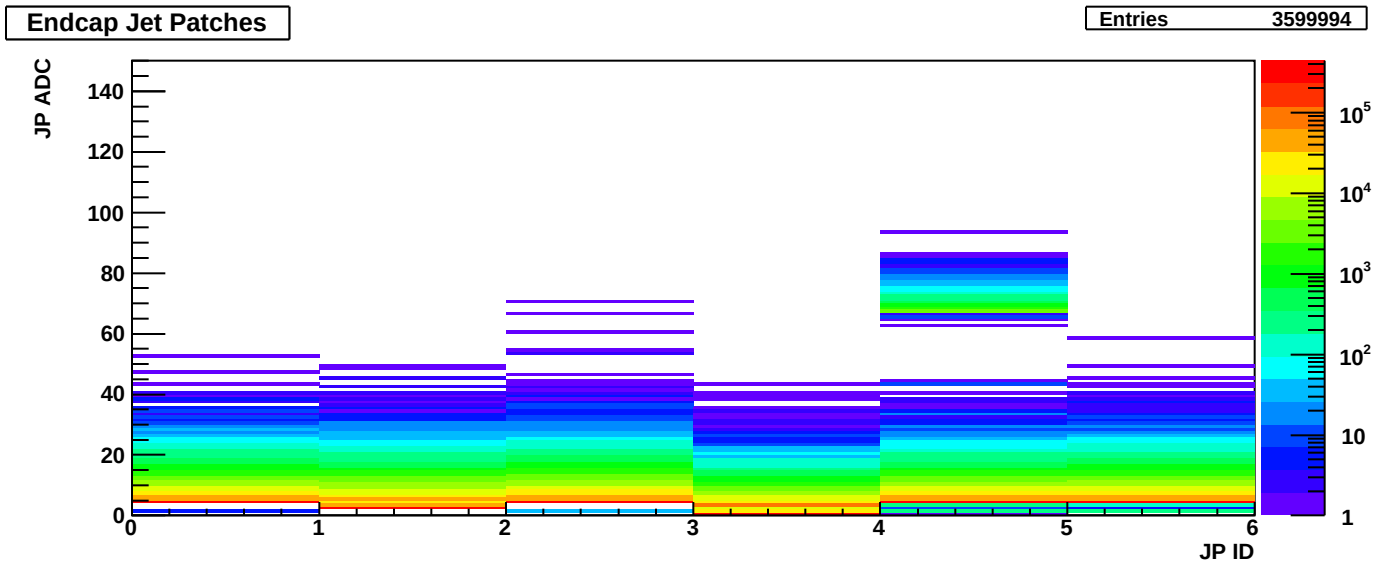

Barrel EMC L0 Input - High Tower
Entries 1.799997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

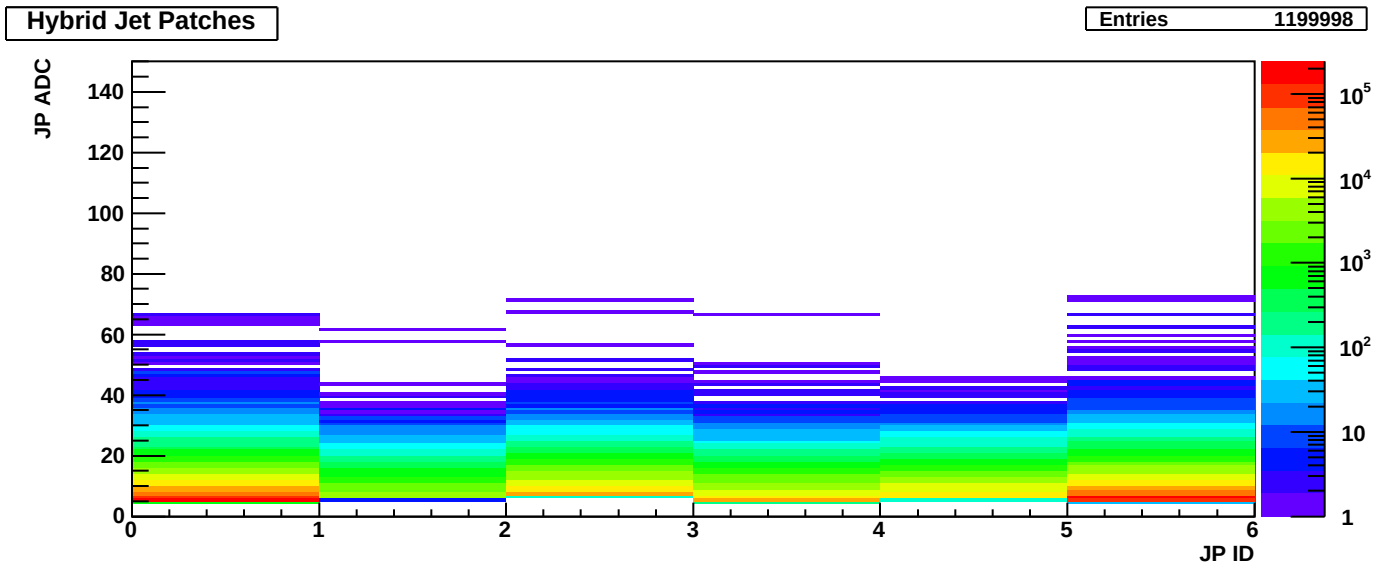

Barrel EMC L0 Input - Patch Sum
Entries 1.799997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

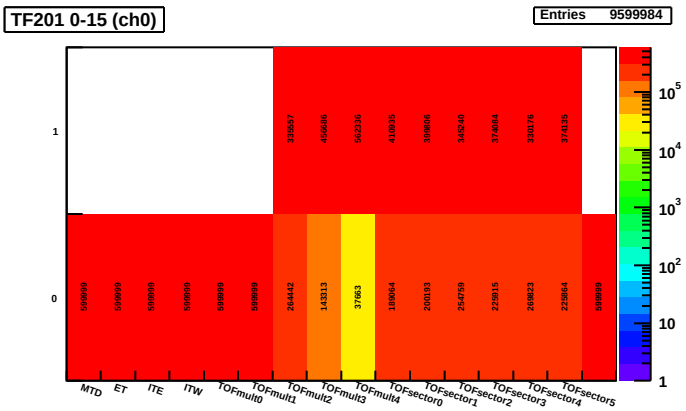

Endcap EMC L0 Input - High Tower
Entries 5.399991e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

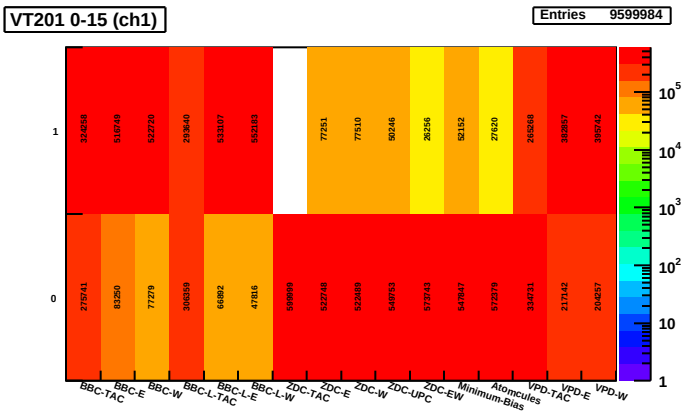

Endcap EMC L0 Input - Patch Sum
Entries 5.399991e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

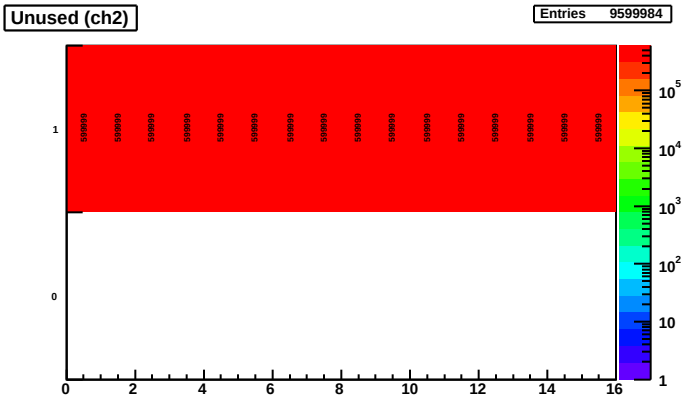

Barrel Jet Patches
Entries 1.079998e+07
JP ADC
JP ID

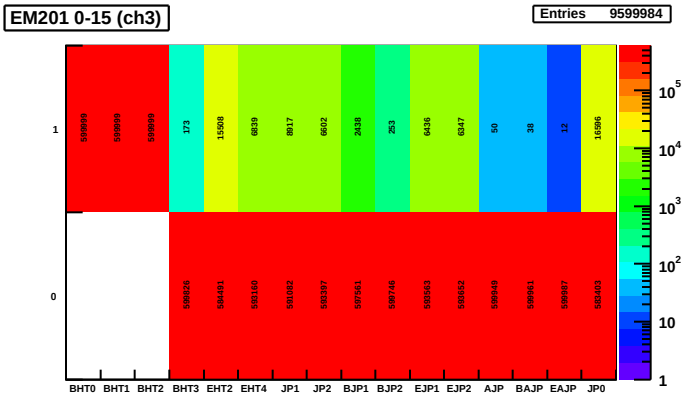

Endcap Jet Patches
Entries 3599994
JP ADC
JP ID

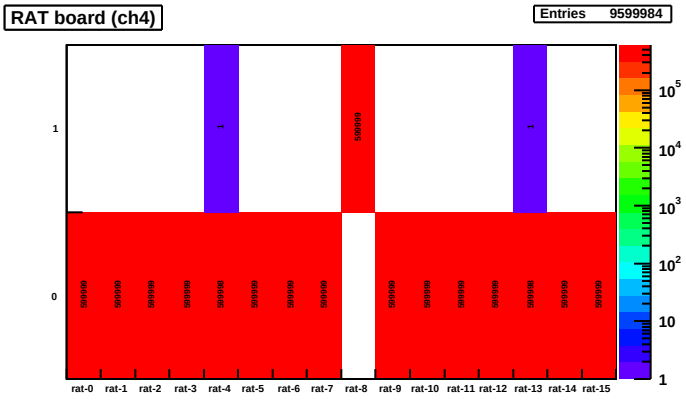

Hybrid Jet Patches
Entries 1199998
JP ADC
JP ID

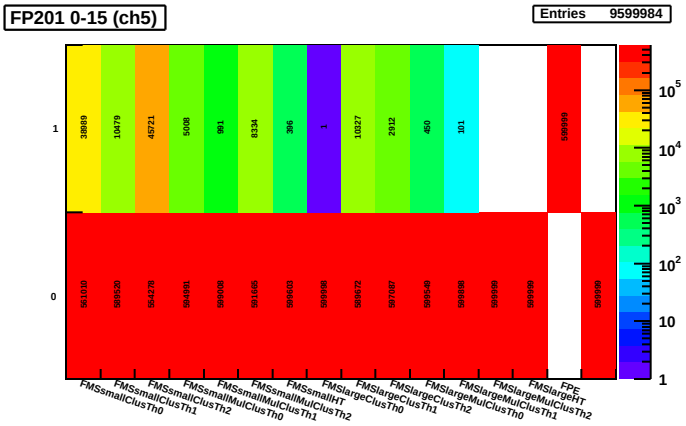

TF201 0-15 (ch0)
Entries 9599984
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

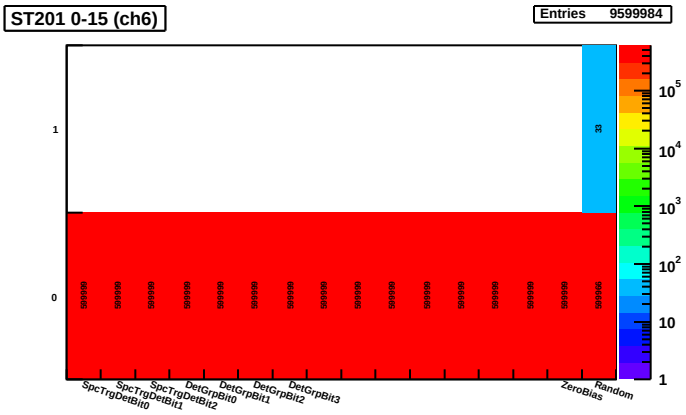

VT201 0-15 (ch1)
Entries 9599984
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

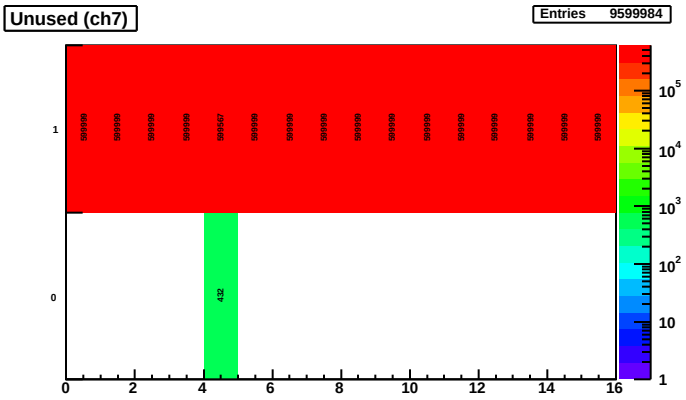

Unused (ch2)
Entries 9599984

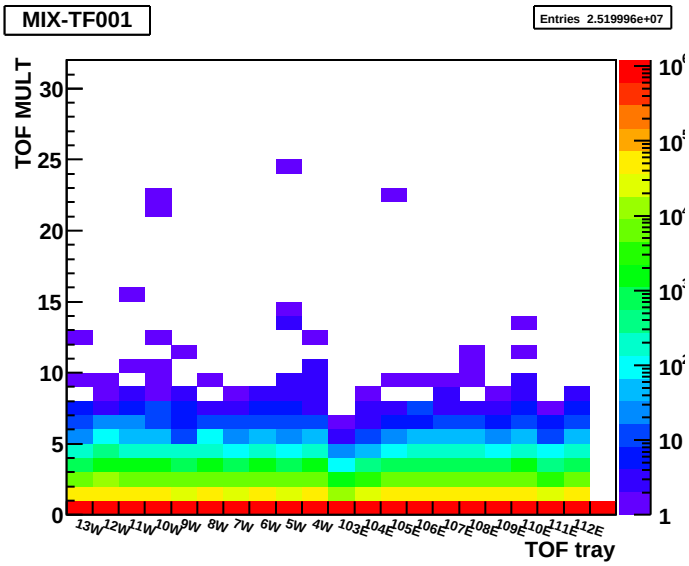

EM201 0-15 (ch3)
Entries 9599984
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

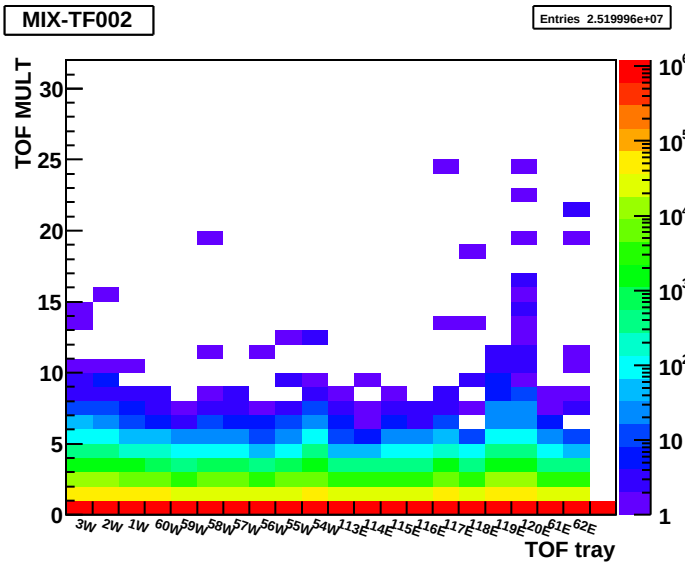

RAT board (ch4)
Entries 9599984
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

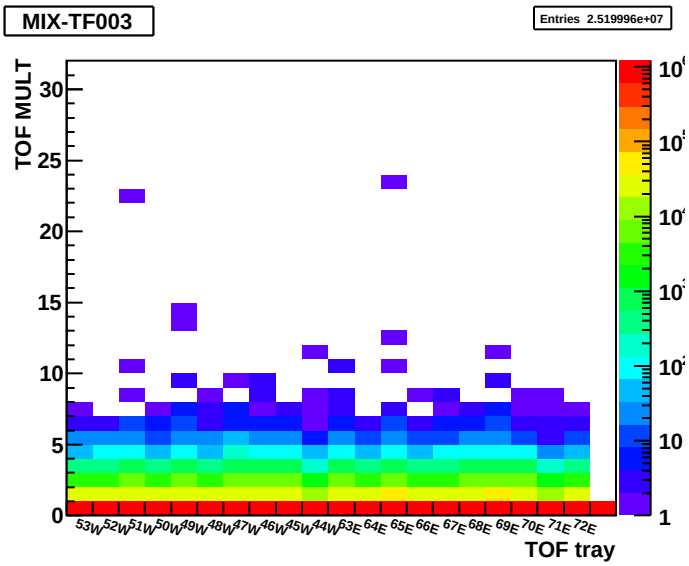

FP201 0-15 (ch5)
Entries 9599984
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

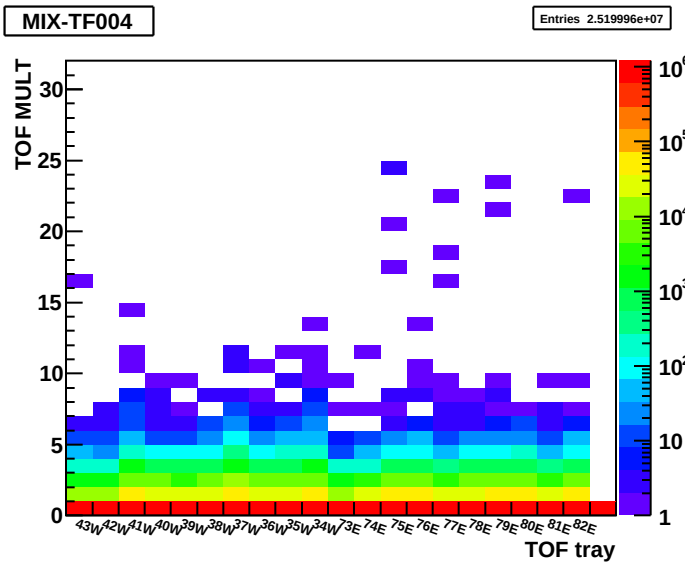

ST201 0-15 (ch6)
Entries 9599984
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

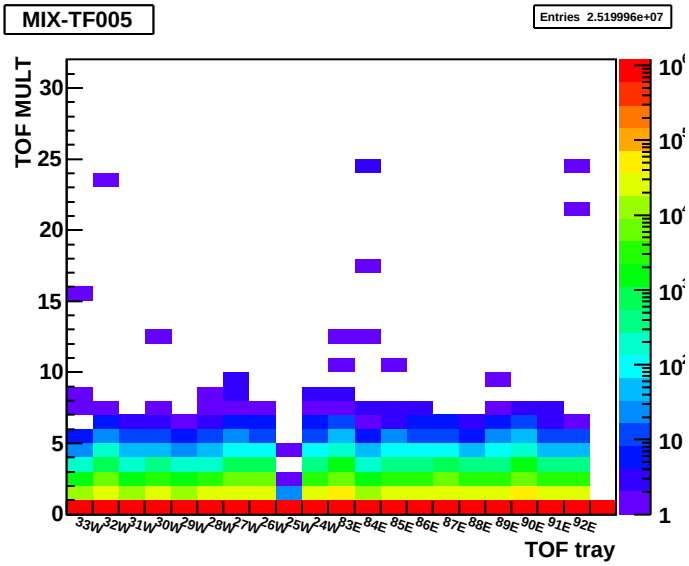

Unused (ch7)
Entries 9599984

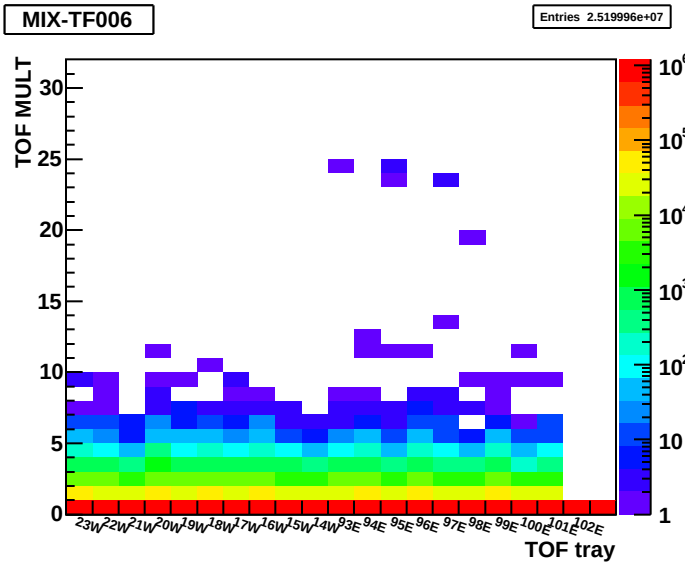

MIX-TF001
Entries 2.519996e+07
TOF MULT
TOF tray

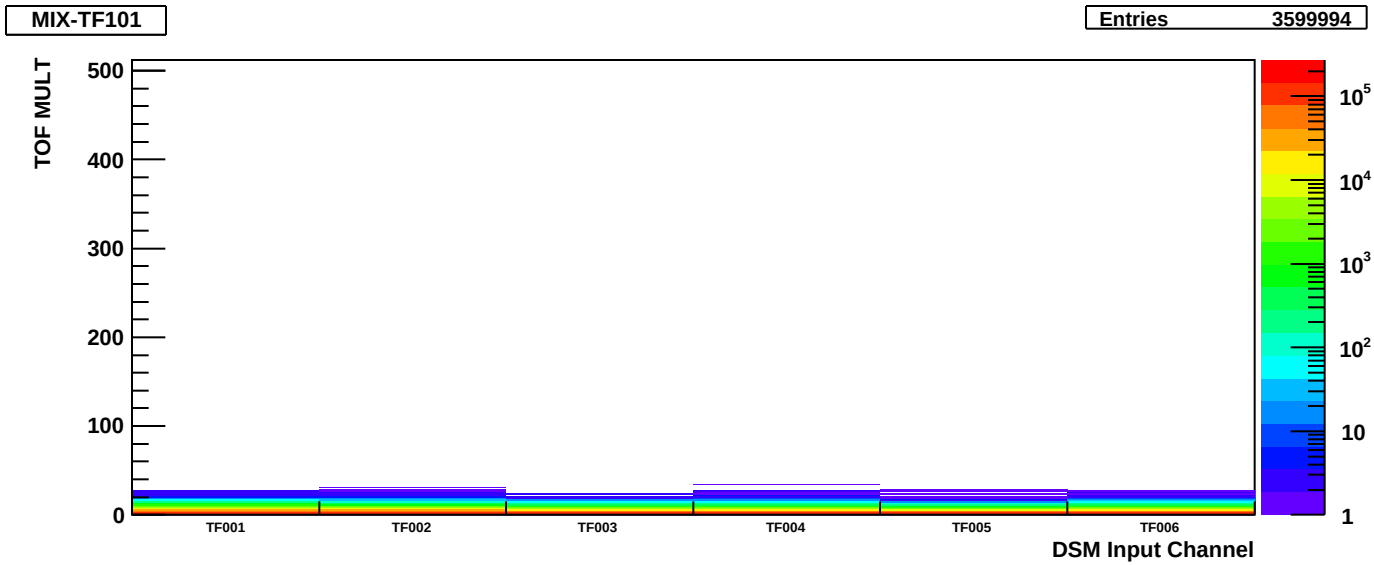

MIX-TF002
Entries 2.519996e+07
TOF MULT
TOF tray

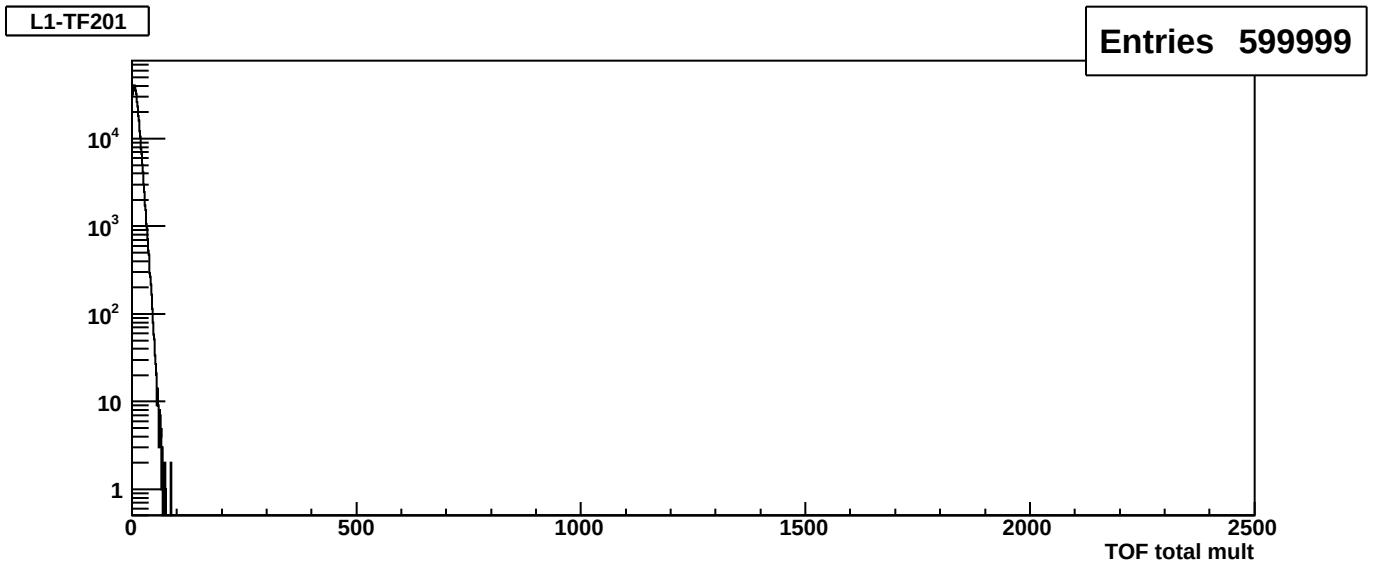

MIX-TF003
Entries 2.519996e+07
TOF MULT
TOF tray

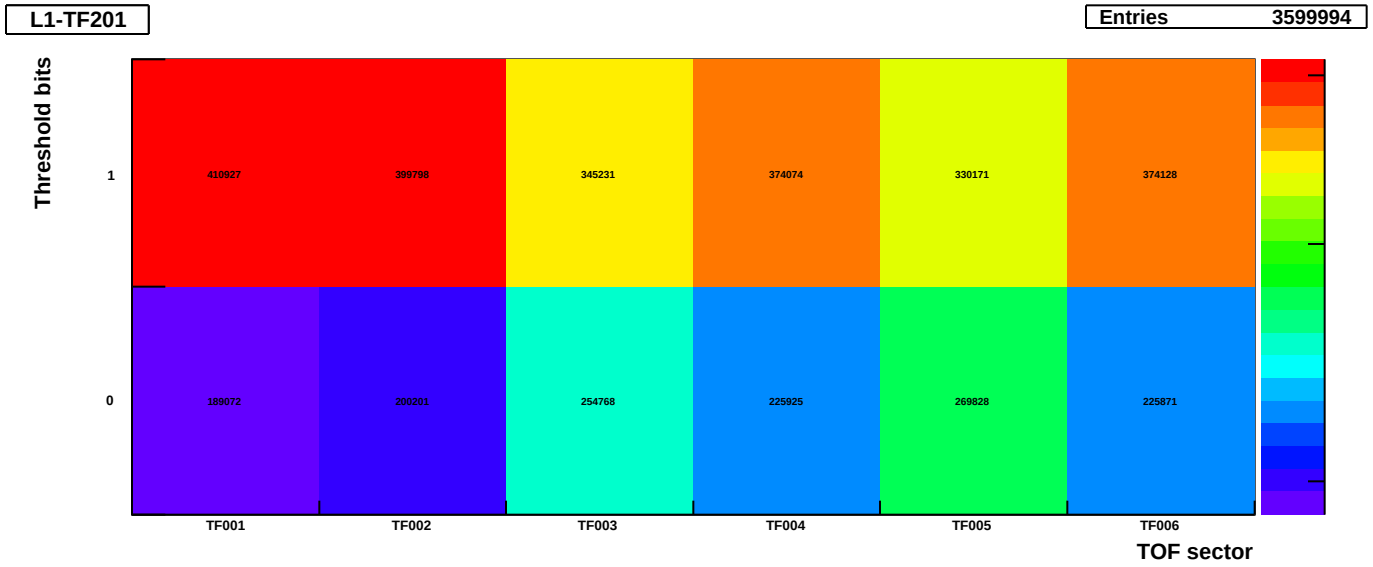

MIX-TF004
Entries 2.519996e+07
TOF MULT
TOF tray

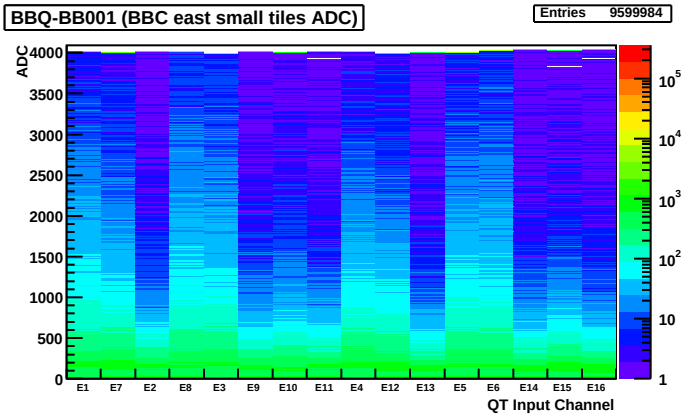

MIX-TF005
Entries 2.519996e+07
TOF MULT
TOF tray

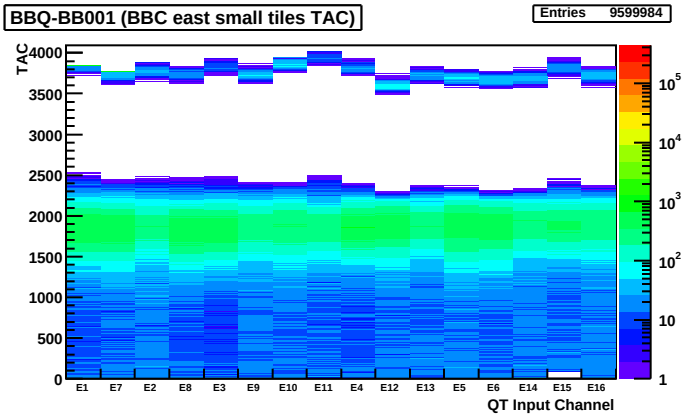

MIX-TF006
Entries 2.519996e+07
TOF MULT
TOF tray

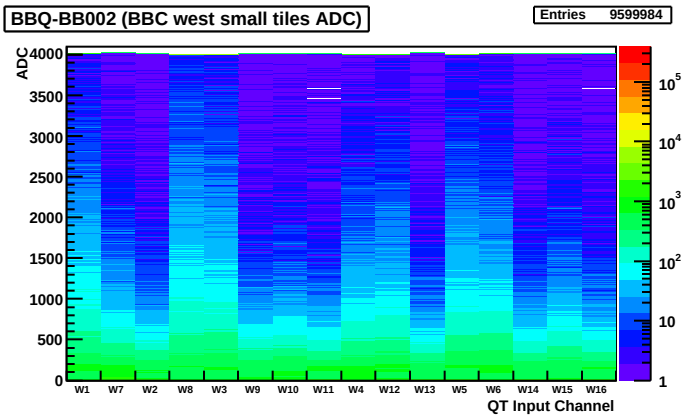

MIX-TF101
Entries 3599994
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

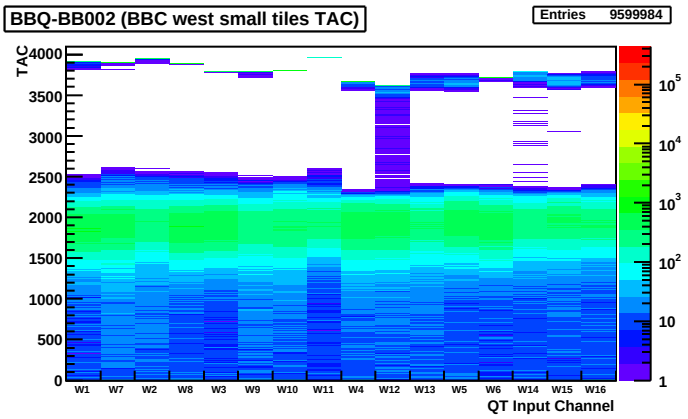

L1-TF201
Entries 599999
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

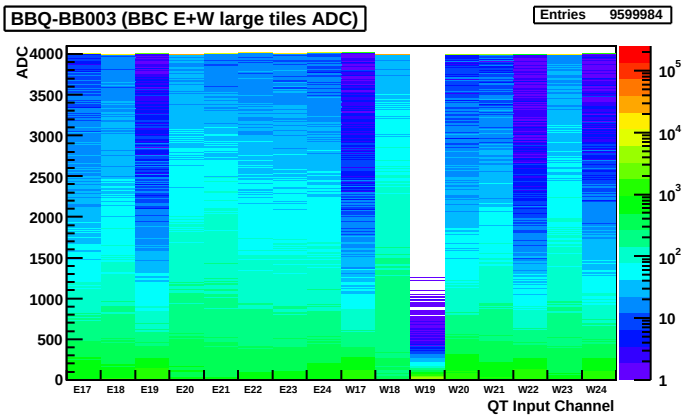

L1-TF201
Entries 3599994
Threshold bits
1
0
410927
399798
345231
374074
330171
374128
189072
200201
254768
225925
269828
225871
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

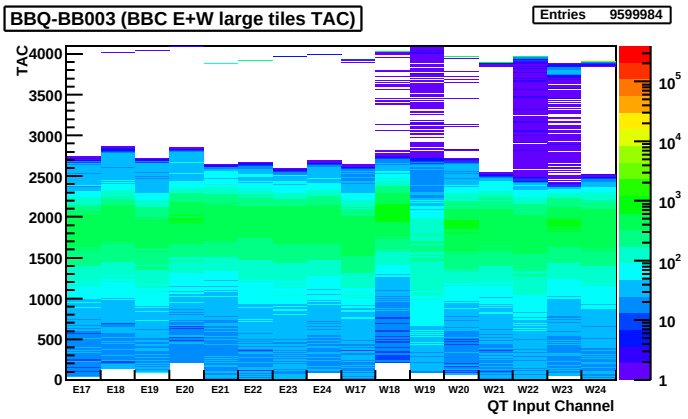

BBQ-BB001 (BBC east small tiles ADC)
Entries 9599984
ADC
QT Input Channel

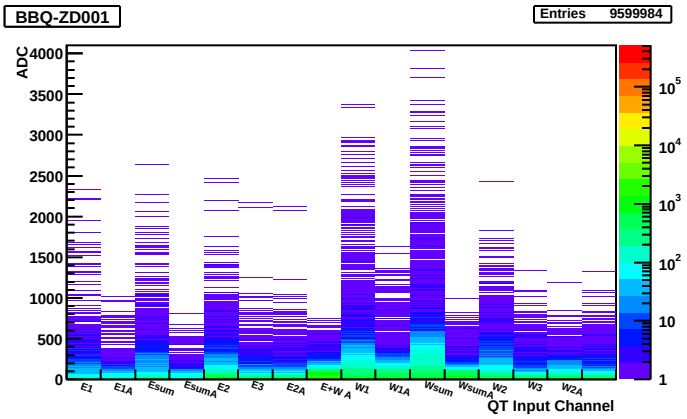

BBQ-BB001 (BBC east small tiles TAC)
Entries 9599984
TAC
QT Input Channel

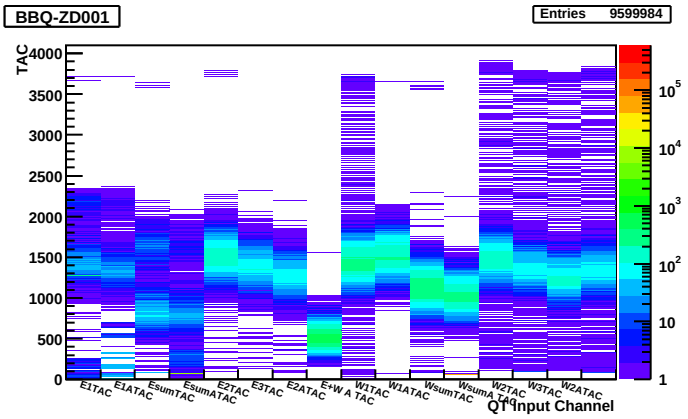

BBQ-BB002 (BBC west small tiles ADC)
Entries 9599984
ADC
QT Input Channel

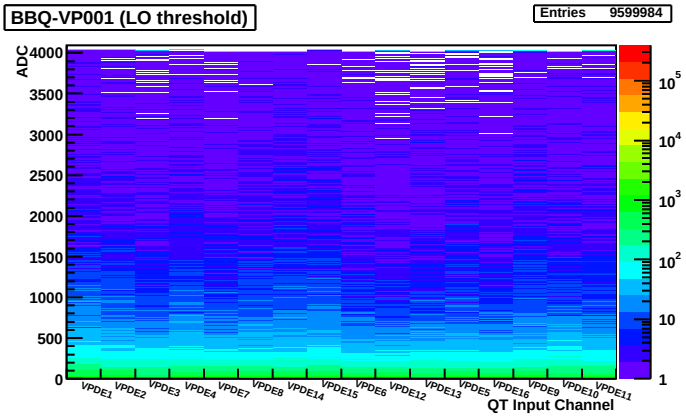

BBQ-BB002 (BBC west small tiles TAC)
Entries 9599984
TAC
QT Input Channel

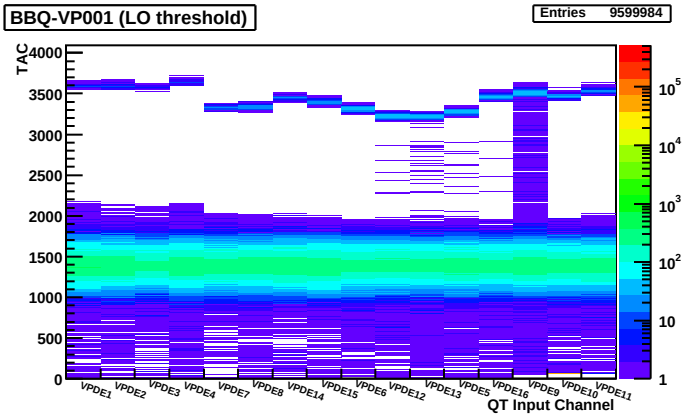

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9599984
ADC
QT Input Channel

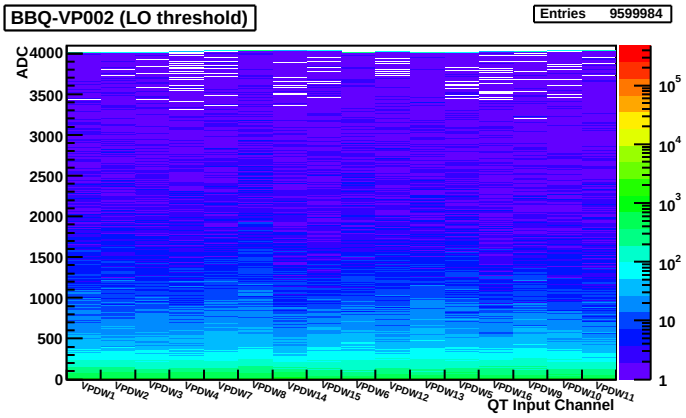

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9599984
TAC
QT Input Channel

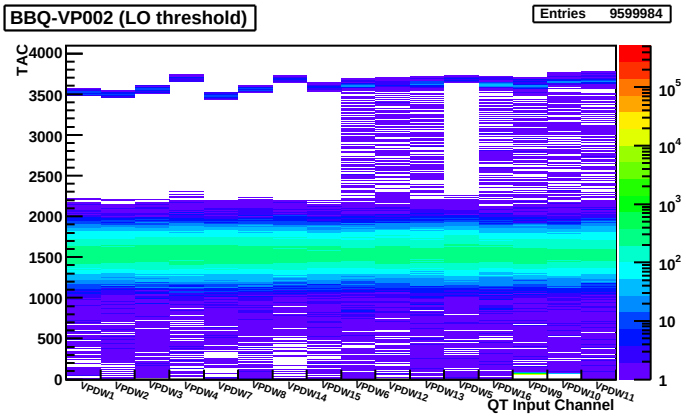

BBQ-ZD001
Entries 9599984
ADC
QT Input Channel

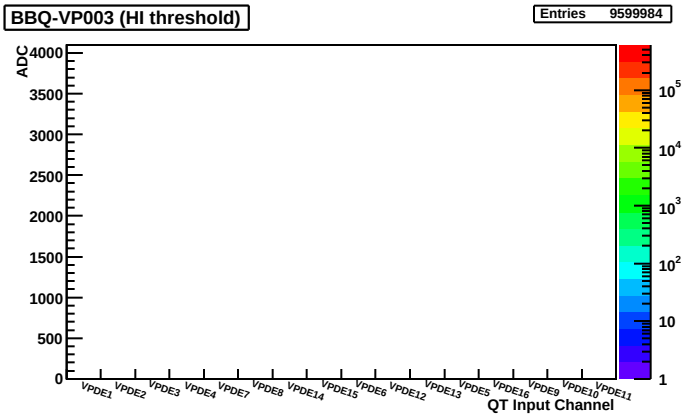

BBQ-ZD001
Entries 9599984
TAC
QT Input Channel

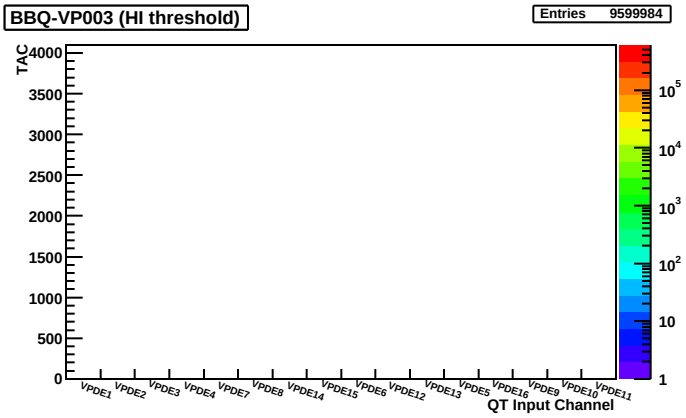

BBQ-VP001 (LO threshold)
Entries 9599984
ADC
QT Input Channel

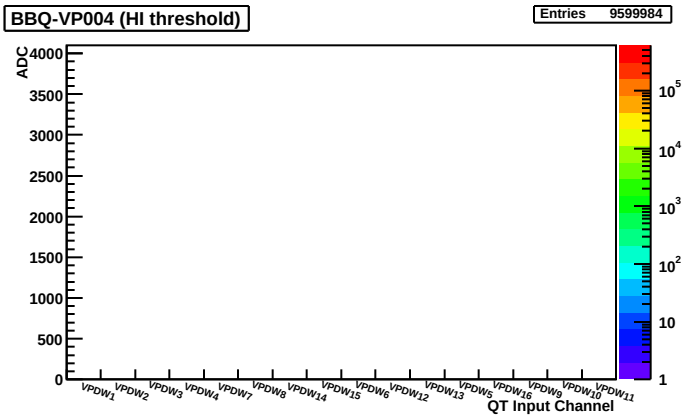

BBQ-VP001 (LO threshold)
Entries 9599984
TAC
QT Input Channel

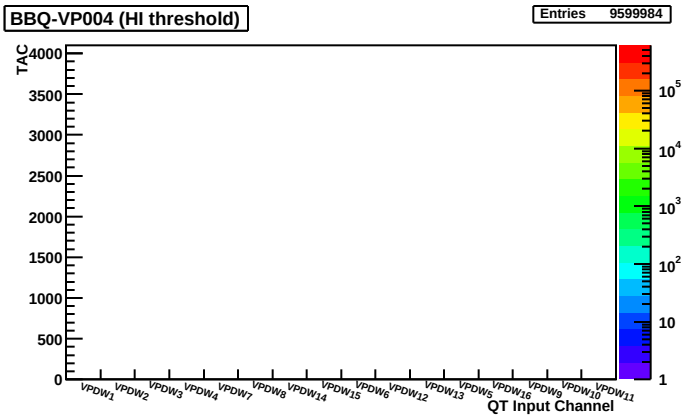

BBQ-VP002 (LO threshold)
Entries 9599984
ADC
QT Input Channel

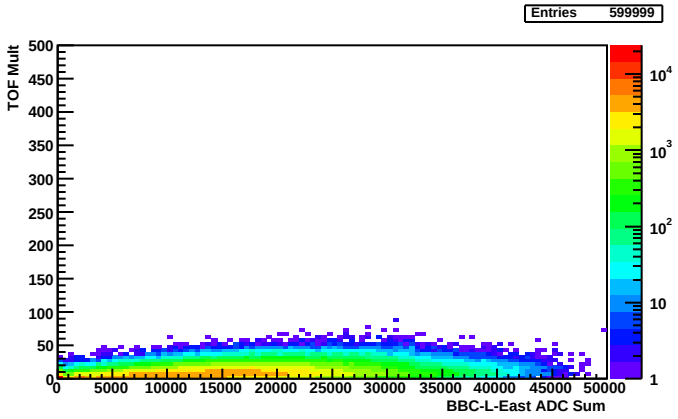

BBQ-VP002 (LO threshold)
Entries 9599984
TAC
QT Input Channel

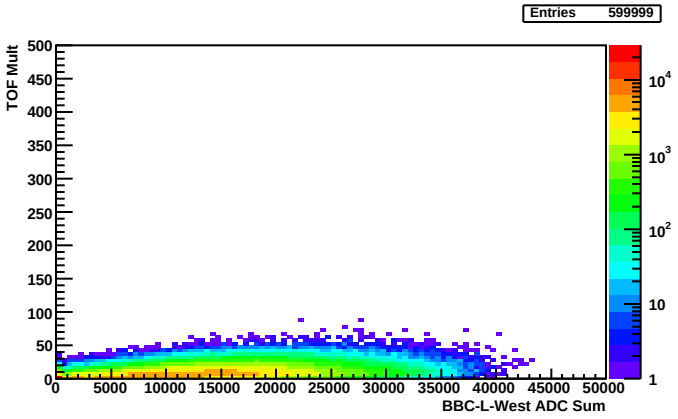

BBQ-VP003 (HI threshold)
Entries 9599984
ADC
QT Input Channel

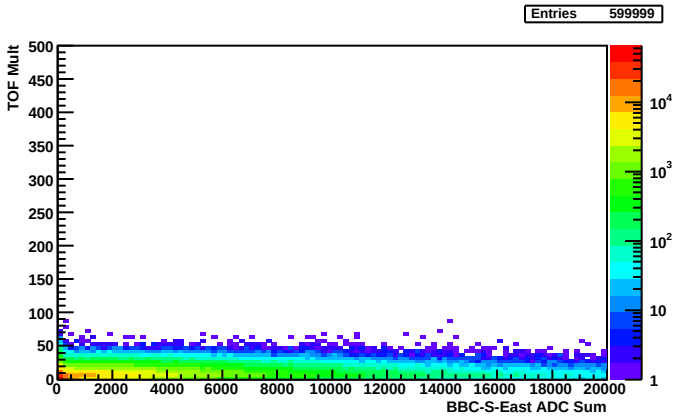

BBQ-VP003 (HI threshold)
Entries 9599984
TAC
QT Input Channel

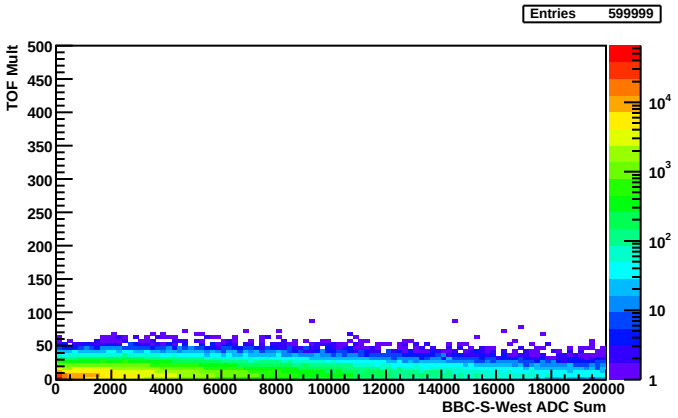

BBQ-VP004 (HI threshold)
Entries 9599984
ADC
QT Input Channel

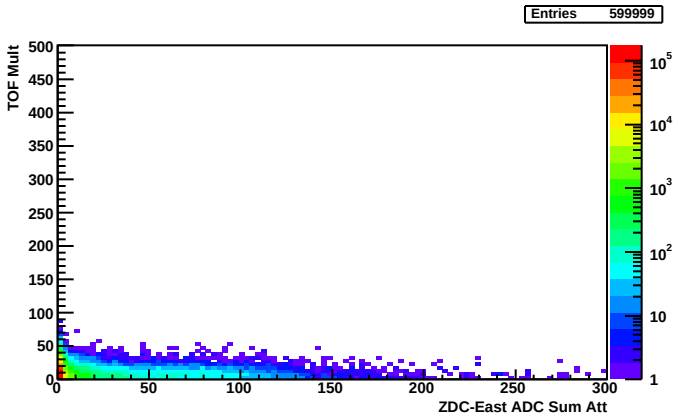

BBQ-VP004 (HI threshold)
Entries 9599984
TAC
QT Input Channel

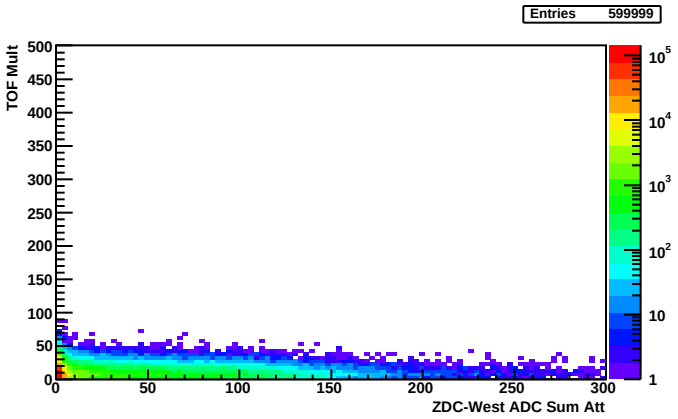

Entries 599999
TOF Mult
BBC-L-East ADC Sum

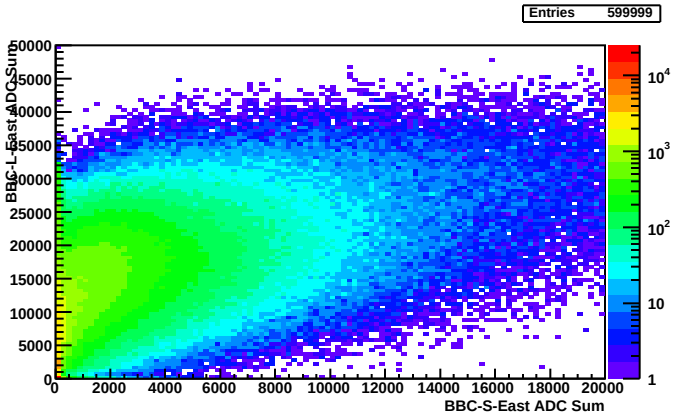

Entries 599999
TOF Mult
BBC-L-West ADC Sum

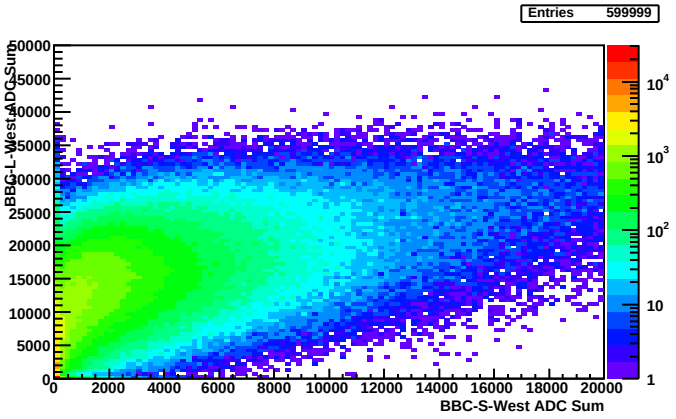

Entries 599999
TOF Mult
BBC-S-East ADC Sum

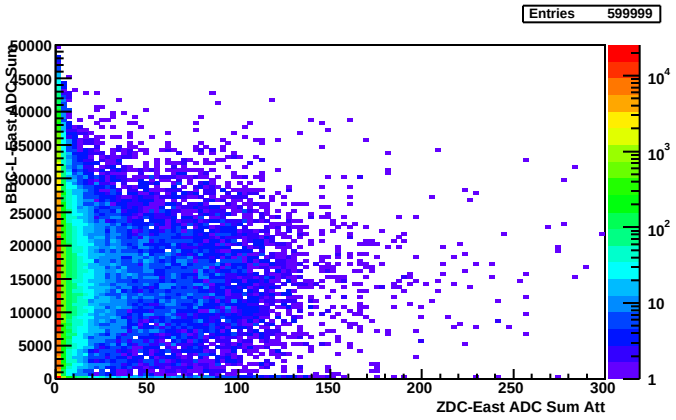

Entries 599999
TOF Mult
BBC-S-West ADC Sum

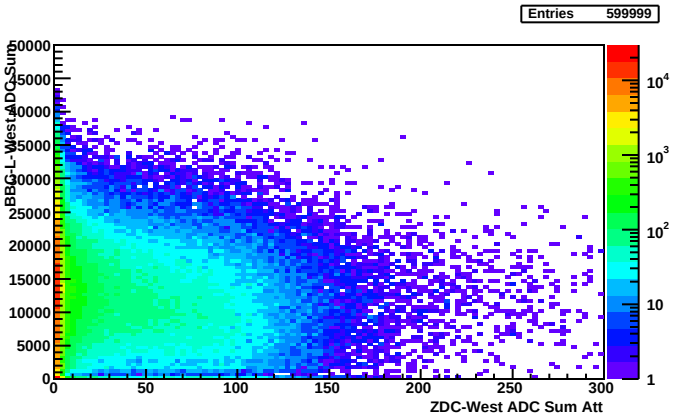

Entries 599999
TOF Mult
ZDC-East ADC Sum Att

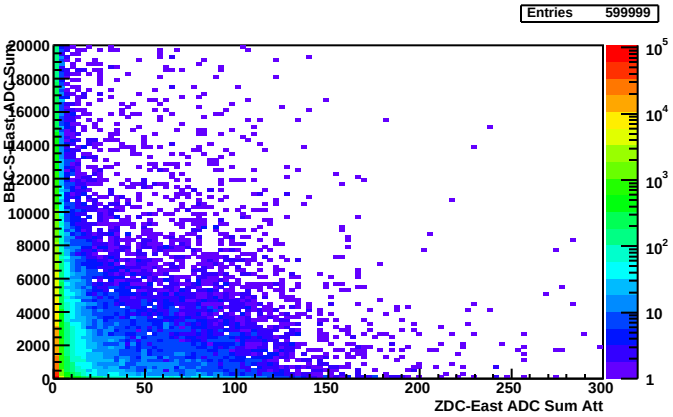

Entries 599999
TOF Mult
ZDC-West ADC Sum Att

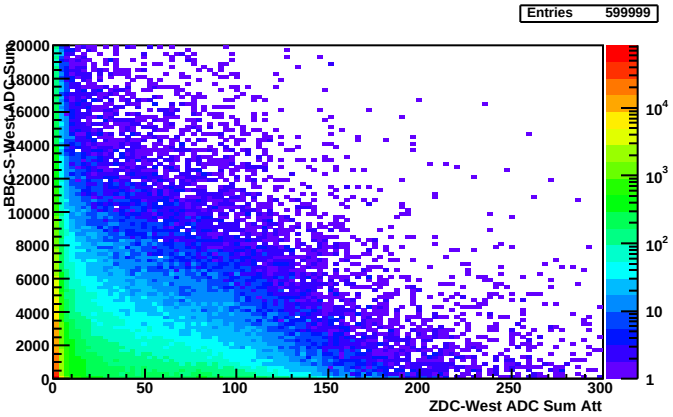

Entries 599999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

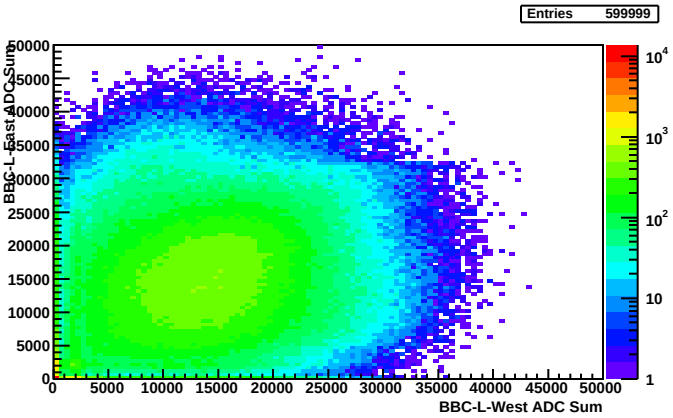

Entries 599999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

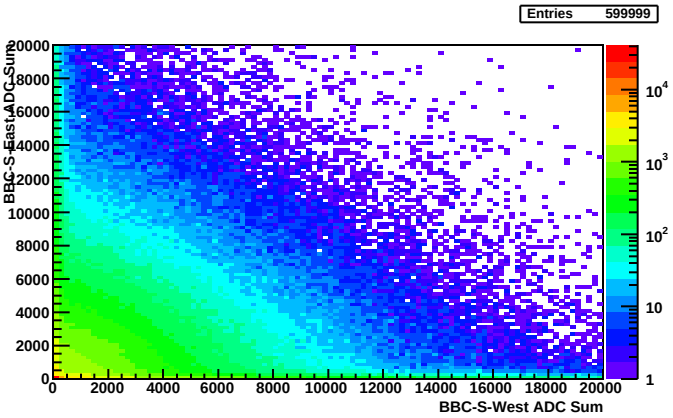

Entries 599999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

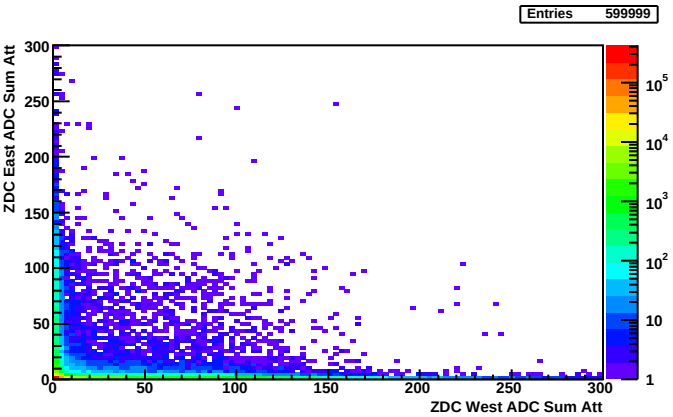

Entries 599999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

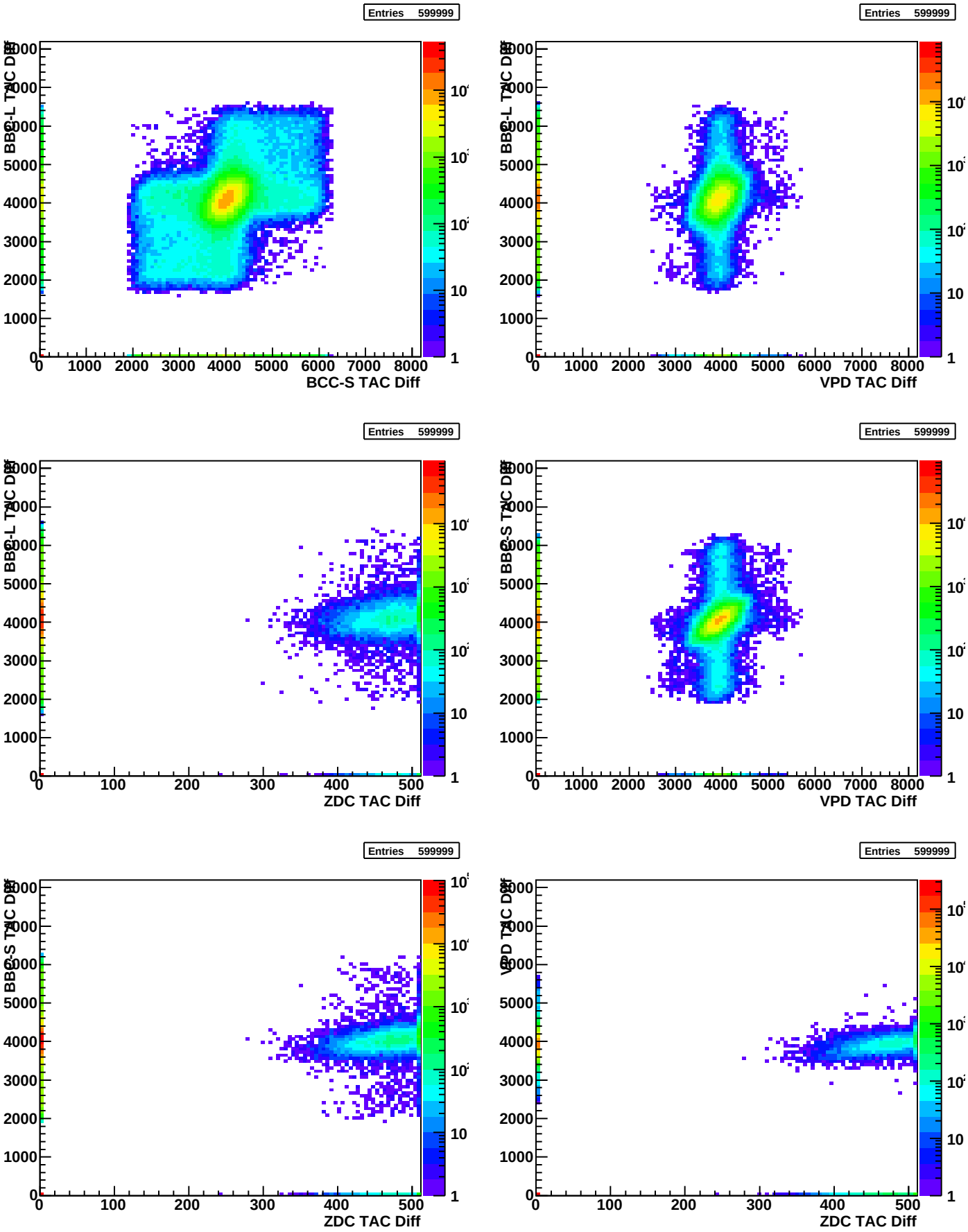

Entries 599999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

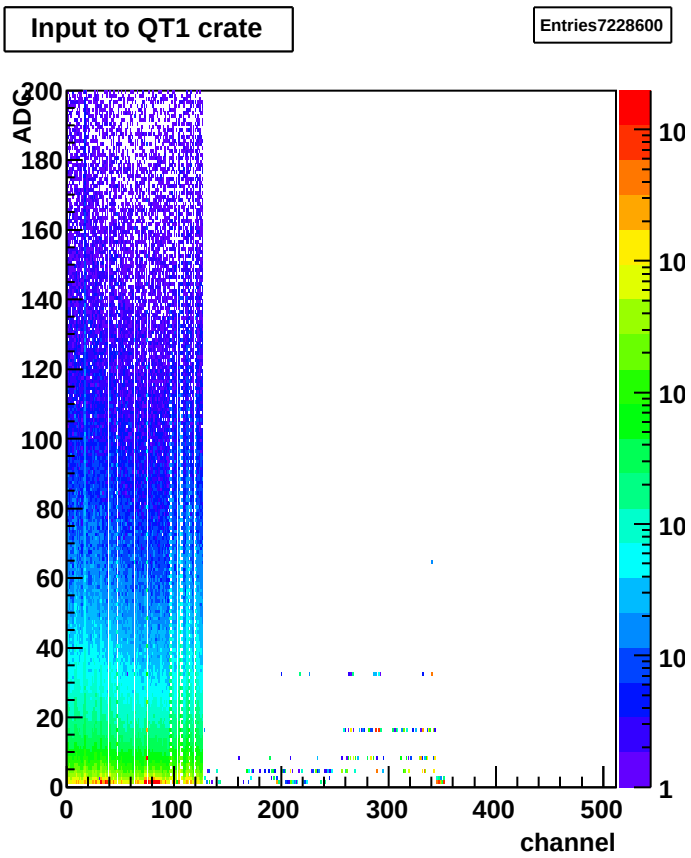

Entries 599999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

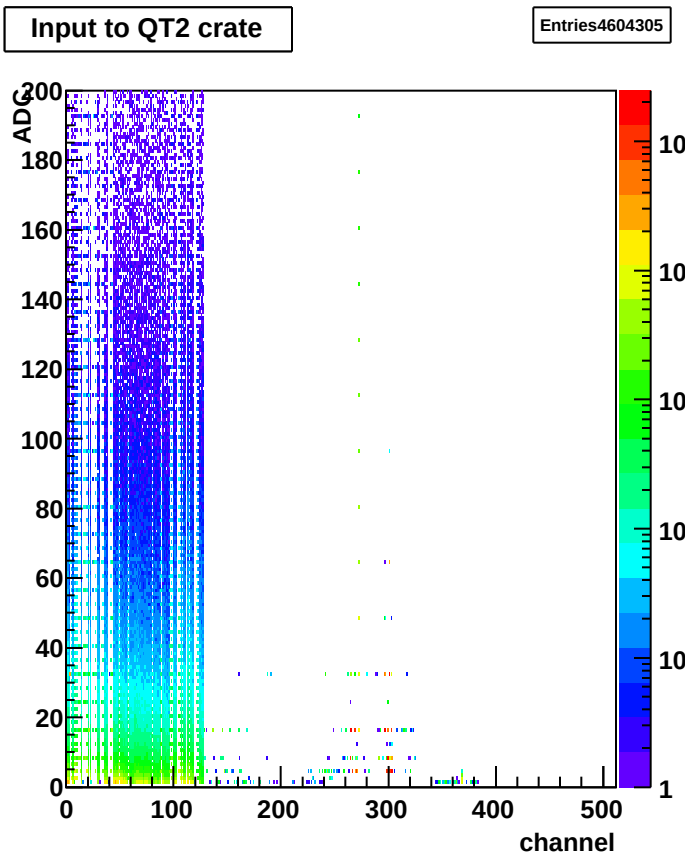

Entries 599999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

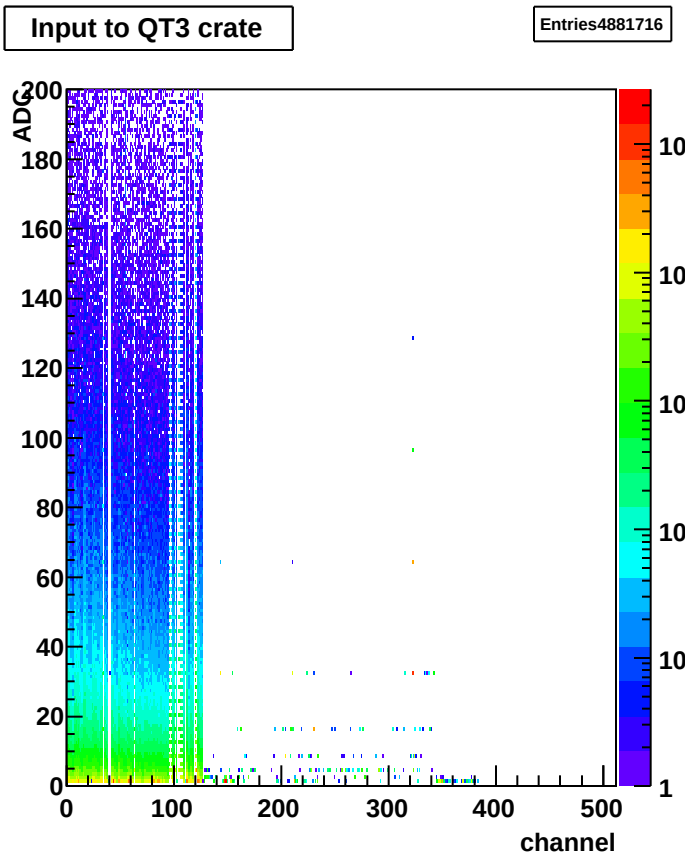

Entries 599999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

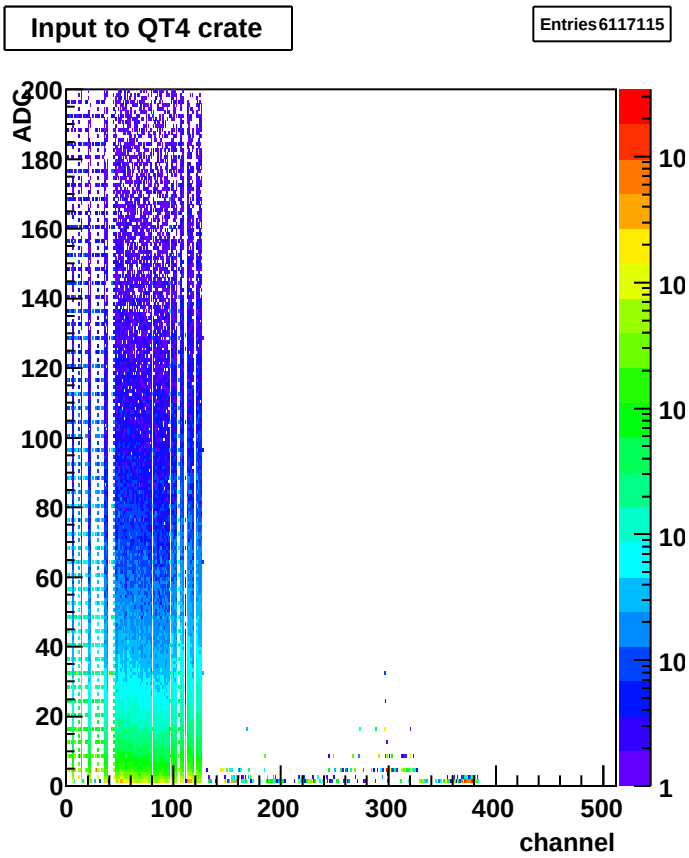

Entries 599999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

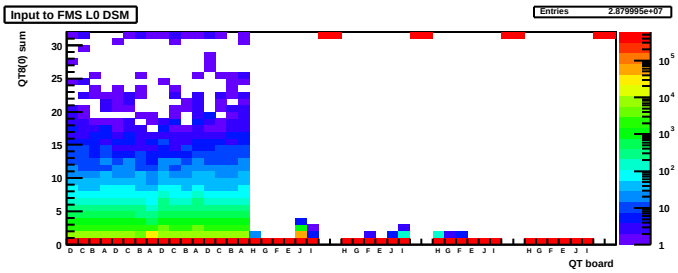

Entries 599999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 599999
BBC-L TAC Diff
VPD TAC Diff
Entries 599999
BBC-L TAC Diff
ZDC TAC Diff
Entries 599999
BBC-S TAC Diff
VPD TAC Diff
Entries 599999
BBC-S TAC Diff
ZDC TAC Diff
Entries 599999
VPD TAC Diff
ZDC TAC Diff

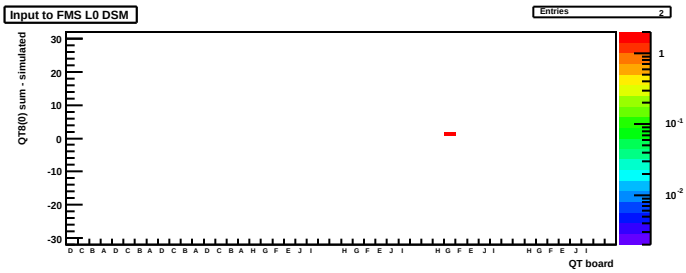

Input to QT1 crate
Entries7228600
ADC
channel

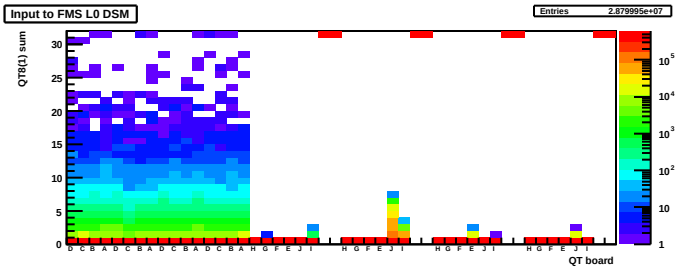

Input to QT2 crate
Entries4604305
ADC
channel

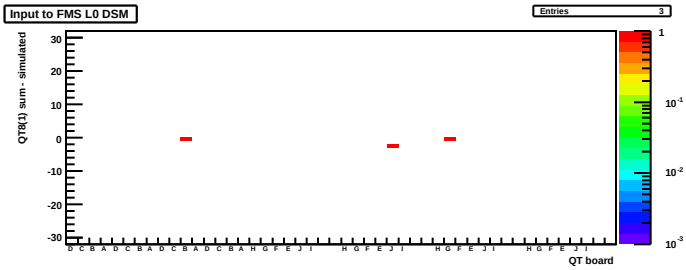

Input to QT3 crate
Entries4881716
ADC
channel

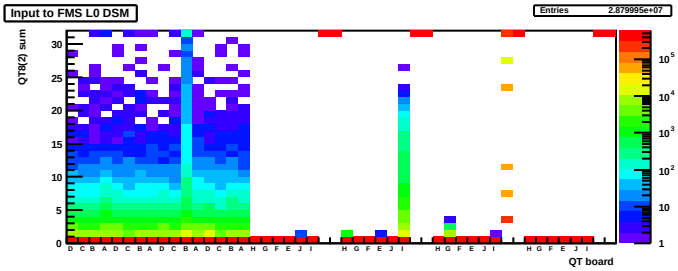

Input to QT4 crate
Entries6117115
ADC
channel

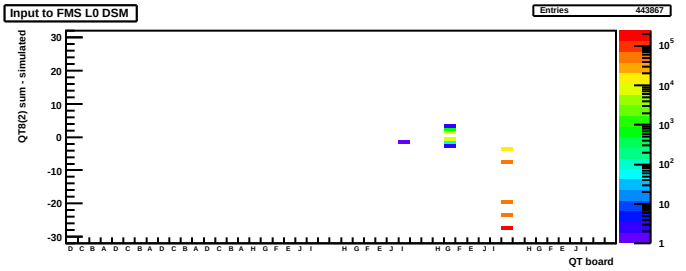

Input to FMS L0 DSM
Entries 2.879995e+07
QT8(0) sum
QT board

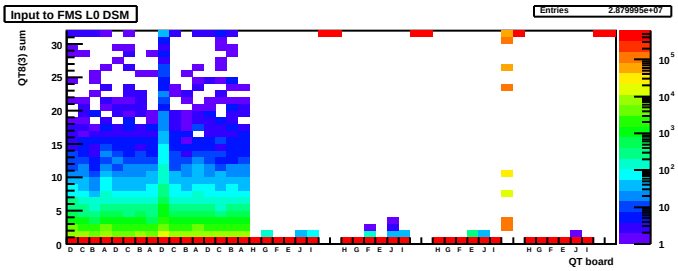

Input to FMS L0 DSM
Entries 2
QT8(0) sum - simulated
QT board

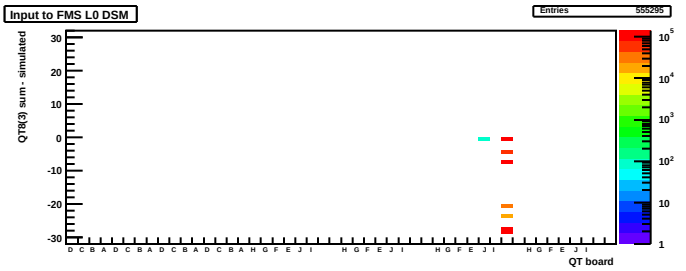

Input to FMS L0 DSM
Entries 2.879995e+07
QT8(1) sum
QT board

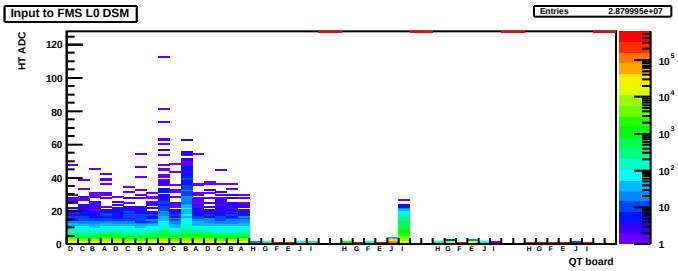

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

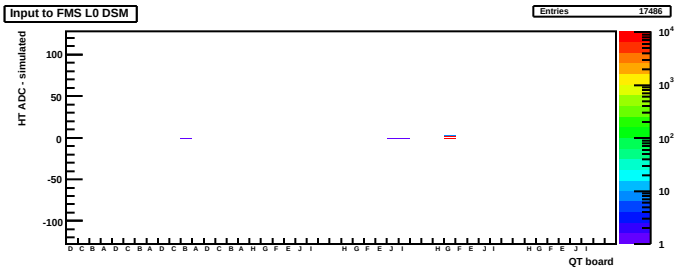

Input to FMS L0 DSM
Entries 2.879995e+07
QT8(2) sum
QT board

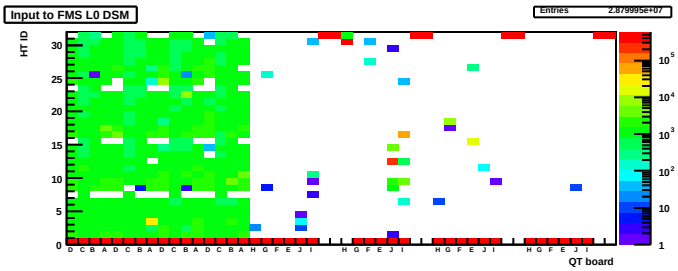

Input to FMS L0 DSM
Entries 443867
QT8(2) sum - simulated
QT board

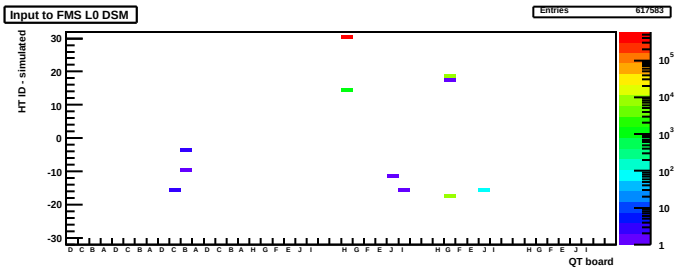

Input to FMS L0 DSM
Entries 2.879995e+07
QT8(3) sum
QT board

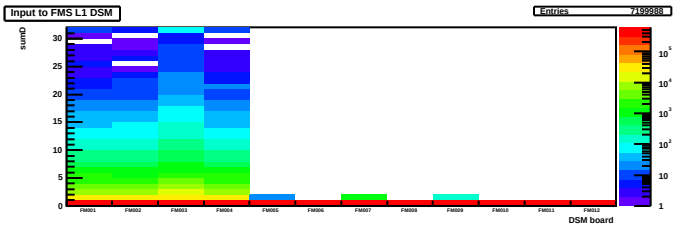

Input to FMS L0 DSM
Entries 555295
QT8(3) sum - simulated
QT board

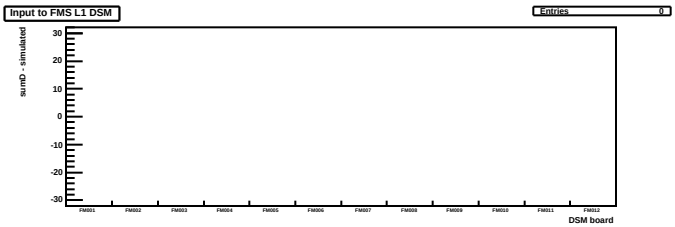

Input to FMS L0 DSM
Entries 2.879995e+07
HT ADC
QT board

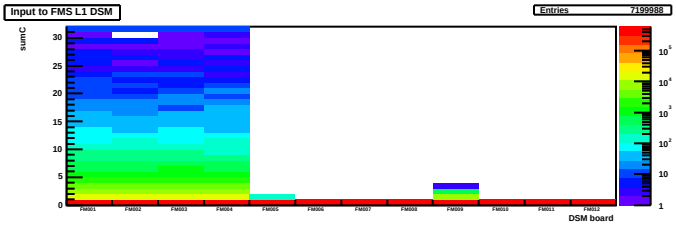

Input to FMS L0 DSM
Entries 17486
HT ADC - simulated
QT board

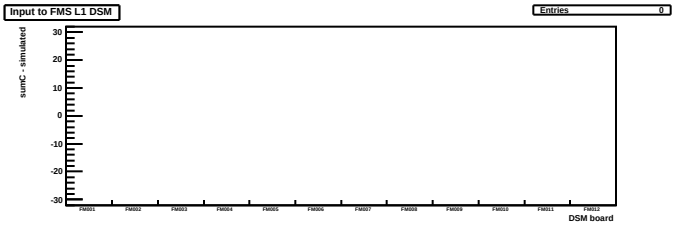

Input to FMS L0 DSM
Entries 2.879995e+07
HT ID
QT board

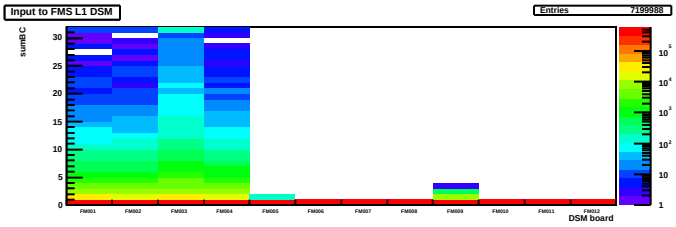

Input to FMS L0 DSM
Entries 617583
HT ID - simulated
QT board

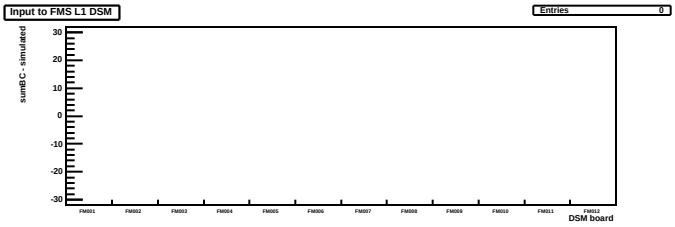

Input to FMS L1 DSM
Entries 7199988
sumD
DSM board

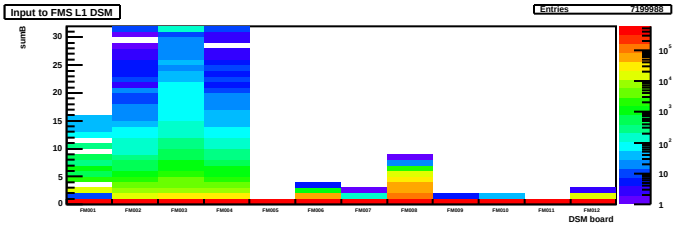

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

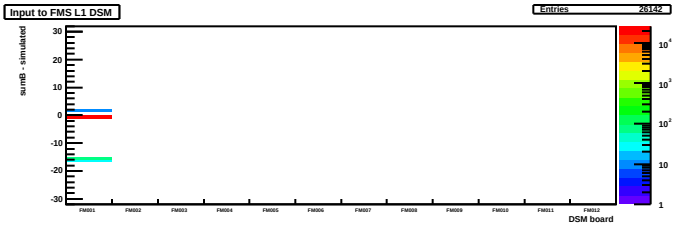

Input to FMS L1 DSM
Entries 7199988
sumC
DSM board

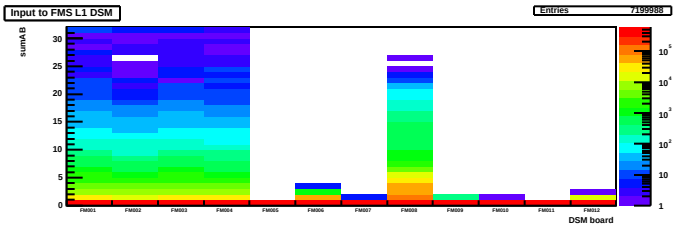

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

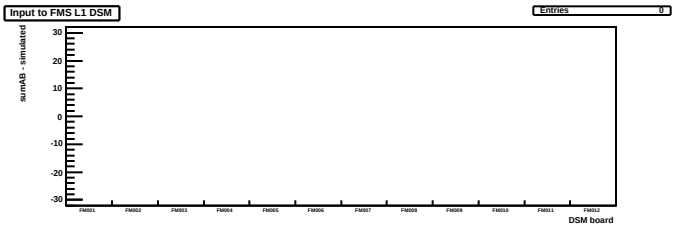

Input to FMS L1 DSM
Entries 7199988
sumBC
DSM board

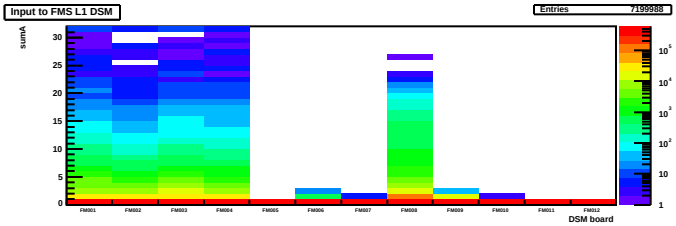

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

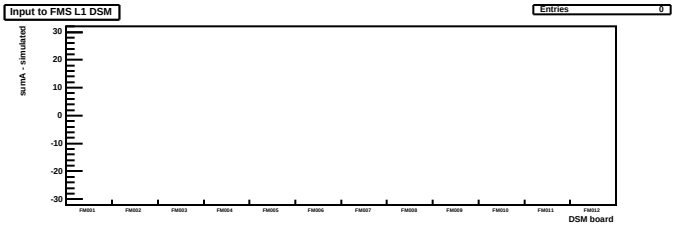

Input to FMS L1 DSM
Entries 7199988
sumB
DSM board

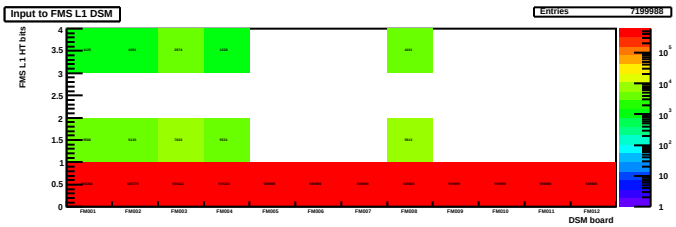

Input to FMS L1 DSM
Entries 26142
sumB - simulated
DSM board

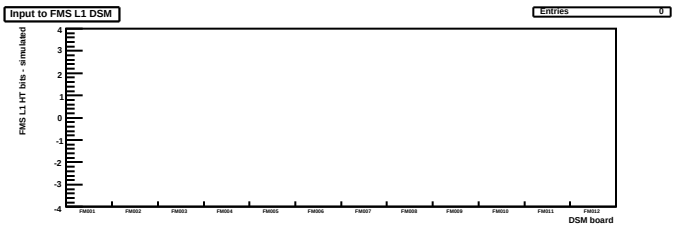

Input to FMS L1 DSM
Entries 7199988
sumAB
DSM board

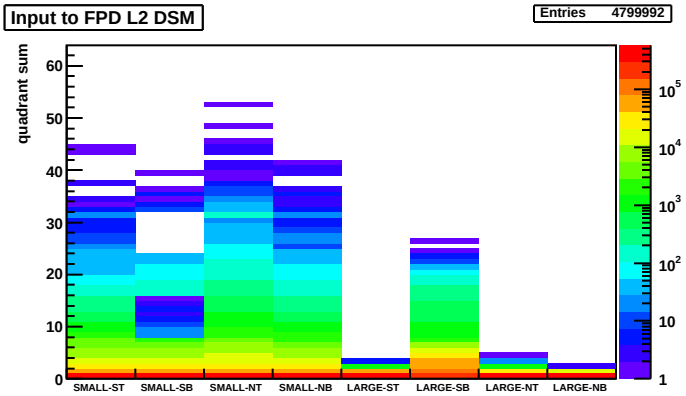

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

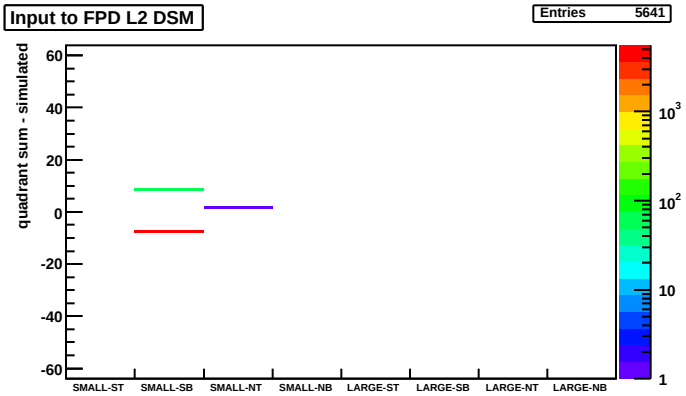

Input to FMS L1 DSM
Entries 7199988
sumA
DSM board

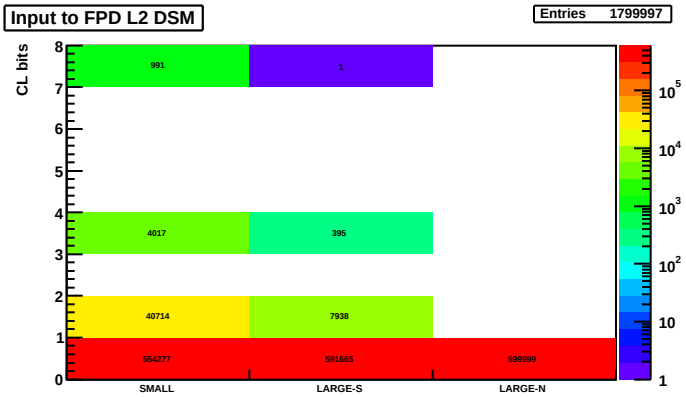

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

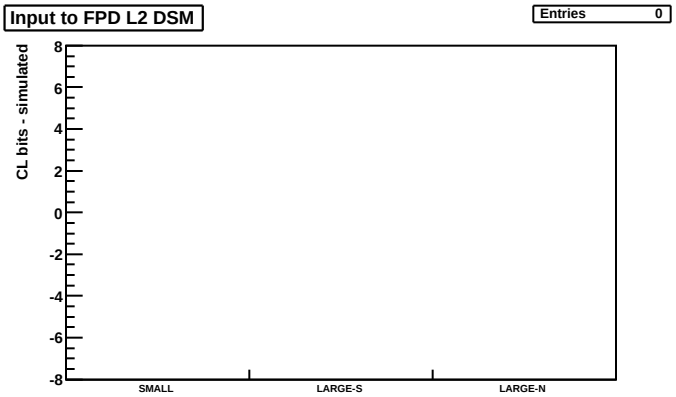

Input to FMS L1 DSM
Entries 7199988
FMS L1 HT bits
DSM board

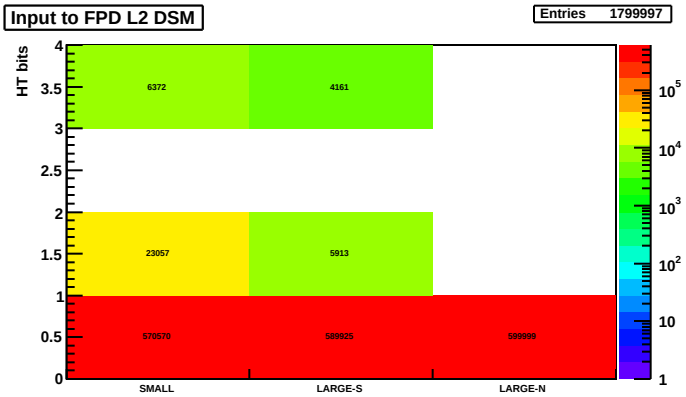

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

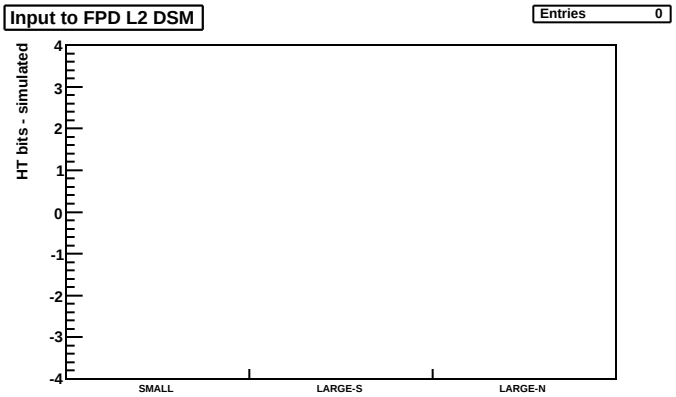

Input to FPD L2 DSM
Entries 4799992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

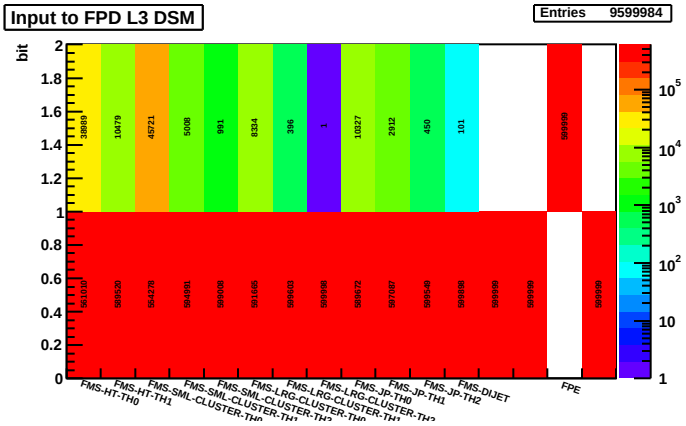

Input to FPD L2 DSM
Entries 5641
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

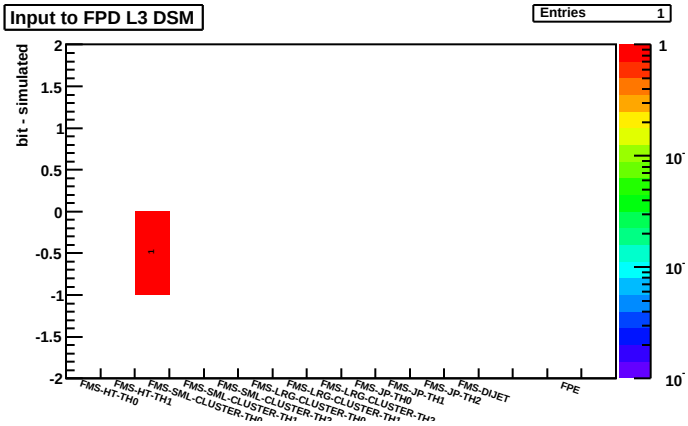

Input to FPD L2 DSM
Entries 1799997
CL bits
991
1
4017
395
40714
7938
554277
591665
599999
SMALL
LARGE-S
LARGE-N

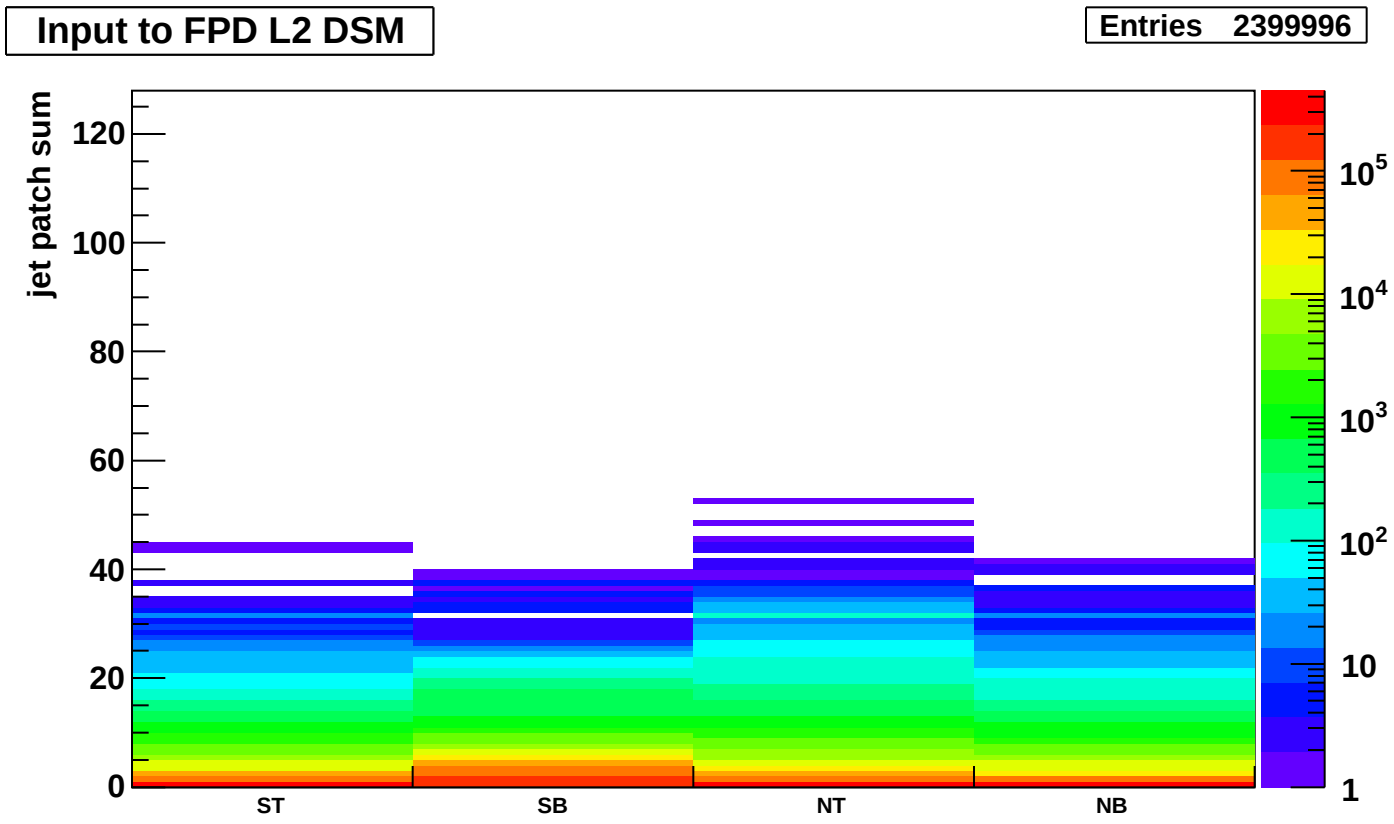

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

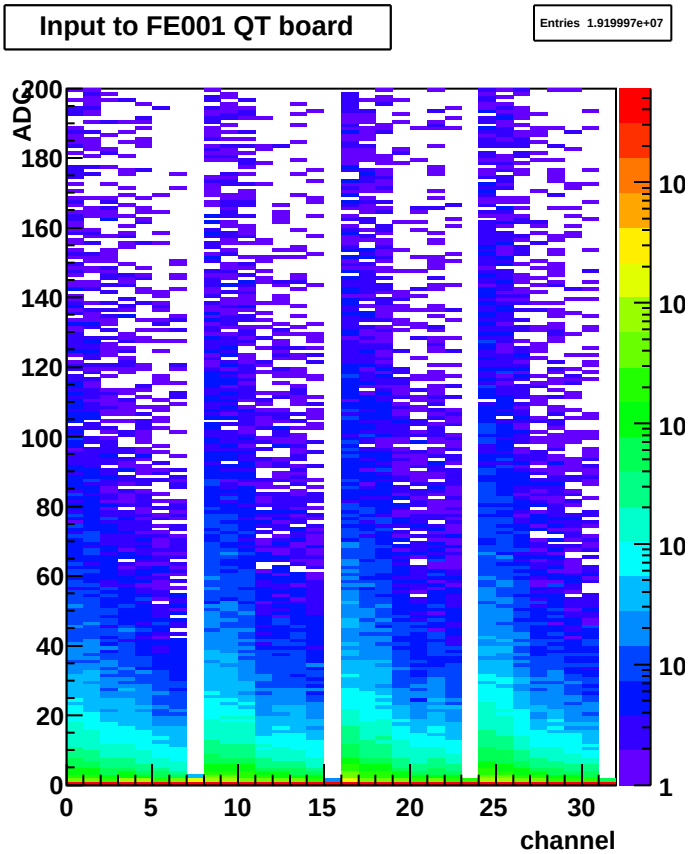

Input to FPD L2 DSM
Entries 1799997
HT bits
6372
4161
23057
5913
570570
589925
599999
SMALL
LARGE-S
LARGE-N

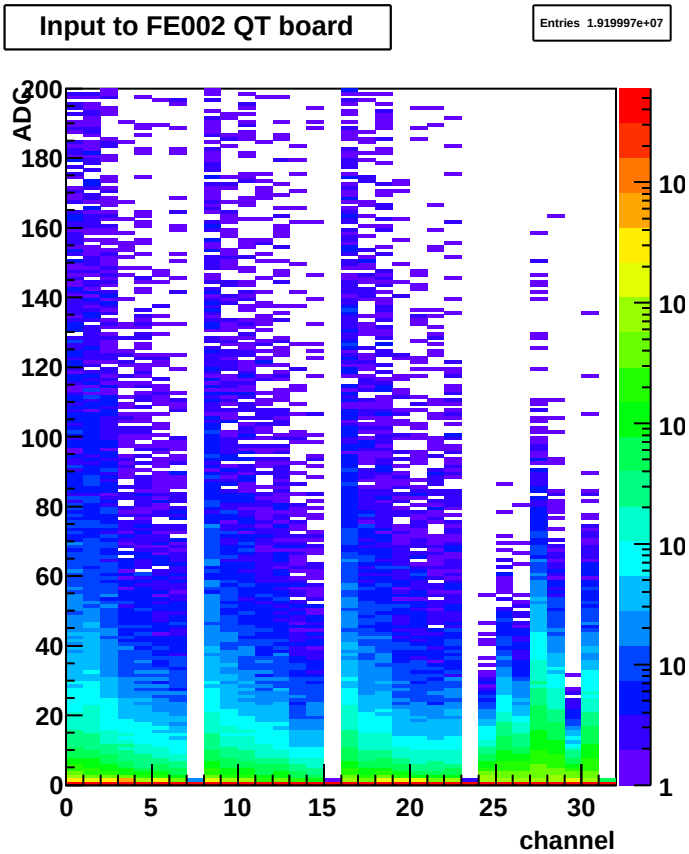

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

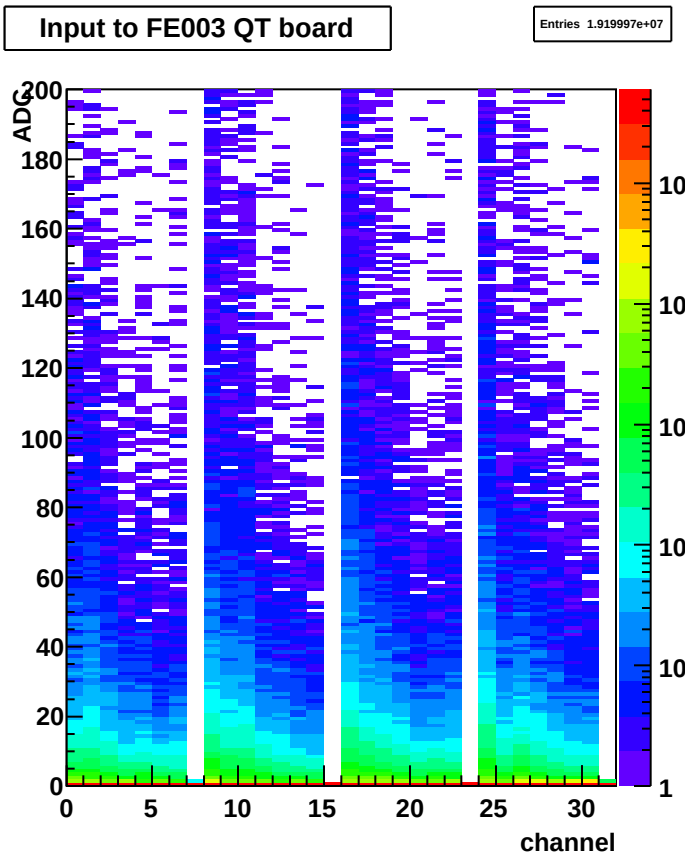

Input to FPD L3 DSM
Entries 9599984
bit
38869
10479
45721
5008
991
8334
396
1
10327
2912
450
101
599999
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

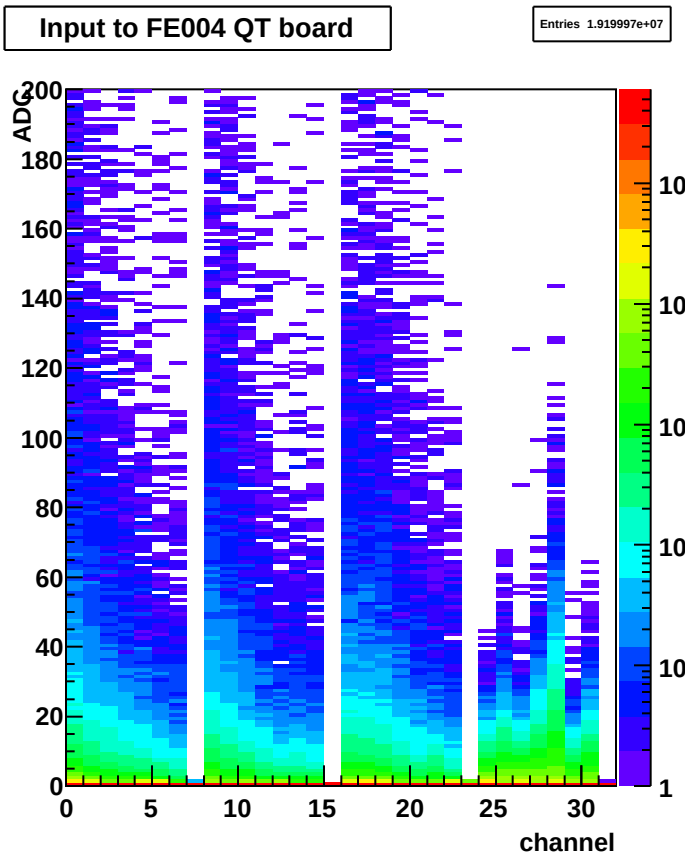

Input to FPD L3 DSM
Entries 1
bit - simulated
1
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

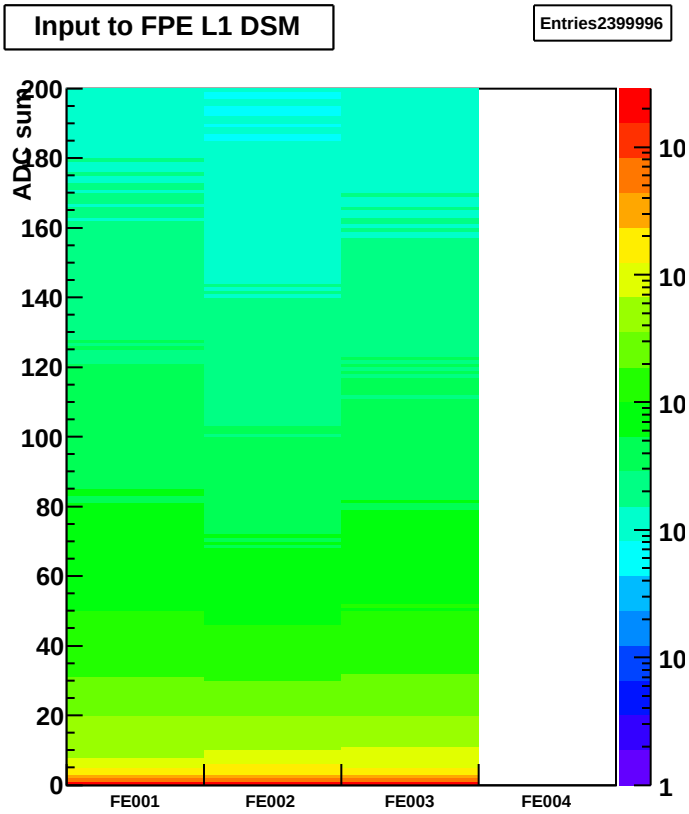

Input to FPD L2 DSM
Entries 2399996
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

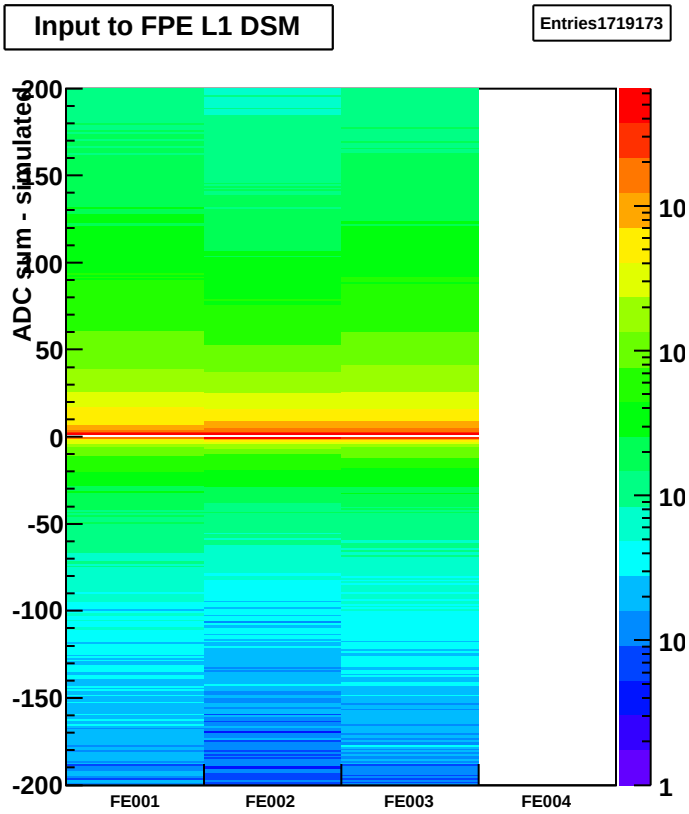

Input to FE001 QT board
Entries 1.919997e+07
ADC
channel

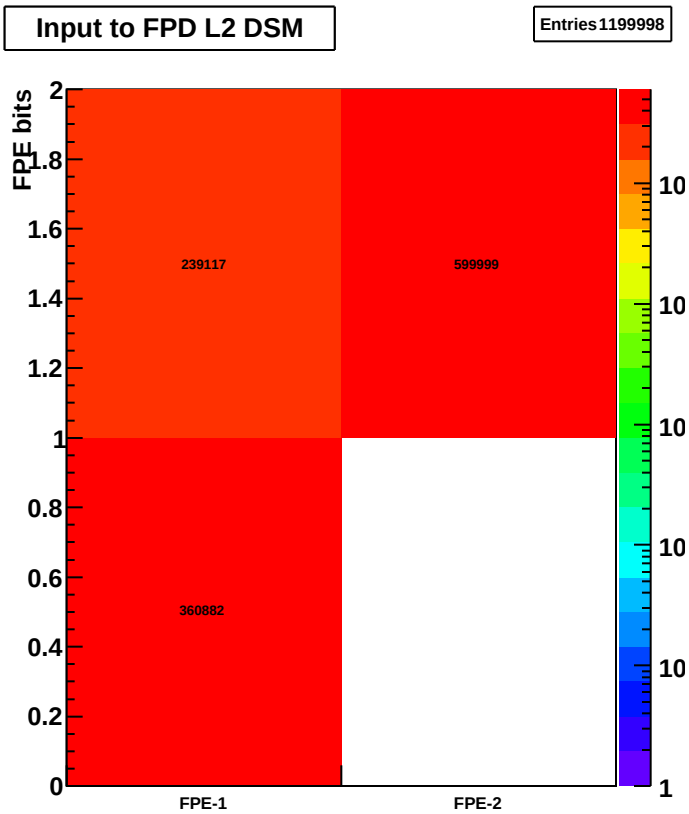

Input to FE002 QT board
Entries 1.919997e+07
ADC
channel

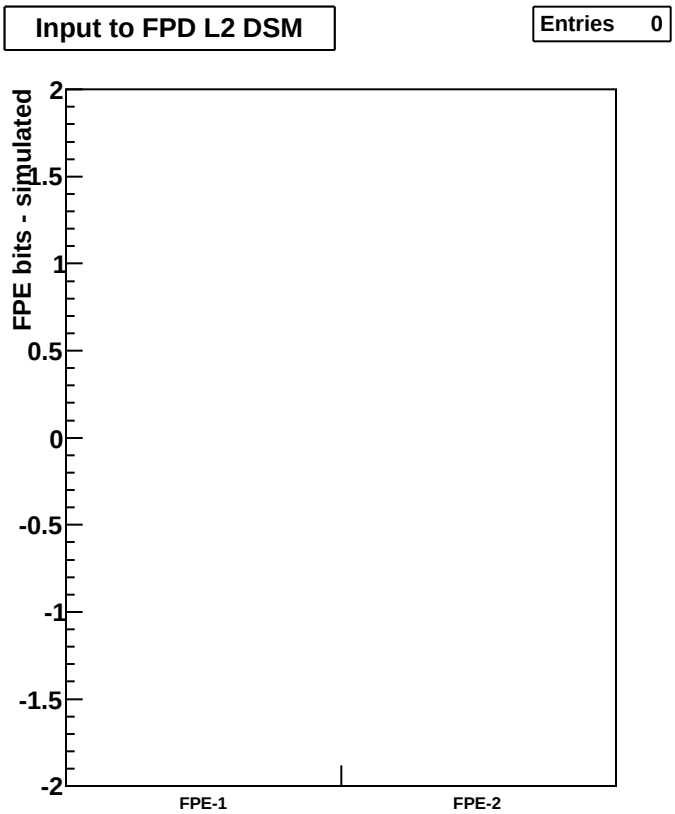

Input to FE003 QT board
Entries 1.919997e+07
ADC
channel


Input to FE004 QT board
Entries 1.919997e+07
ADC
channel


Input to FPE L1 DSM
Entries2399996
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries1719173
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1199998
FPE bits
239117
599999
360882
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2