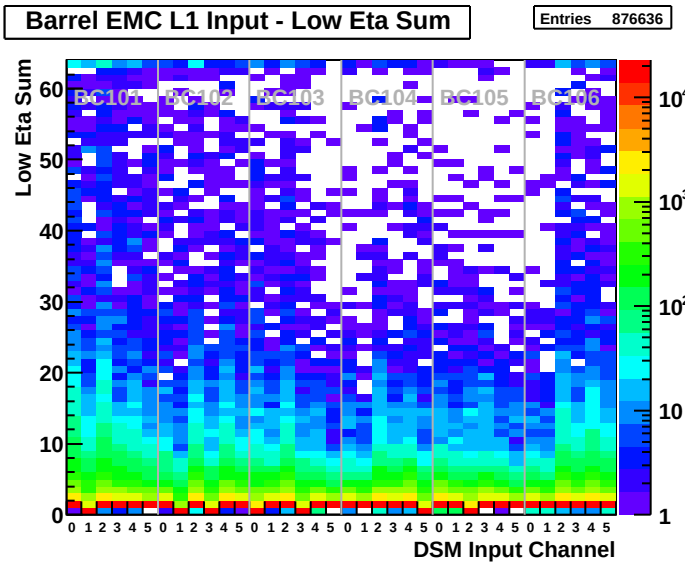
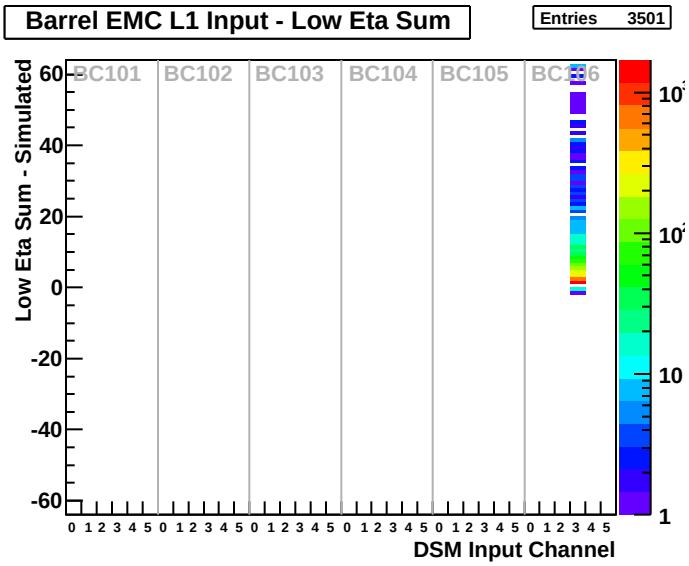
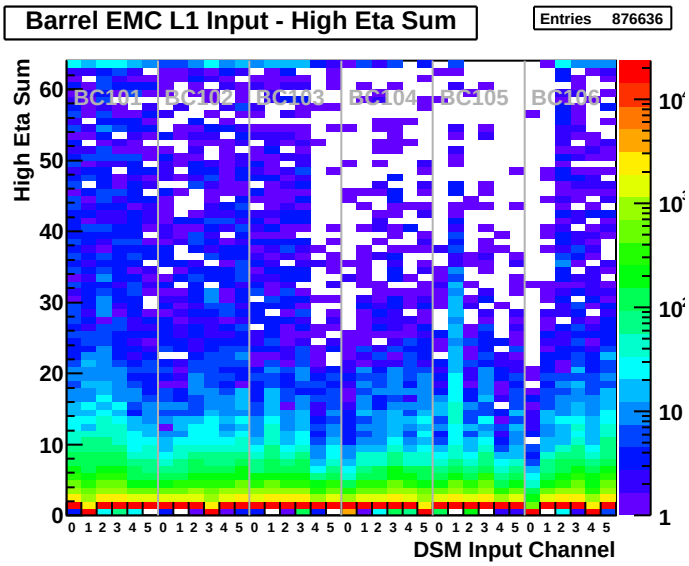
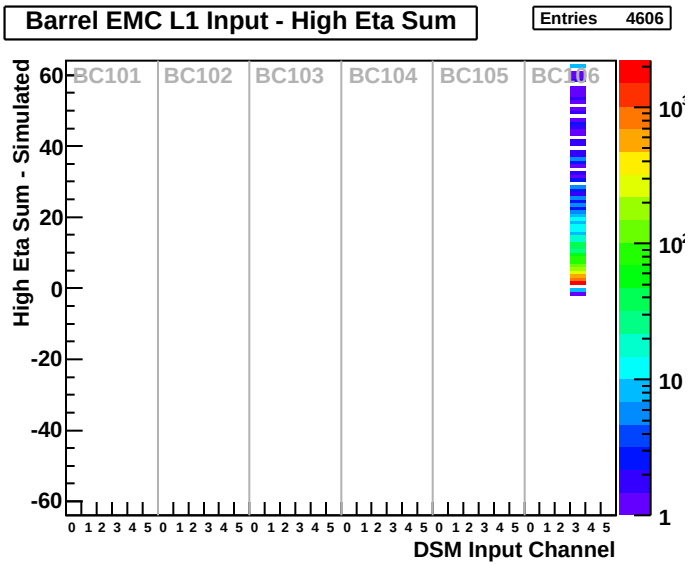
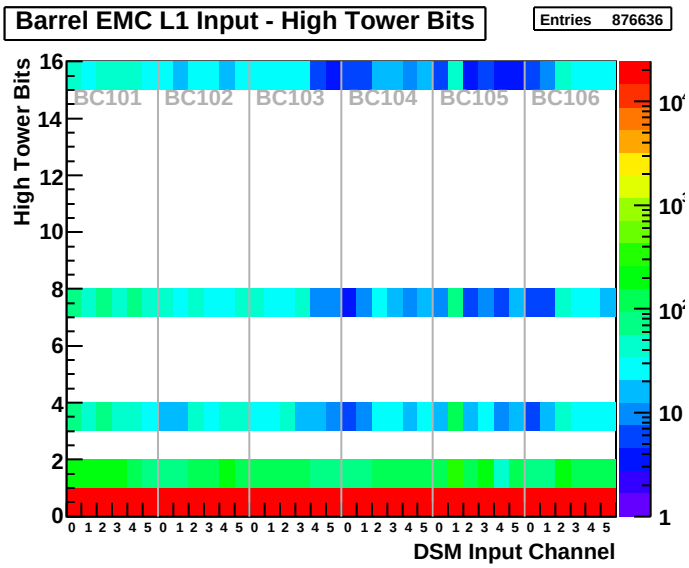
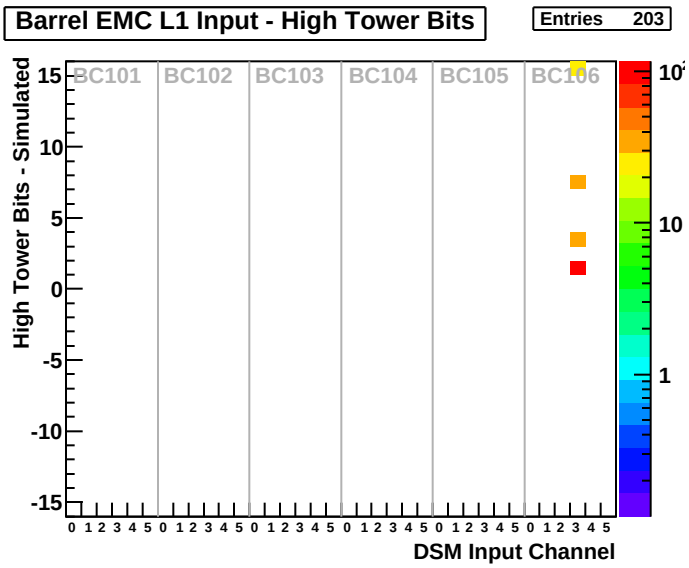

Barrel EMC L1 Input - Low Eta Sum
Entries 876636
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

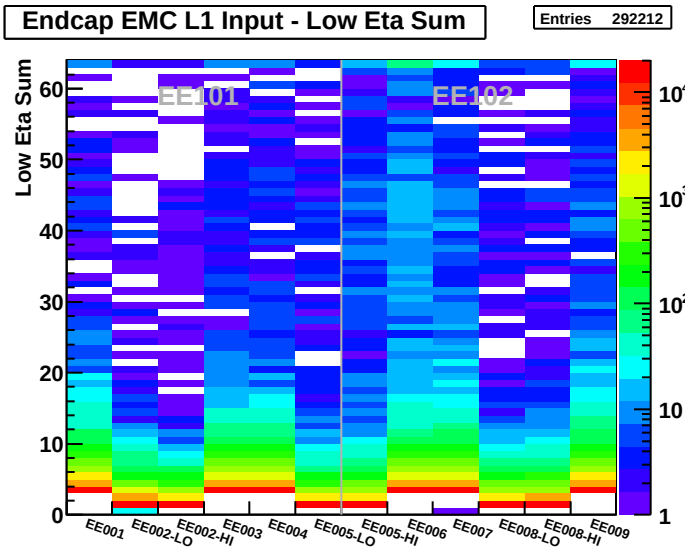

Barrel EMC L1 Input - Low Eta Sum
Entries 3501
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

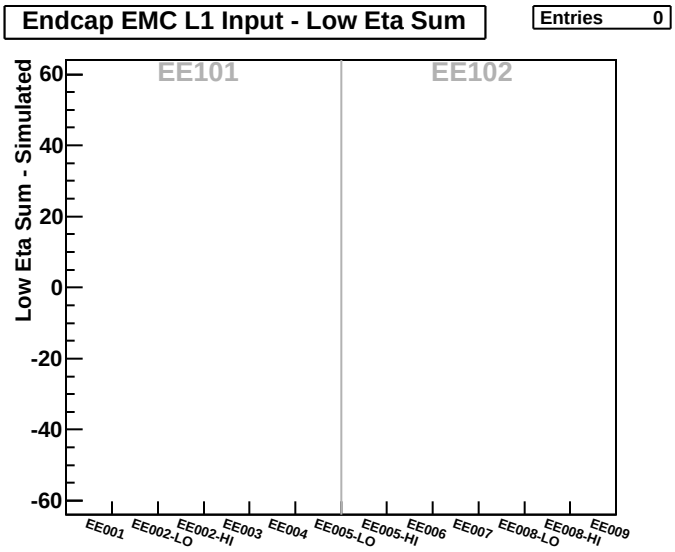

Barrel EMC L1 Input - High Eta Sum
Entries 876636
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

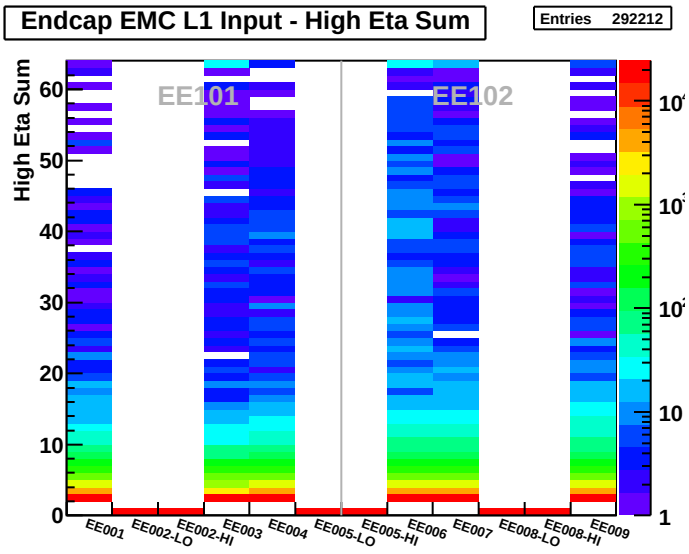

Barrel EMC L1 Input - High Eta Sum
Entries 4606
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

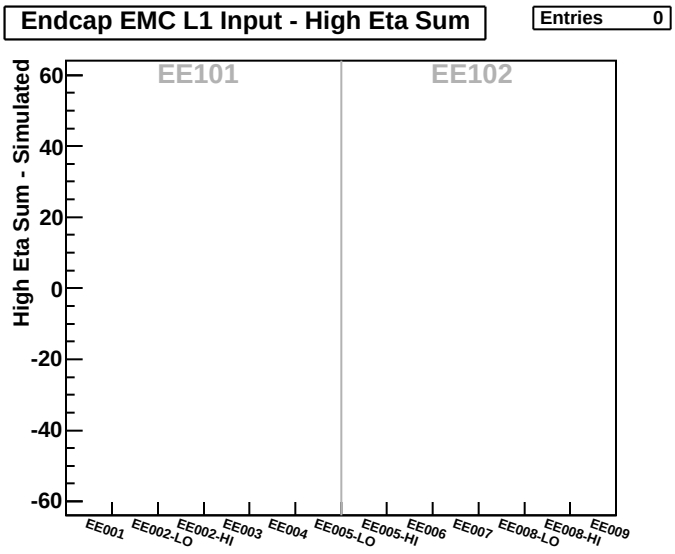

Barrel EMC L1 Input - High Tower Bits
Entries 876636
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

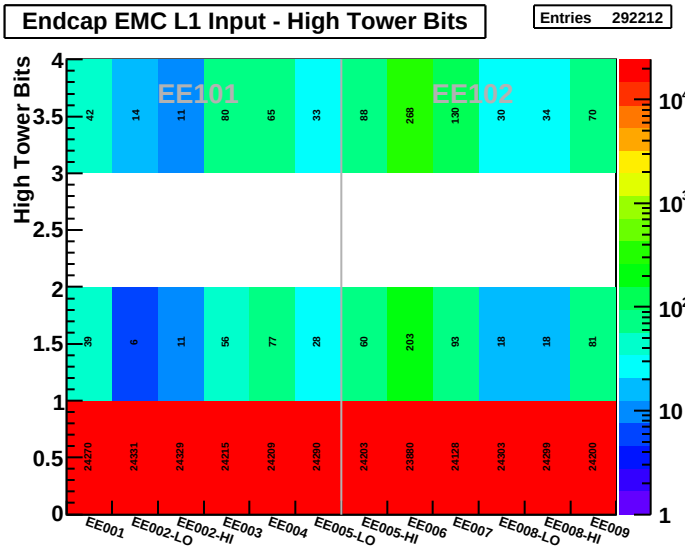

Barrel EMC L1 Input - High Tower Bits
Entries 203
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

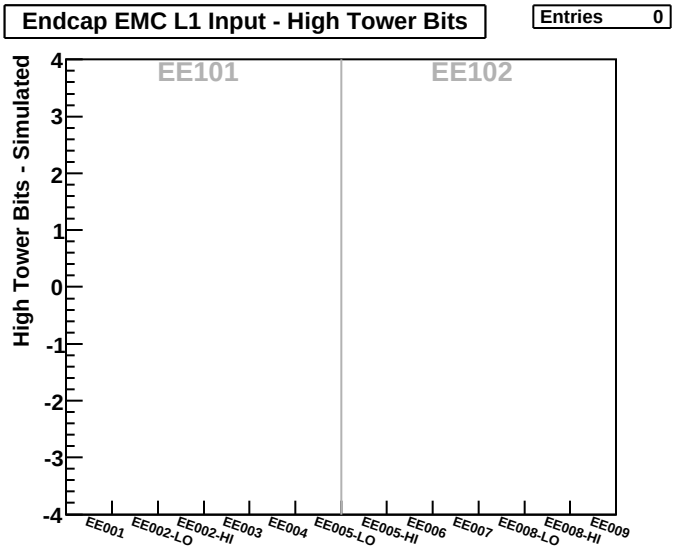

Endcap EMC L1 Input - Low Eta Sum
Entries 292212
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

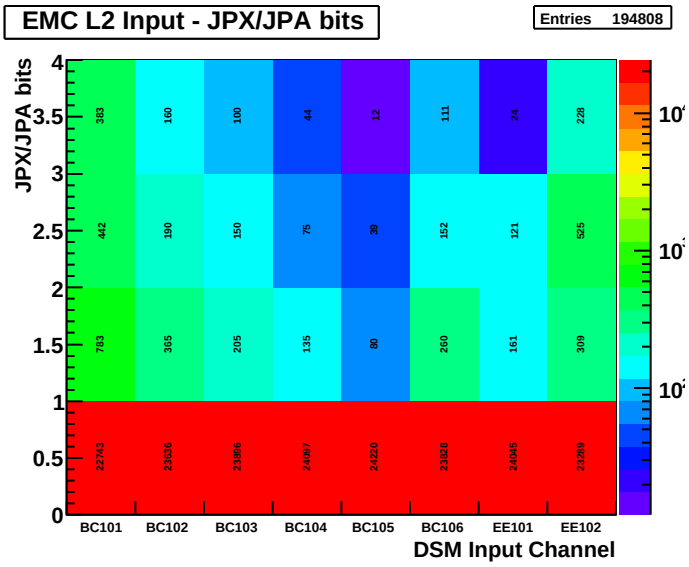

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

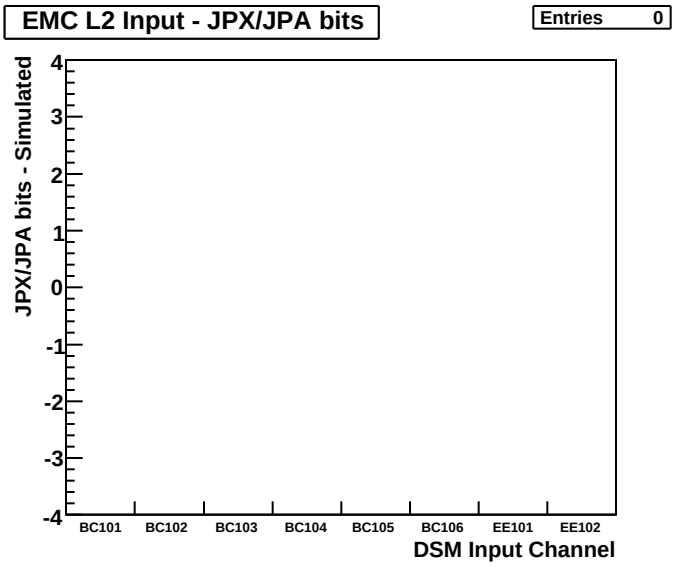

Endcap EMC L1 Input - High Eta Sum
Entries 292212
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

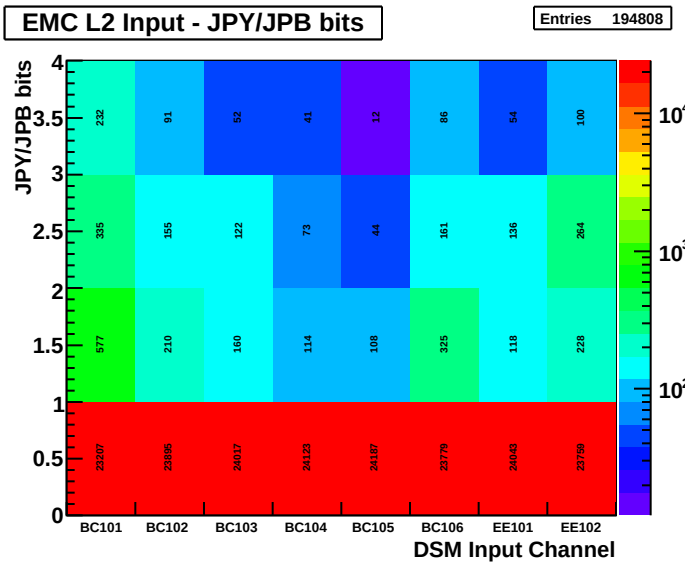

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

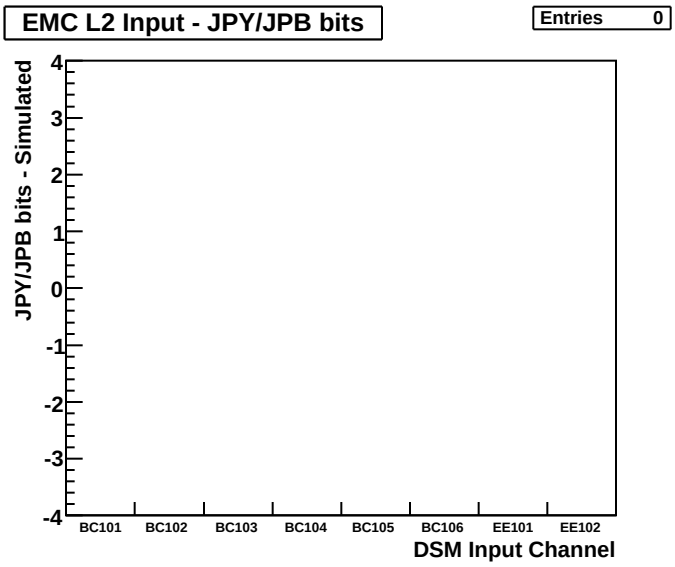

Endcap EMC L1 Input - High Tower Bits
Entries 292212
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

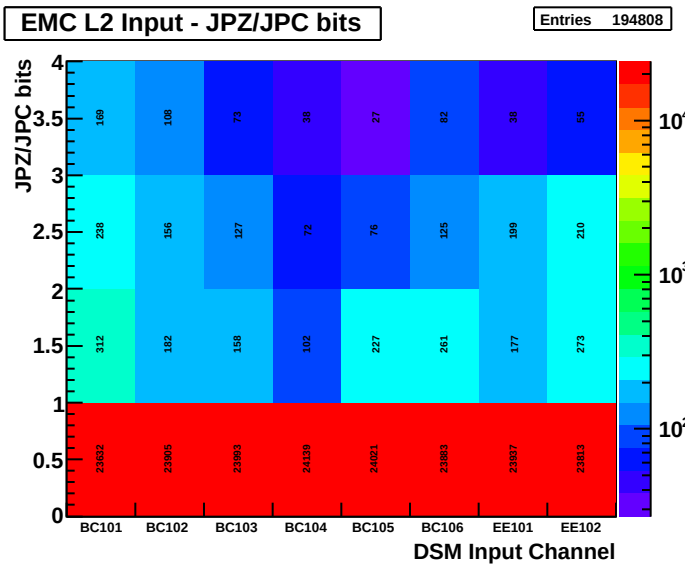

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

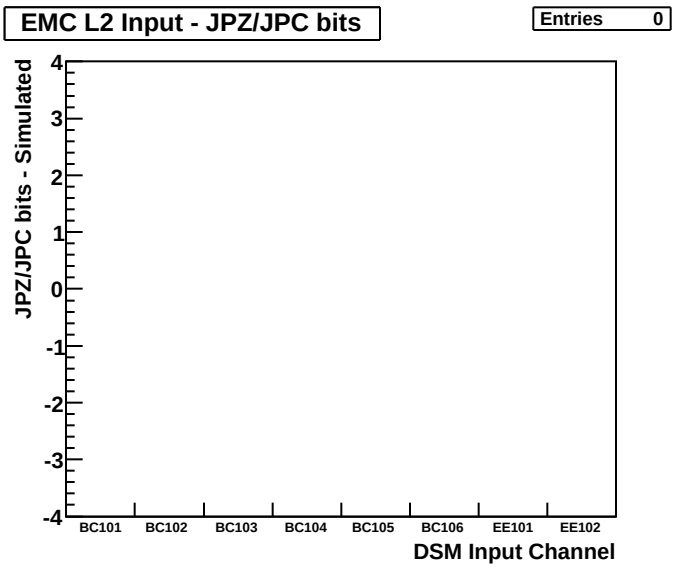

EMC L2 Input - JPX/JPA bits
Entries 194808
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
383 160 100 44 12 111 24 228
442 190 150 75 39 152 121 525
783 365 205 135 80 260 161 309
22743 23636 23896 24097 24220 23928 24045 23289

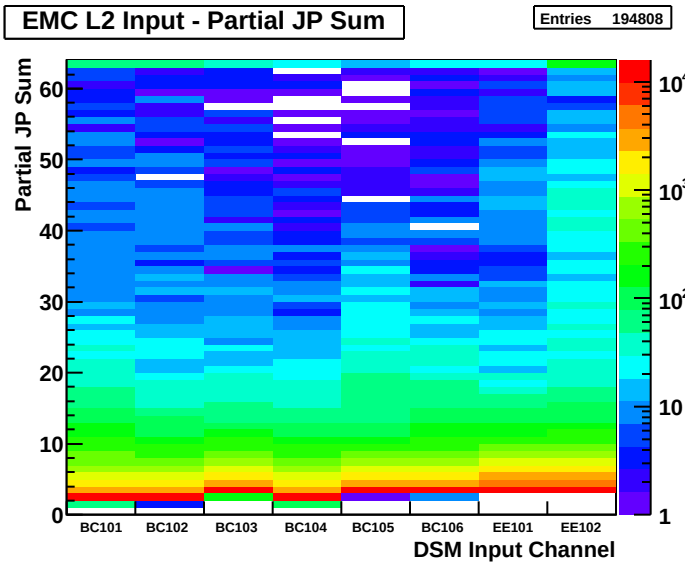

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

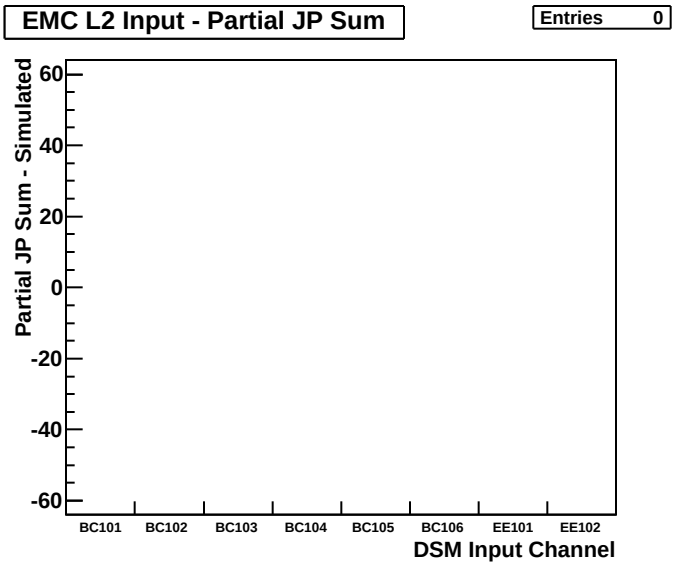

EMC L2 Input - JPY/JPB bits
Entries 194808
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
232 91 52 41 12 86 54 100
335 155 122 73 44 161 136 264
577 210 160 114 108 325 118 228
23207 23895 24017 24123 24187 23779 24043 23759

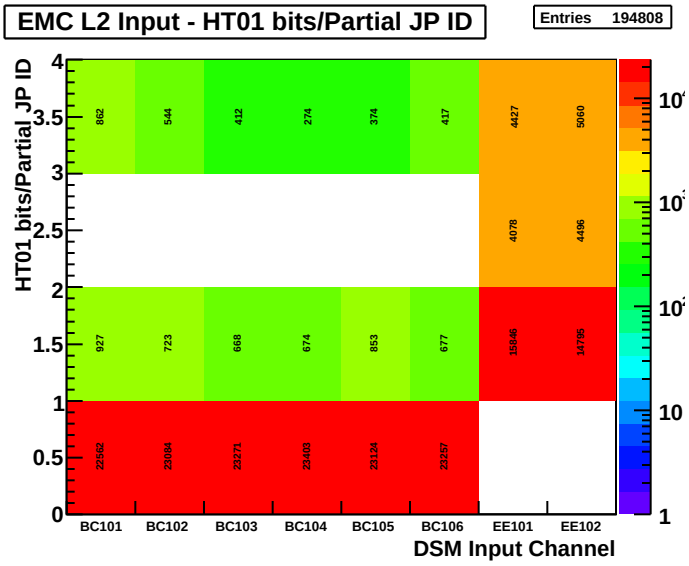

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

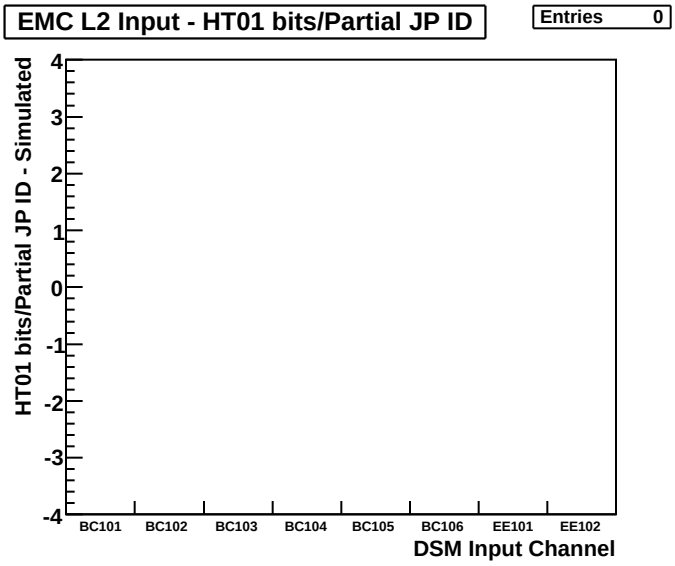

EMC L2 Input - JPZ/JPC bits
Entries 194808
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
189 108 73 38 27 82 38 55
238 156 127 72 76 125 199 210
312 182 158 102 227 261 177 273
23832 23905 23993 24139 24021 23883 23937 23813

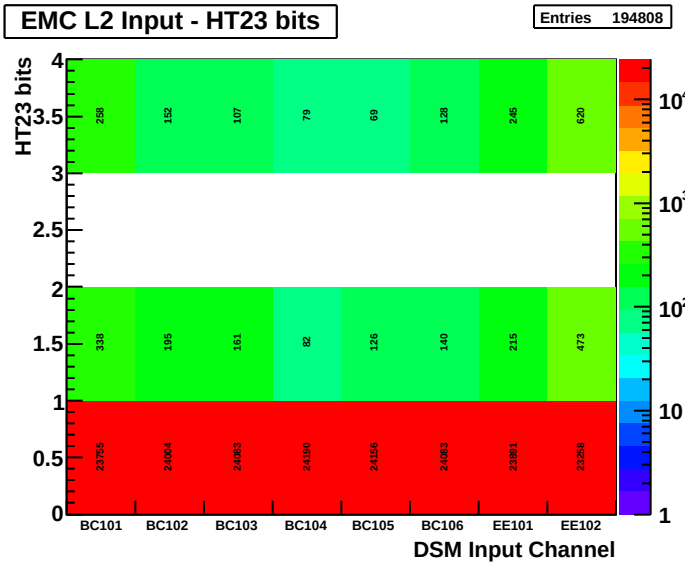

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

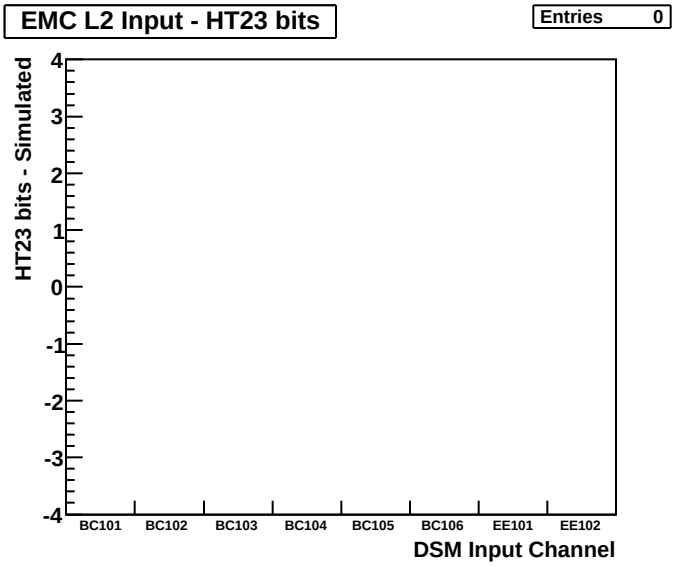

EMC L2 Input - Partial JP Sum
Entries 194808
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

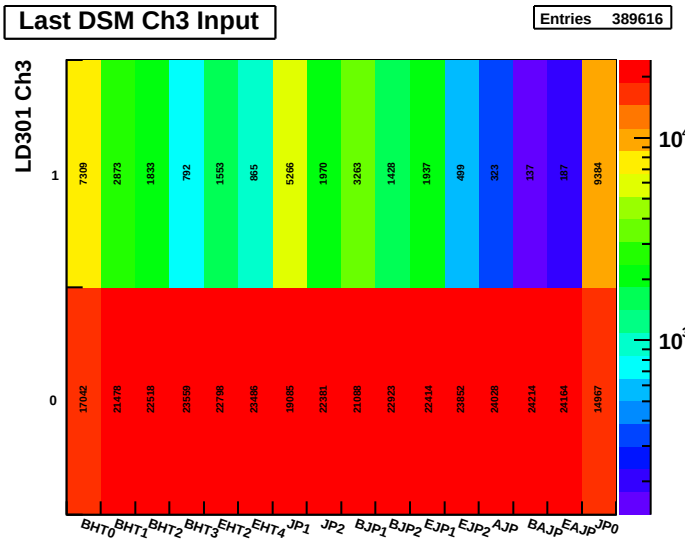

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

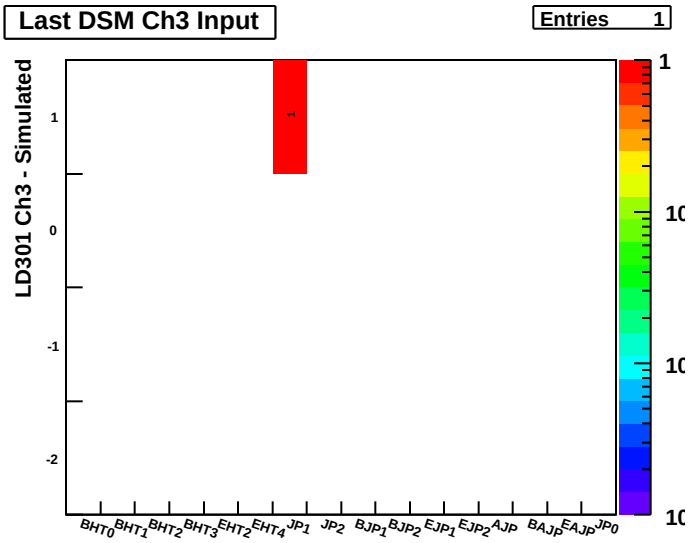

EMC L2 Input - HT01 bits/Partial JP ID
Entries 194808
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

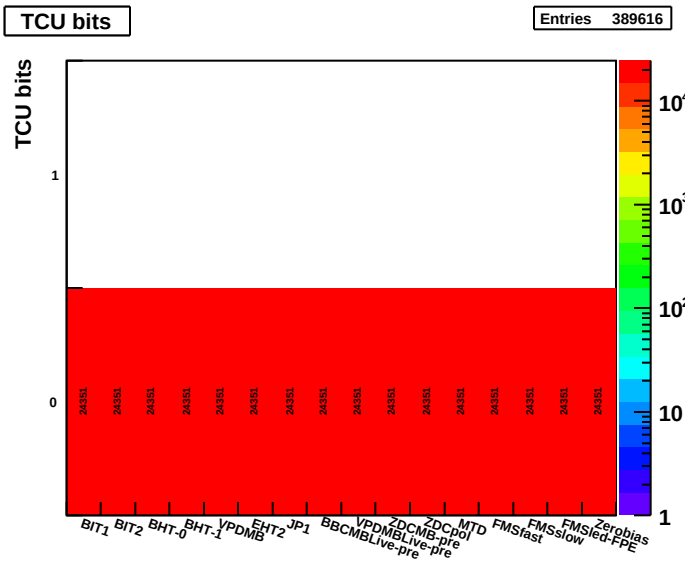

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

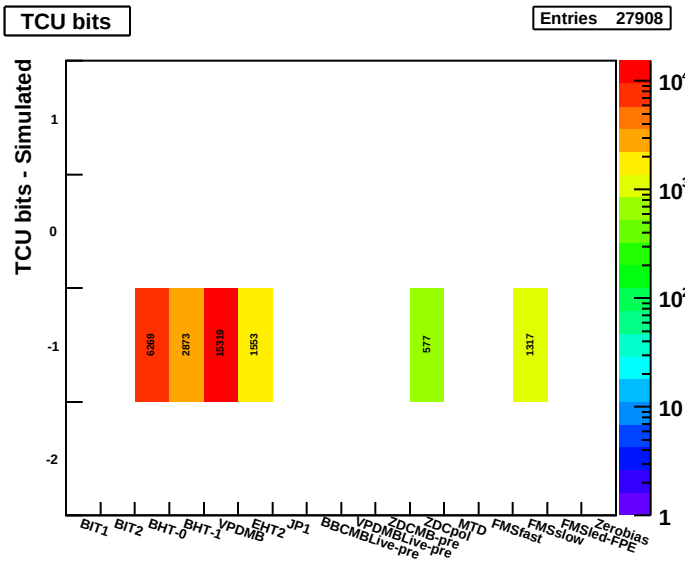

EMC L2 Input - HT23 bits
Entries 194808
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

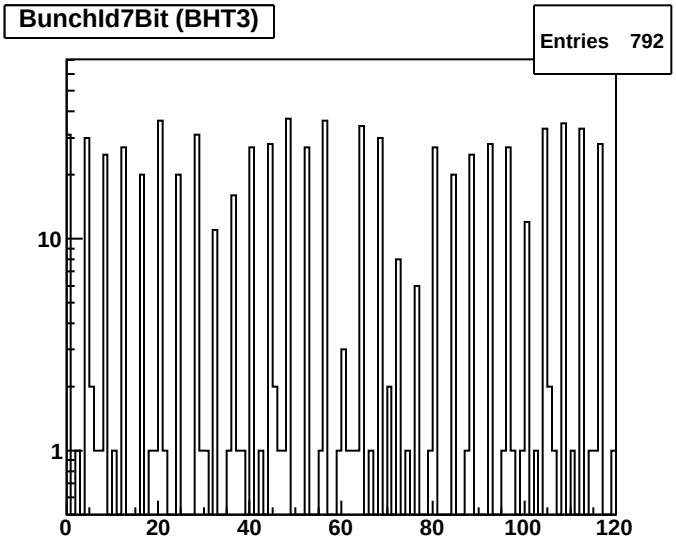

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

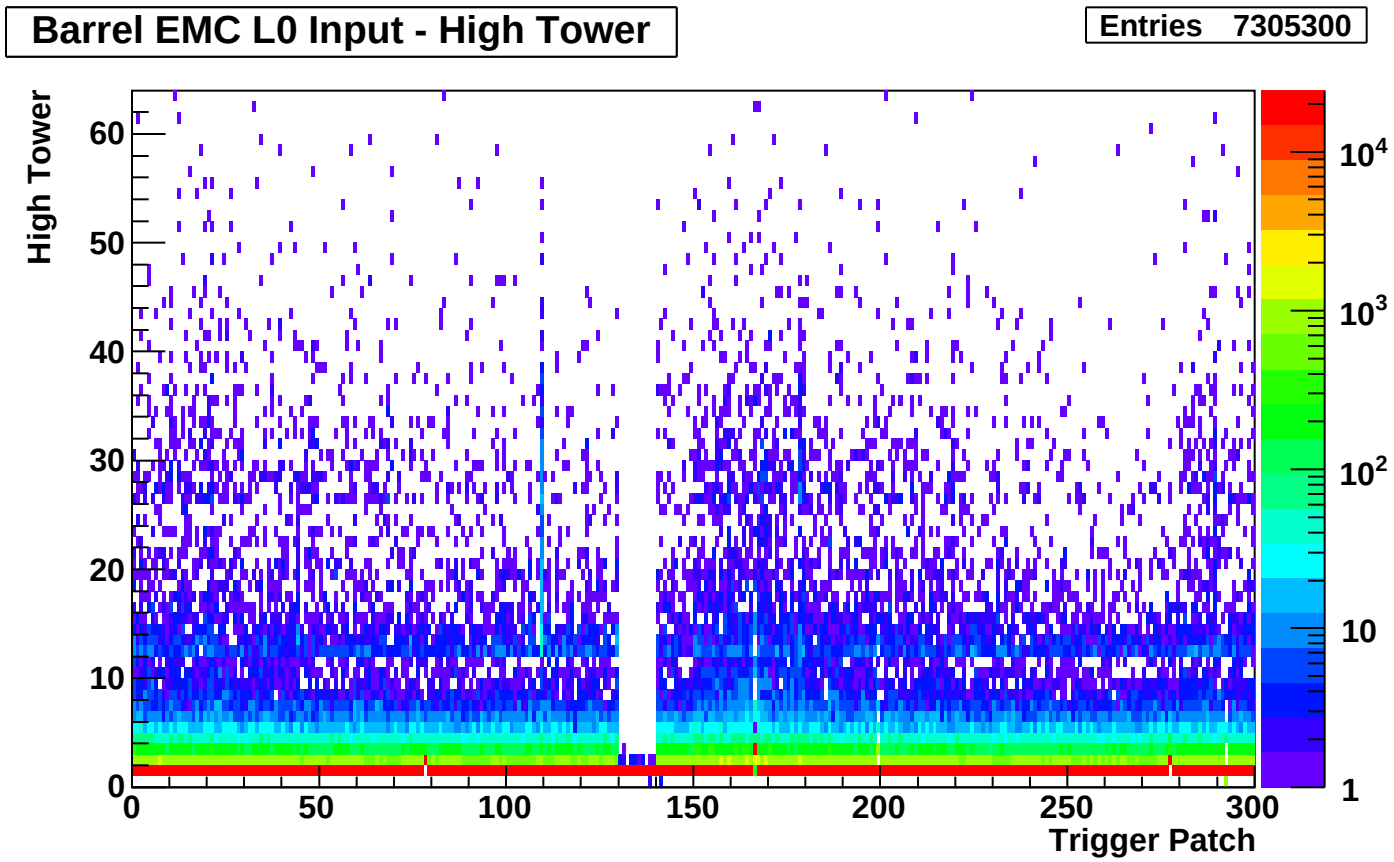

Last DSM Ch3 Input
Entries 389616
LD301 Ch3
7309 2873 1833 792 1553 865 5266 1970 3263 1428 1937 499 323 137 187 9384
17042 21478 22518 23559 22798 23466 19085 22381 21088 22923 22414 23852 24028 24214 24164 14967
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

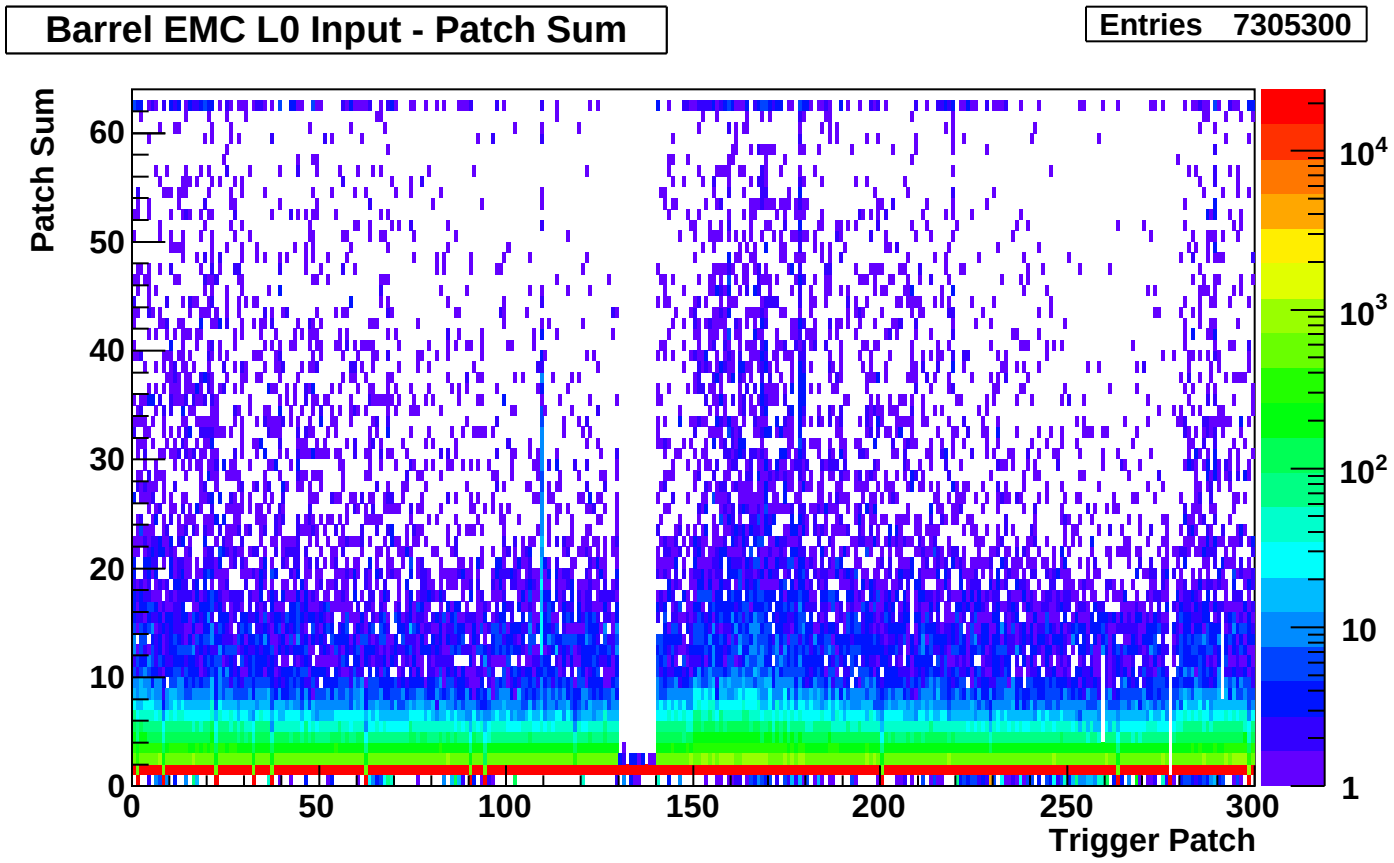

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

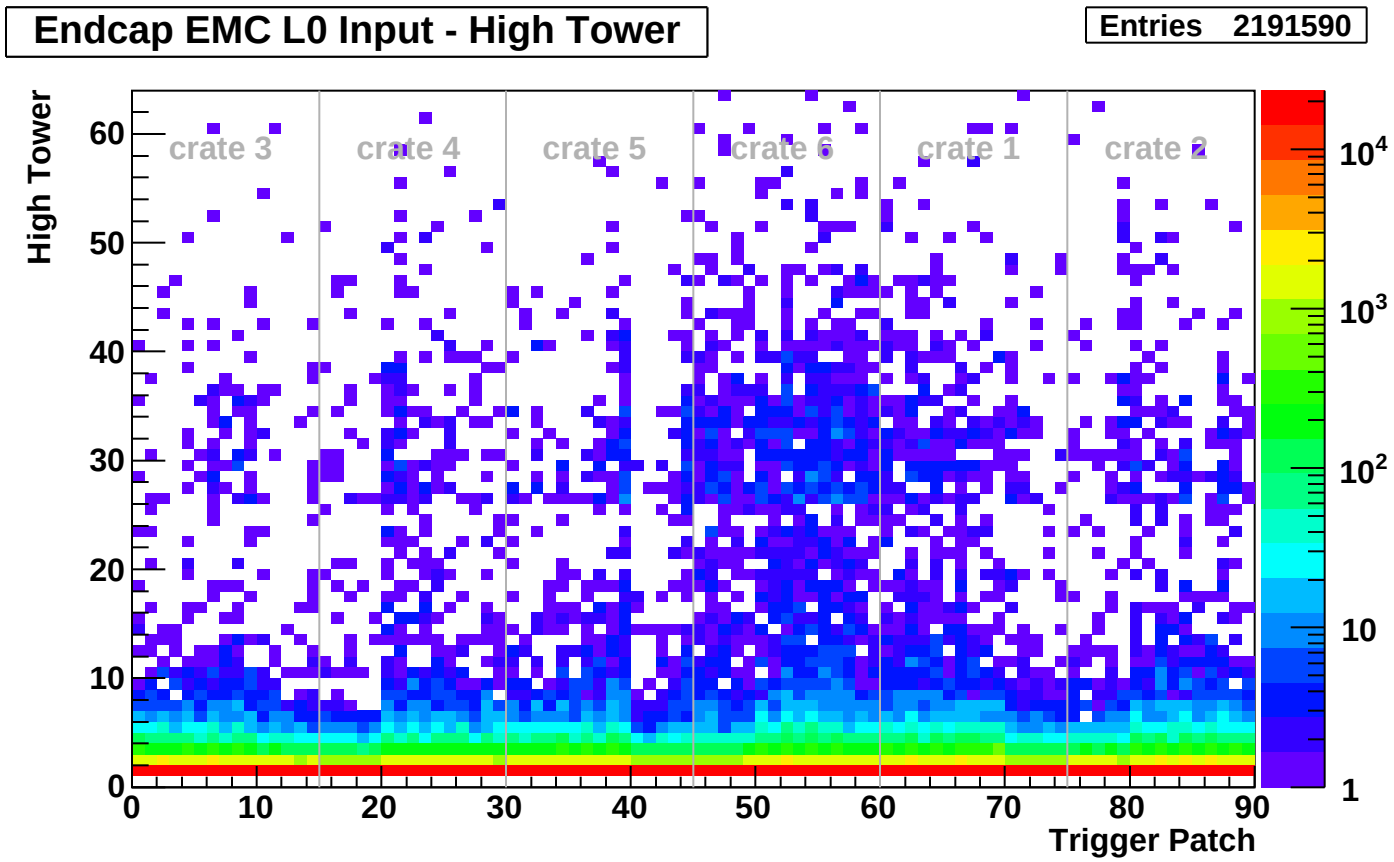

TCU bits
Entries 389616
TCU bits
24351
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

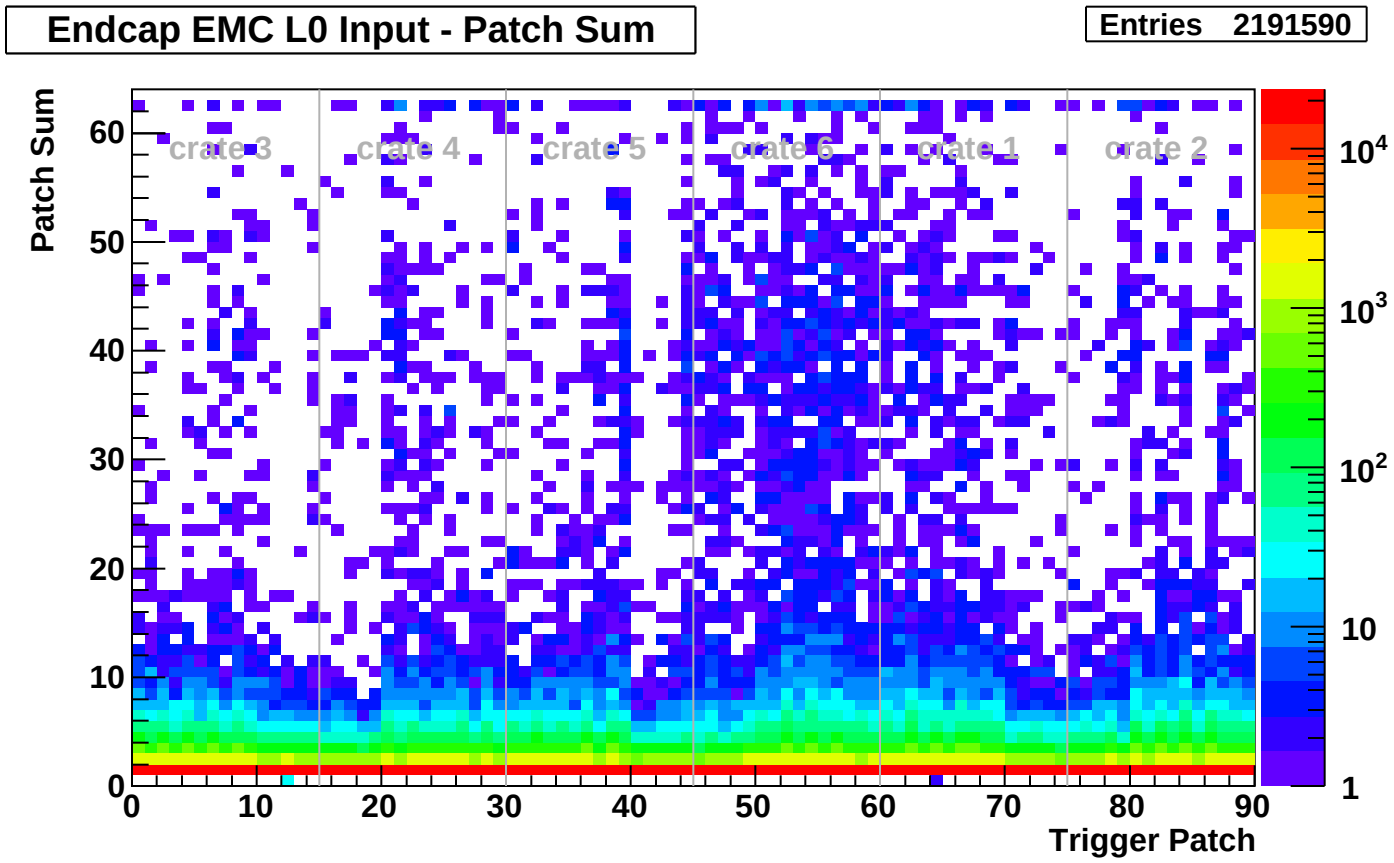

TCU bits
Entries 27908
TCU bits - Simulated
6269 2873 15319 1553 577 1317
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

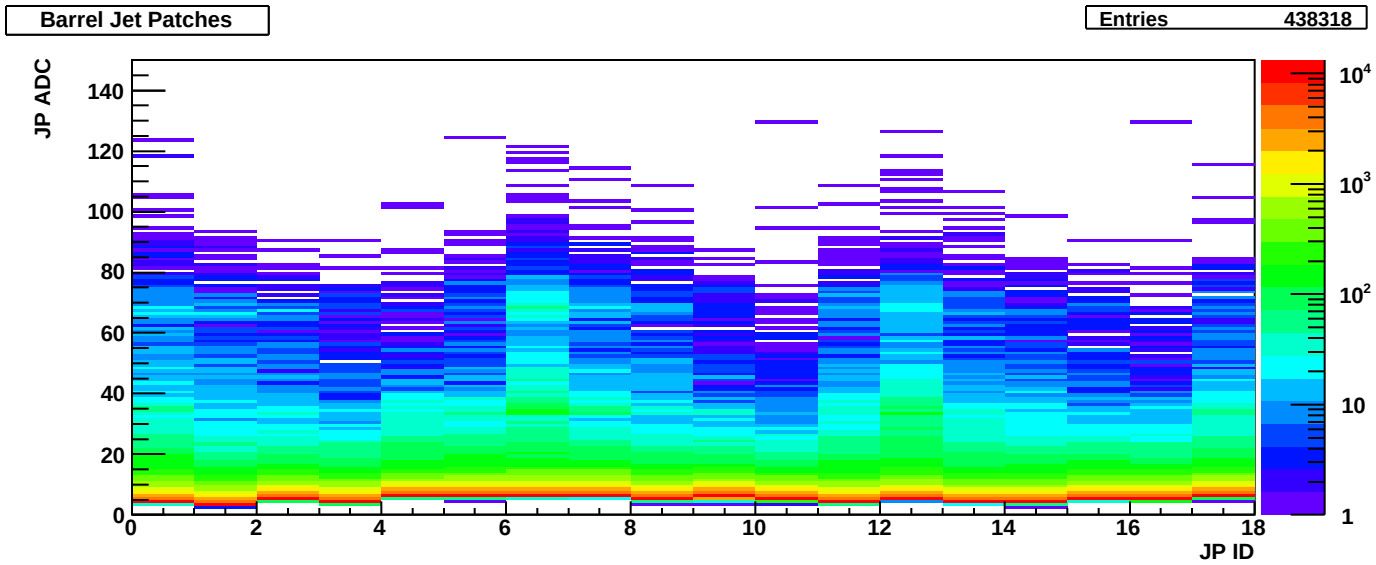

BunchId7Bit (BHT3)
Entries 792
0 20 40 60 80 100 120

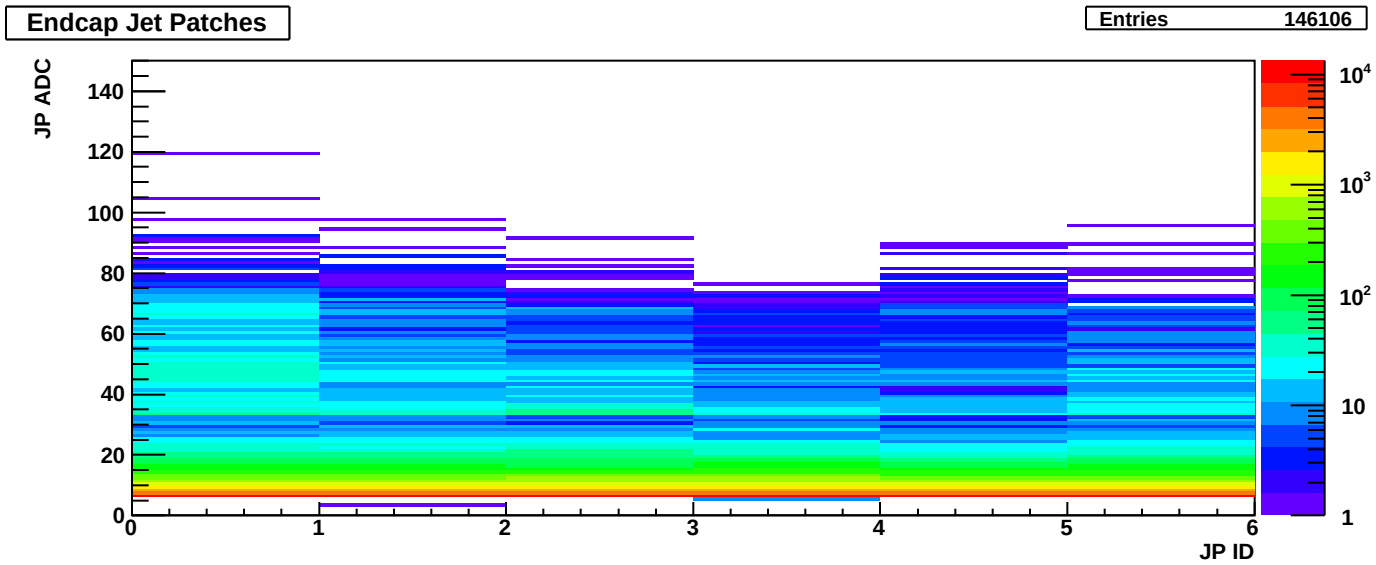

Barrel EMC L0 Input - High Tower
Entries 7305300
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

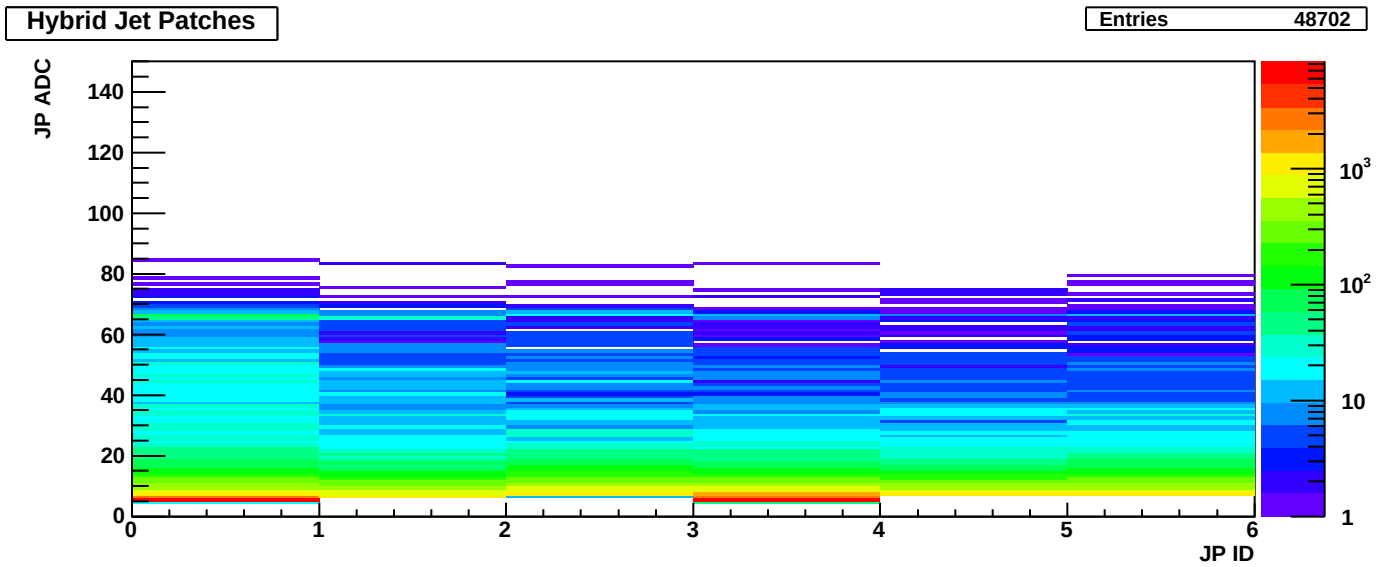

Barrel EMC L0 Input - Patch Sum
Entries 7305300
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

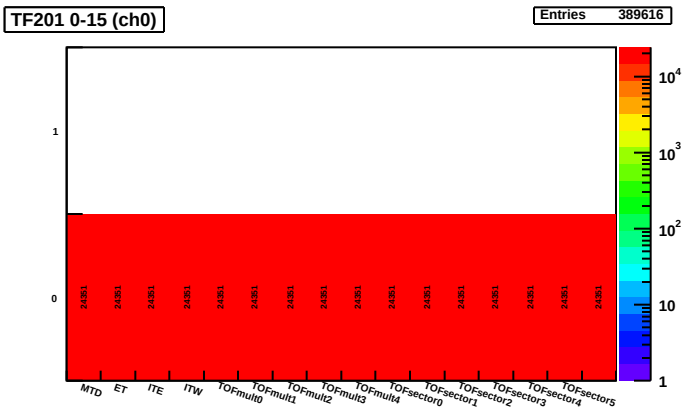

Endcap EMC L0 Input - High Tower
Entries 2191590
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

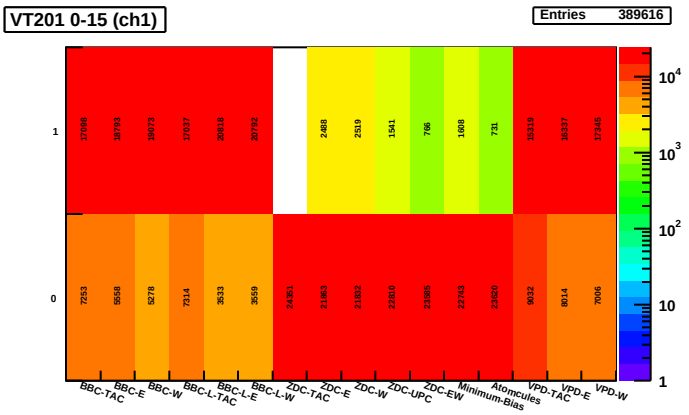

Endcap EMC L0 Input - Patch Sum
Entries 2191590
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

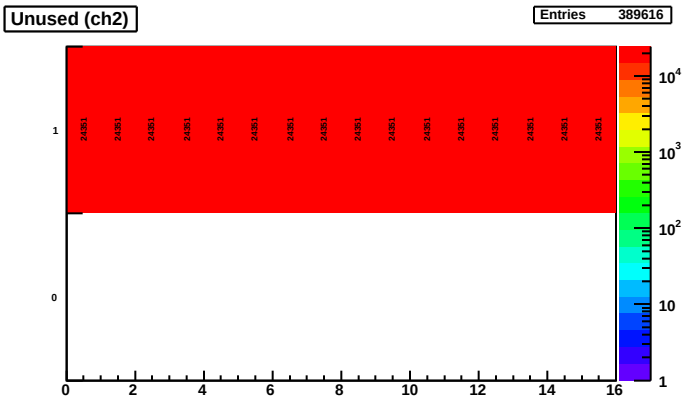

Barrel Jet Patches
Entries 438318
JP ADC
JP ID

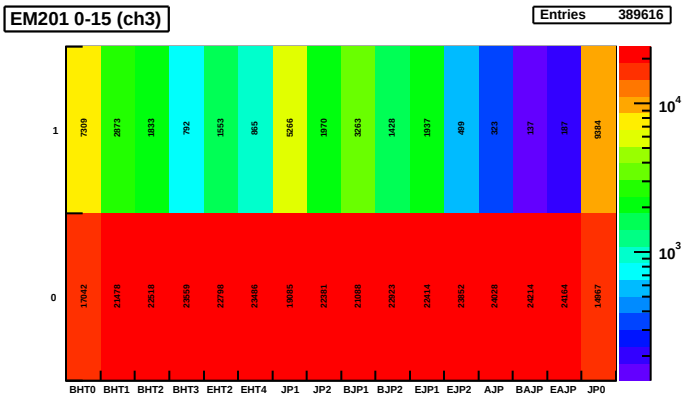

Endcap Jet Patches
Entries 146106
JP ADC
JP ID

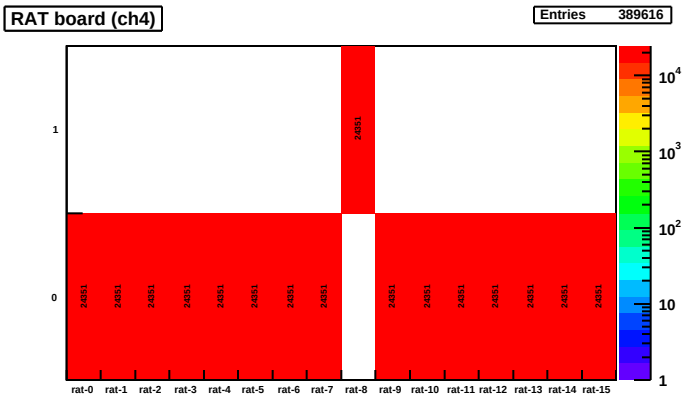

Hybrid Jet Patches
Entries 48702
JP ADC
JP ID

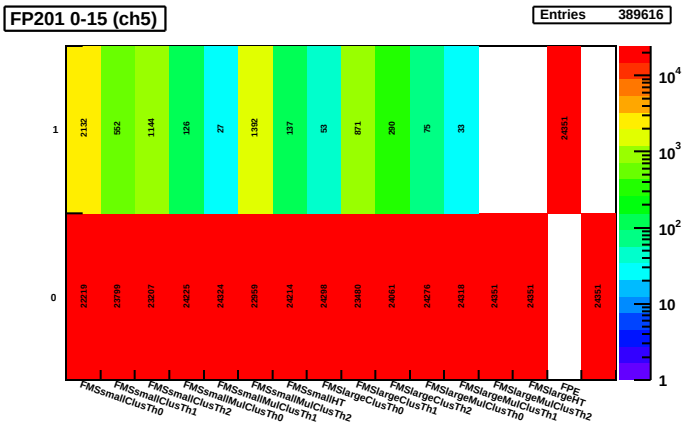

TF201 0-15 (ch0)
Entries 389616
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

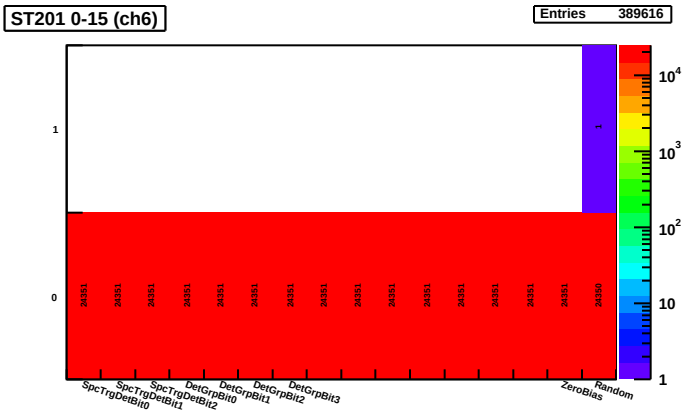

VT201 0-15 (ch1)
Entries 389616
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

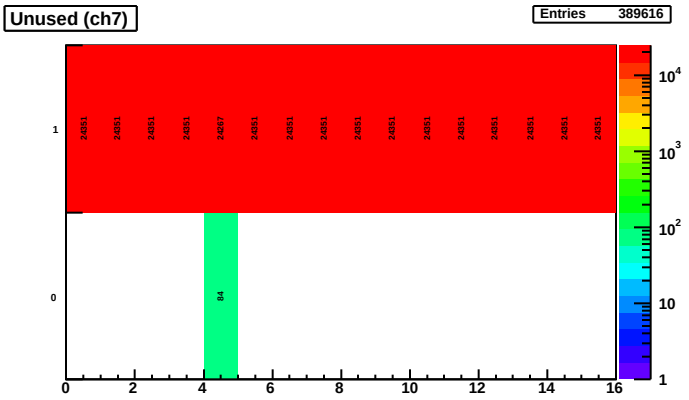

Unused (ch2)
Entries 389616

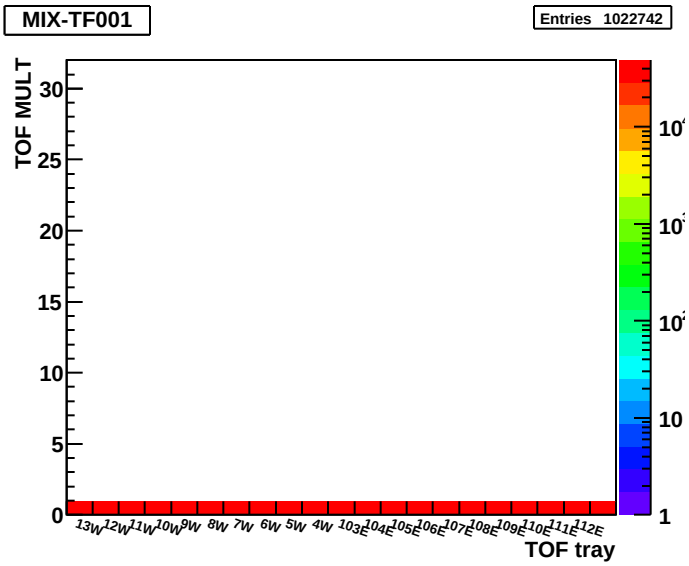

EM201 0-15 (ch3)
Entries 389616
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

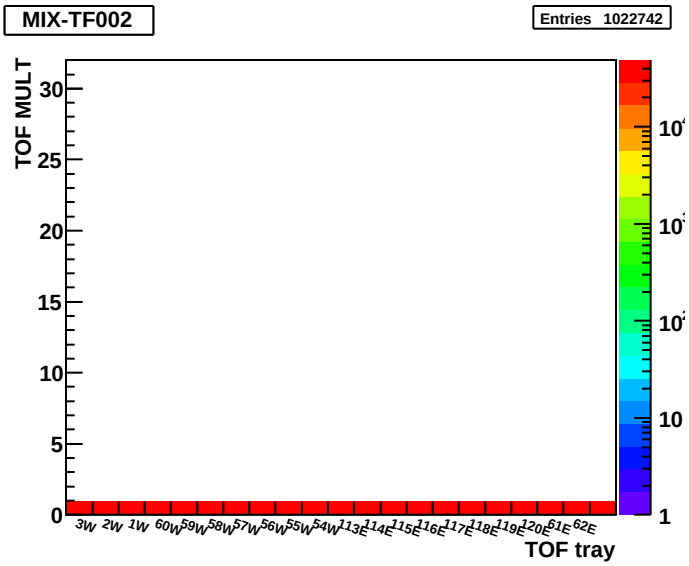

RAT board (ch4)
Entries 389616
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

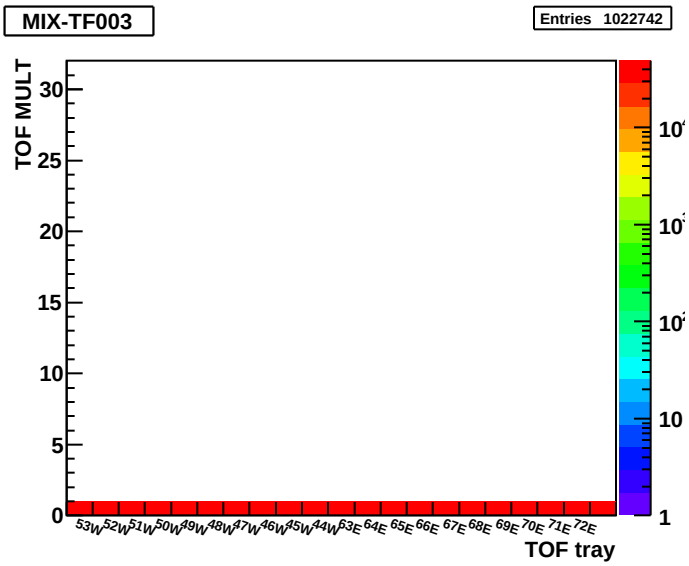

FP201 0-15 (ch5)
Entries 389616
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

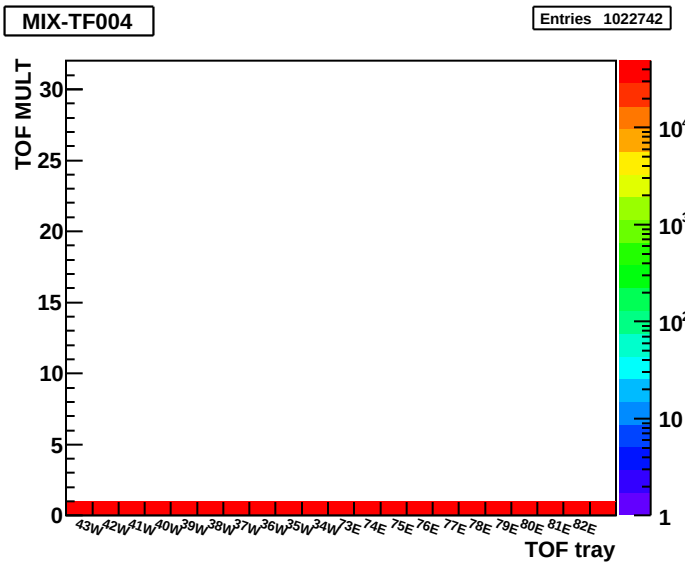

ST201 0-15 (ch6)
Entries 389616
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

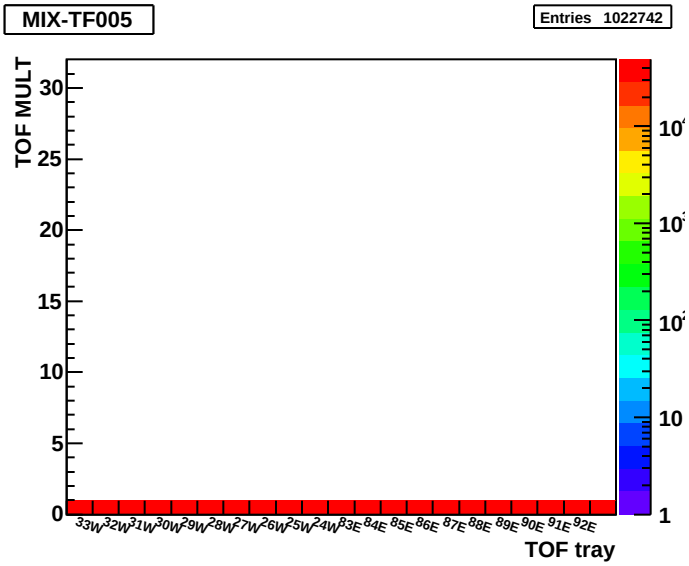

Unused (ch7)
Entries 389616

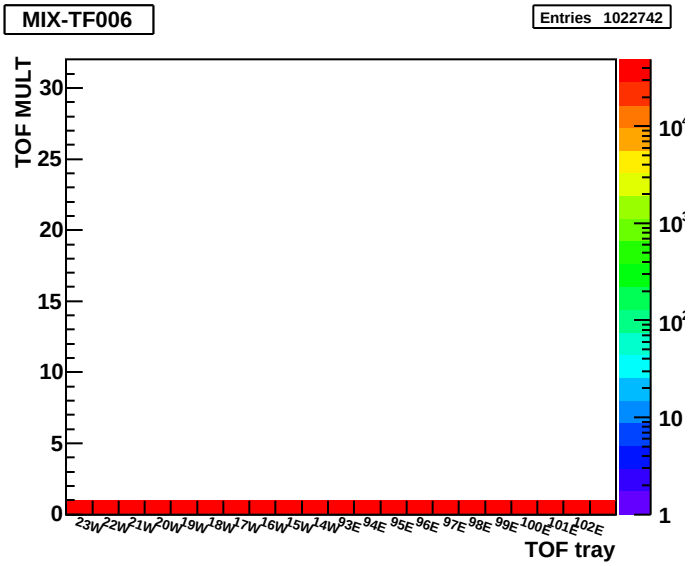

MIX-TF001
Entries 1022742
TOF MULT
TOF tray

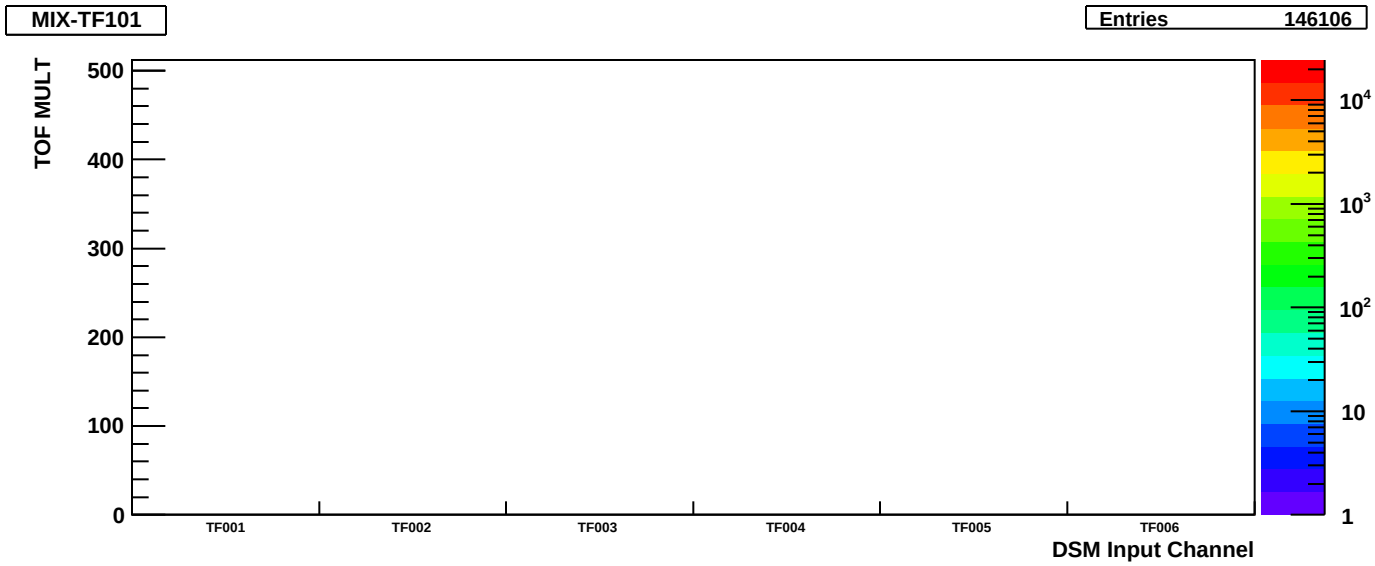

MIX-TF002
Entries 1022742
TOF MULT
TOF tray

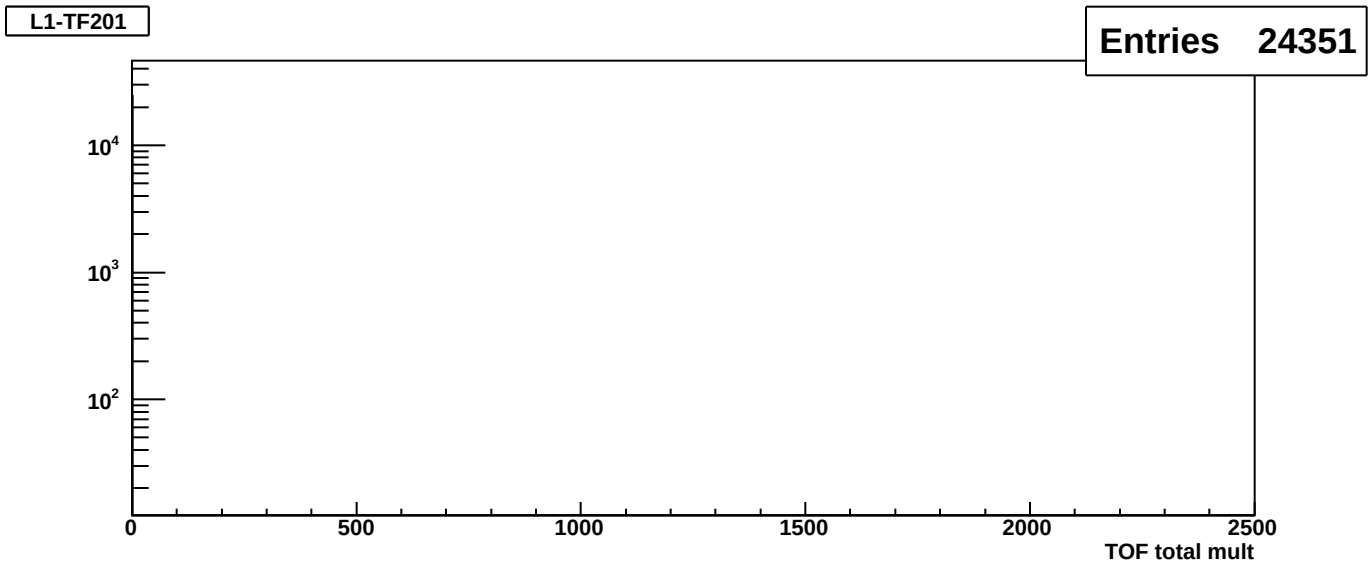

MIX-TF003
Entries 1022742
TOF MULT
TOF tray

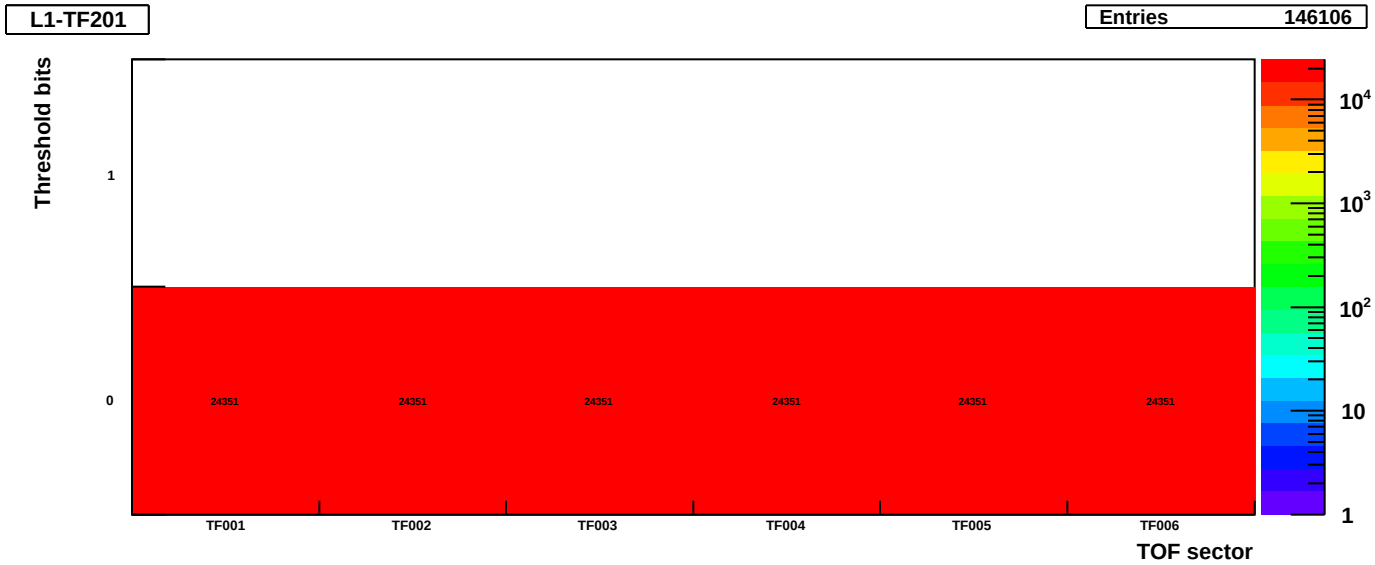

MIX-TF004
Entries 1022742
TOF MULT
TOF tray

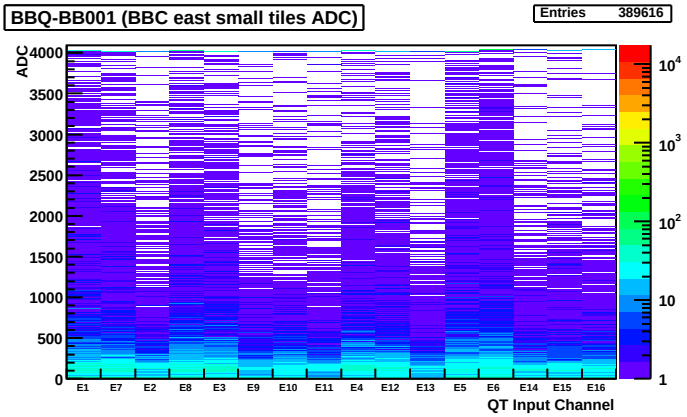

MIX-TF005
Entries 1022742
TOF MULT
TOF tray

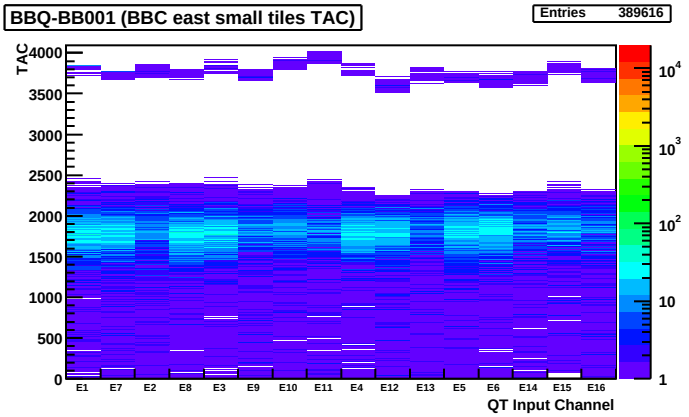

MIX-TF006
Entries 1022742
TOF MULT
TOF tray

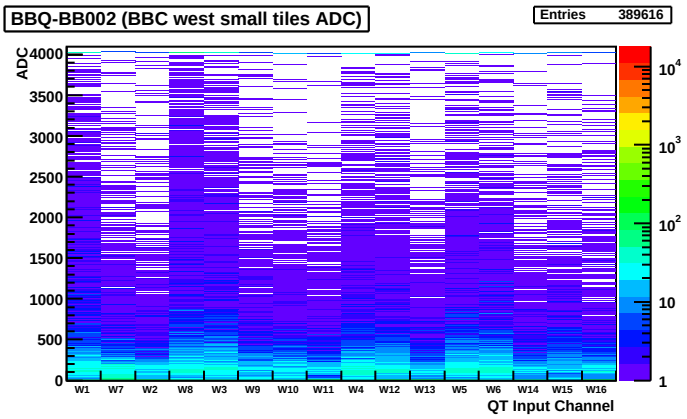

MIX-TF101
Entries 146106
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

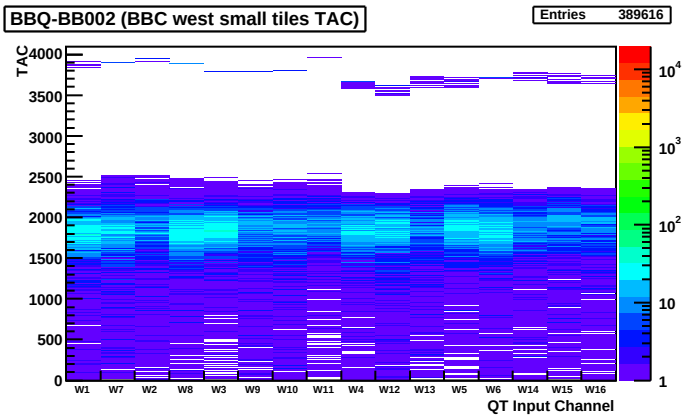

L1-TF201
Entries 24351
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

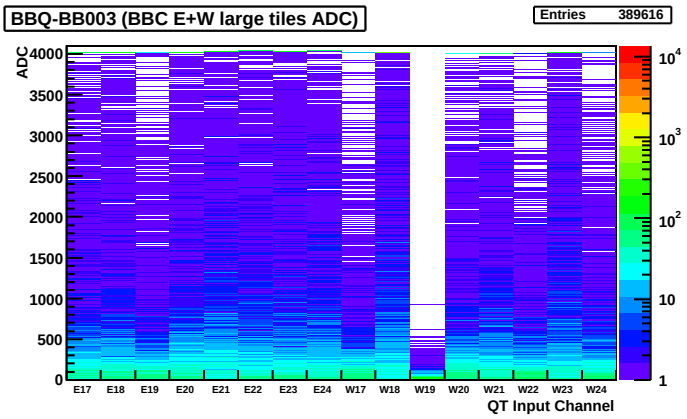

L1-TF201
Entries 146106
Threshold bits
1
0
24351
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

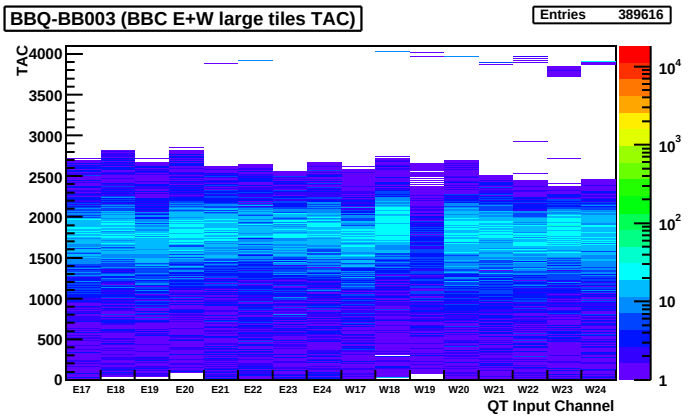

BBQ-BB001 (BBC east small tiles ADC)
Entries 389616
ADC
QT Input Channel

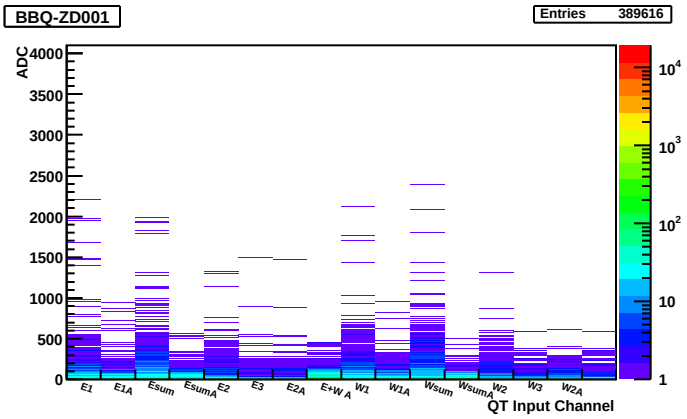

BBQ-BB001 (BBC east small tiles TAC)
Entries 389616
TAC
QT Input Channel

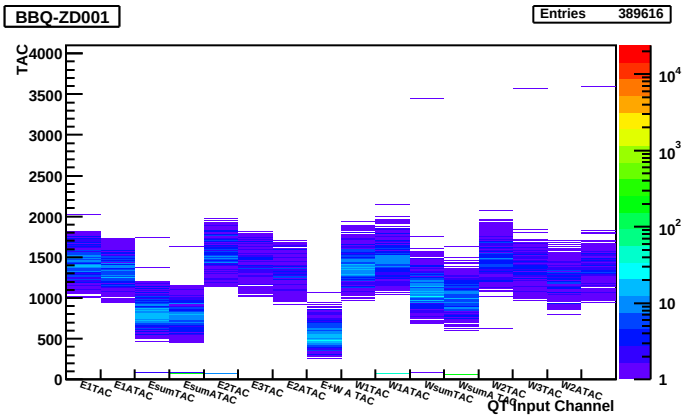

BBQ-BB002 (BBC west small tiles ADC)
Entries 389616
ADC
QT Input Channel

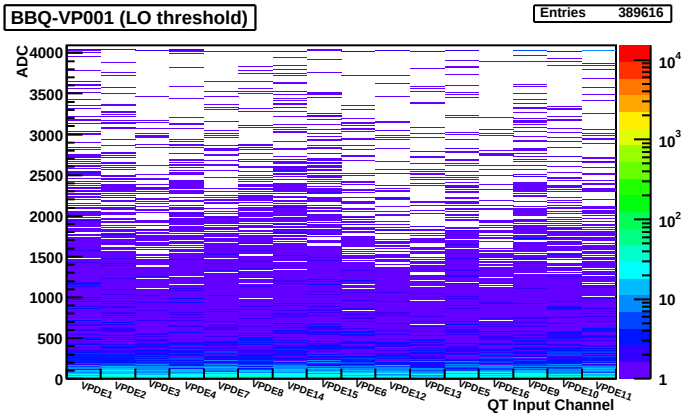

BBQ-BB002 (BBC west small tiles TAC)
Entries 389616
TAC
QT Input Channel

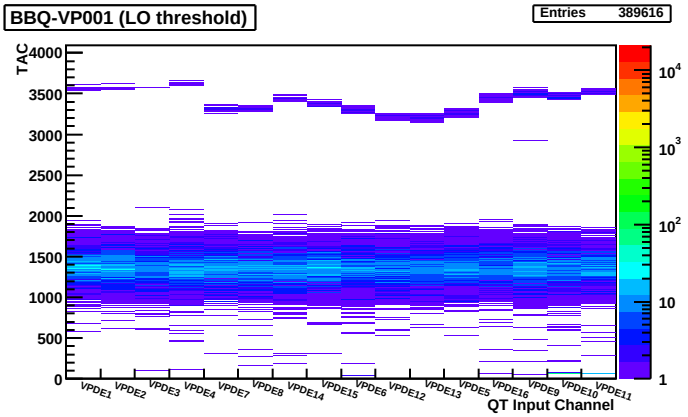

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 389616
ADC
QT Input Channel

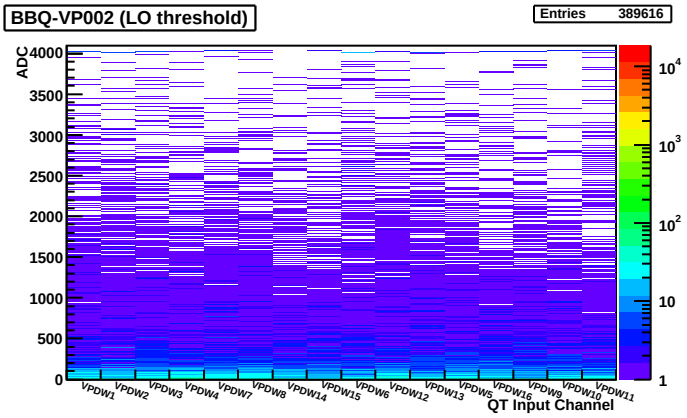

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 389616
TAC
QT Input Channel

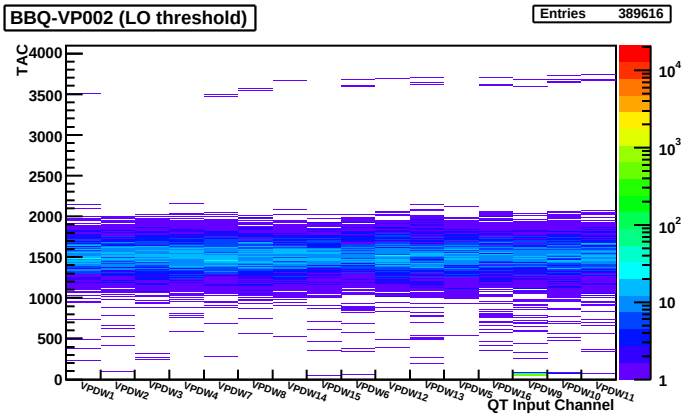

BBQ-ZD001
Entries 389616
ADC
QT Input Channel

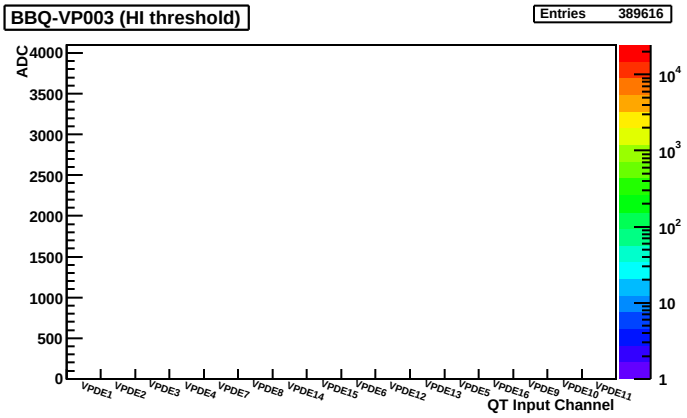

BBQ-ZD001
Entries 389616
TAC
QT Input Channel

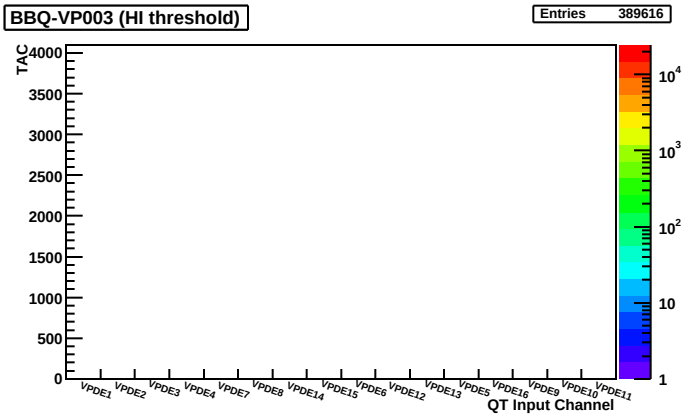

BBQ-VP001 (LO threshold)
Entries 389616
ADC
QT Input Channel

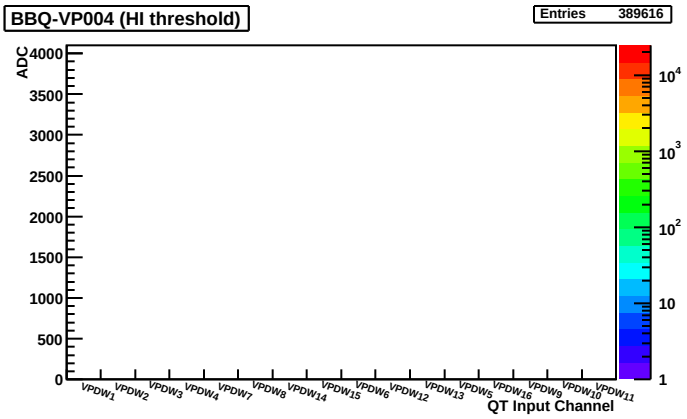

BBQ-VP001 (LO threshold)
Entries 389616
TAC
QT Input Channel

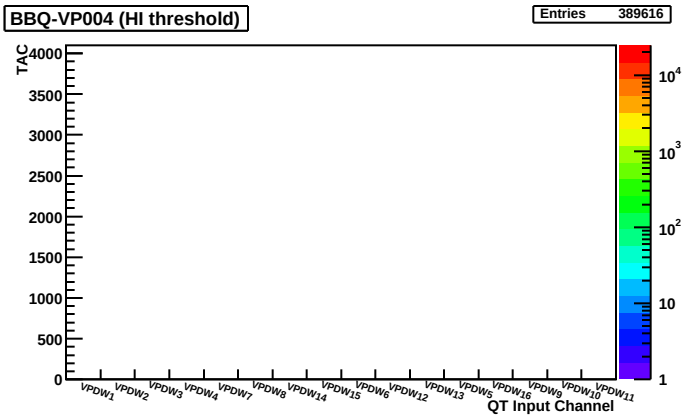

BBQ-VP002 (LO threshold)
Entries 389616
ADC
QT Input Channel

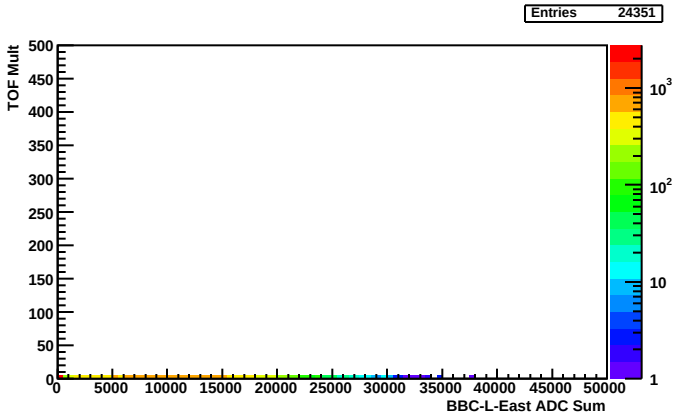

BBQ-VP002 (LO threshold)
Entries 389616
TAC
QT Input Channel

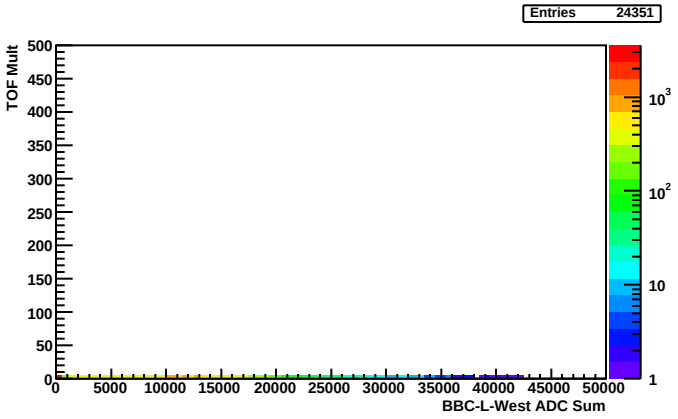

BBQ-VP003 (HI threshold)
Entries 389616
ADC
QT Input Channel

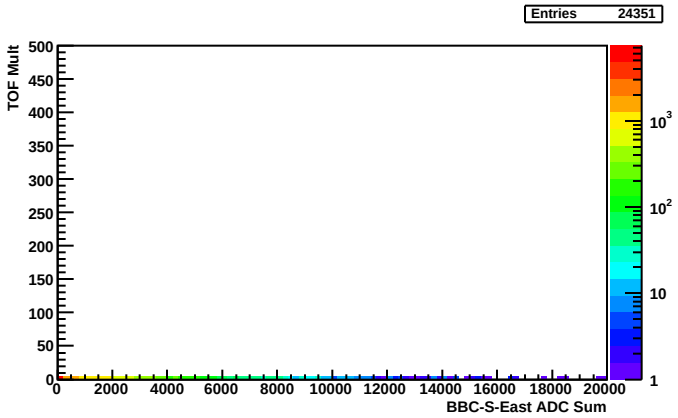

BBQ-VP003 (HI threshold)
Entries 389616
TAC
QT Input Channel

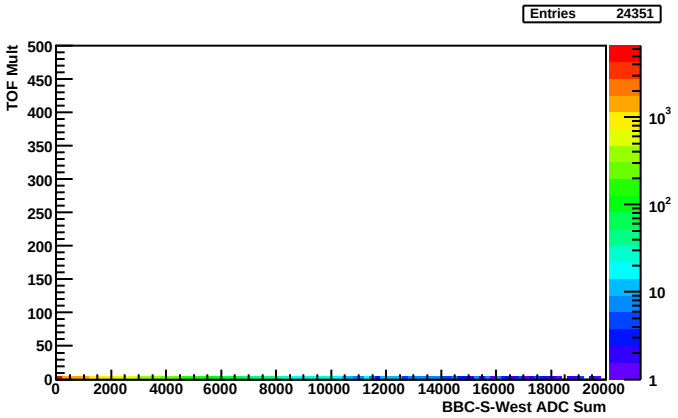

BBQ-VP004 (HI threshold)
Entries 389616
ADC
QT Input Channel

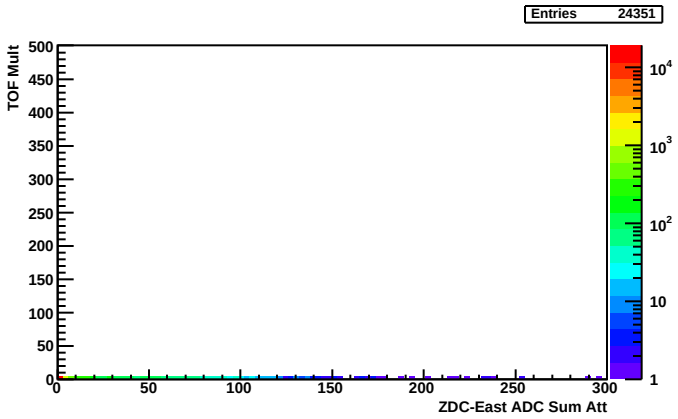

BBQ-VP004 (HI threshold)
Entries 389616
TAC
QT Input Channel

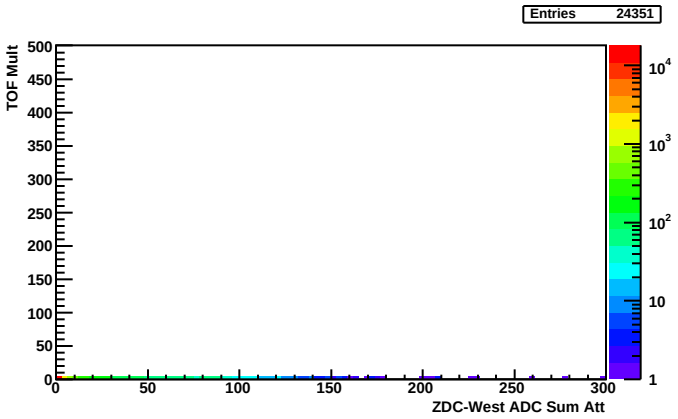

Entries 24351
TOF Mult
BBC-L-East ADC Sum

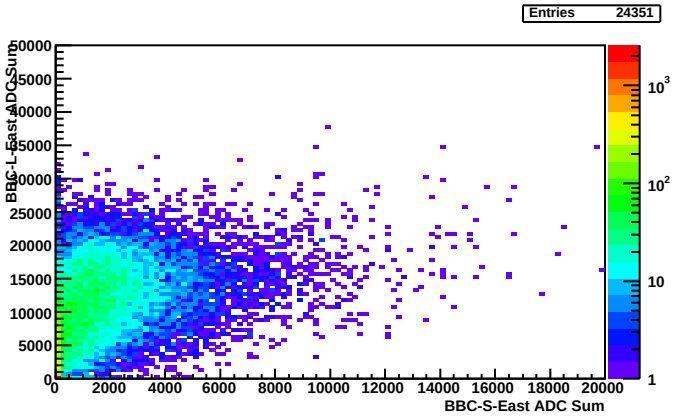

Entries 24351
TOF Mult
BBC-L-West ADC Sum

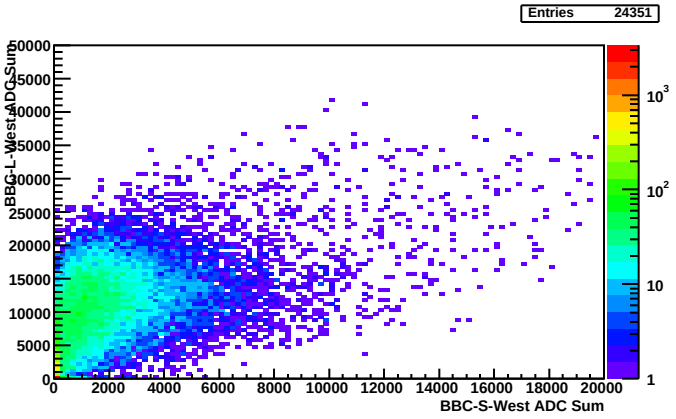

Entries 24351
TOF Mult
BBC-S-East ADC Sum

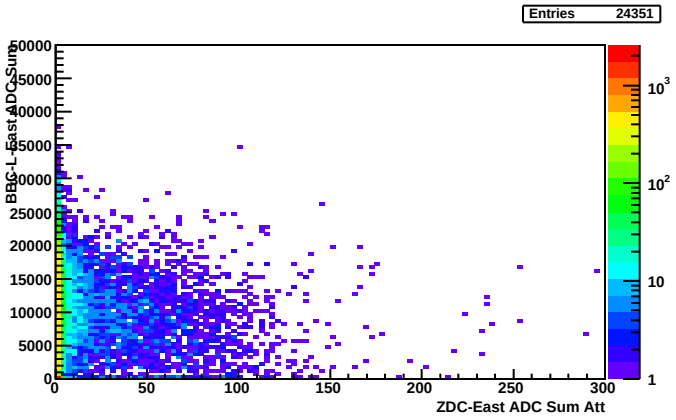

Entries 24351
TOF Mult
BBC-S-West ADC Sum

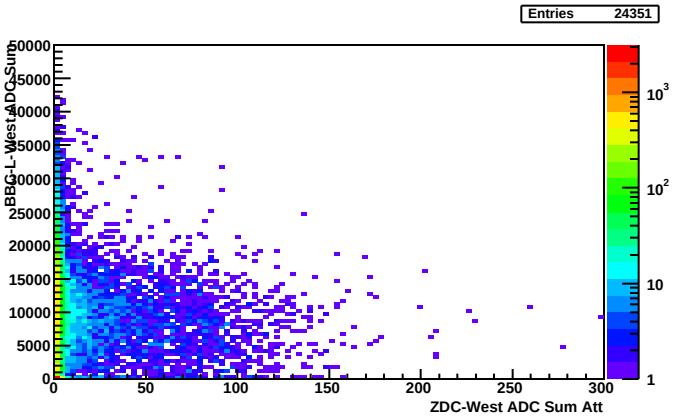

Entries 24351
TOF Mult
ZDC-East ADC Sum Att

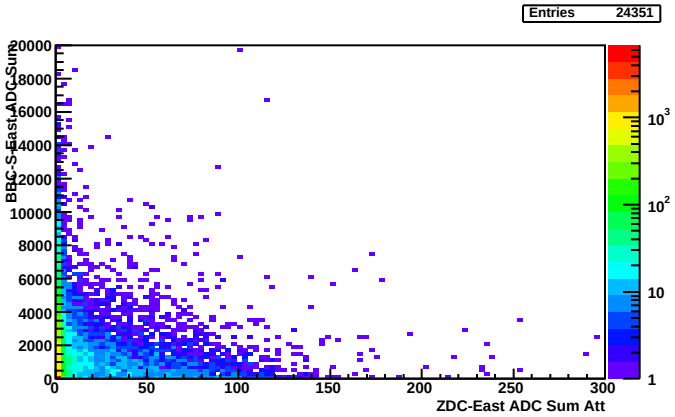

Entries 24351
TOF Mult
ZDC-West ADC Sum Att

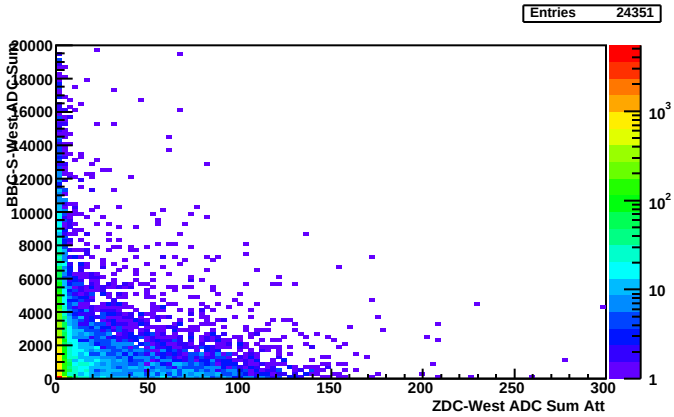

Entries 24351
BBC-L-East ADC Sum
BBC-S-East ADC Sum

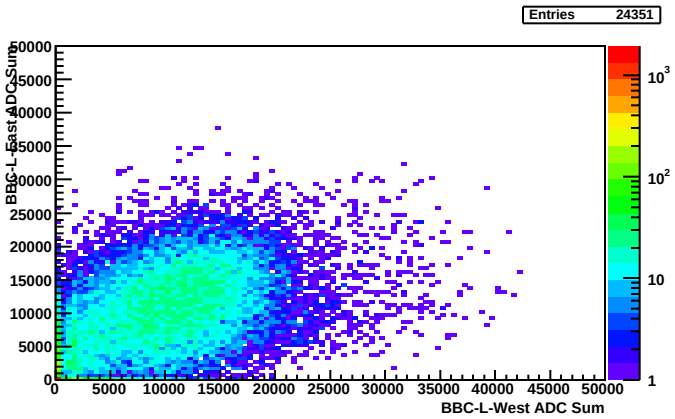

Entries 24351
BBC-L-West ADC Sum
BBC-S-West ADC Sum

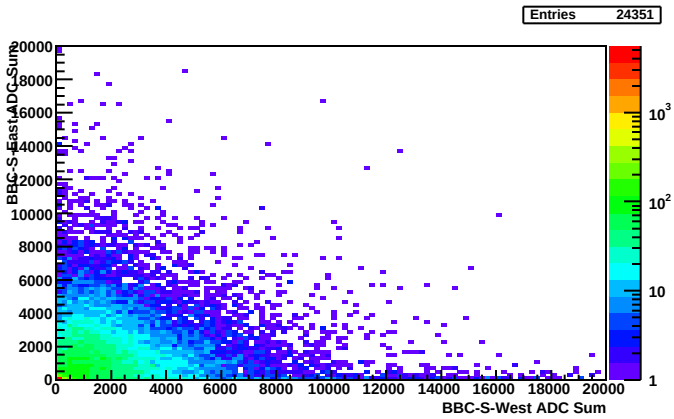

Entries 24351
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

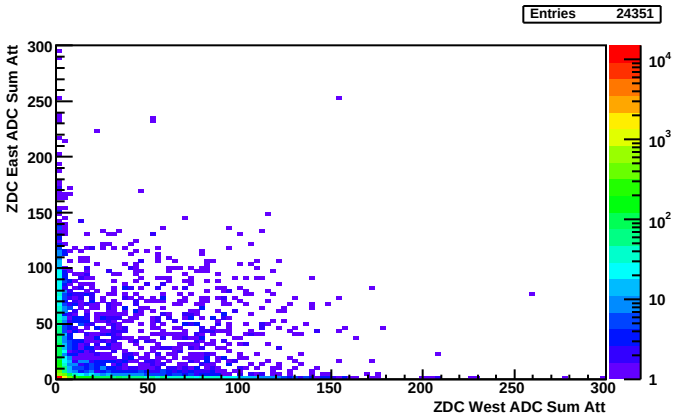

Entries 24351
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

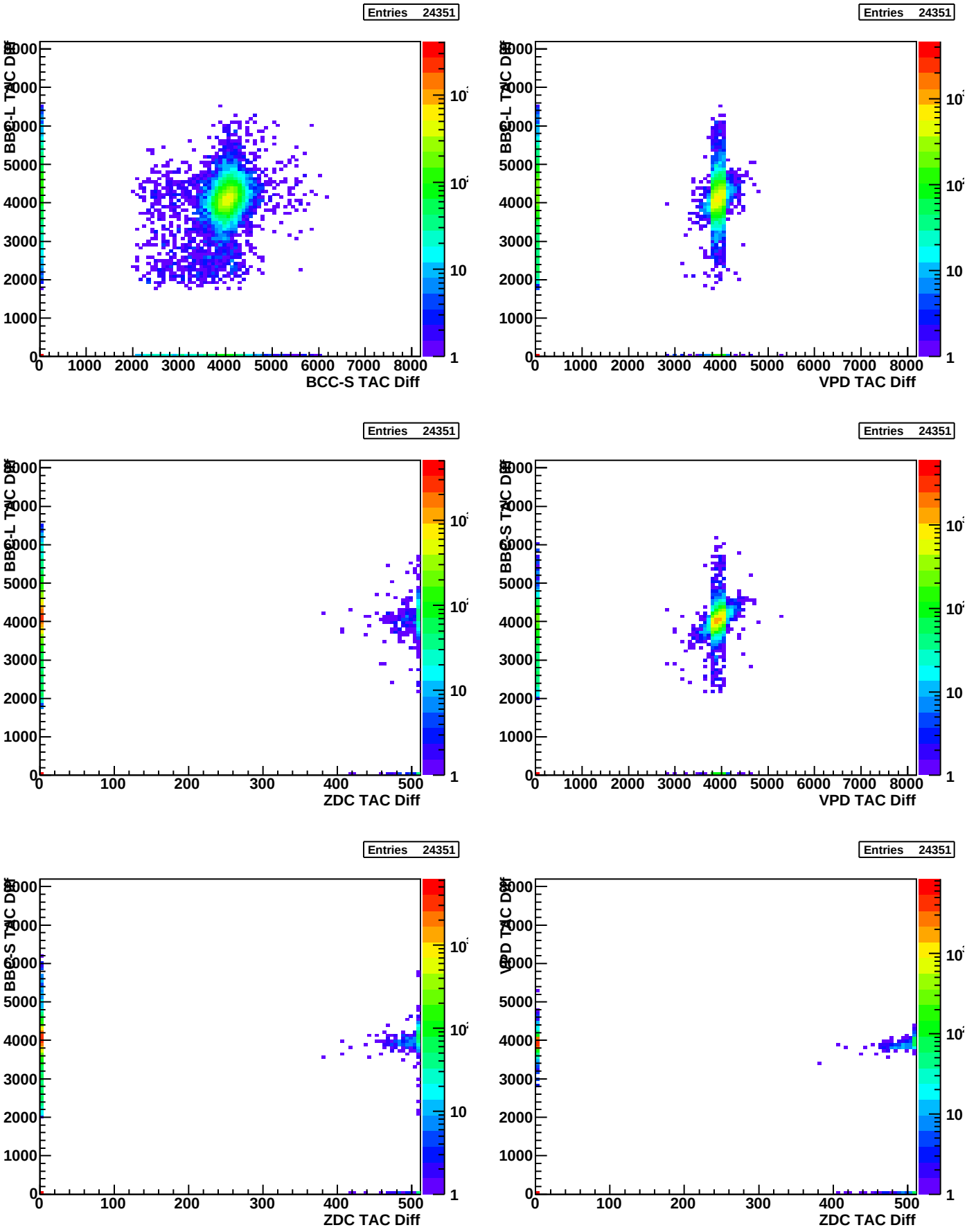

Entries 24351
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

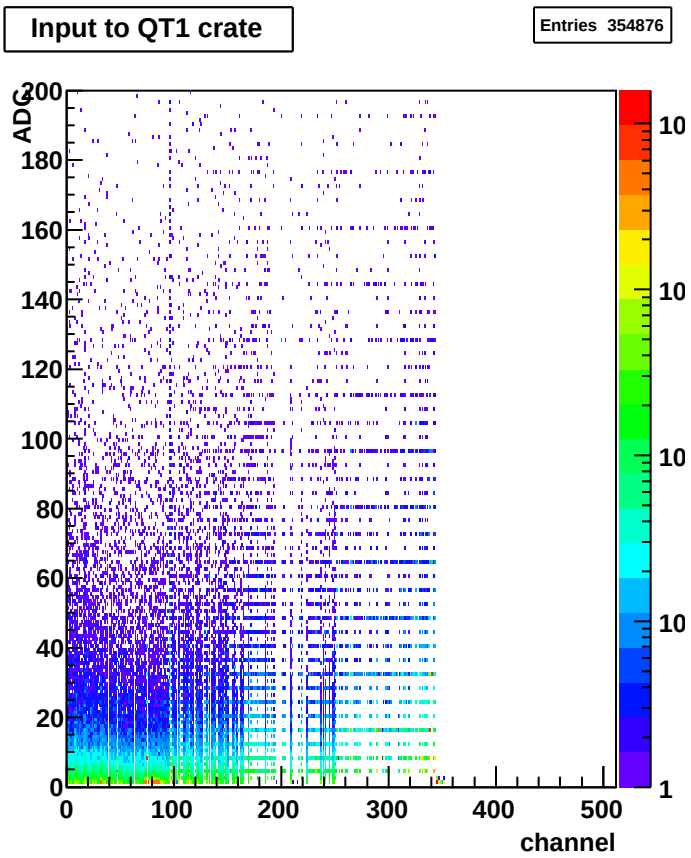

Entries 24351
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

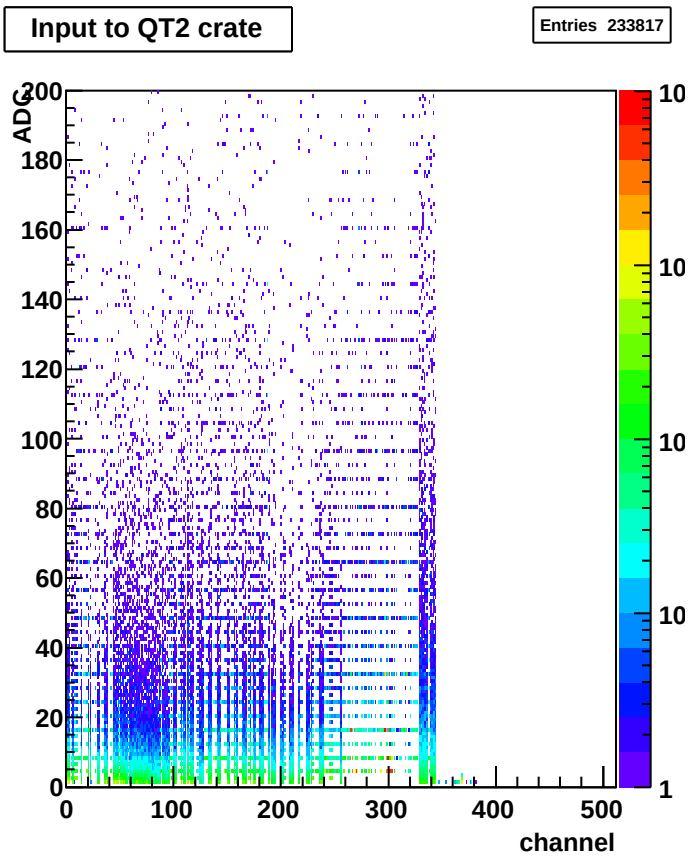

Entries 24351
BBC-L-East ADC Sum
BBC-L-West ADC Sum

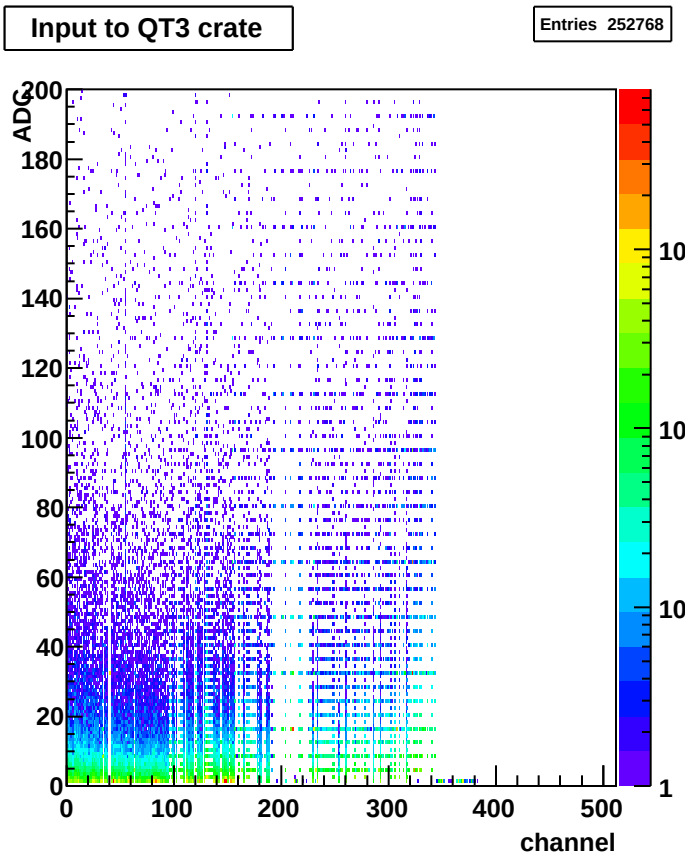

Entries 24351
BBC-S-East ADC Sum
BBC-S-West ADC Sum

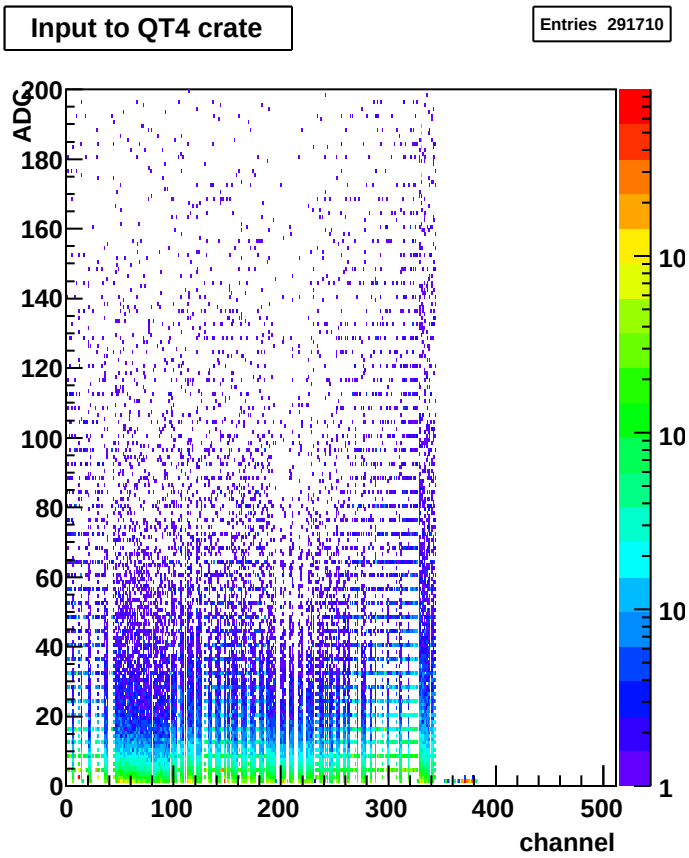

Entries 24351
ZDC East ADC Sum Att
ZDC West ADC Sum Att

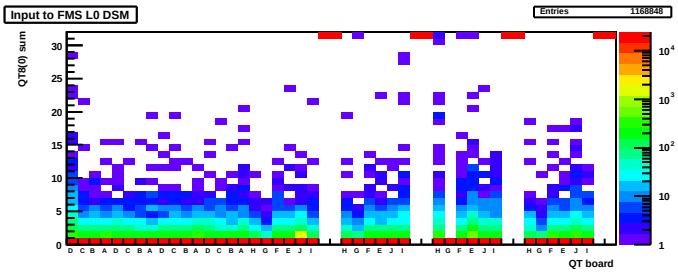

Entries 24351
BBC-L TAC Diff
BCC-S TAC Diff
Entries 24351
BBC-L TAC Diff
VPD TAC Diff
Entries 24351
BBC-L TAC Diff
ZDC TAC Diff
Entries 24351
BBC-S TAC Diff
VPD TAC Diff
Entries 24351
BBC-S TAC Diff
ZDC TAC Diff
Entries 24351
VPD TAC Diff
ZDC TAC Diff

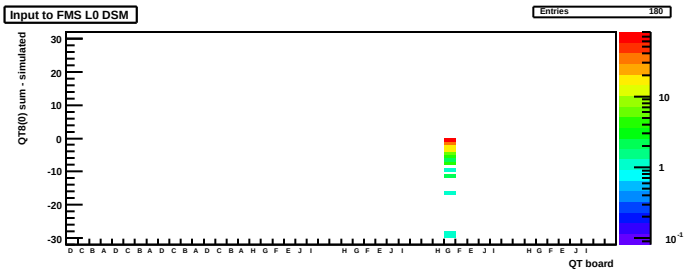

Input to QT1 crate
Entries 354876
ADC
channel

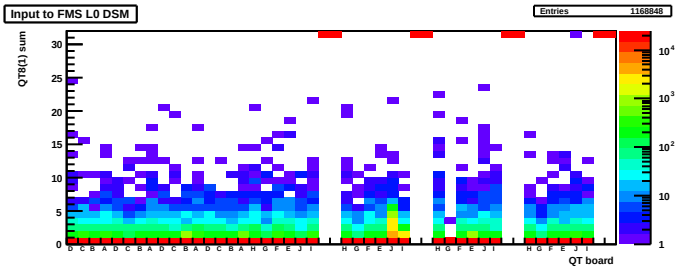

Input to QT2 crate
Entries 233817
ADC
channel

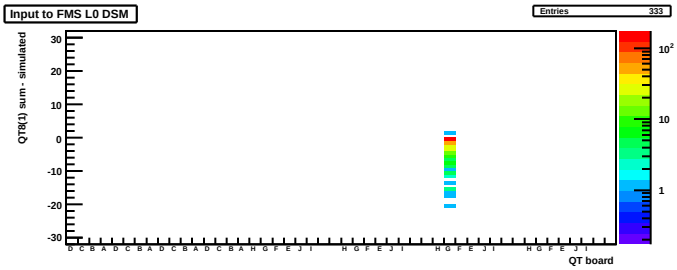

Input to QT3 crate
Entries 252768
ADC
channel

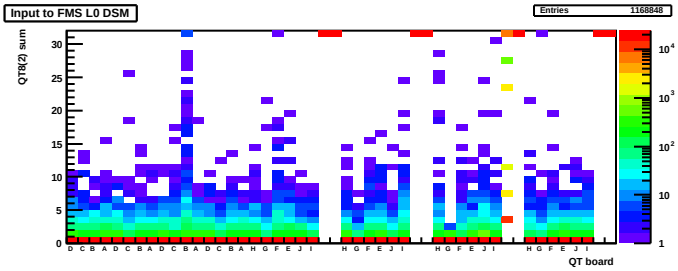

Input to QT4 crate
Entries 291710
ADC
channel

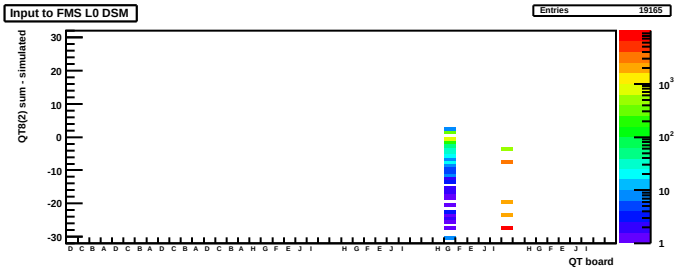

Input to FMS L0 DSM
Entries 1168848
QT8(0) sum
QT board

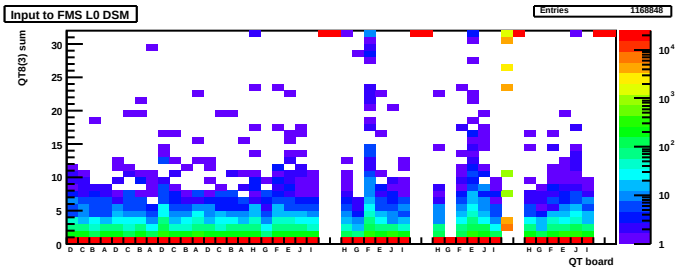

Input to FMS L0 DSM
Entries 180
QT8(0) sum - simulated
QT board

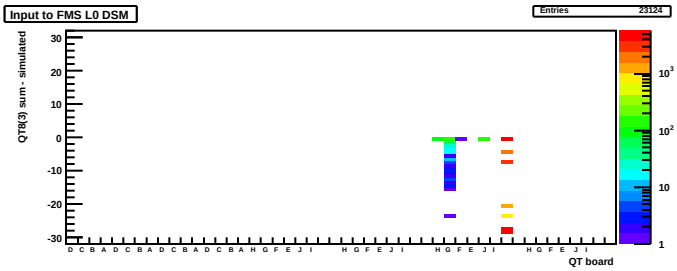

Input to FMS L0 DSM
Entries 1168848
QT8(1) sum
QT board

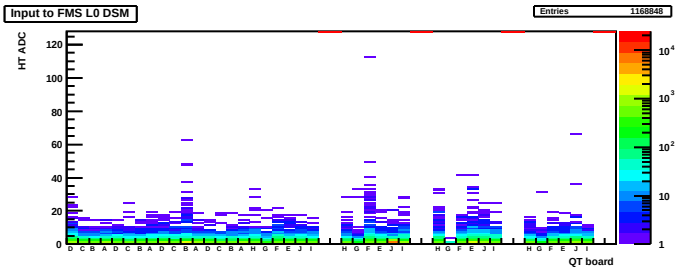

Input to FMS L0 DSM
Entries 333
QT8(1) sum - simulated
QT board

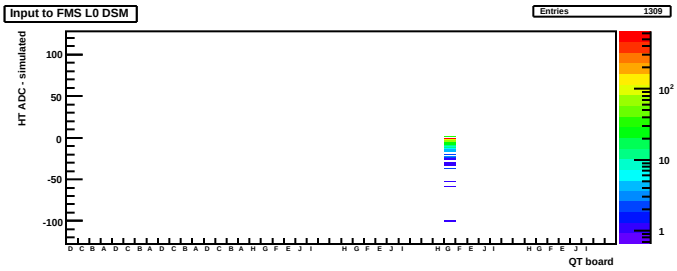

Input to FMS L0 DSM
Entries 1168848
QT8(2) sum
QT board

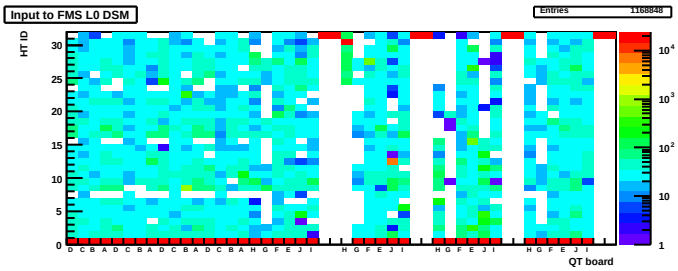

Input to FMS L0 DSM
Entries 19165
QT8(2) sum - simulated
QT board

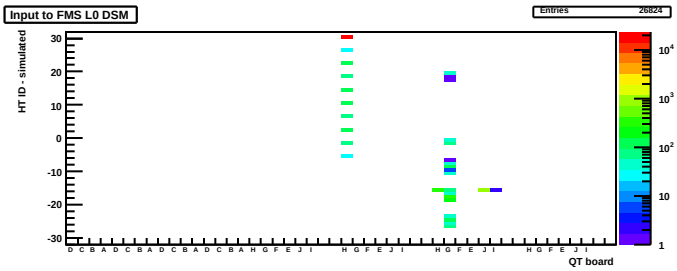

Input to FMS L0 DSM
Entries 1168848
QT8(3) sum
QT board

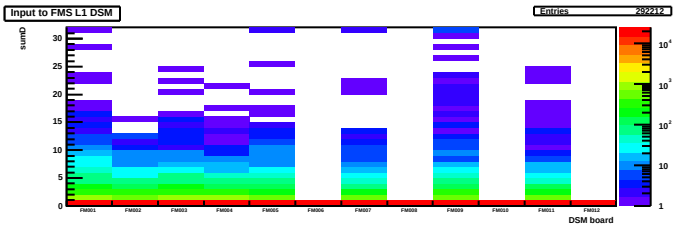

Input to FMS L0 DSM
Entries 23124
QT8(3) sum - simulated
QT board

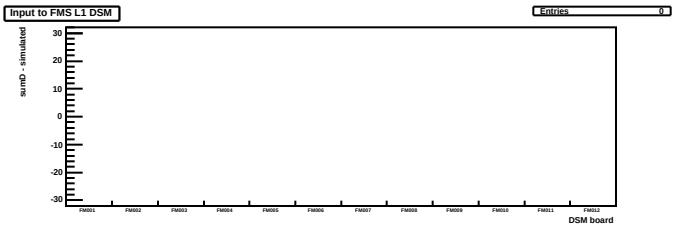

Input to FMS L0 DSM
Entries 1168848
HT ADC
QT board

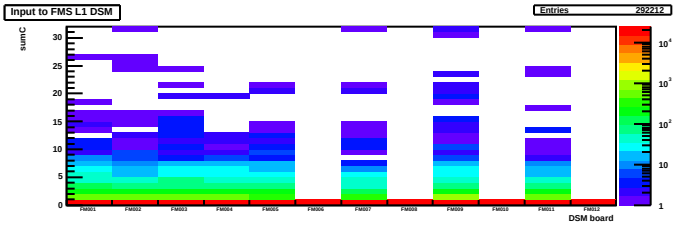

Input to FMS L0 DSM
Entries 1309
HT ADC - simulated
QT board

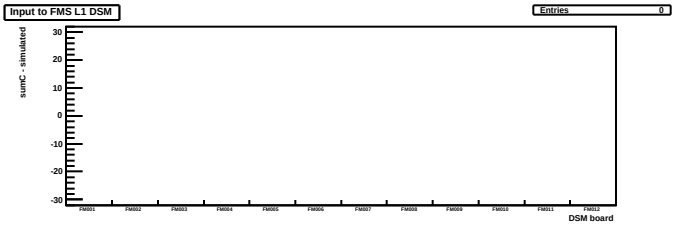

Input to FMS L0 DSM
Entries 1168848
HT ID
QT board

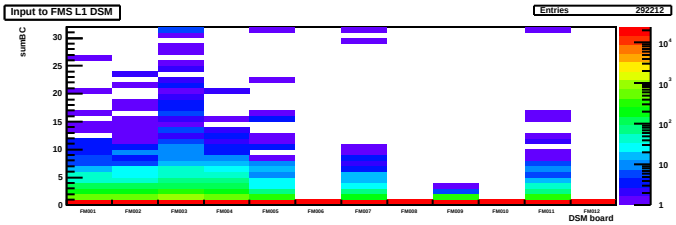

Input to FMS L0 DSM
Entries 26824
HT ID - simulated
QT board

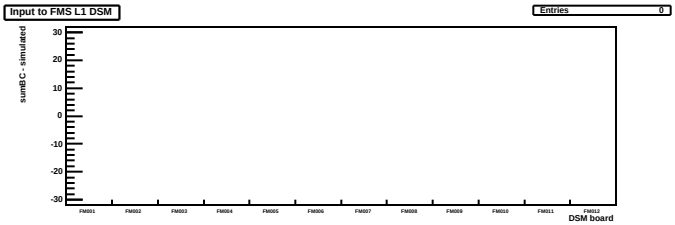

Input to FMS L1 DSM
Entries 292212
sumD
DSM board

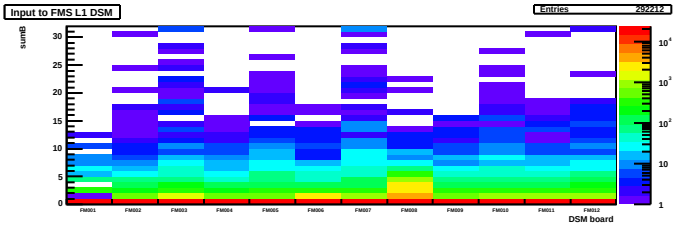

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

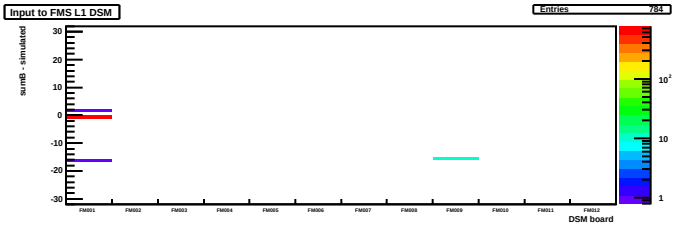

Input to FMS L1 DSM
Entries 292212
sumC
DSM board

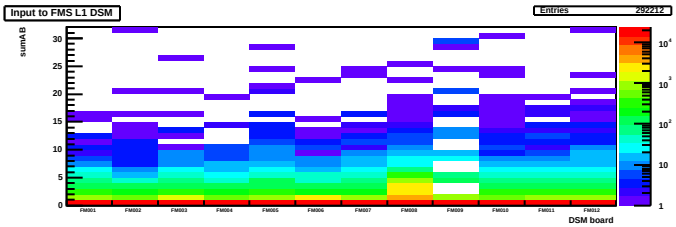

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

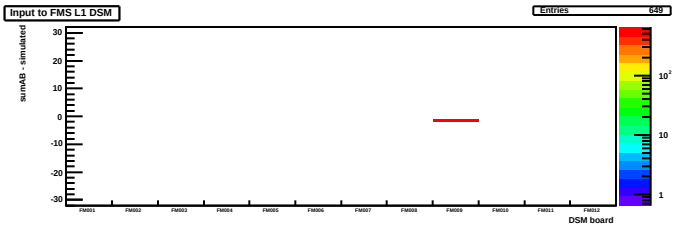

Input to FMS L1 DSM
Entries 292212
sumBC
DSM board

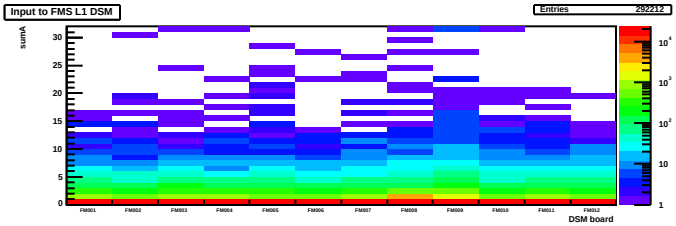

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

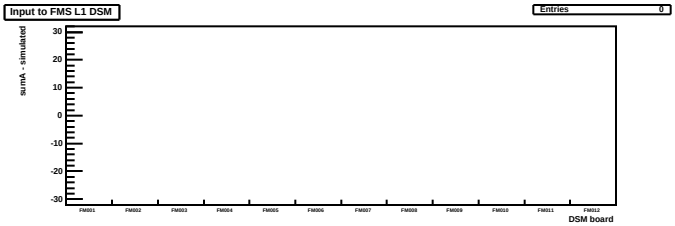

Input to FMS L1 DSM
Entries 292212
sumB
DSM board

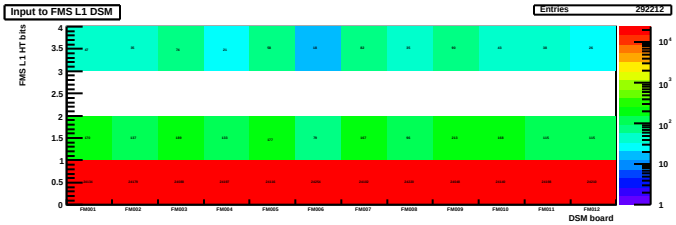

Input to FMS L1 DSM
Entries 784
sumB - simulated
DSM board

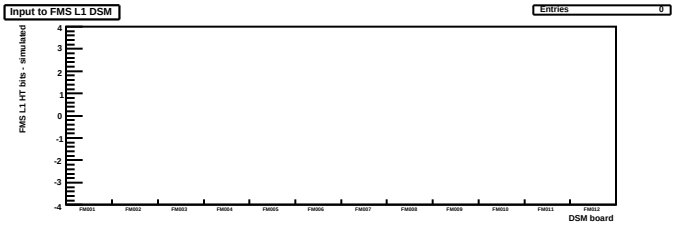

Input to FMS L1 DSM
Entries 292212
sumAB
DSM board

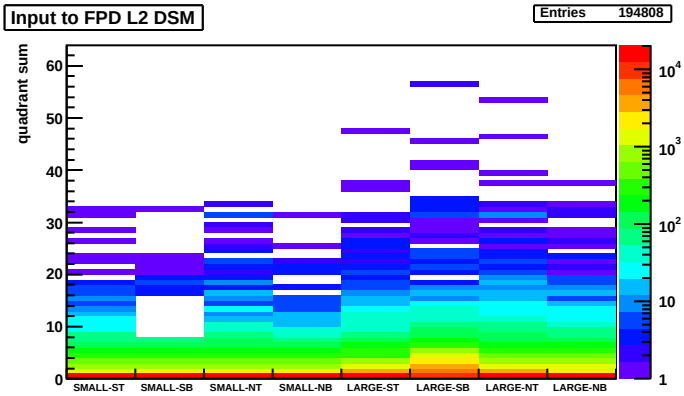

Input to FMS L1 DSM
Entries 649
sumAB - simulated
DSM board

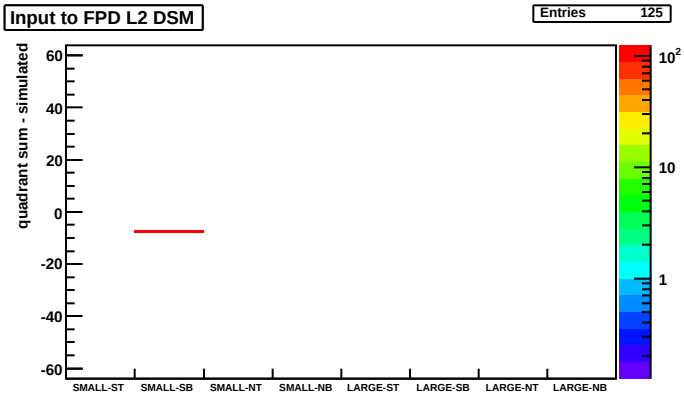

Input to FMS L1 DSM
Entries 292212
sumA
DSM board

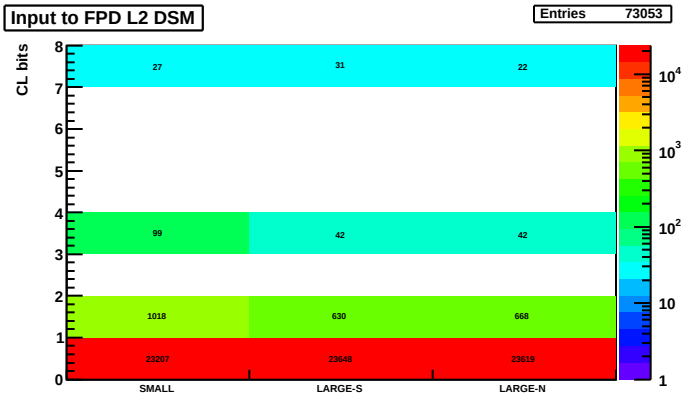

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

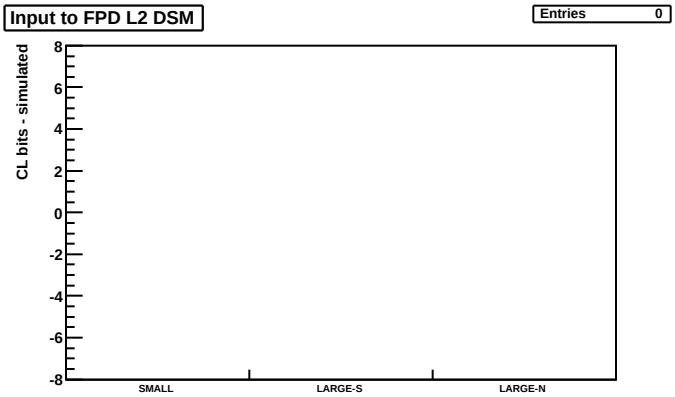

Input to FMS L1 DSM
Entries 292212
FMS L1 HT bits
DSM board

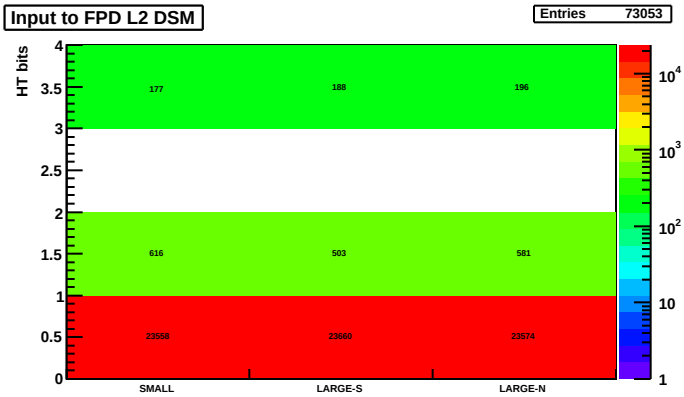

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

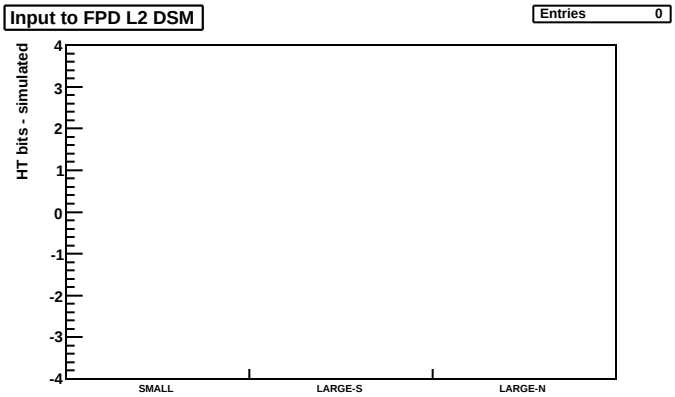

Input to FPD L2 DSM
Entries 194808
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

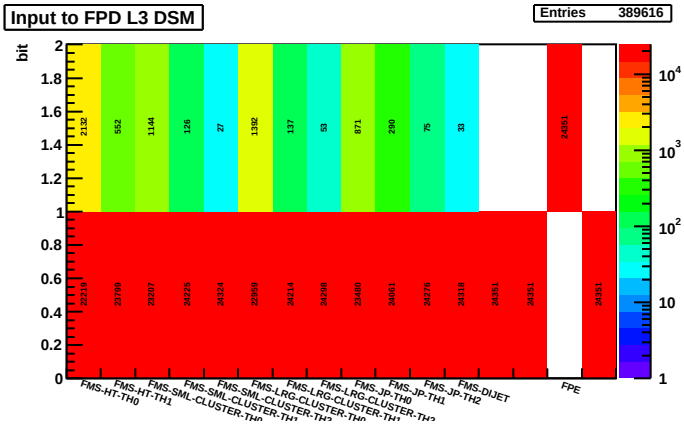

Input to FPD L2 DSM
Entries 125
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

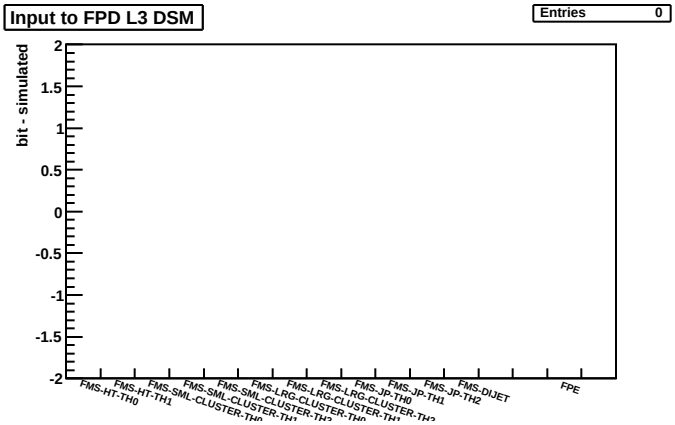

Input to FPD L2 DSM
Entries 73053
CL bits
27
31
22
99
42
42
1018
630
668
23207
23648
23619
SMALL
LARGE-S
LARGE-N

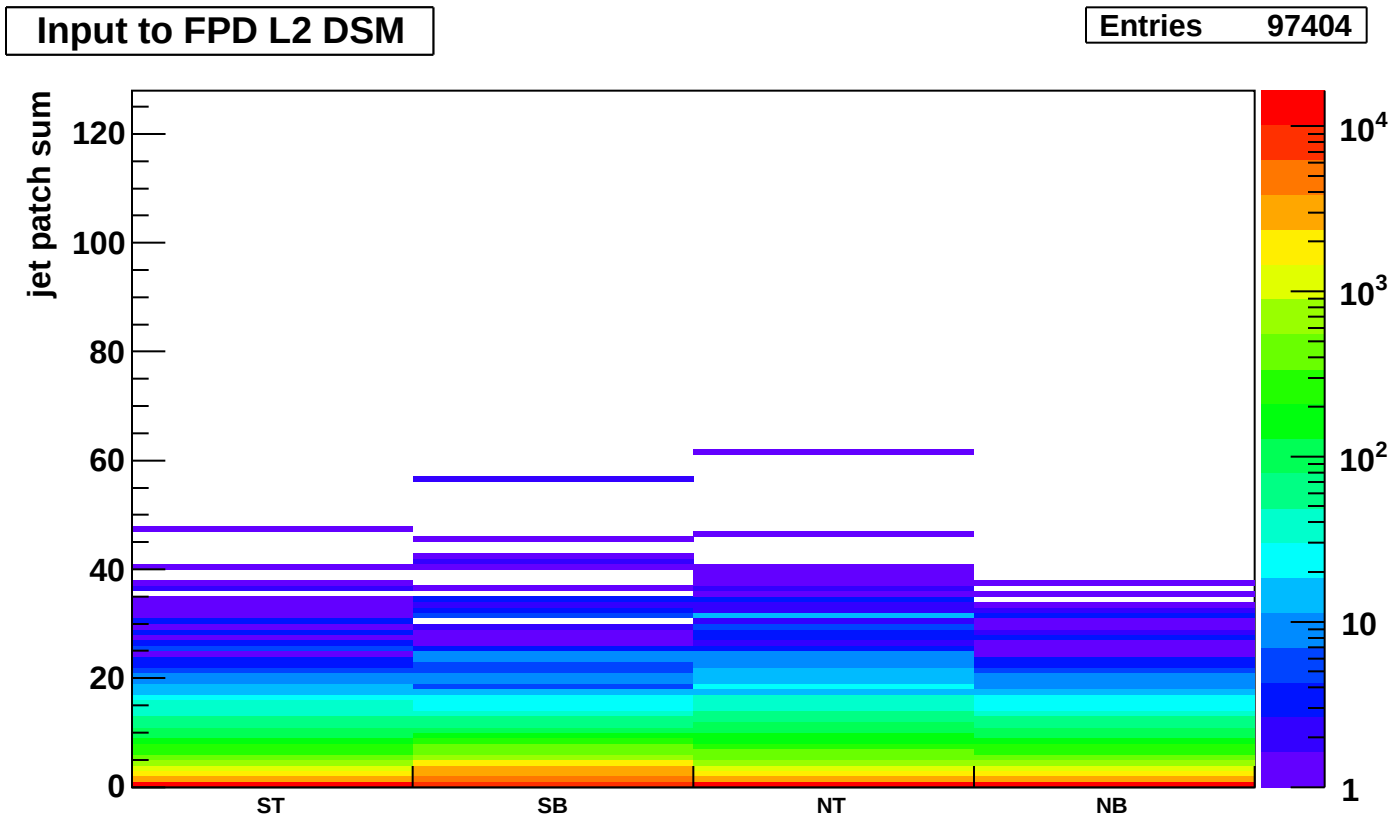

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

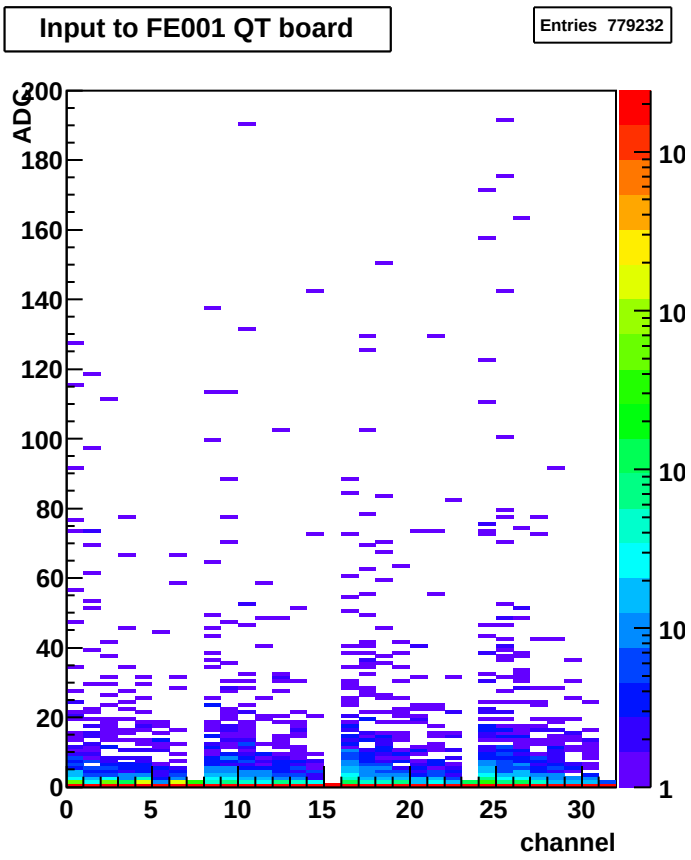

Input to FPD L2 DSM
Entries 73053
HT bits
177
188
196
616
503
581
23558
23660
23574
SMALL
LARGE-S
LARGE-N

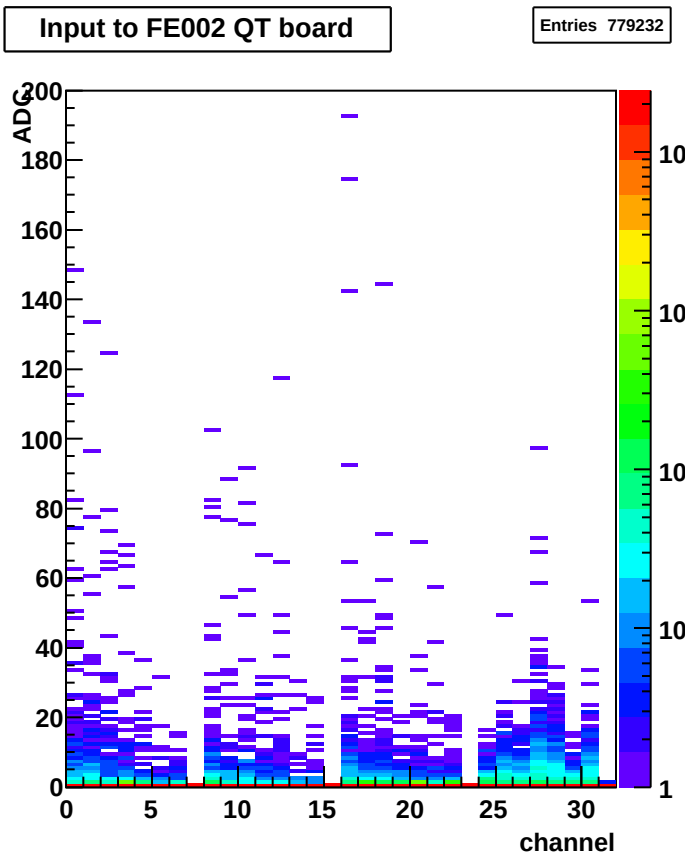

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

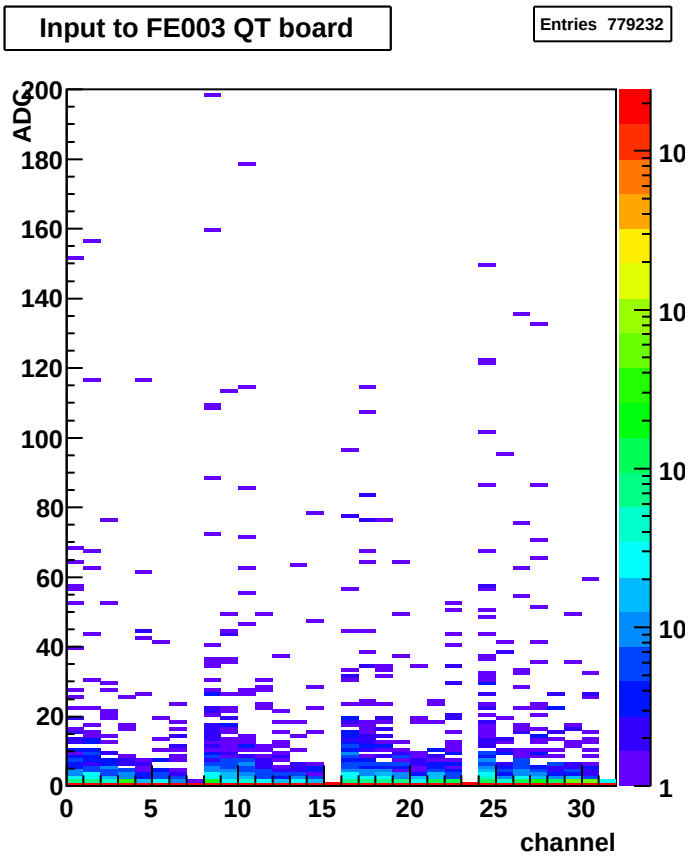

Input to FPD L3 DSM
Entries 389616
bit
2132
552
1144
126
27
1382
137
53
871
290
75
33
24351
22719
22799
22207
24225
24324
22959
24214
24298
22480
24061
24276
24318
24351
24351
24351
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

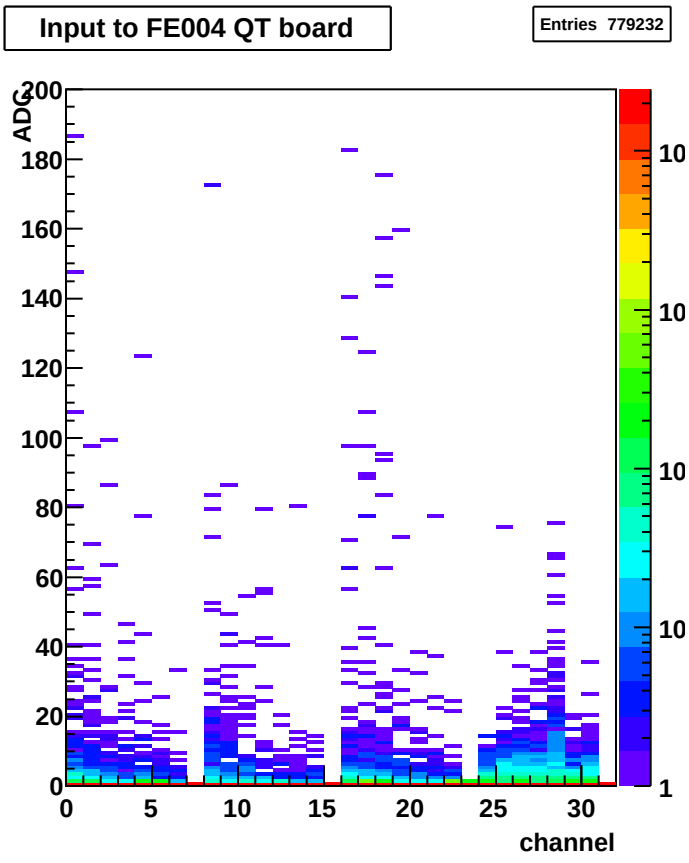

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

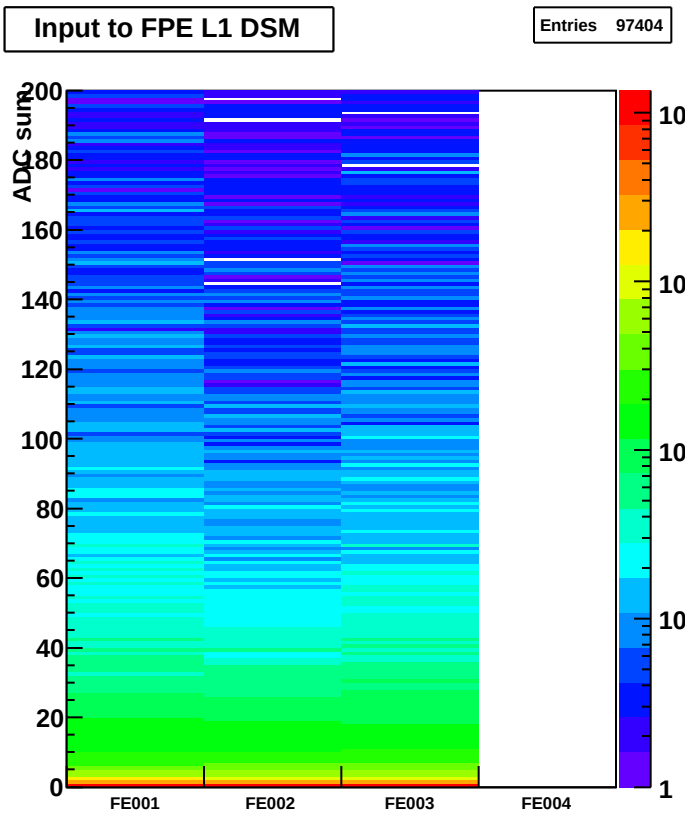

Input to FPD L2 DSM
Entries 97404
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

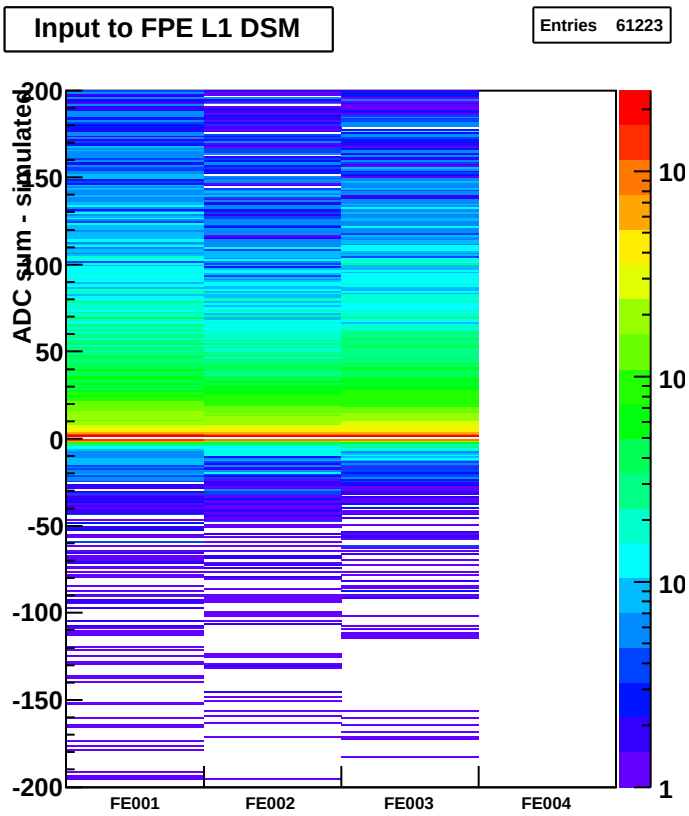

Input to FE001 QT board
Entries 779232
ADC
channel

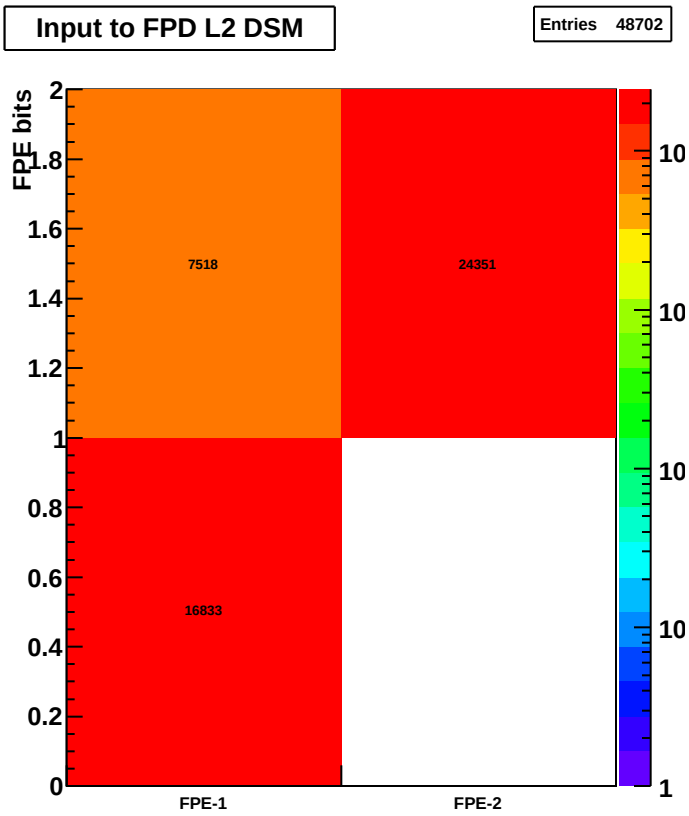

Input to FE002 QT board
Entries 779232
ADC
channel

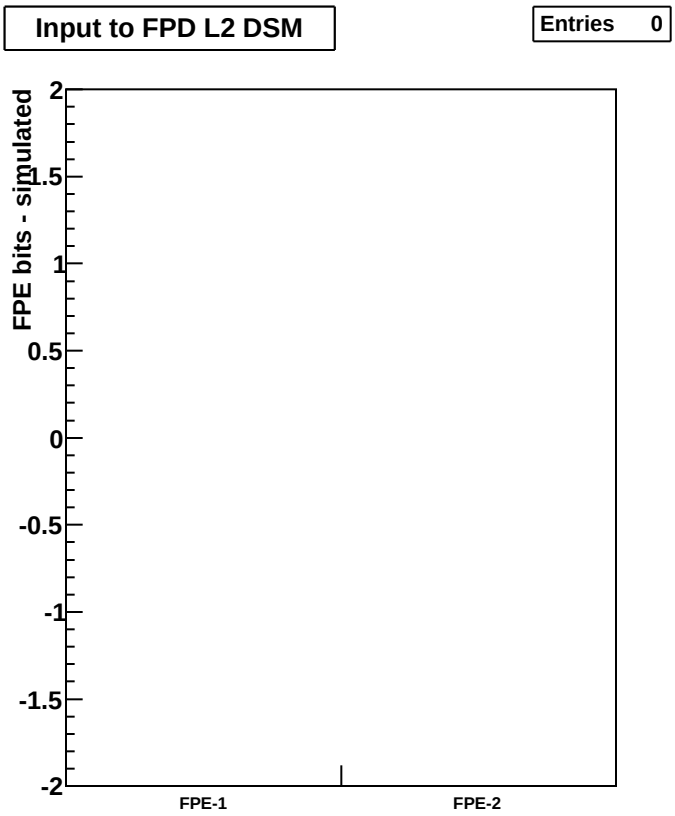

Input to FE003 QT board
Entries 779232
ADC
channel


Input to FE004 QT board
Entries 779232
ADC
channel


Input to FPE L1 DSM
Entries 97404
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 61223
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 48702
FPE bits
7518
24351
16833
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2