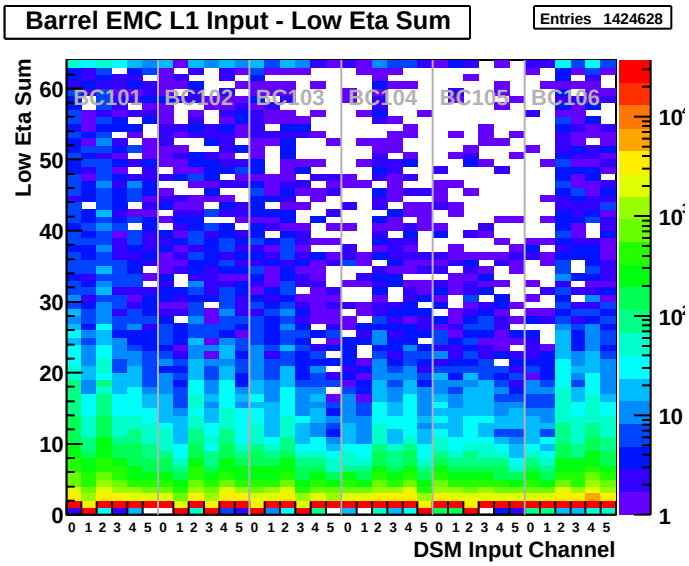
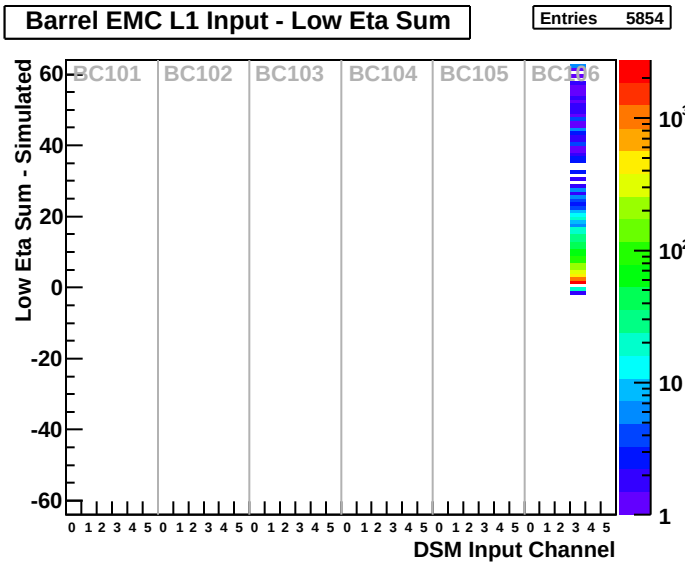
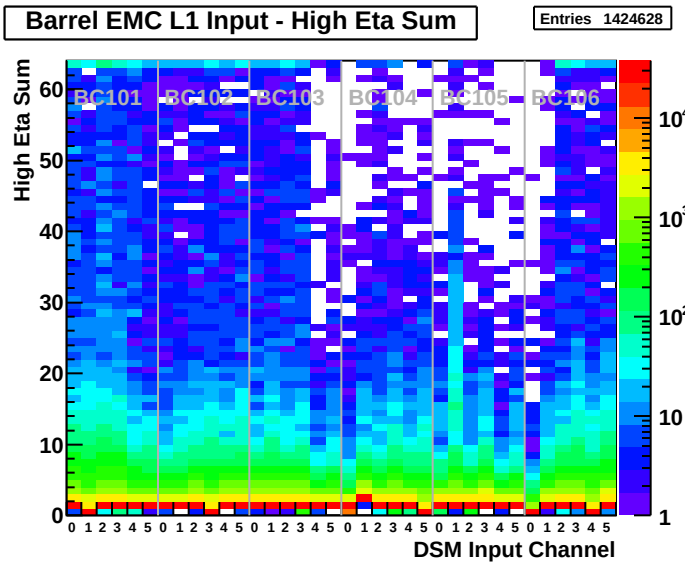
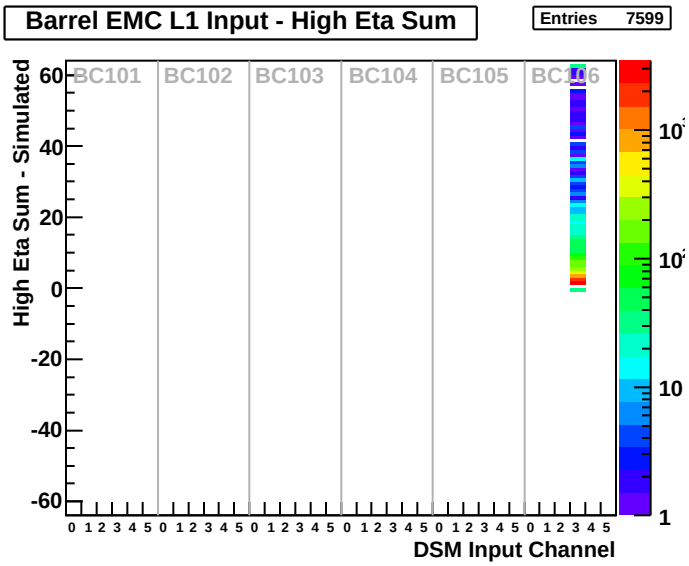
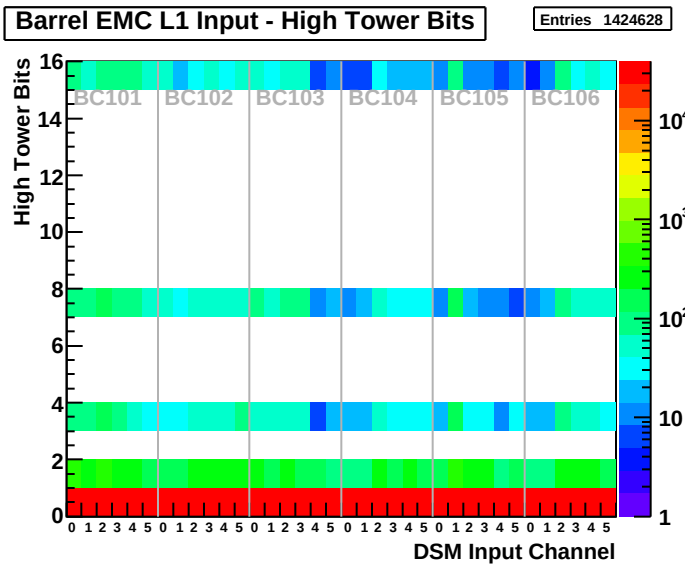
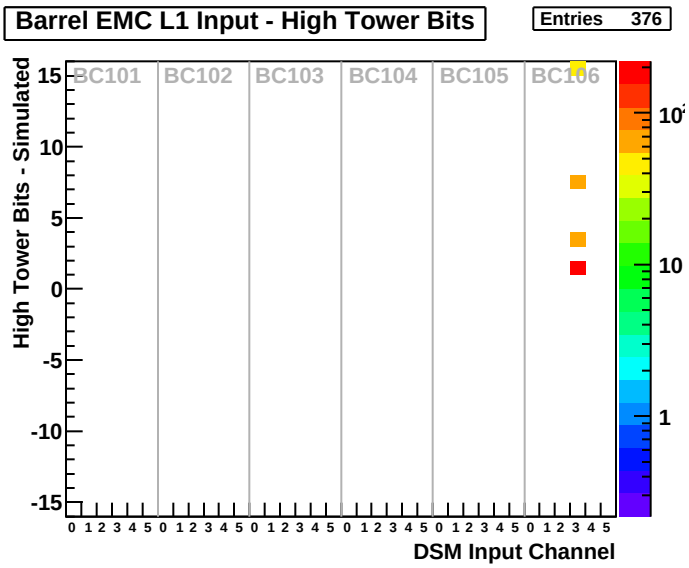

Barrel EMC L1 Input - Low Eta Sum
Entries 1424628
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

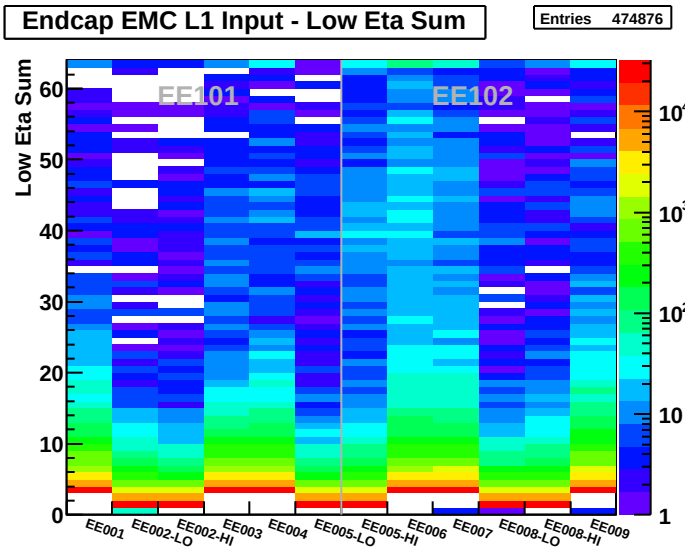

Barrel EMC L1 Input - Low Eta Sum
Entries 5854
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

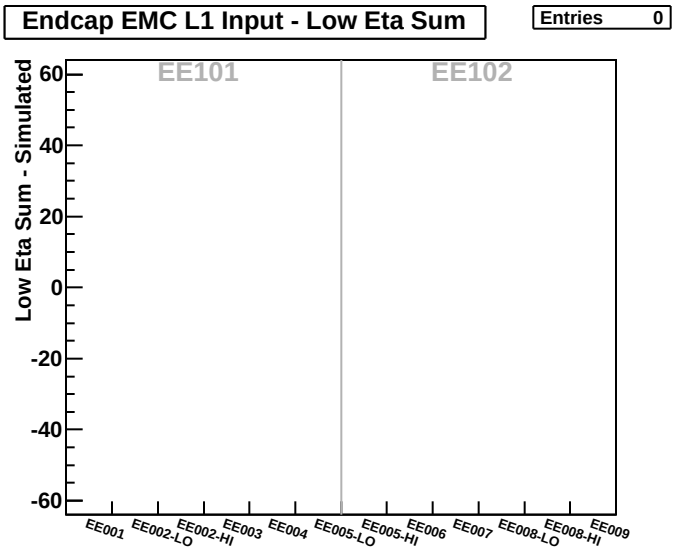

Barrel EMC L1 Input - High Eta Sum
Entries 1424628
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

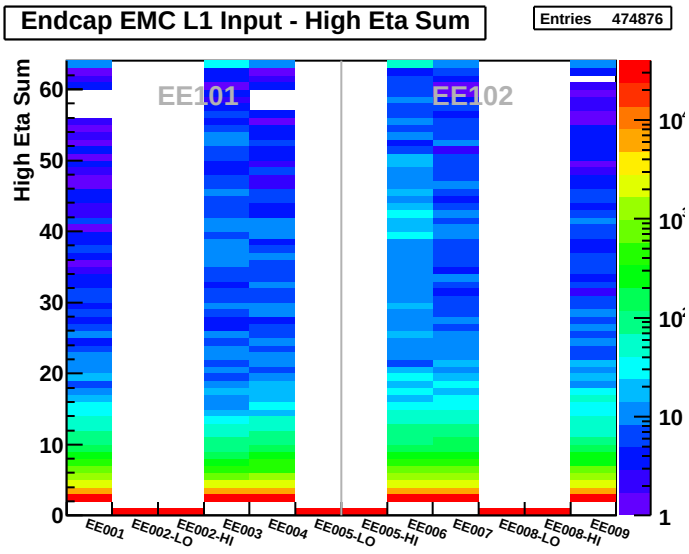

Barrel EMC L1 Input - High Eta Sum
Entries 7599
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

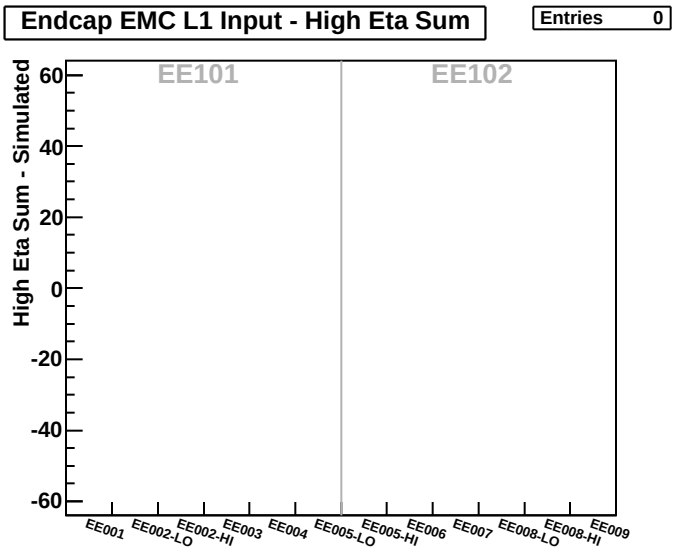

Barrel EMC L1 Input - High Tower Bits
Entries 1424628
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

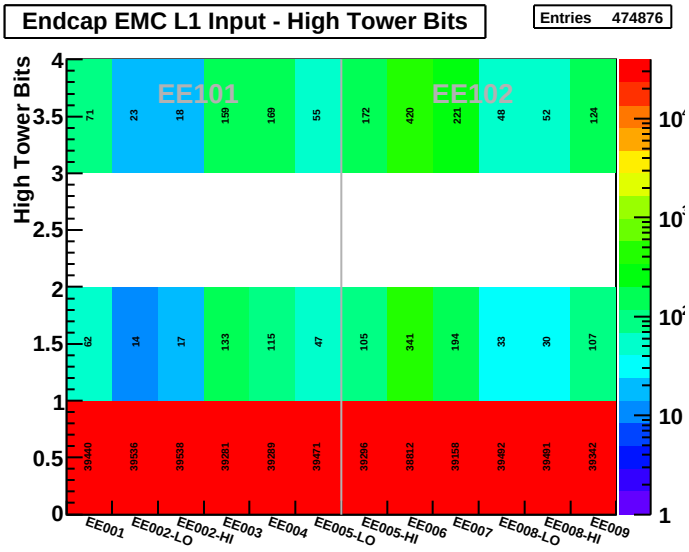

Barrel EMC L1 Input - High Tower Bits
Entries 376
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

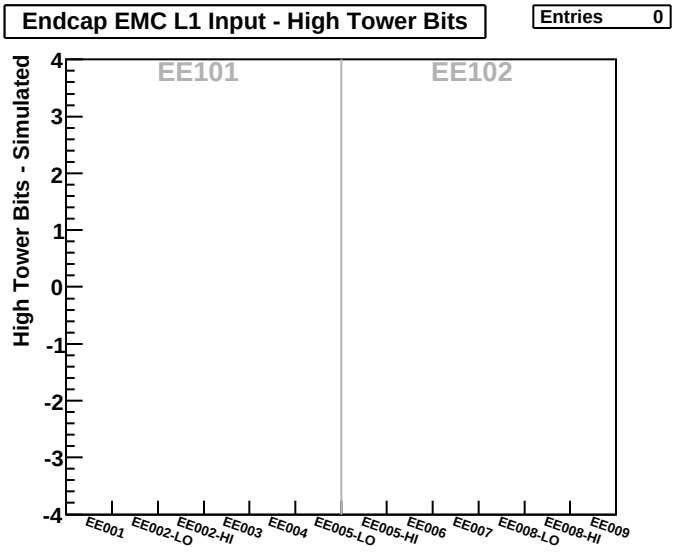

Endcap EMC L1 Input - Low Eta Sum
Entries 474876
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

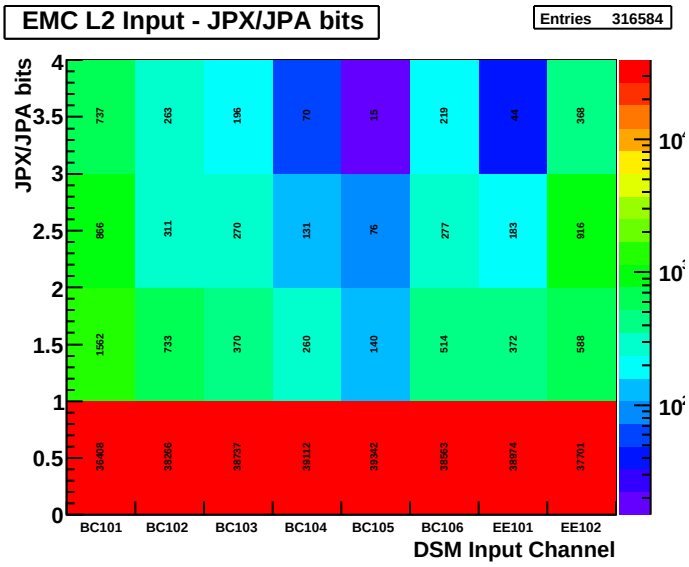

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

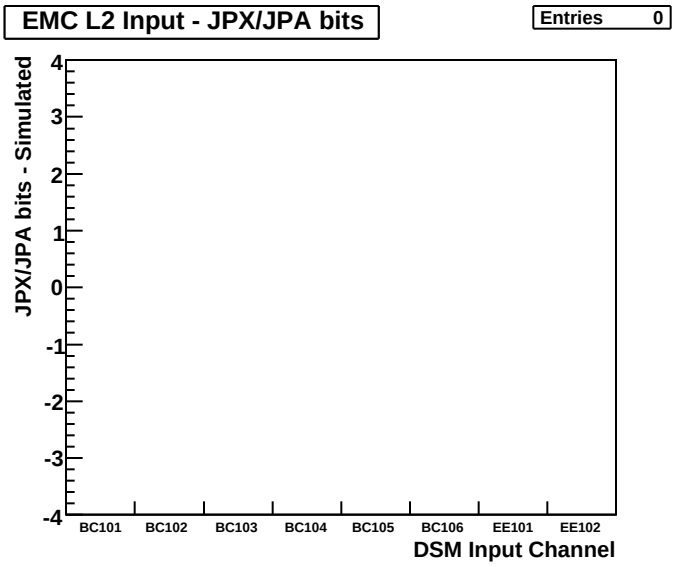

Endcap EMC L1 Input - High Eta Sum
Entries 474876
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

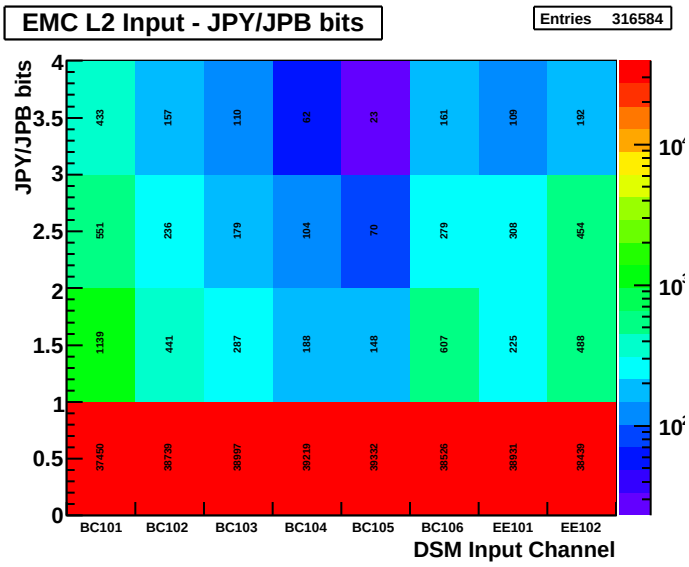

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

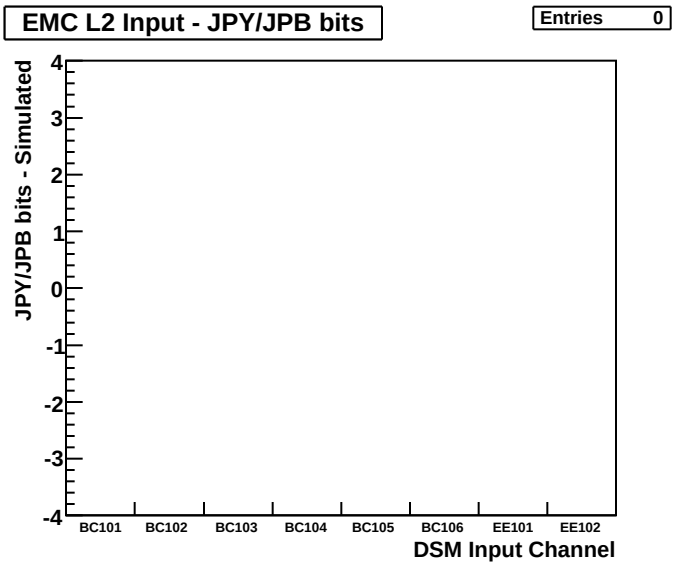

Endcap EMC L1 Input - High Tower Bits
Entries 474876
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

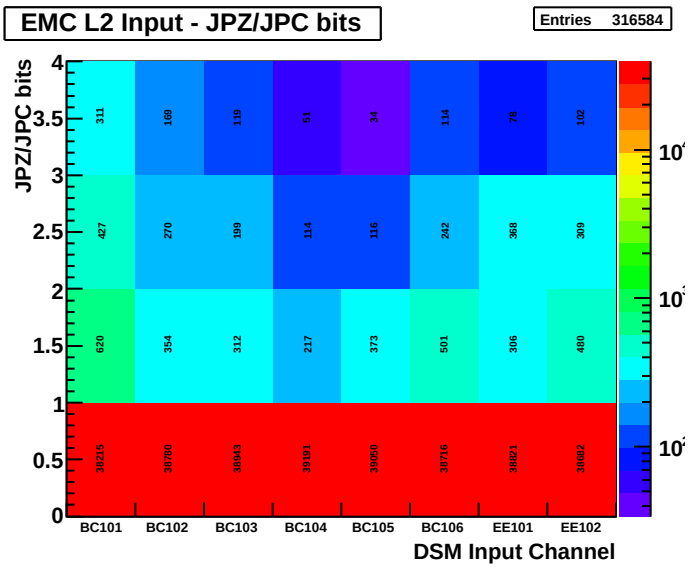

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

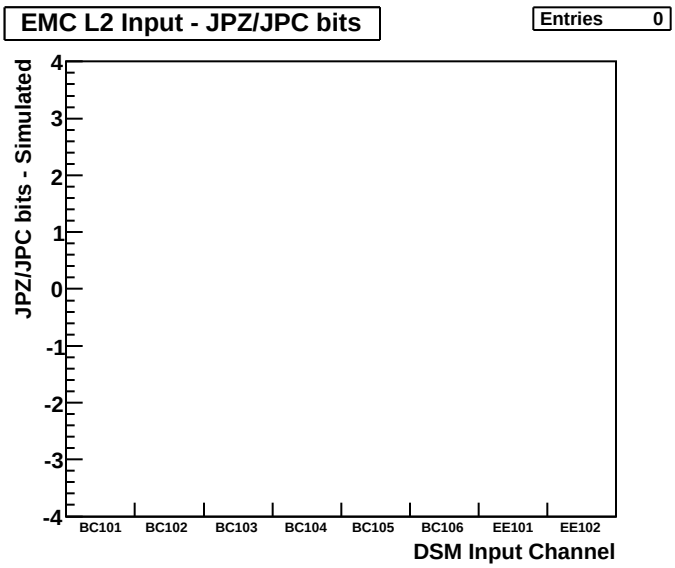

EMC L2 Input - JPX/JPA bits
Entries 316584
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
737 263 196 70 15 219 44 368
866 311 270 131 76 277 183 916
1562 733 370 260 140 514 372 588
36408 38266 38737 39112 39342 38563 38974 37701

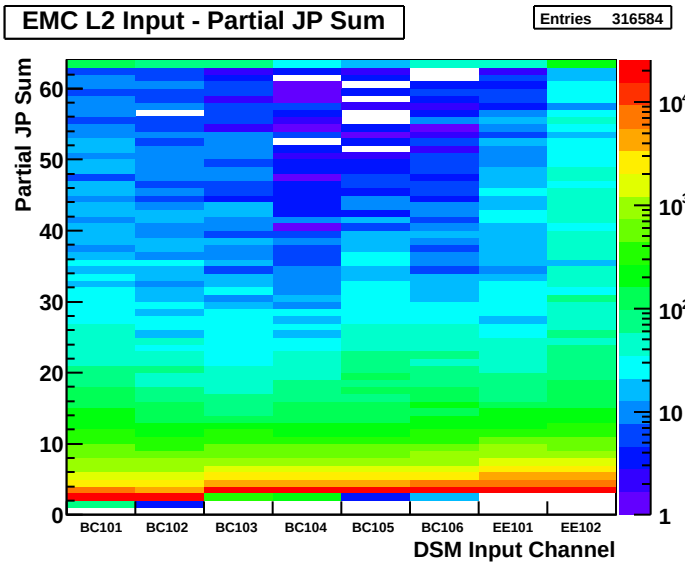

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

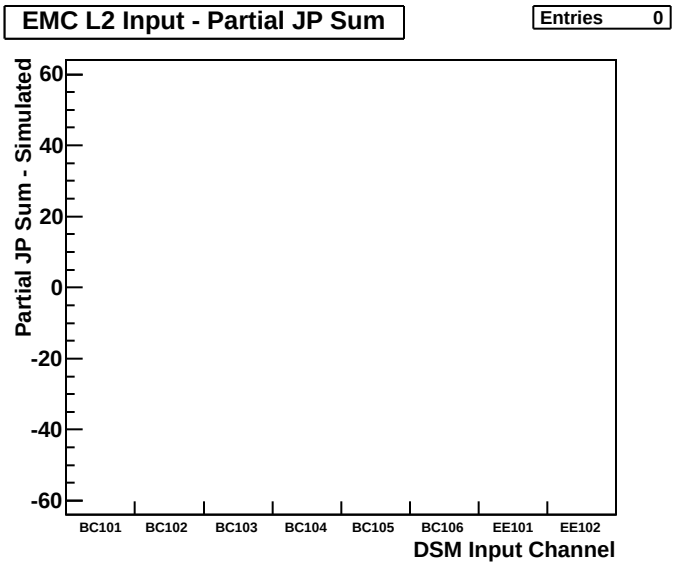

EMC L2 Input - JPY/JPB bits
Entries 316584
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
433 157 110 62 23 161 109 192
551 226 179 104 70 279 308 454
1139 441 287 188 148 607 225 488
37450 38739 38997 39219 39332 38526 38931 38439

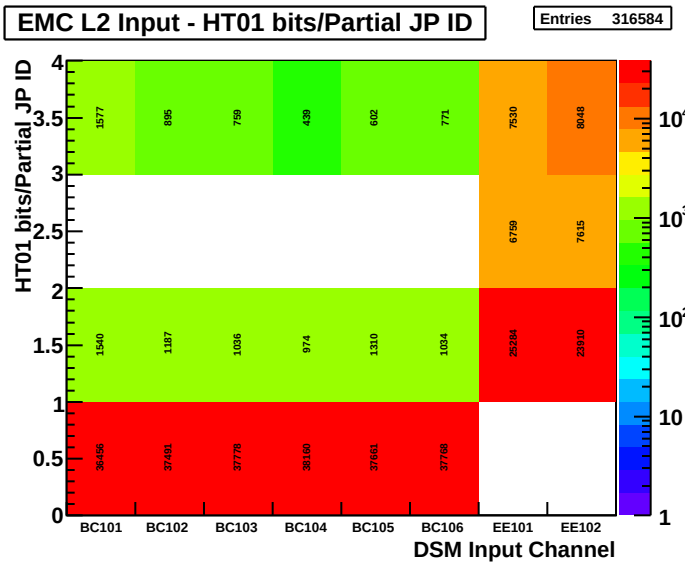

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

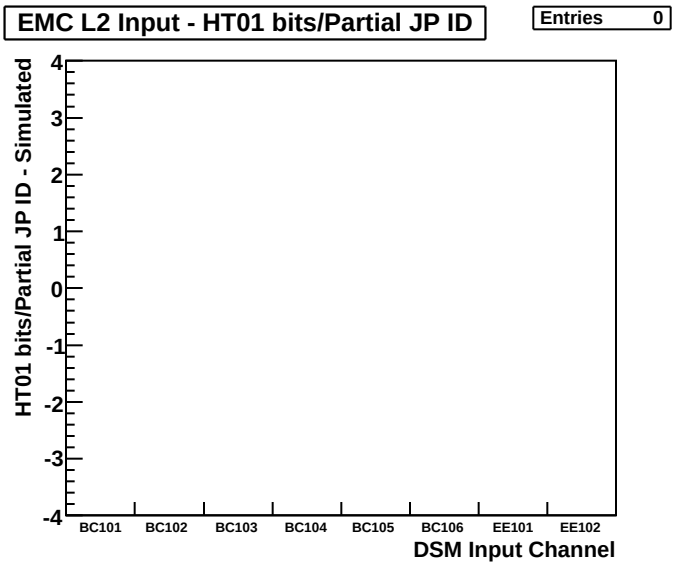

EMC L2 Input - JPZ/JPC bits
Entries 316584
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
311 169 119 51 34 114 78 102
427 270 199 114 116 242 368 309
620 354 312 217 373 501 306 480
38215 38780 38943 39191 39050 38716 38821 38682

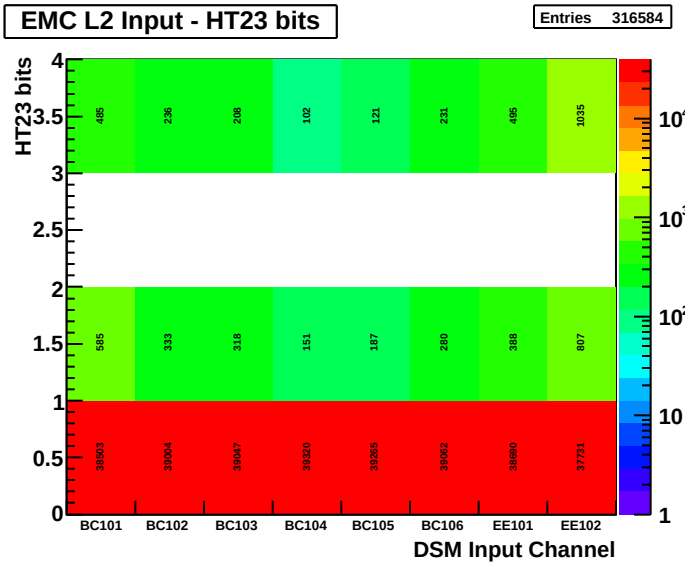

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

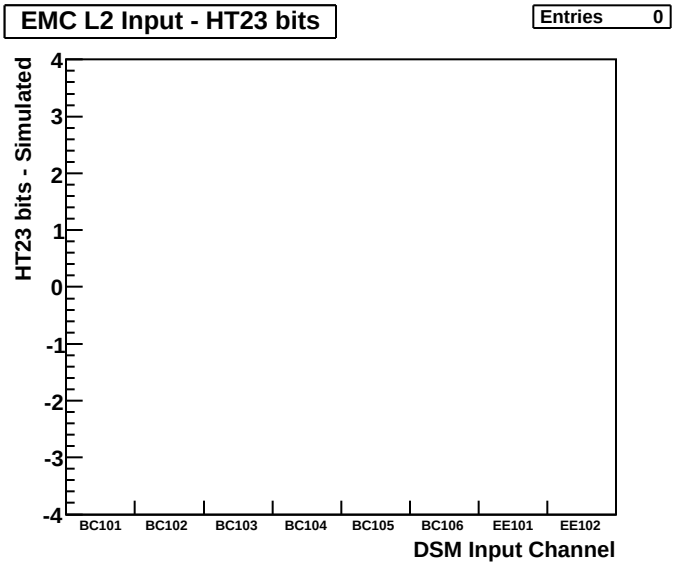

EMC L2 Input - Partial JP Sum
Entries 316584
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

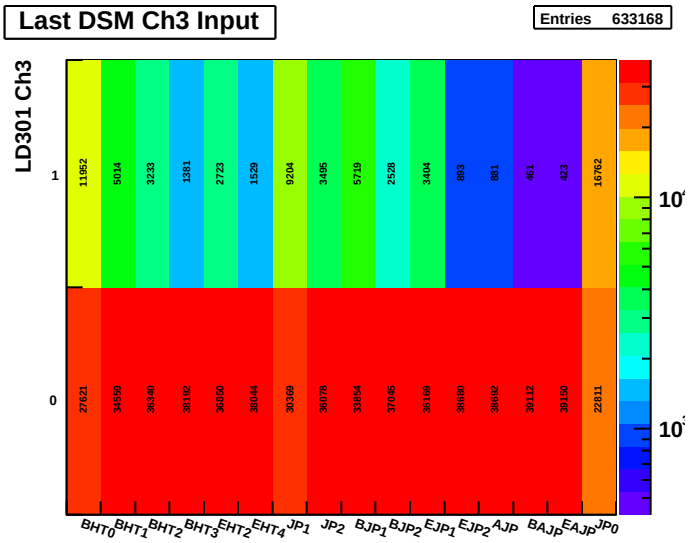

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

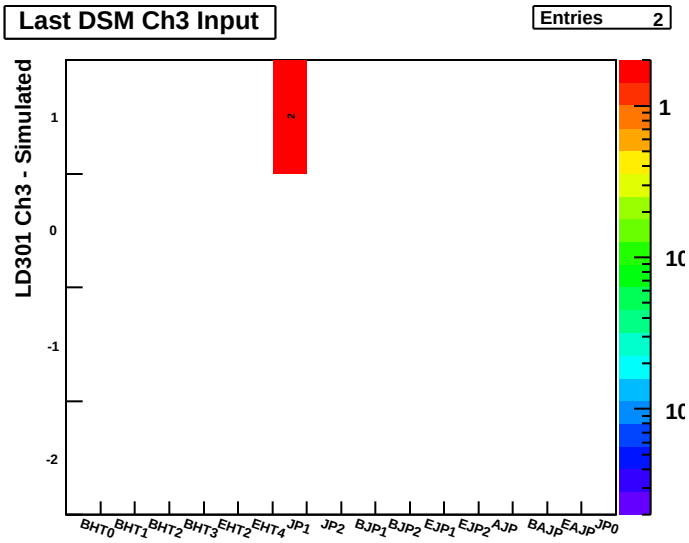

EMC L2 Input - HT01 bits/Partial JP ID
Entries 316584
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

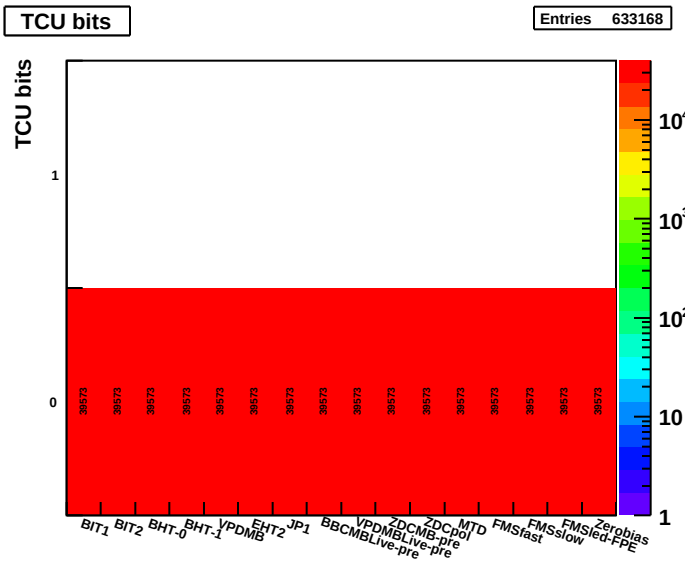

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

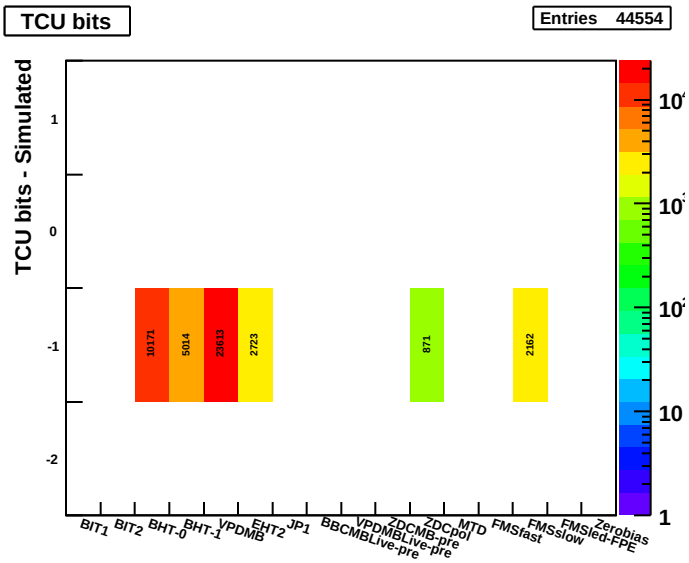

EMC L2 Input - HT23 bits
Entries 316584
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

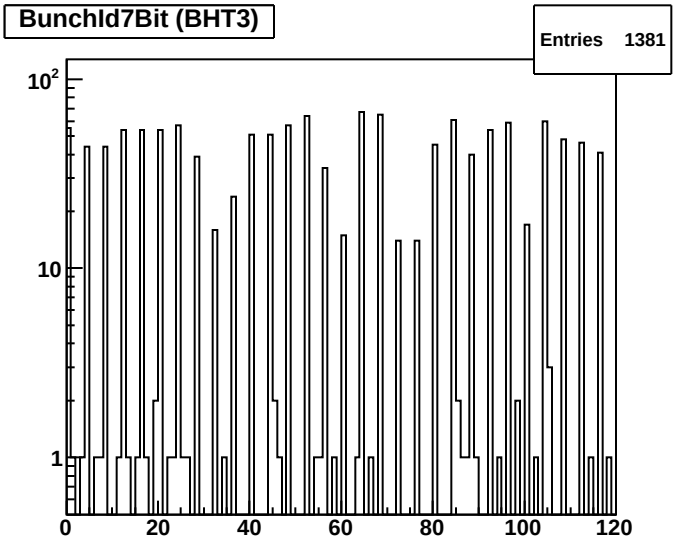

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

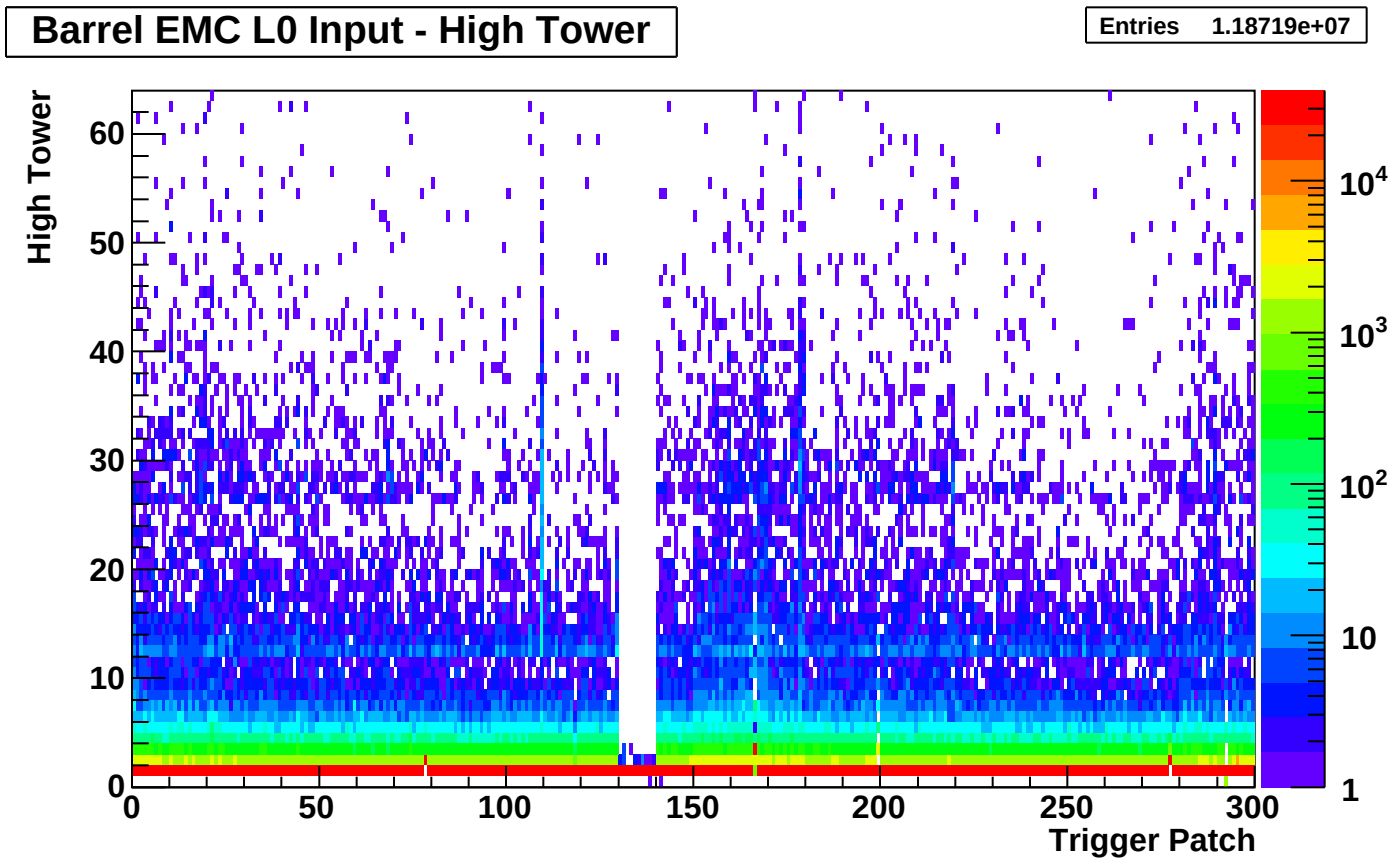

Last DSM Ch3 Input
Entries 633168
LD301 Ch3
1
0
11952 5014 3233 1381 2723 1529 9204 3495 5719 2528 3404 893 881 451 423 16762
27621 34559 35340 35192 35850 36044 30369 36078 33854 37045 36169 38680 38692 39112 39150 22811
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

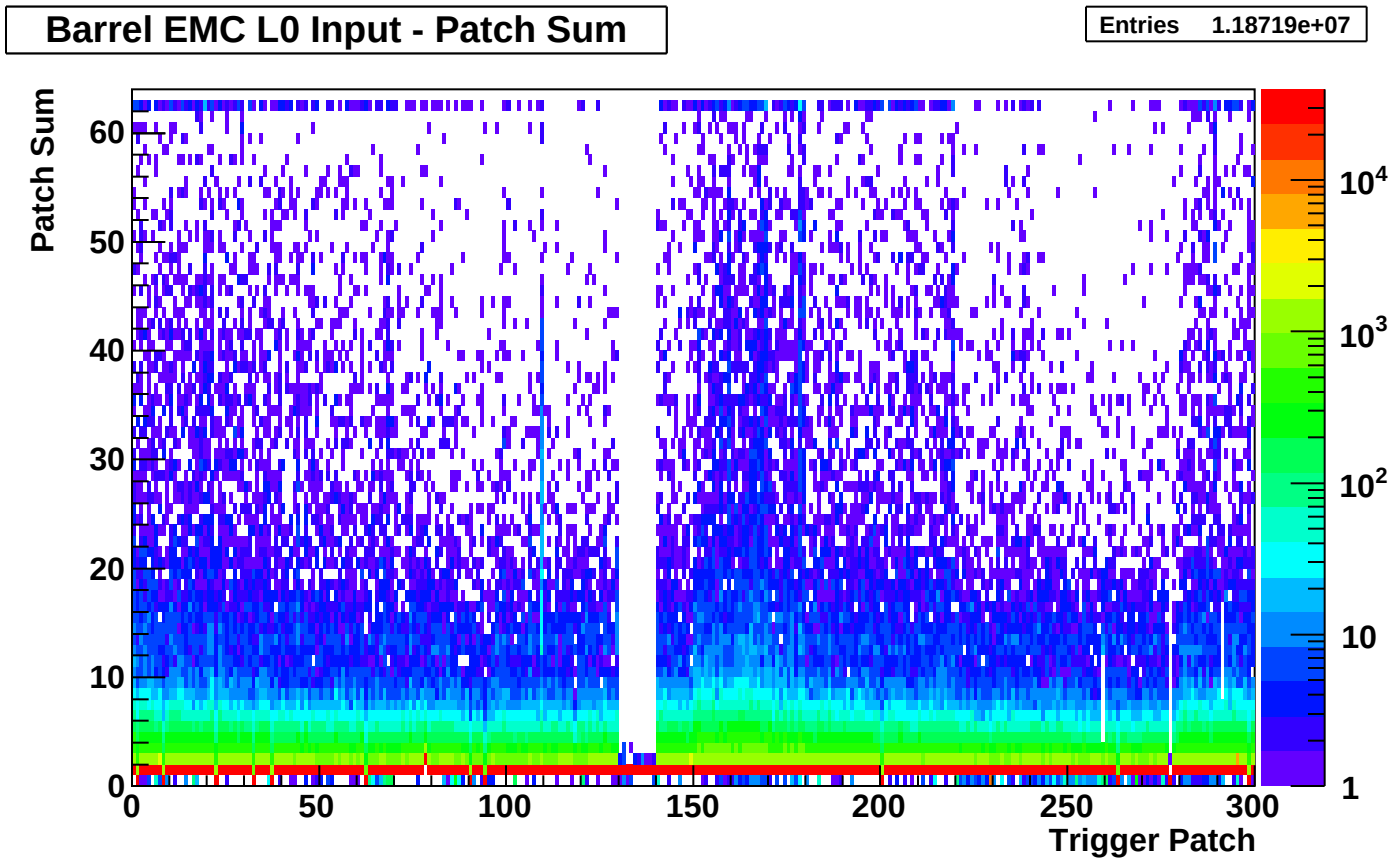

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
1
0
-1
-2
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

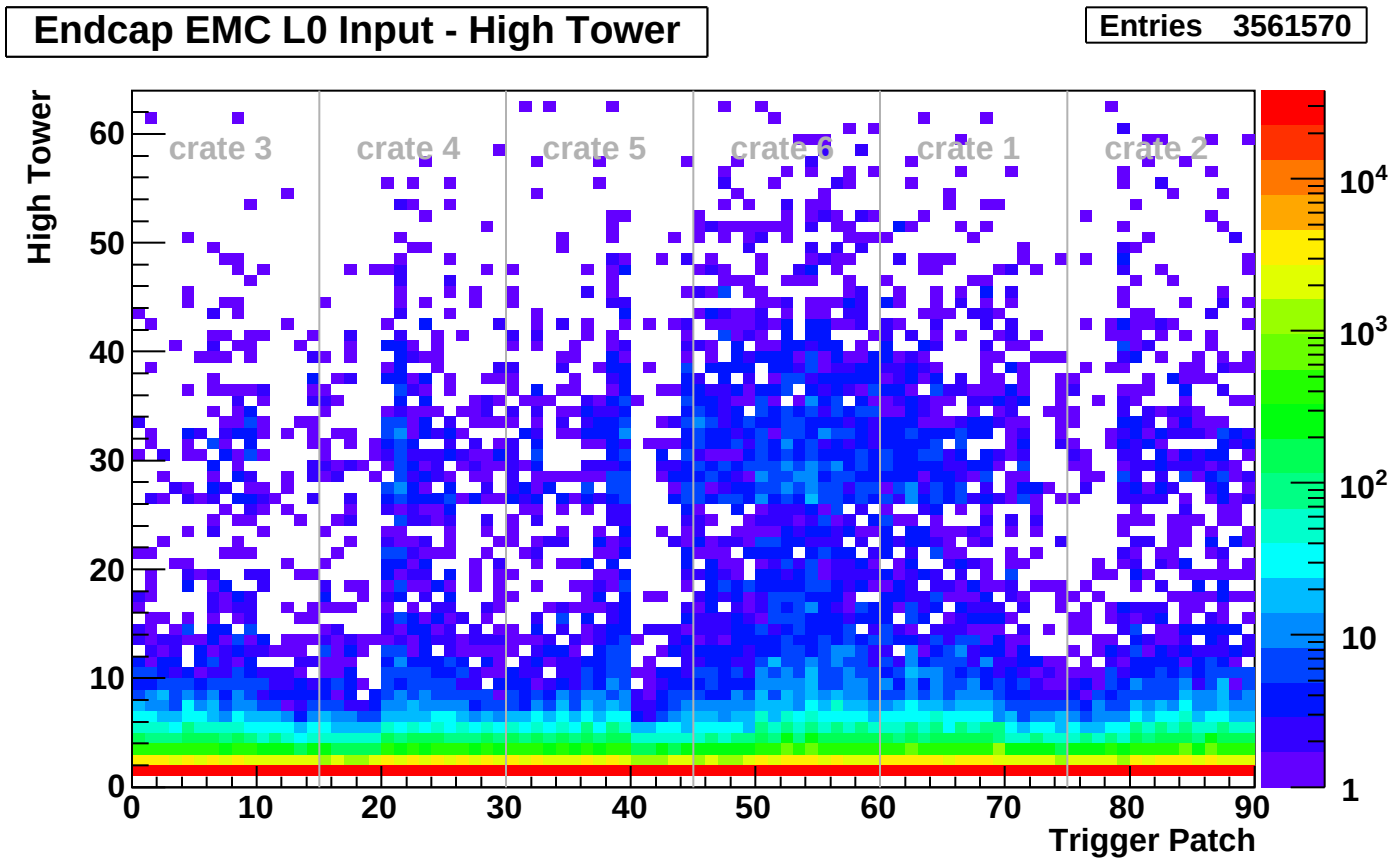

TCU bits
Entries 633168
TCU bits
1
0
39573 39573 39573 39573 39573 39573 39573 39573 39573 39573 39573 39573 39573 39573 39573 39573
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

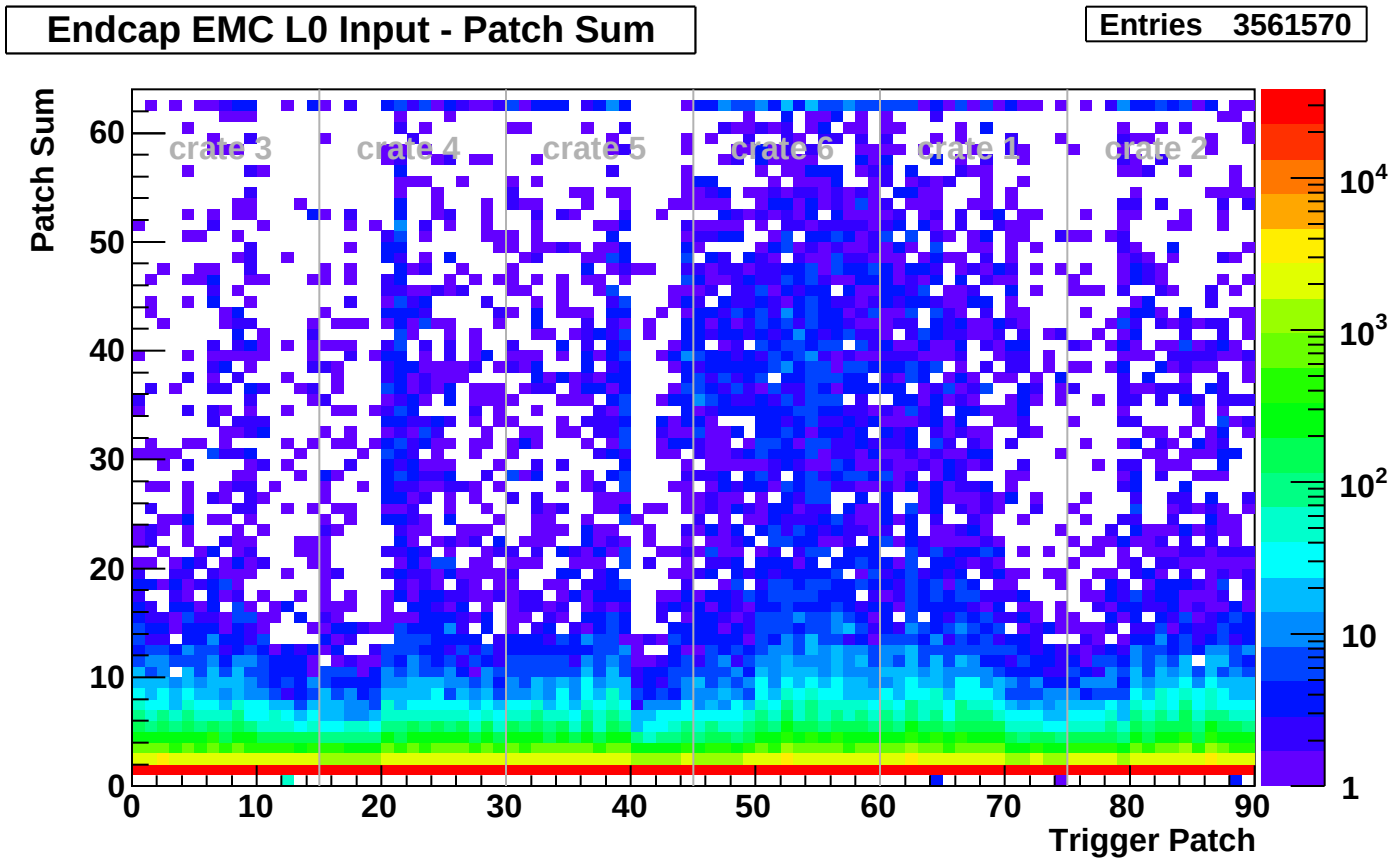

TCU bits
Entries 44554
TCU bits - Simulated
1
0
-1
-2
10171 5014 23613 2723 871 2162
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

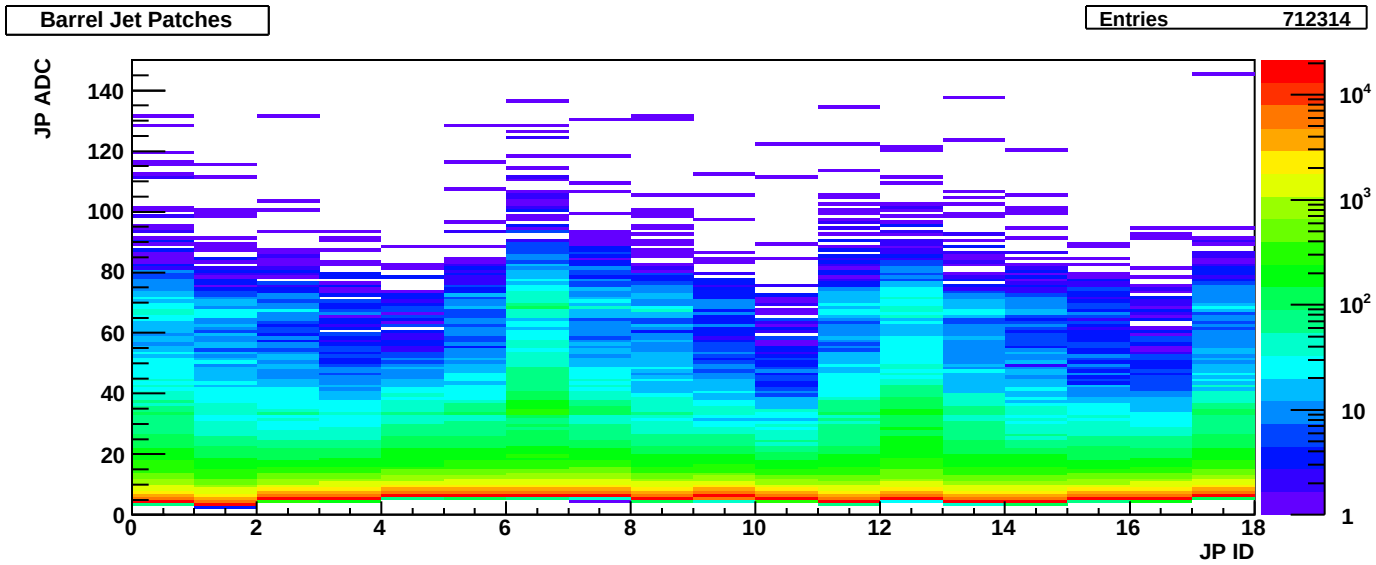

BunchId7Bit (BHT3)
Entries 1381
10^2
10
1
0
20
40
60
80
100
120

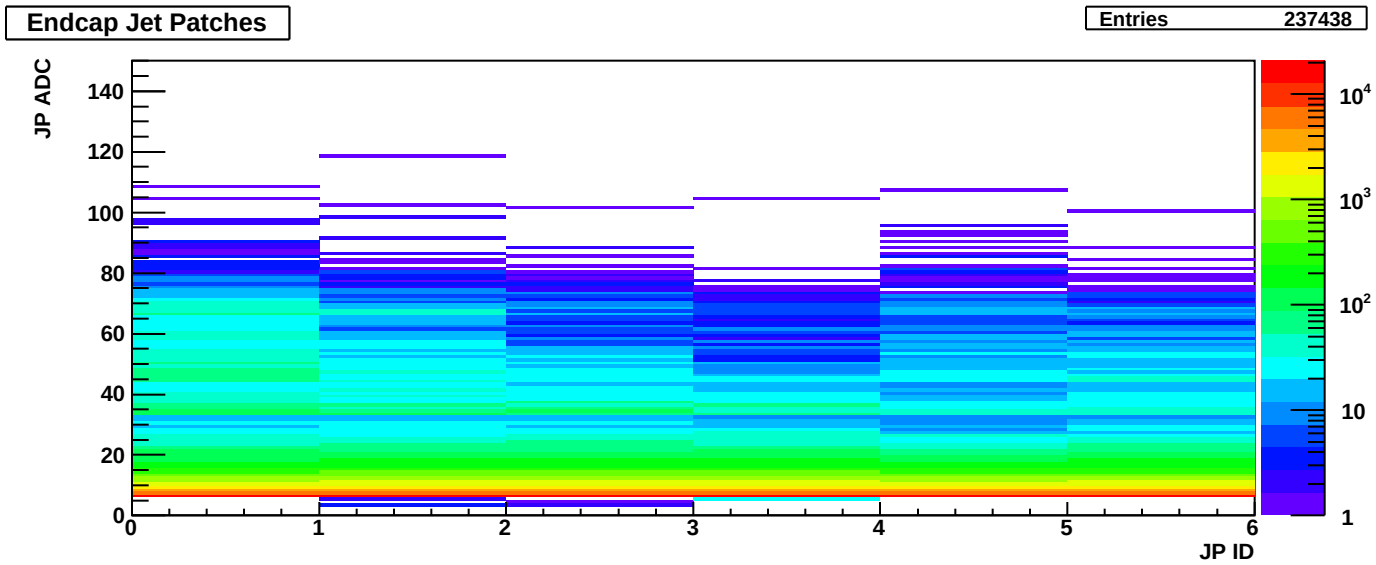

Barrel EMC L0 Input - High Tower
Entries 1.18719e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

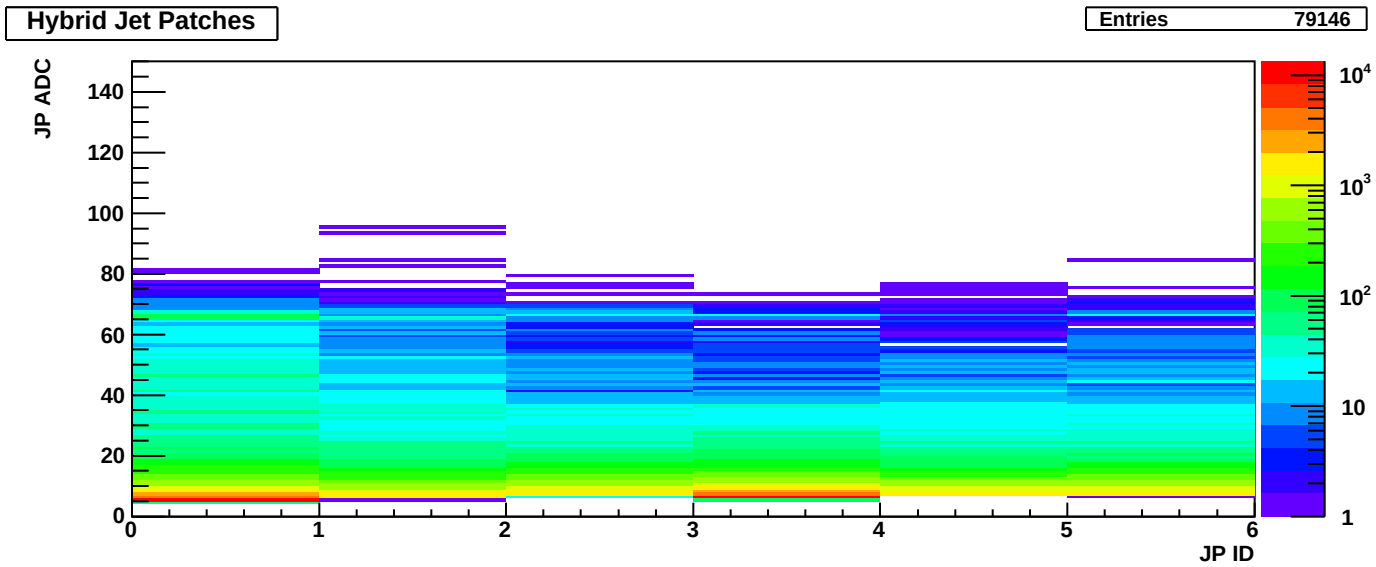

Barrel EMC L0 Input - Patch Sum
Entries 1.18719e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

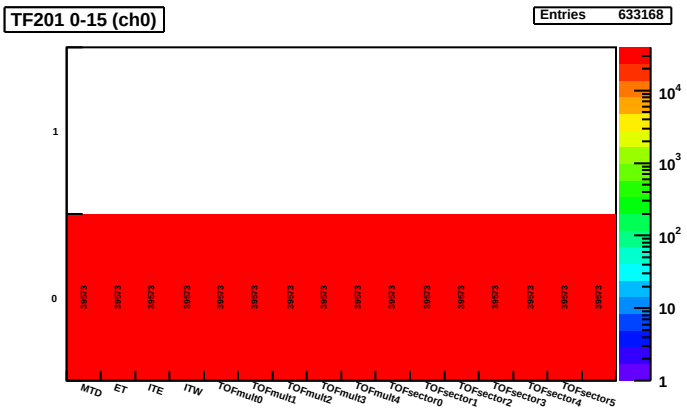

Endcap EMC L0 Input - High Tower
Entries 3561570
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

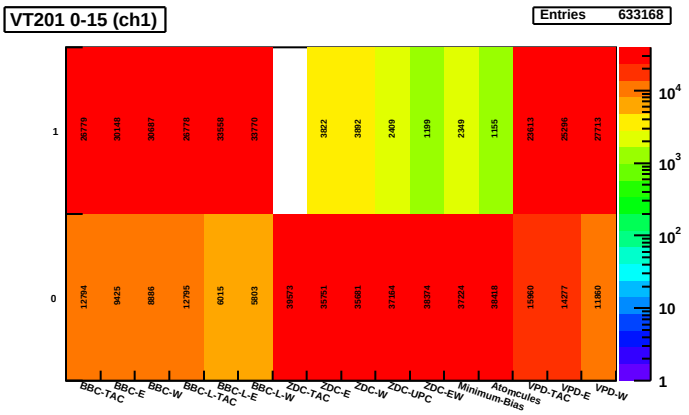

Endcap EMC L0 Input - Patch Sum
Entries 3561570
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

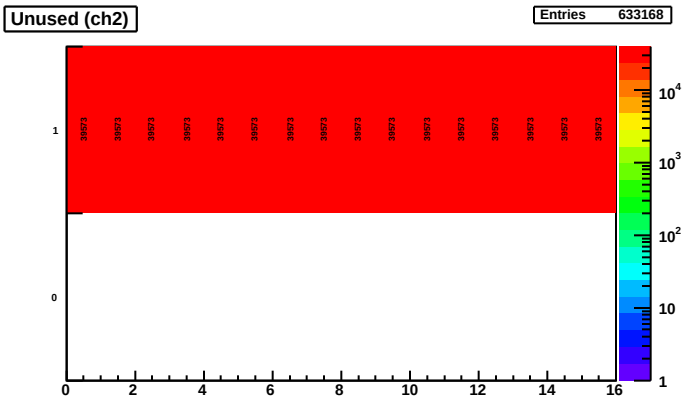

Barrel Jet Patches
Entries 712314
JP ADC
JP ID

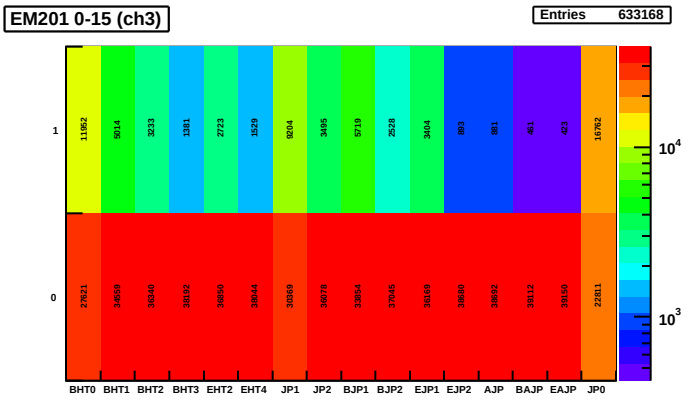

Endcap Jet Patches
Entries 237438
JP ADC
JP ID

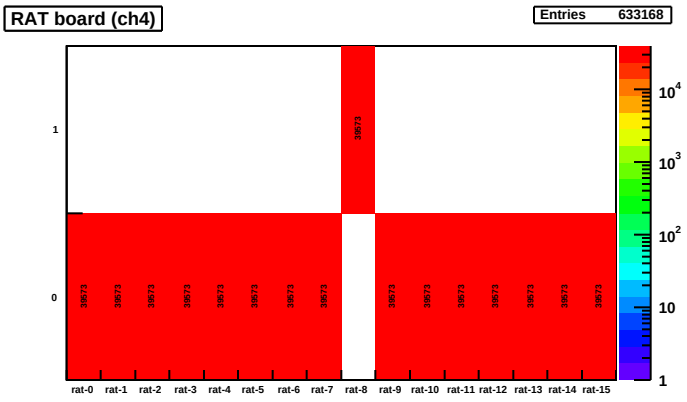

Hybrid Jet Patches
Entries 79146
JP ADC
JP ID

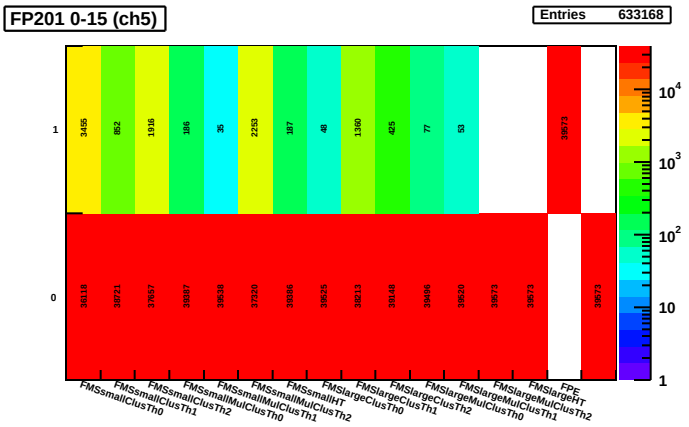

TF201 0-15 (ch0)
Entries 633168
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

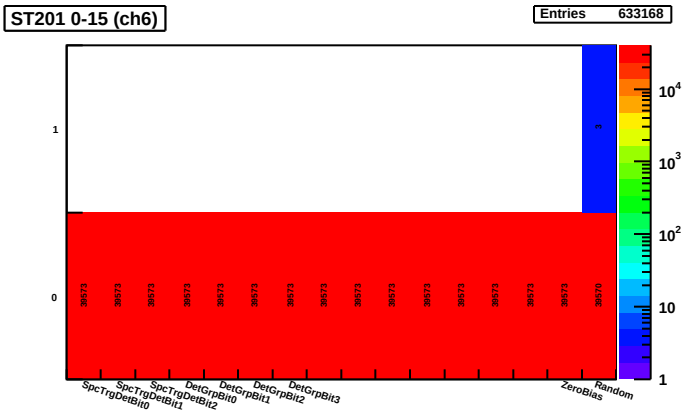

VT201 0-15 (ch1)
Entries 633168
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

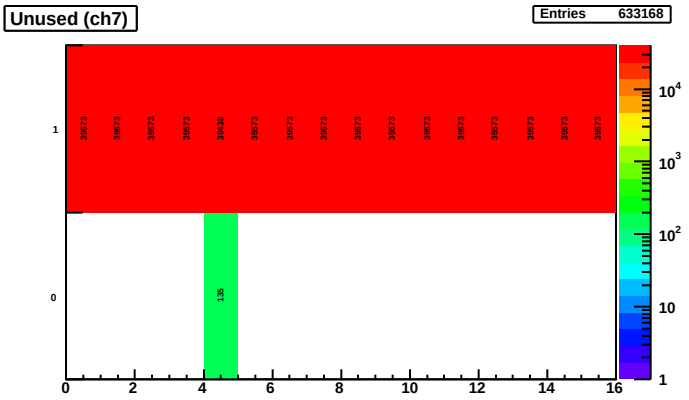

Unused (ch2)
Entries 633168

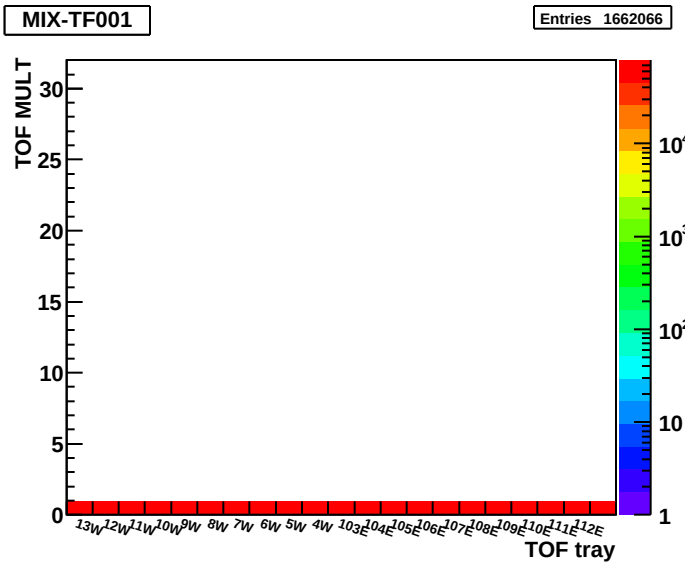

EM201 0-15 (ch3)
Entries 633168
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

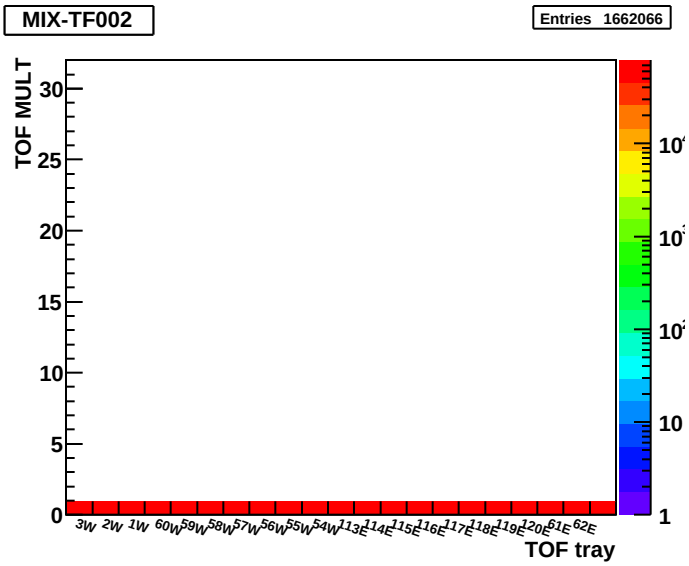

RAT board (ch4)
Entries 633168
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

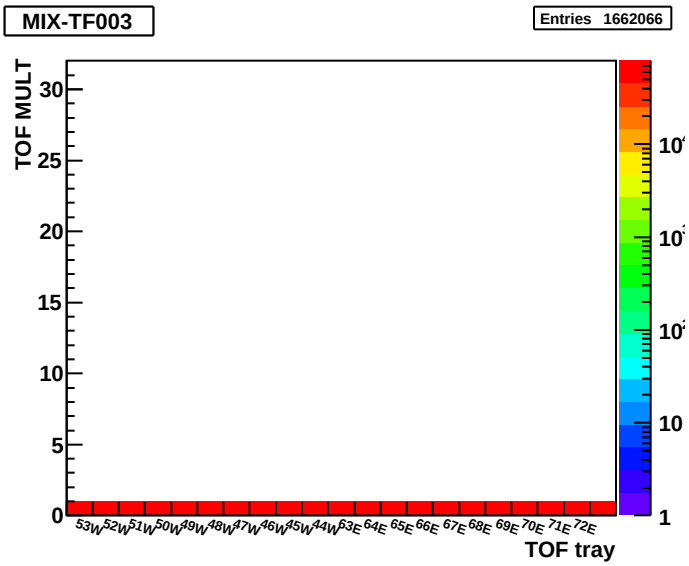

FP201 0-15 (ch5)
Entries 633168
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

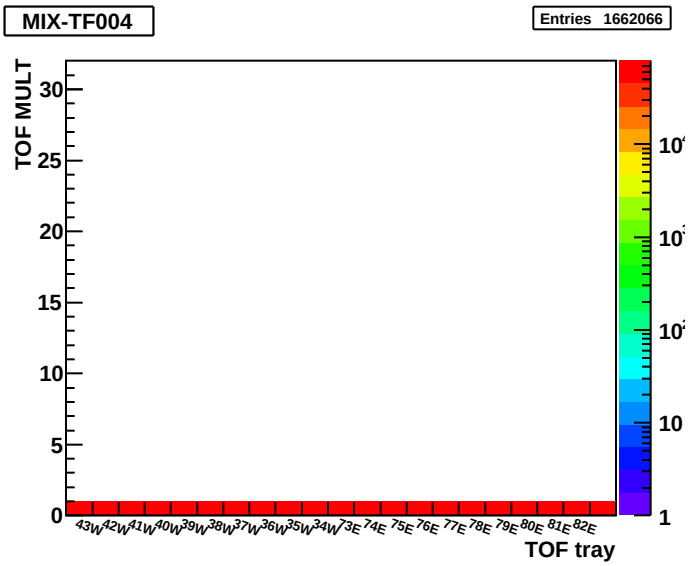

ST201 0-15 (ch6)
Entries 633168
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

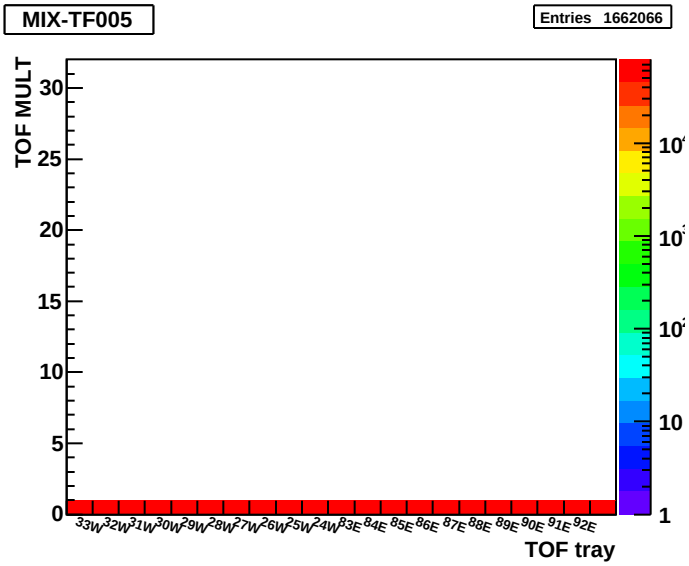

Unused (ch7)
Entries 633168

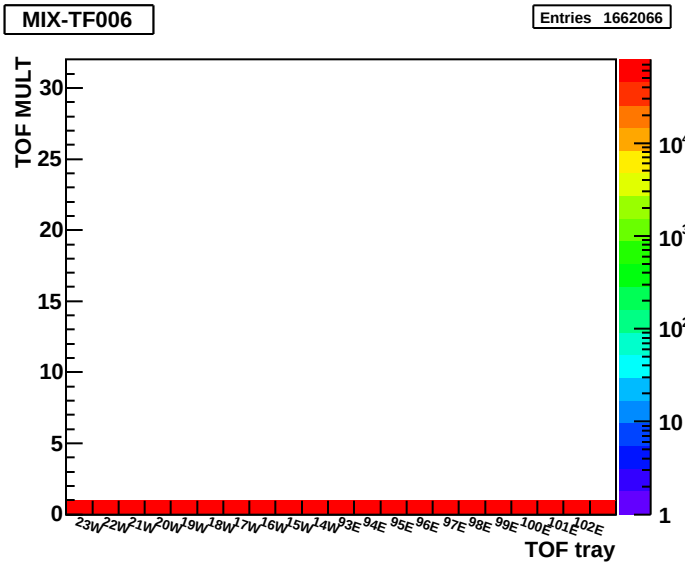

MIX-TF001
Entries 1662066
TOF MULT
TOF tray

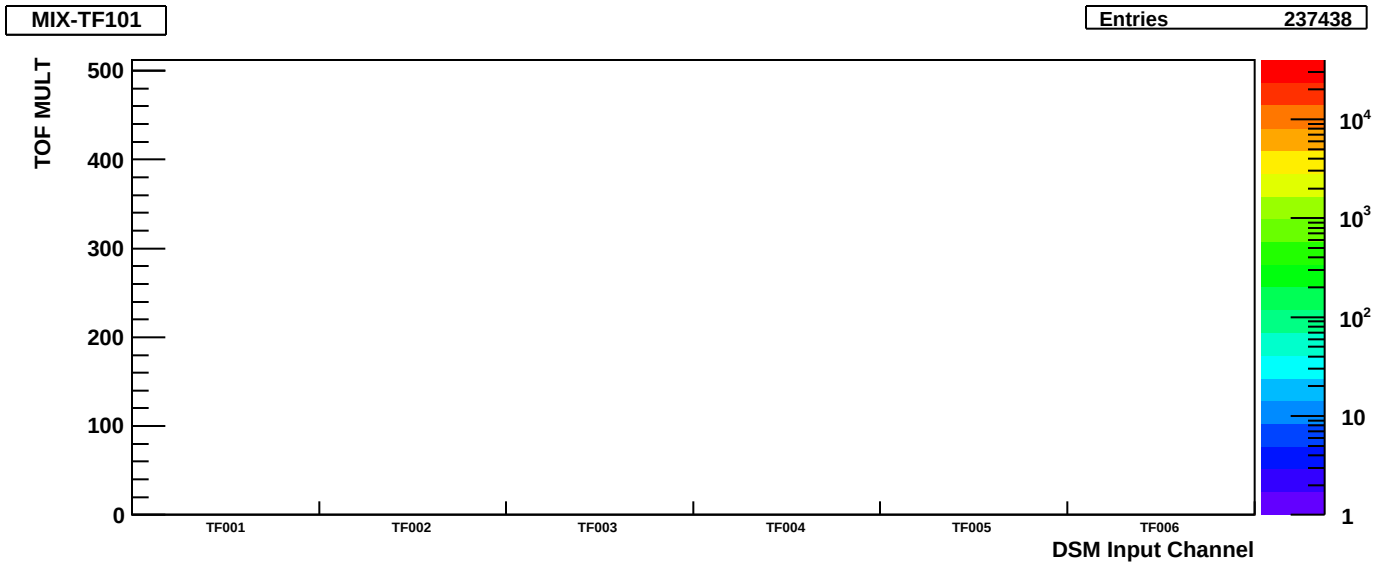

MIX-TF002
Entries 1662066
TOF MULT
TOF tray

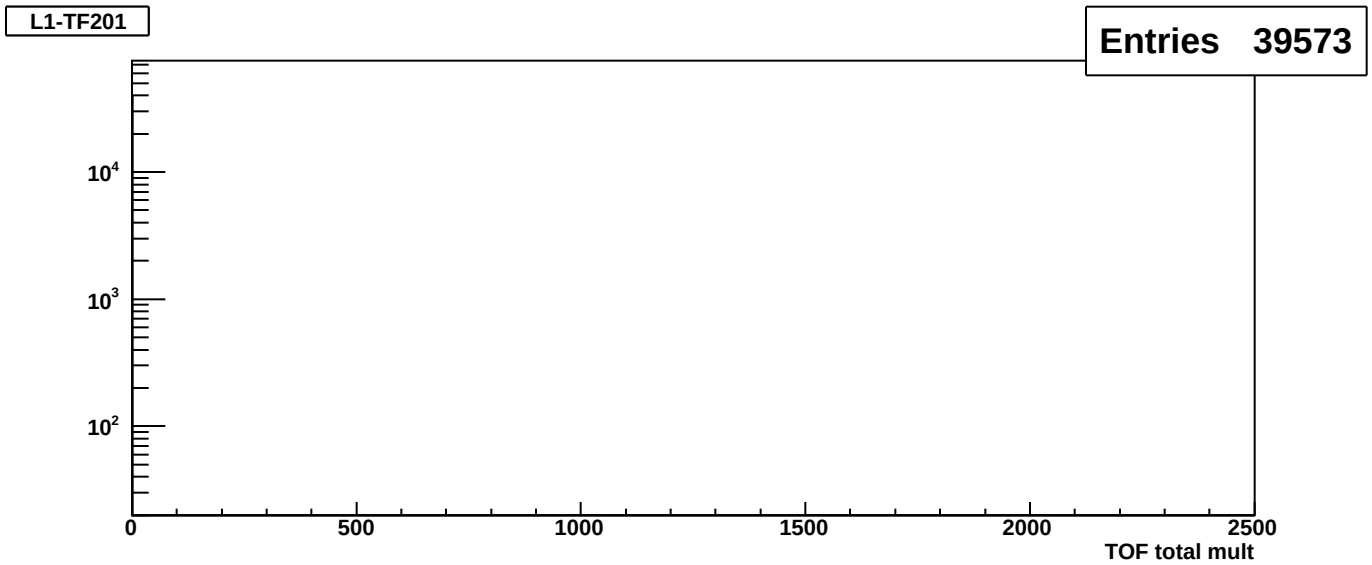

MIX-TF003
Entries 1662066
TOF MULT
TOF tray

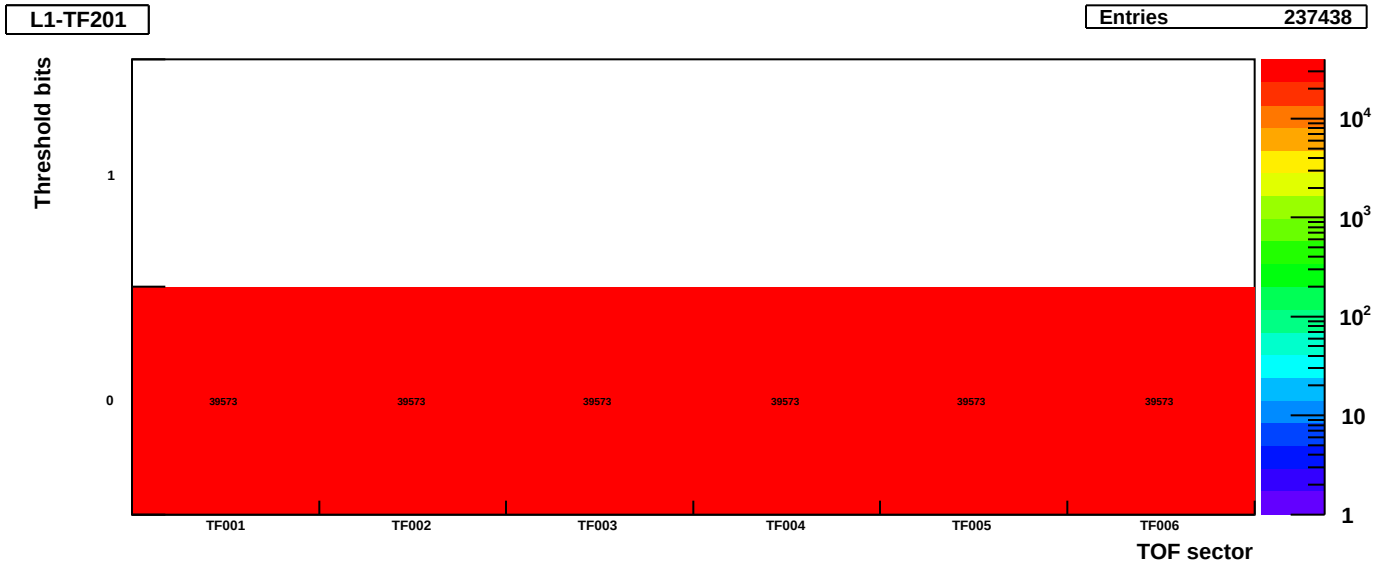

MIX-TF004
Entries 1662066
TOF MULT
TOF tray

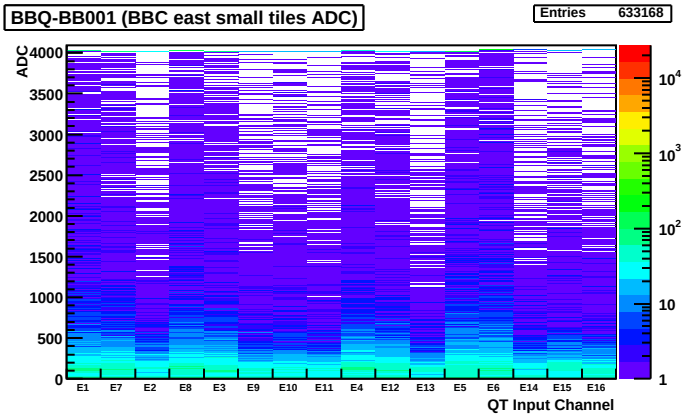

MIX-TF005
Entries 1662066
TOF MULT
TOF tray

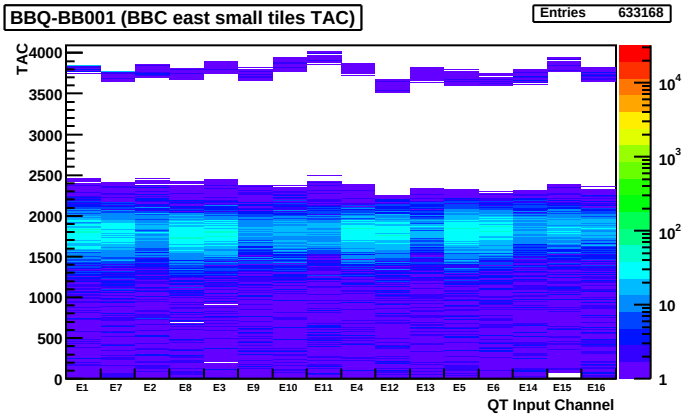

MIX-TF006
Entries 1662066
TOF MULT
TOF tray

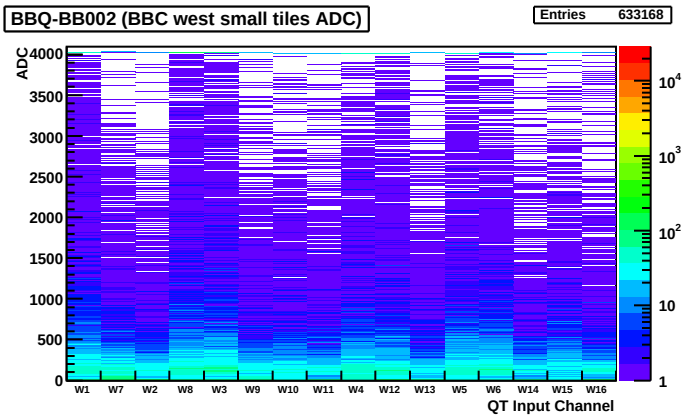

MIX-TF101
Entries 237438
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

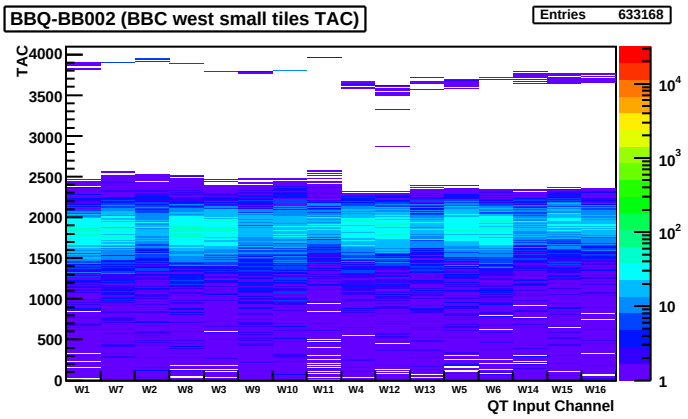

L1-TF201
Entries 39573
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

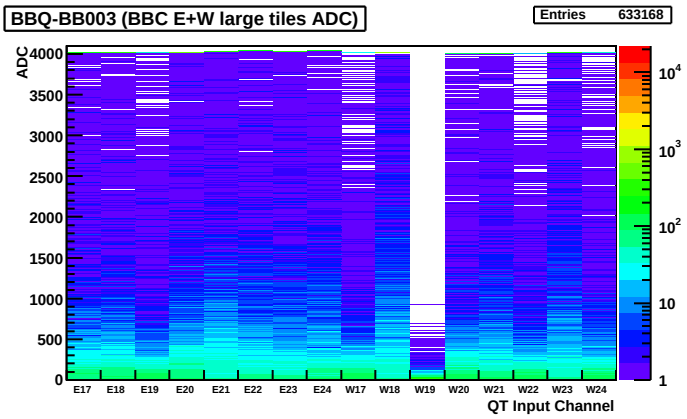

L1-TF201
Entries 237438
Threshold bits
1
0
39573
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

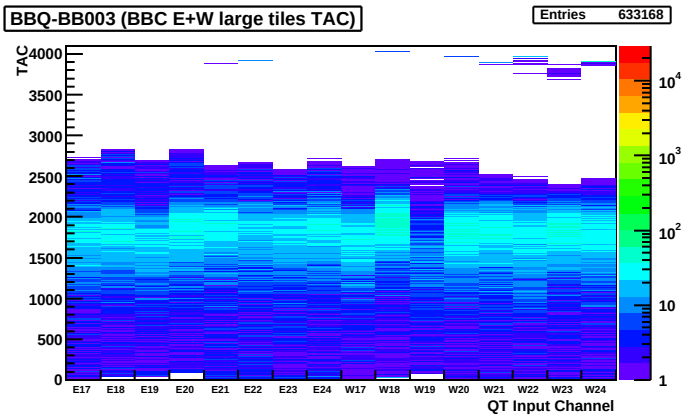

BBQ-BB001 (BBC east small tiles ADC)
Entries 633168
ADC
QT Input Channel

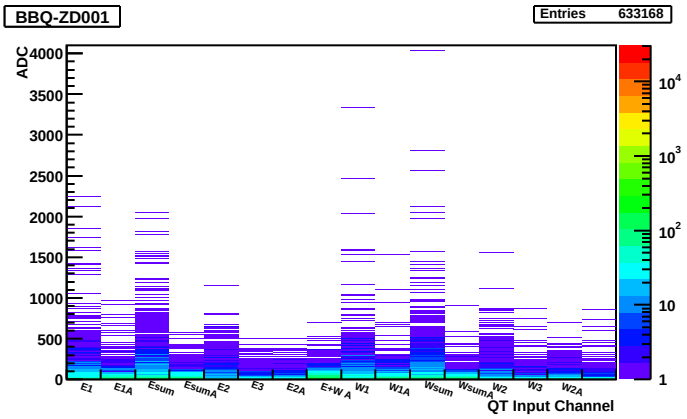

BBQ-BB001 (BBC east small tiles TAC)
Entries 633168
TAC
QT Input Channel

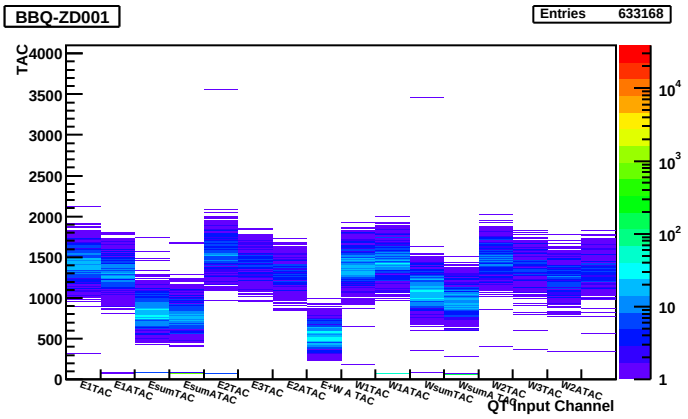

BBQ-BB002 (BBC west small tiles ADC)
Entries 633168
ADC
QT Input Channel

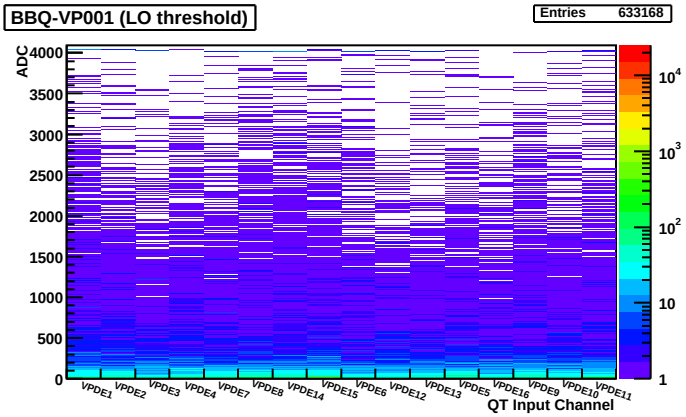

BBQ-BB002 (BBC west small tiles TAC)
Entries 633168
TAC
QT Input Channel

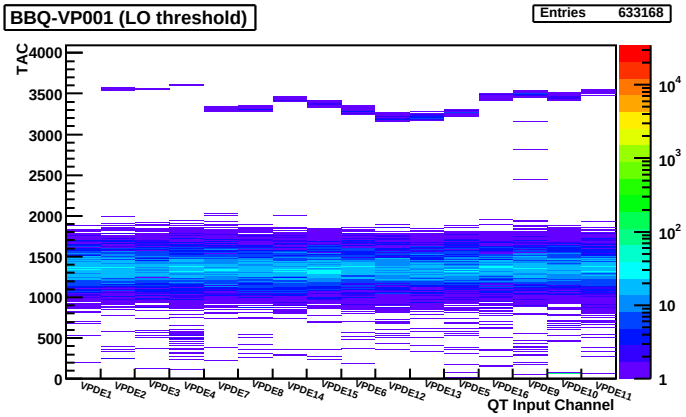

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 633168
ADC
QT Input Channel

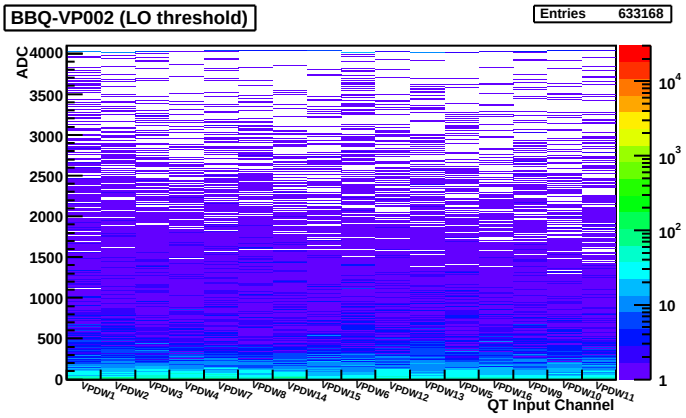

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 633168
TAC
QT Input Channel

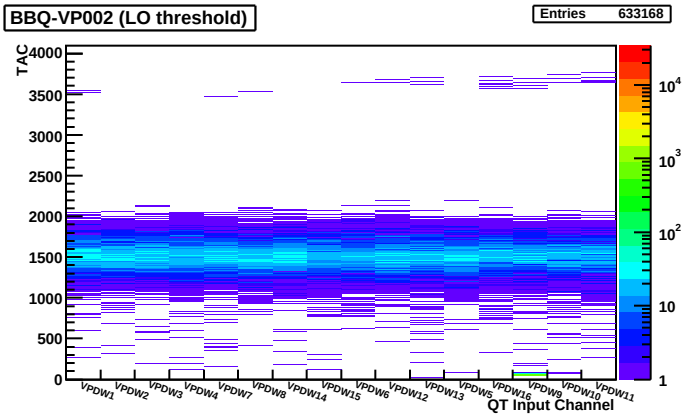

BBQ-ZD001
Entries 633168
ADC
QT Input Channel

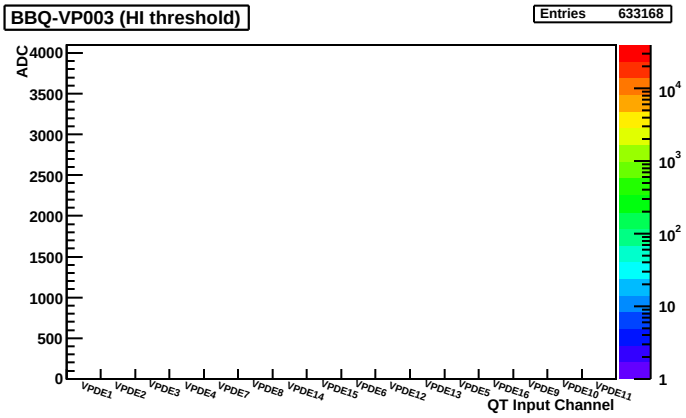

BBQ-ZD001
Entries 633168
TAC
QT Input Channel

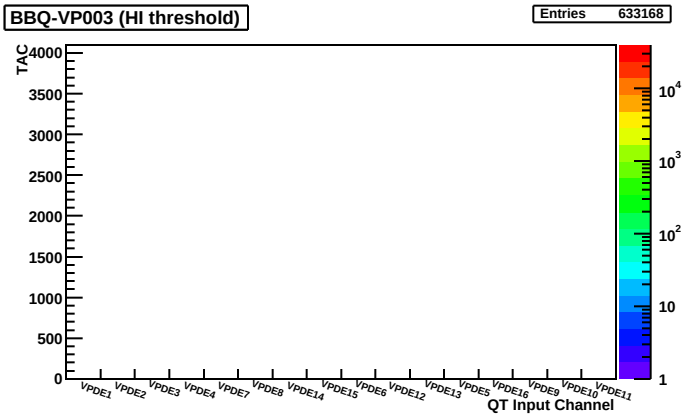

BBQ-VP001 (LO threshold)
Entries 633168
ADC
QT Input Channel

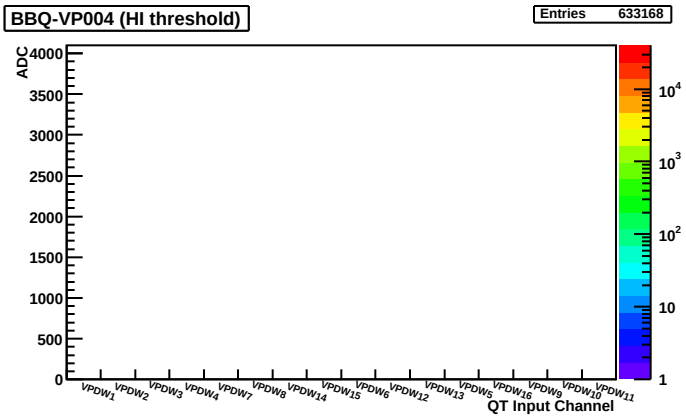

BBQ-VP001 (LO threshold)
Entries 633168
TAC
QT Input Channel

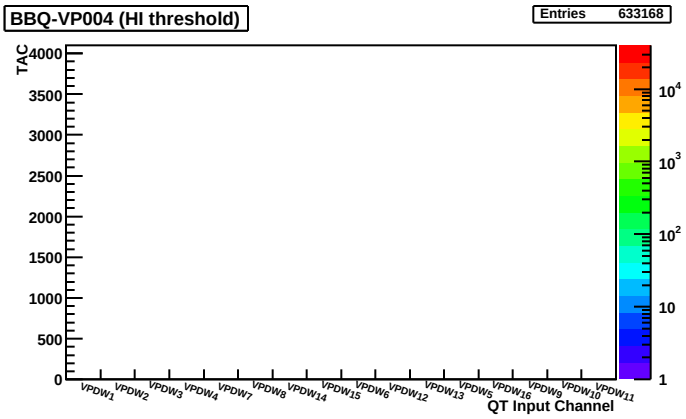

BBQ-VP002 (LO threshold)
Entries 633168
ADC
QT Input Channel

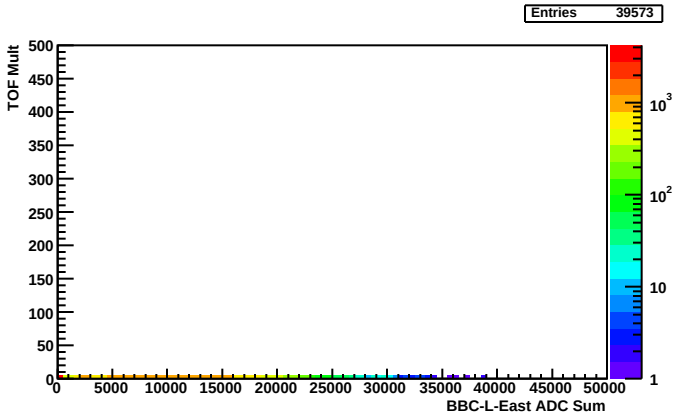

BBQ-VP002 (LO threshold)
Entries 633168
TAC
QT Input Channel

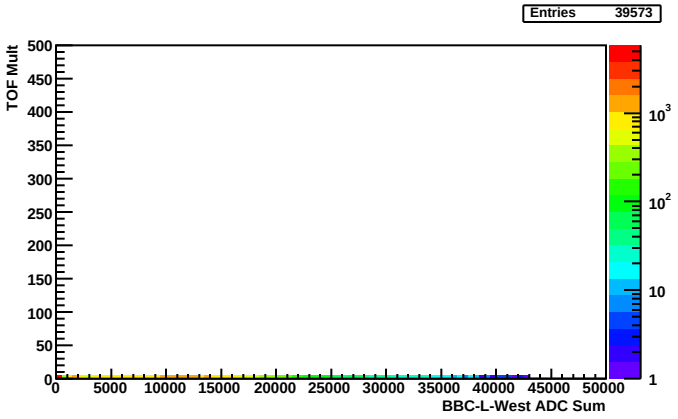

BBQ-VP003 (HI threshold)
Entries 633168
ADC
QT Input Channel

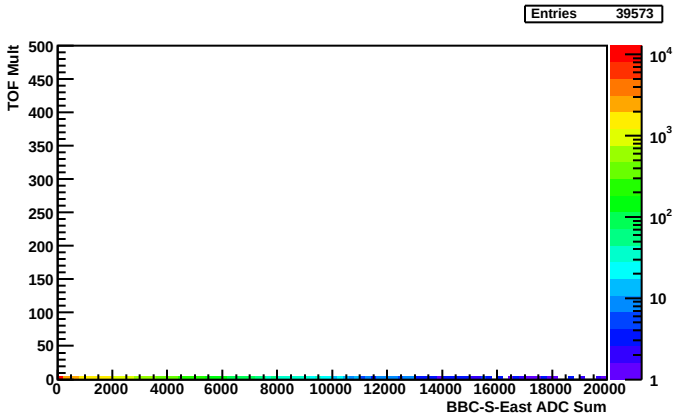

BBQ-VP003 (HI threshold)
Entries 633168
TAC
QT Input Channel

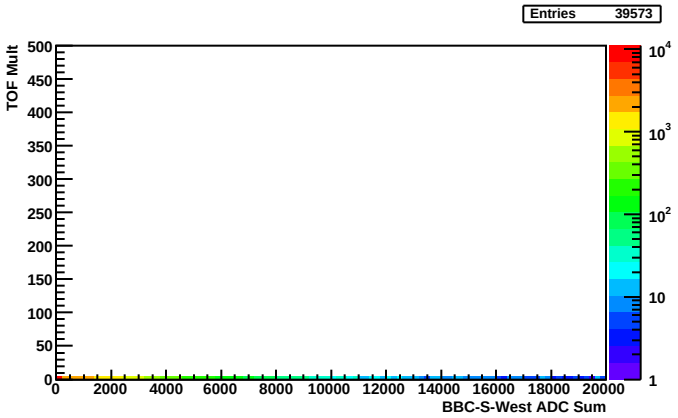

BBQ-VP004 (HI threshold)
Entries 633168
ADC
QT Input Channel

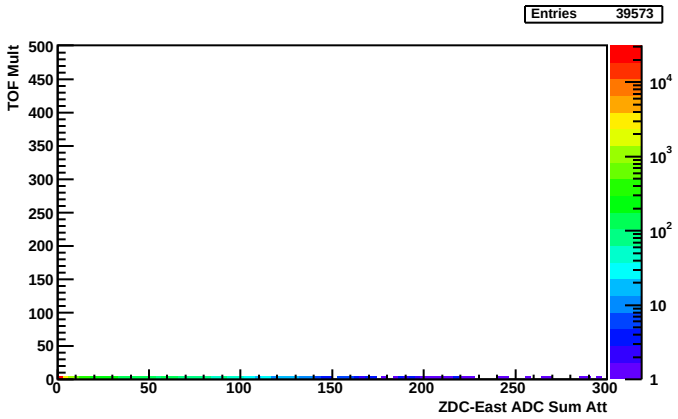

BBQ-VP004 (HI threshold)
Entries 633168
TAC
QT Input Channel

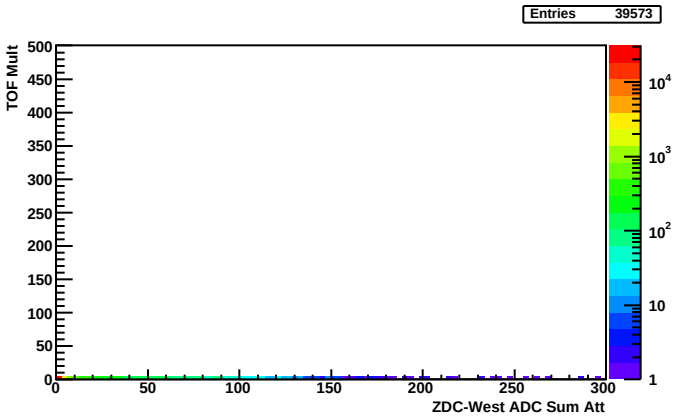

Entries 39573
TOF Mult
BBC-L-East ADC Sum

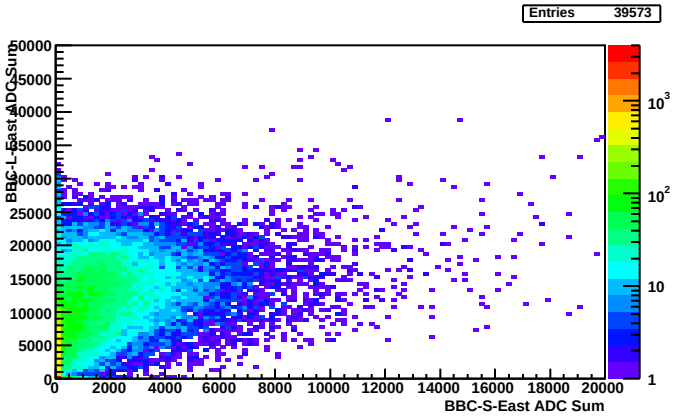

Entries 39573
TOF Mult
BBC-L-West ADC Sum

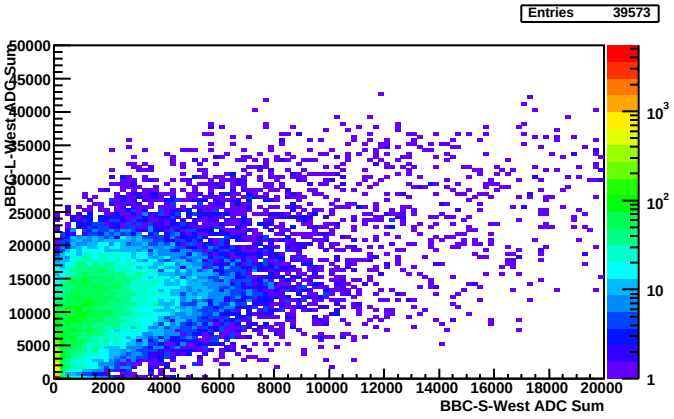

Entries 39573
TOF Mult
BBC-S-East ADC Sum

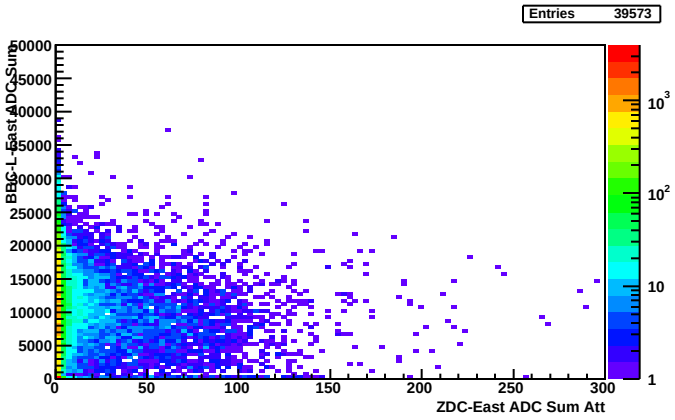

Entries 39573
TOF Mult
BBC-S-West ADC Sum

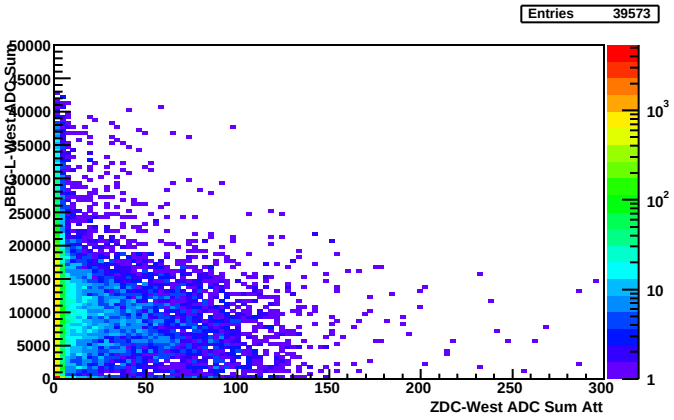

Entries 39573
TOF Mult
ZDC-East ADC Sum Att

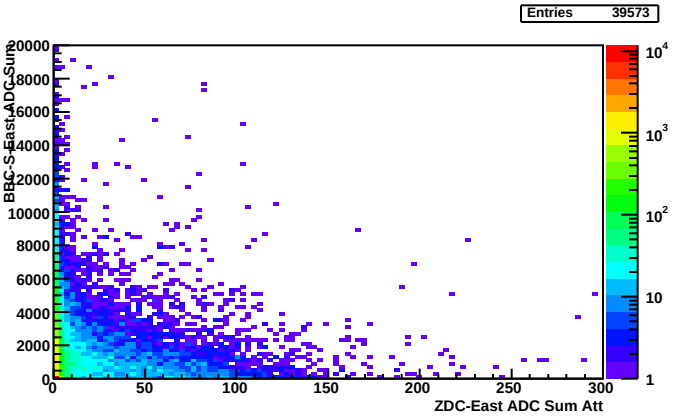

Entries 39573
TOF Mult
ZDC-West ADC Sum Att

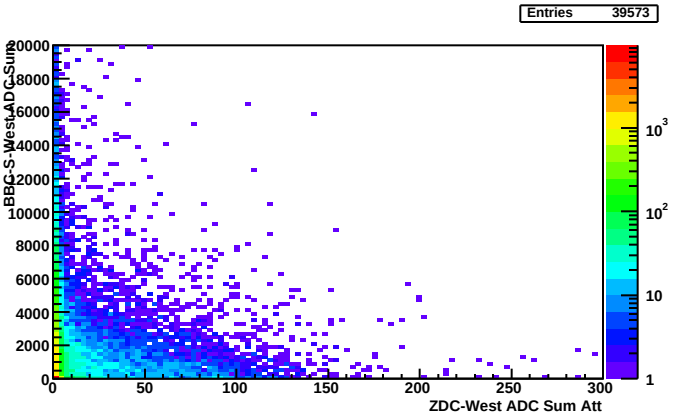

Entries 39573
BBC-L-East ADC Sum
BBC-S-East ADC Sum

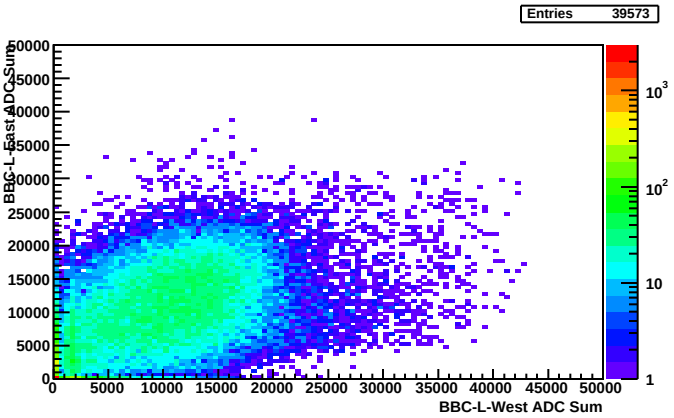

Entries 39573
BBC-L-West ADC Sum
BBC-S-West ADC Sum

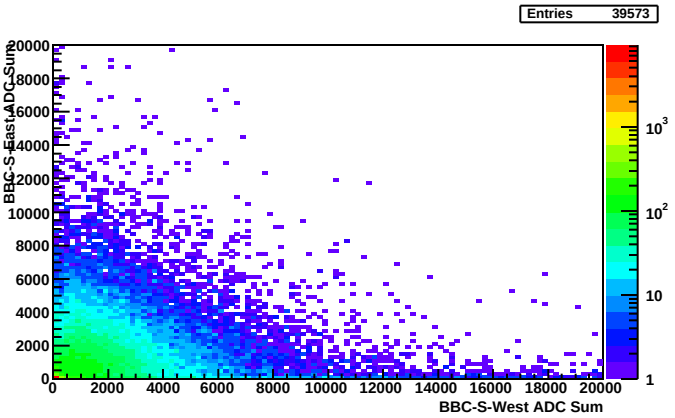

Entries 39573
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

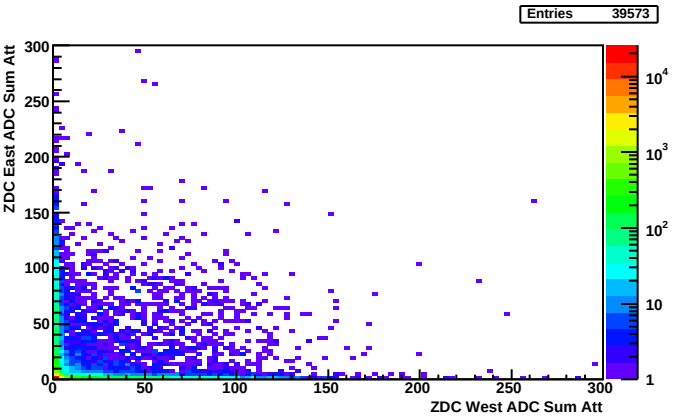

Entries 39573
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

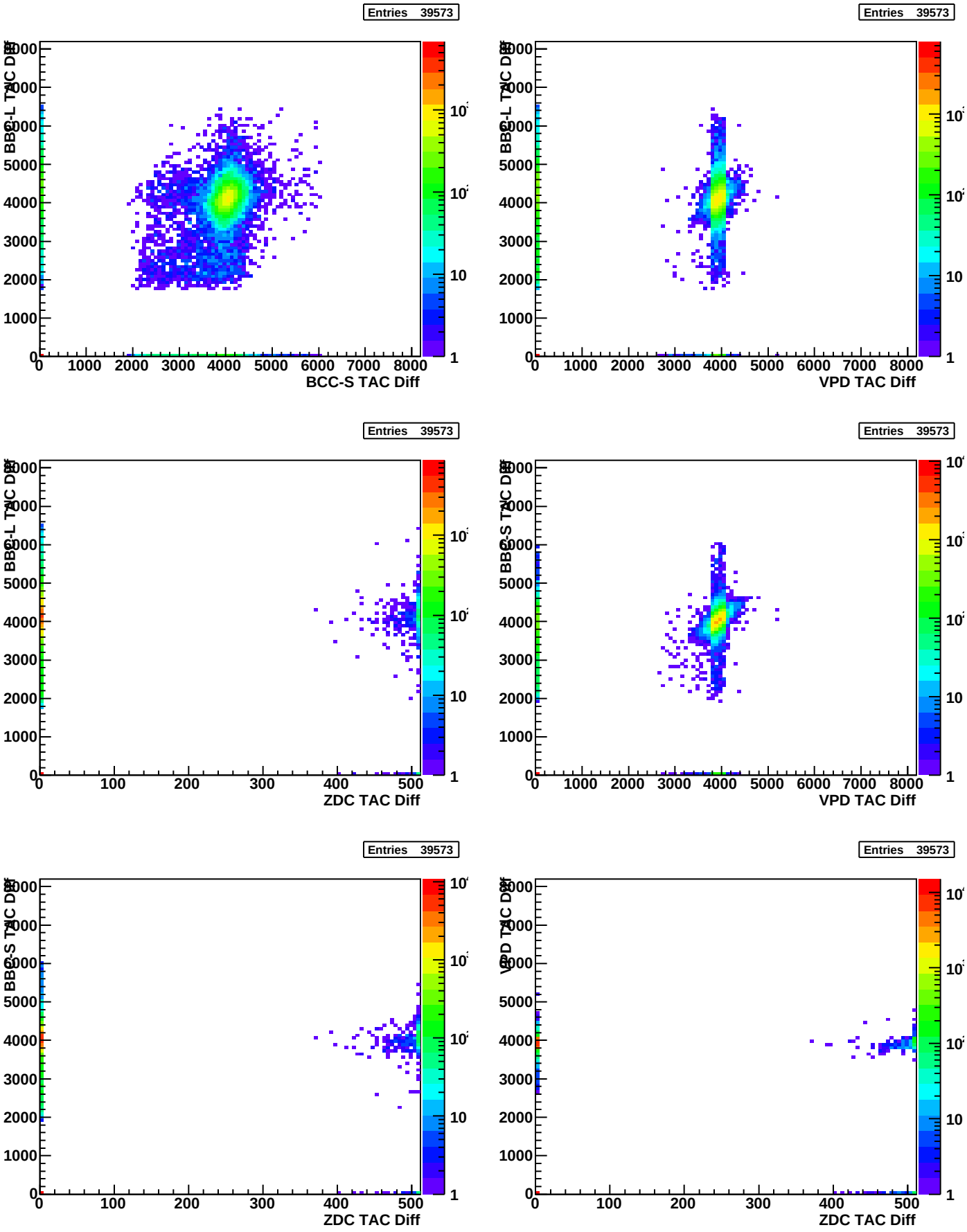

Entries 39573
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

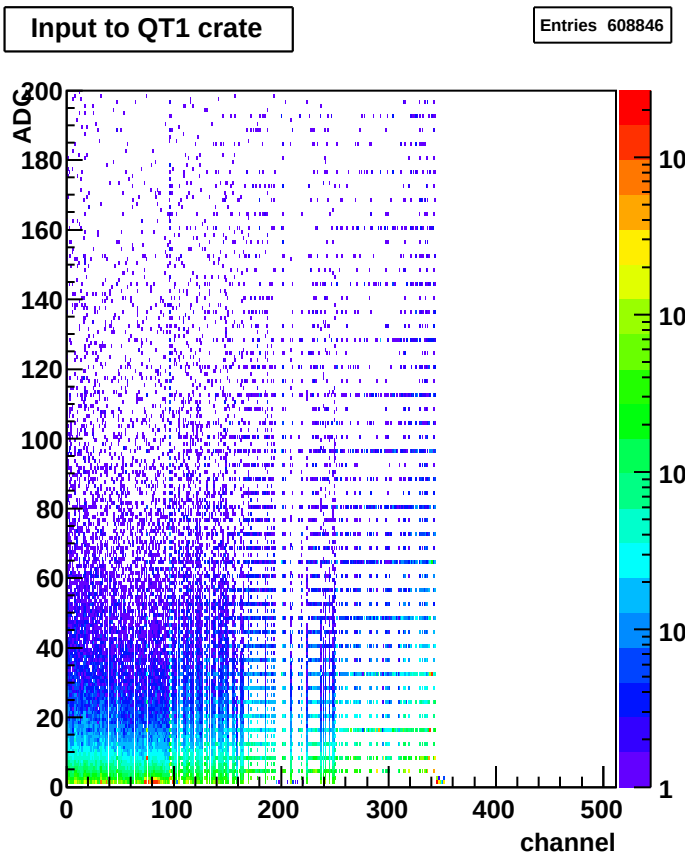

Entries 39573
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

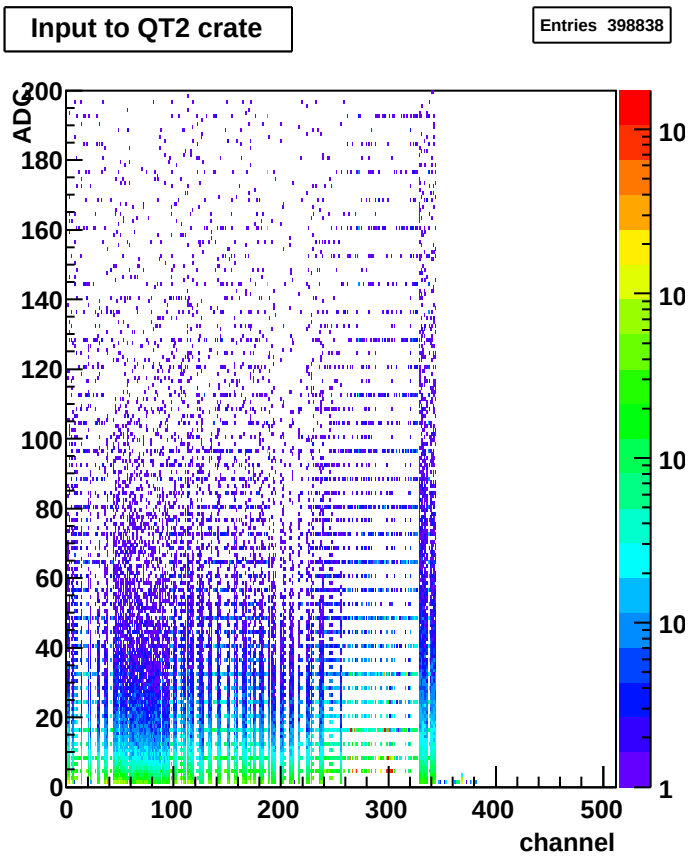

Entries 39573
BBC-L-East ADC Sum
BBC-L-West ADC Sum

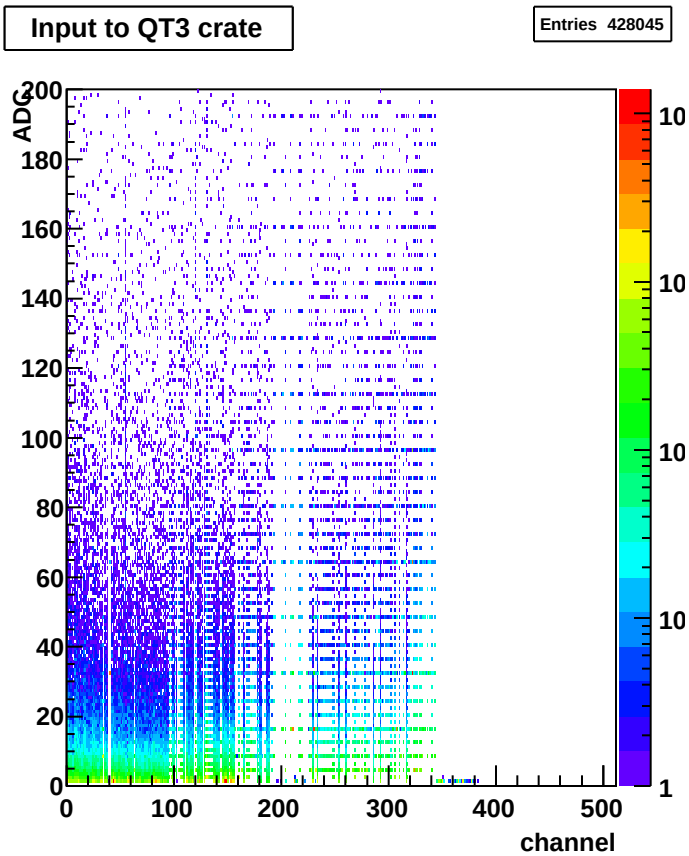

Entries 39573
BBC-S-East ADC Sum
BBC-S-West ADC Sum

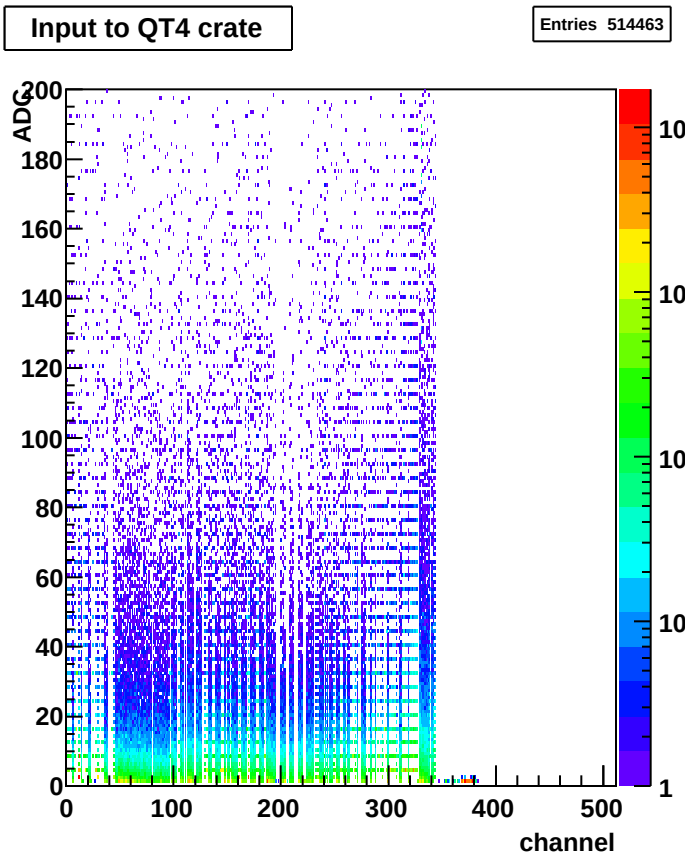

Entries 39573
ZDC East ADC Sum Att
ZDC West ADC Sum Att

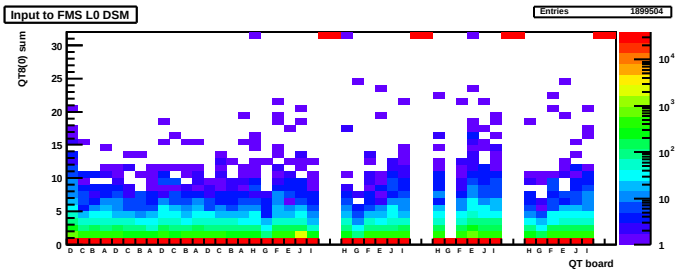

Entries 39573
BBC-L TAC Diff
BCC-S TAC Diff
Entries 39573
BBC-L TAC Diff
VPD TAC Diff
Entries 39573
BBC-L TAC Diff
ZDC TAC Diff
Entries 39573
BBC-S TAC Diff
VPD TAC Diff
Entries 39573
BBC-S TAC Diff
ZDC TAC Diff
Entries 39573
VPD TAC Diff
ZDC TAC Diff

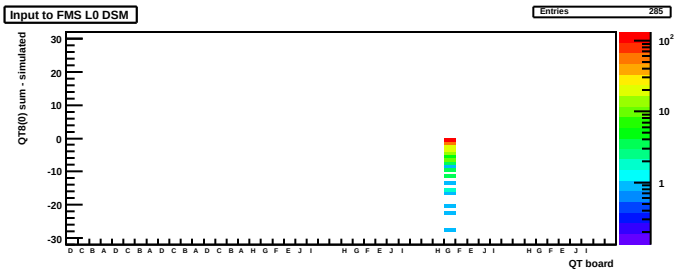

Input to QT1 crate
Entries 608846
ADC
channel

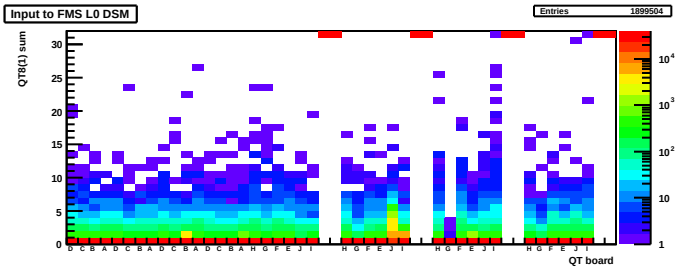

Input to QT2 crate
Entries 398838
ADC
channel

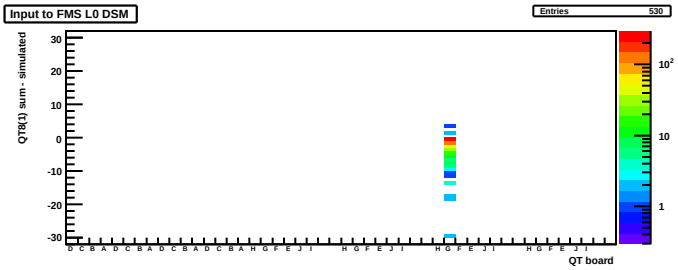

Input to QT3 crate
Entries 428045
ADC
channel

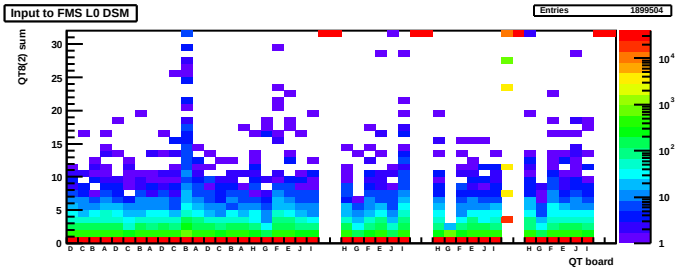

Input to QT4 crate
Entries 514463
ADC
channel

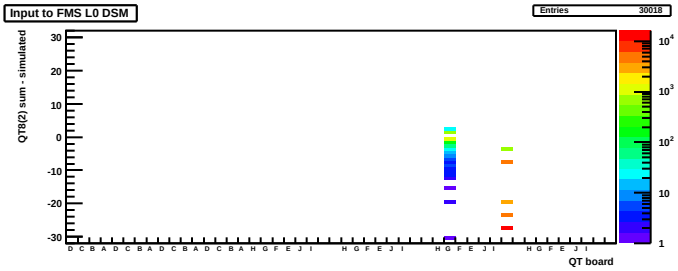

Input to FMS L0 DSM
Entries 1899504
QT8(0) sum
QT board

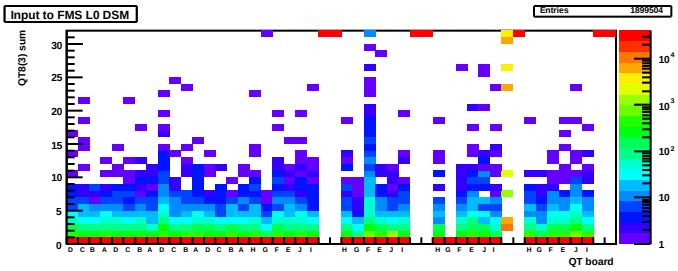

Input to FMS L0 DSM
Entries 285
QT8(0) sum - simulated
QT board

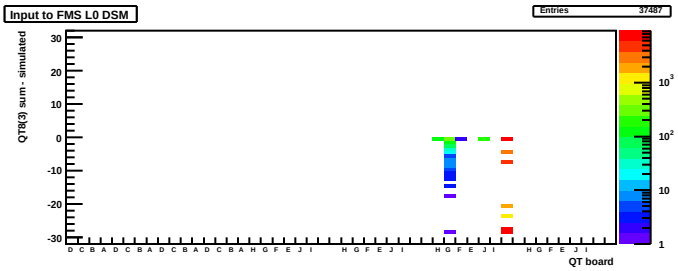

Input to FMS L0 DSM
Entries 1899504
QT8(1) sum
QT board

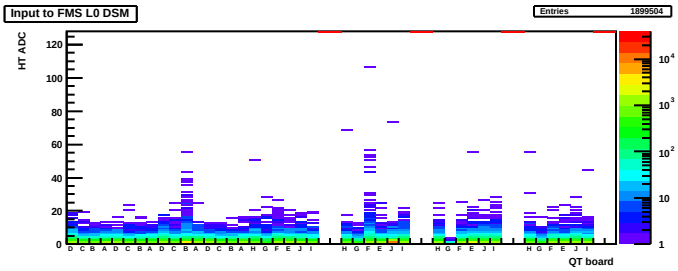

Input to FMS L0 DSM
Entries 530
QT8(1) sum - simulated
QT board

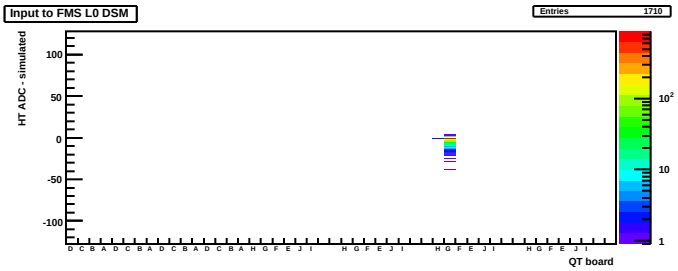

Input to FMS L0 DSM
Entries 1899504
QT8(2) sum
QT board

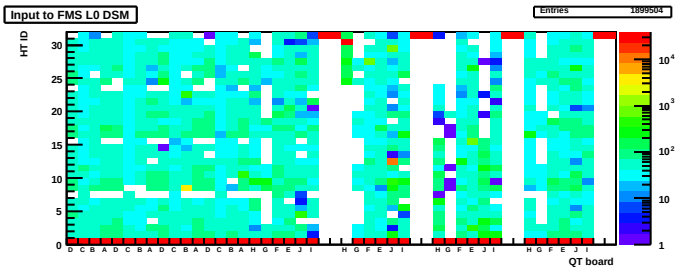

Input to FMS L0 DSM
Entries 30018
QT8(2) sum - simulated
QT board

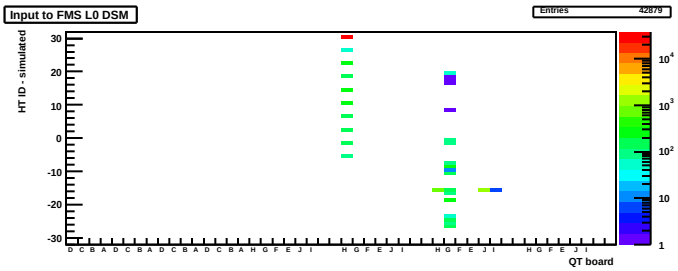

Input to FMS L0 DSM
Entries 1899504
QT8(3) sum
QT board

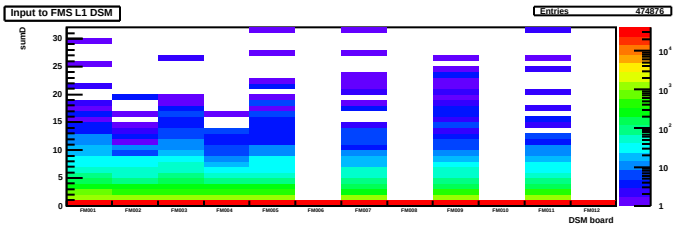

Input to FMS L0 DSM
Entries 37487
QT8(3) sum - simulated
QT board

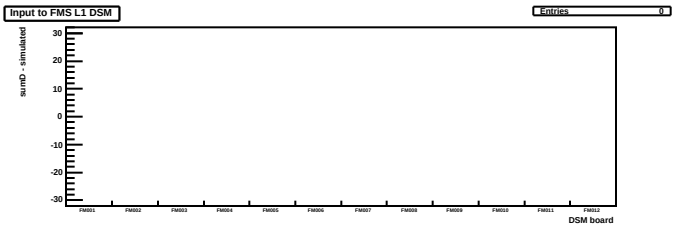

Input to FMS L0 DSM
Entries 1899504
HT ADC
QT board

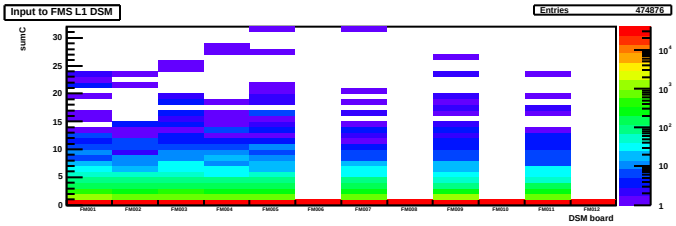

Input to FMS L0 DSM
Entries 1710
HT ADC - simulated
QT board

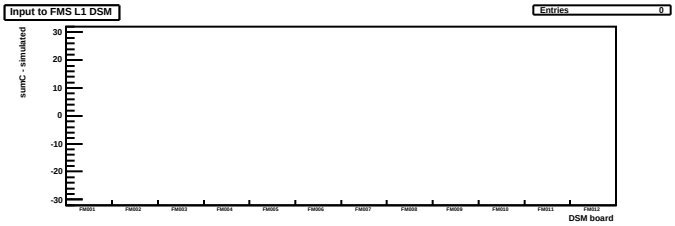

Input to FMS L0 DSM
Entries 1899504
HT ID
QT board

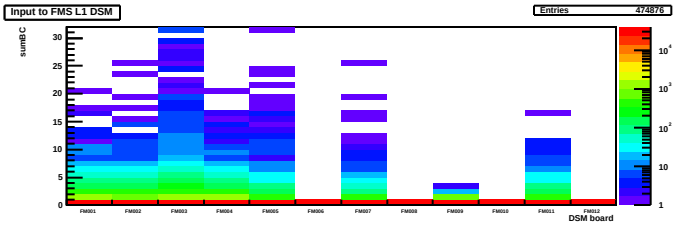

Input to FMS L0 DSM
Entries 42879
HT ID - simulated
QT board

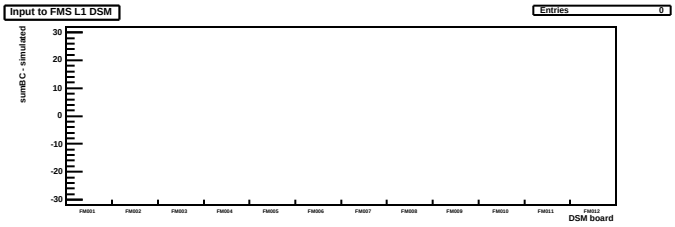

Input to FMS L1 DSM
Entries 474876
sumD
DSM board

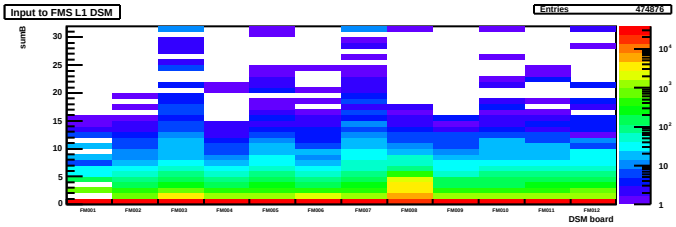

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

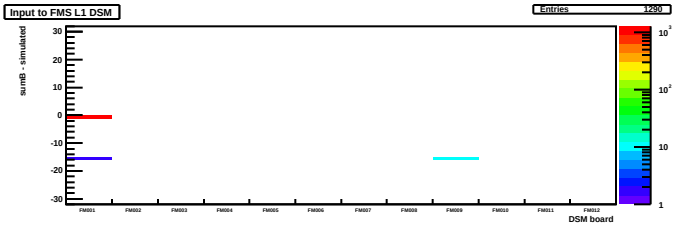

Input to FMS L1 DSM
Entries 474876
sumC
DSM board

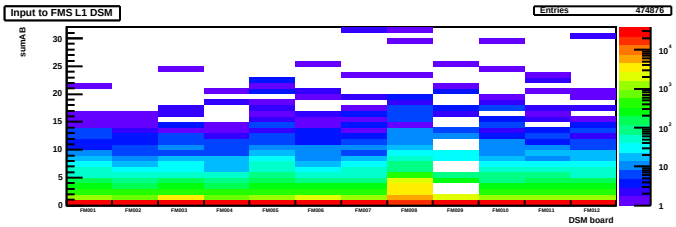

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

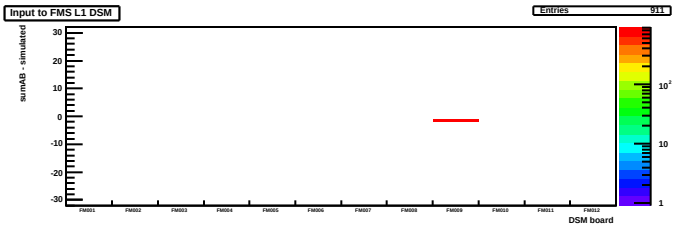

Input to FMS L1 DSM
Entries 474876
sumBC
DSM board

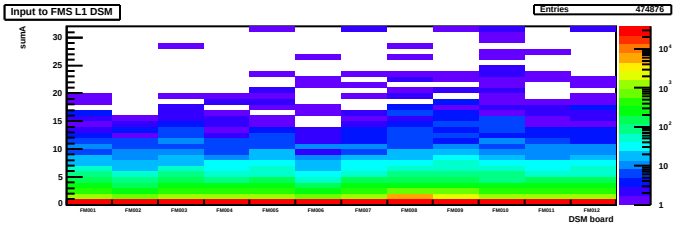

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

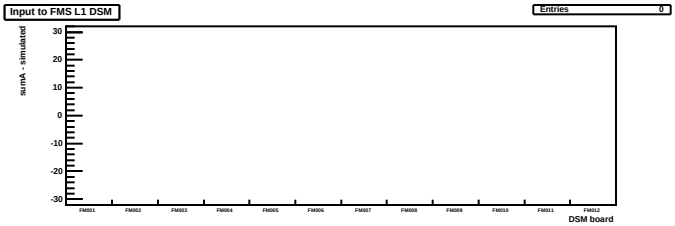

Input to FMS L1 DSM
Entries 474876
sumB
DSM board

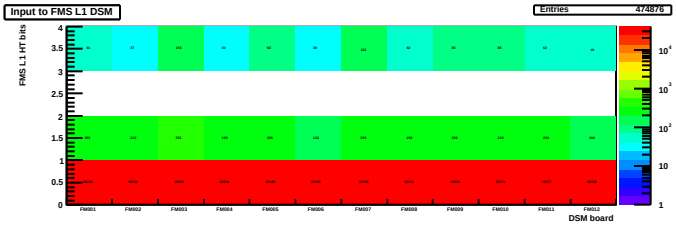

Input to FMS L1 DSM
Entries 1290
sumB - simulated
DSM board

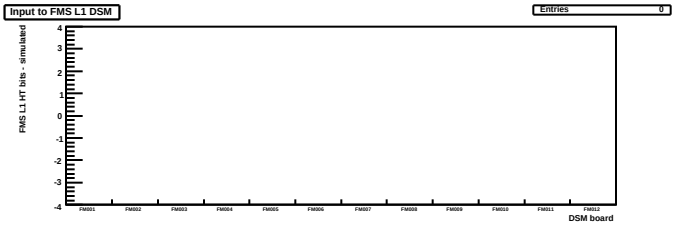

Input to FMS L1 DSM
Entries 474876
sumAB
DSM board

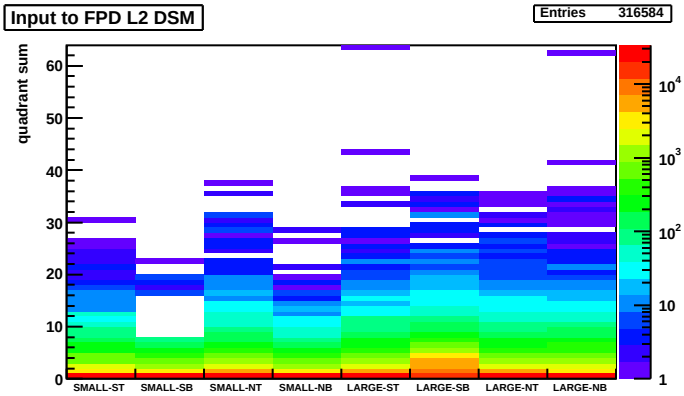

Input to FMS L1 DSM
Entries 911
sumAB - simulated
DSM board

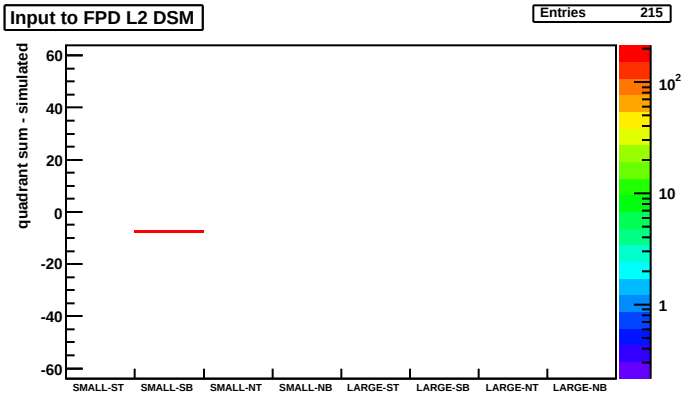

Input to FMS L1 DSM
Entries 474876
sumA
DSM board

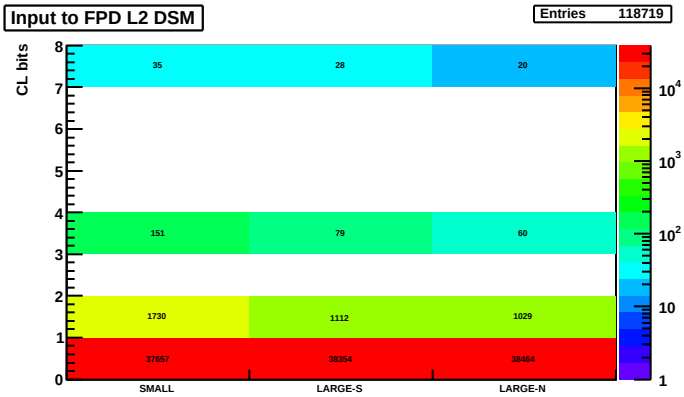

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

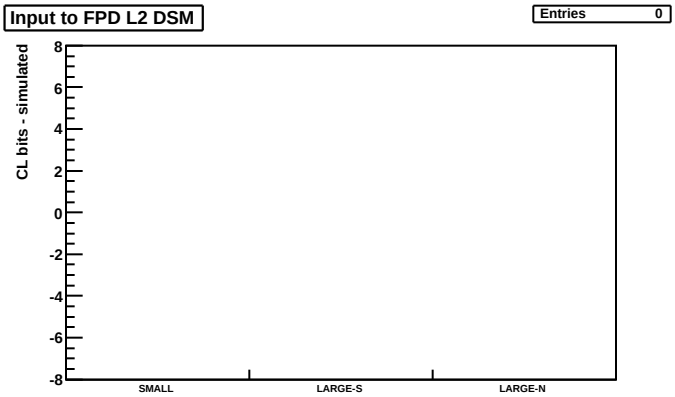

Input to FMS L1 DSM
Entries 474876
FMS L1 HT bits
DSM board

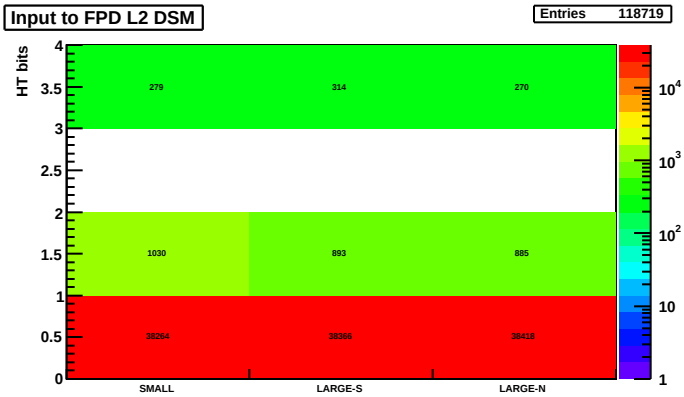

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

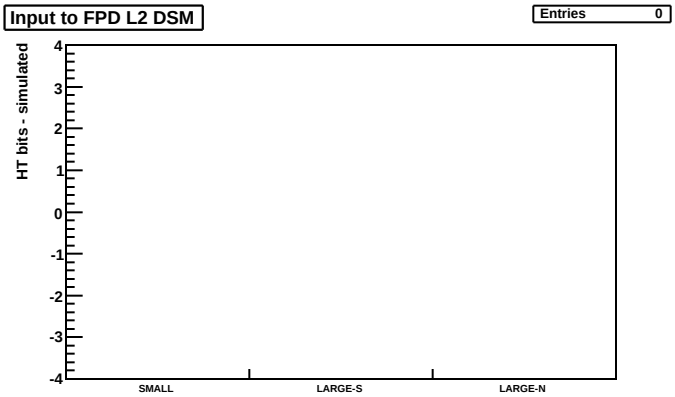

Input to FPD L2 DSM
Entries 316584
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

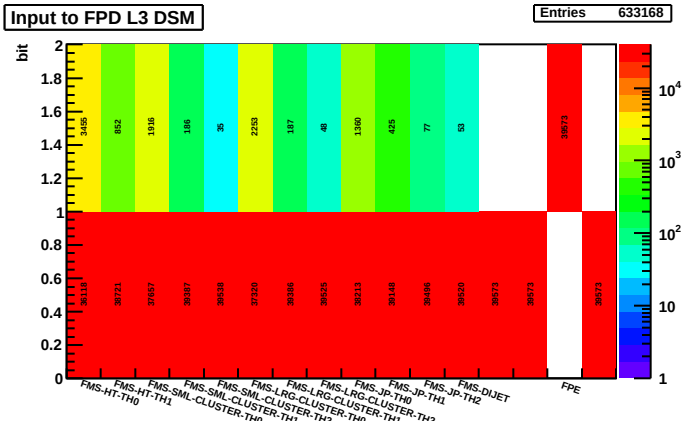

Input to FPD L2 DSM
Entries 215
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

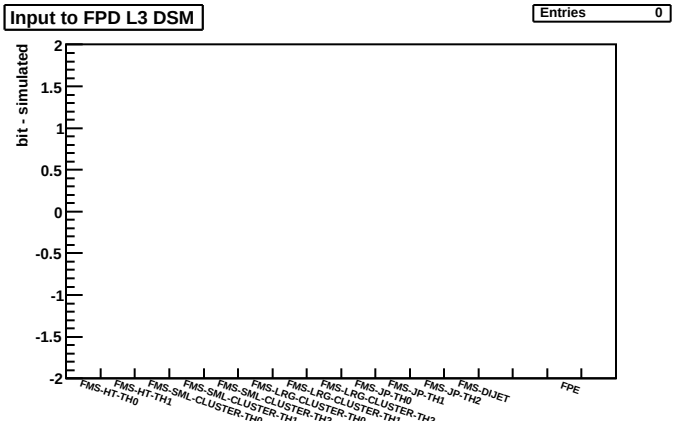

Input to FPD L2 DSM
Entries 118719
CL bits
35
28
20
151
79
60
1730
1112
1029
37657
38354
38464
SMALL
LARGE-S
LARGE-N

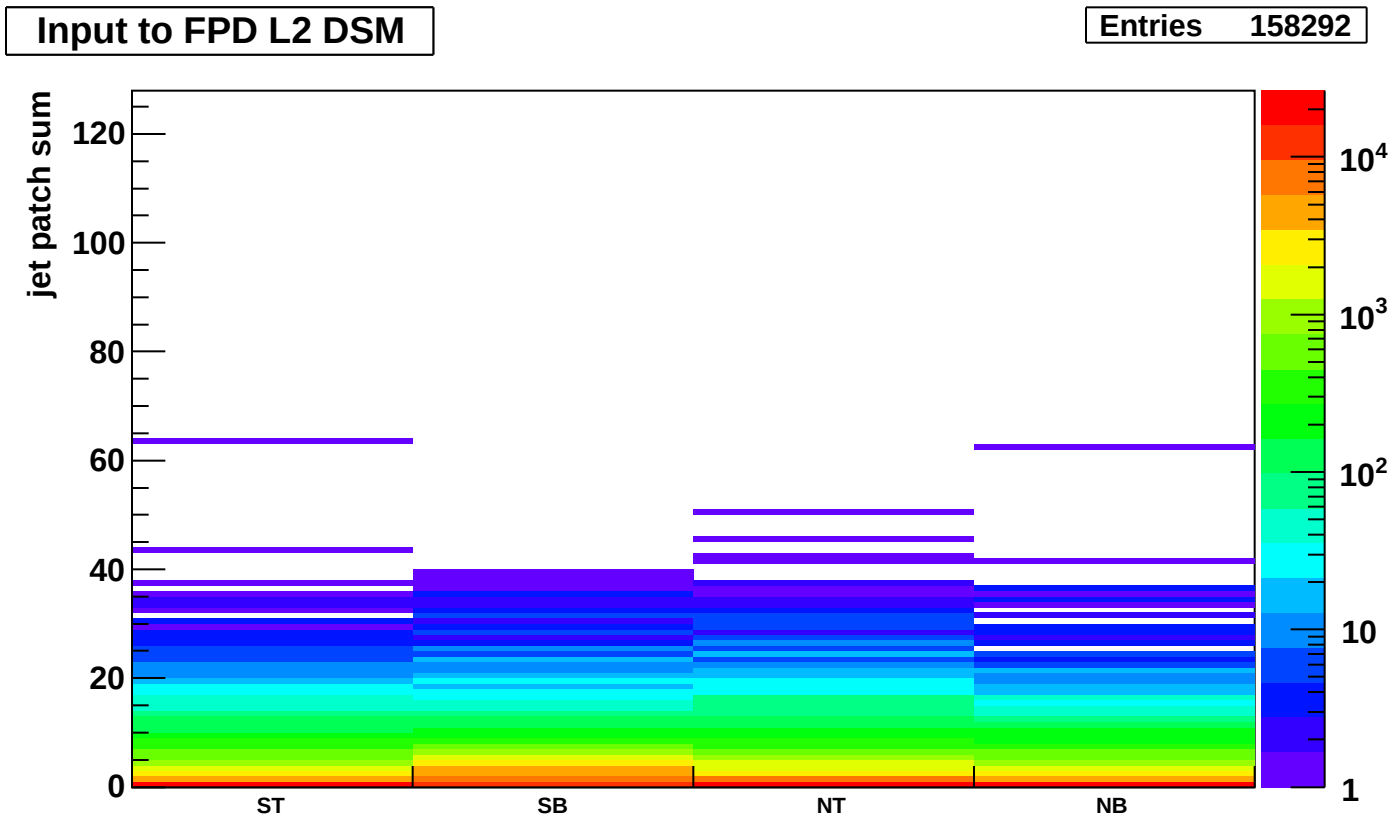

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

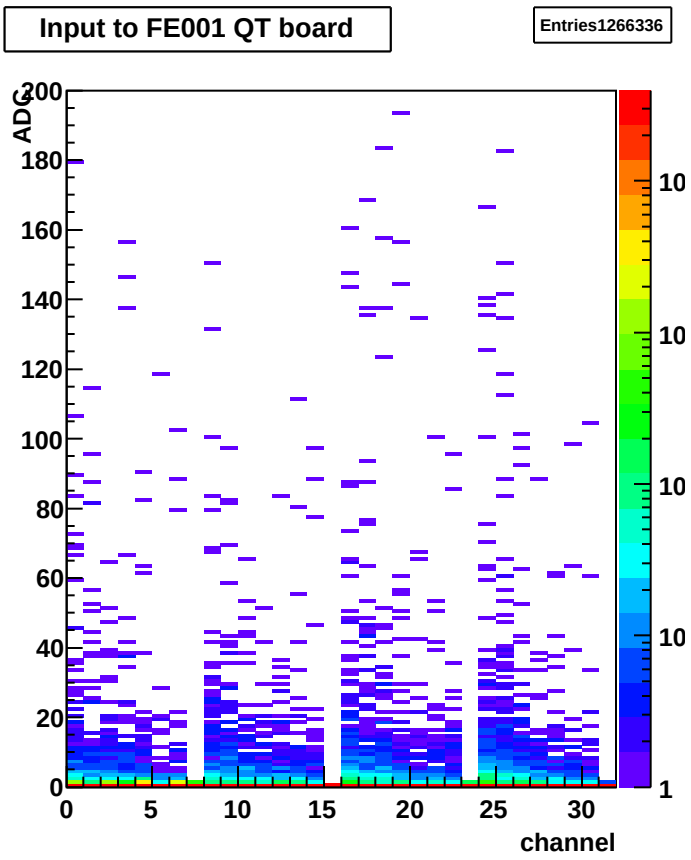

Input to FPD L2 DSM
Entries 118719
HT bits
279
314
270
1030
893
885
38264
38366
38418
SMALL
LARGE-S
LARGE-N

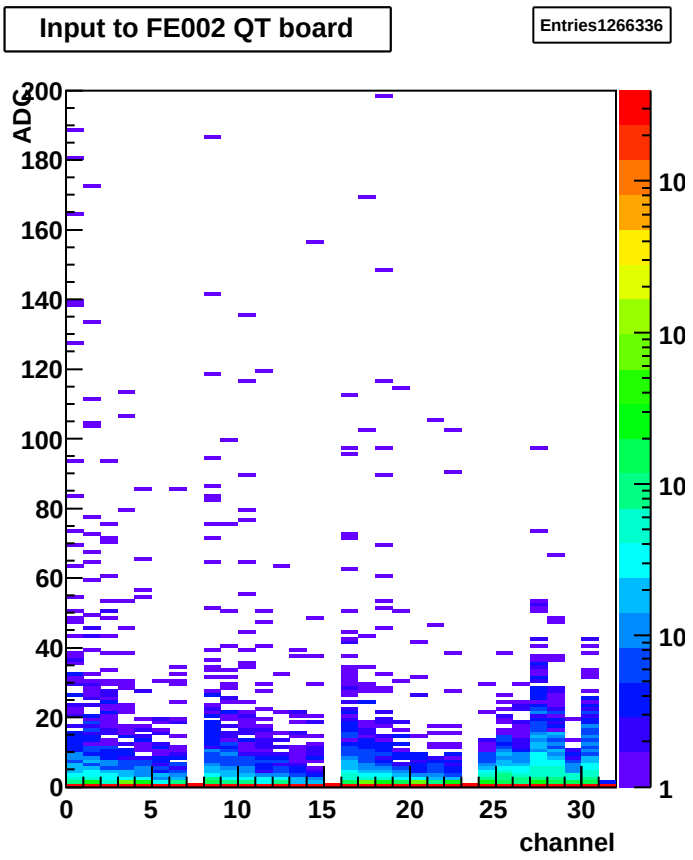

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

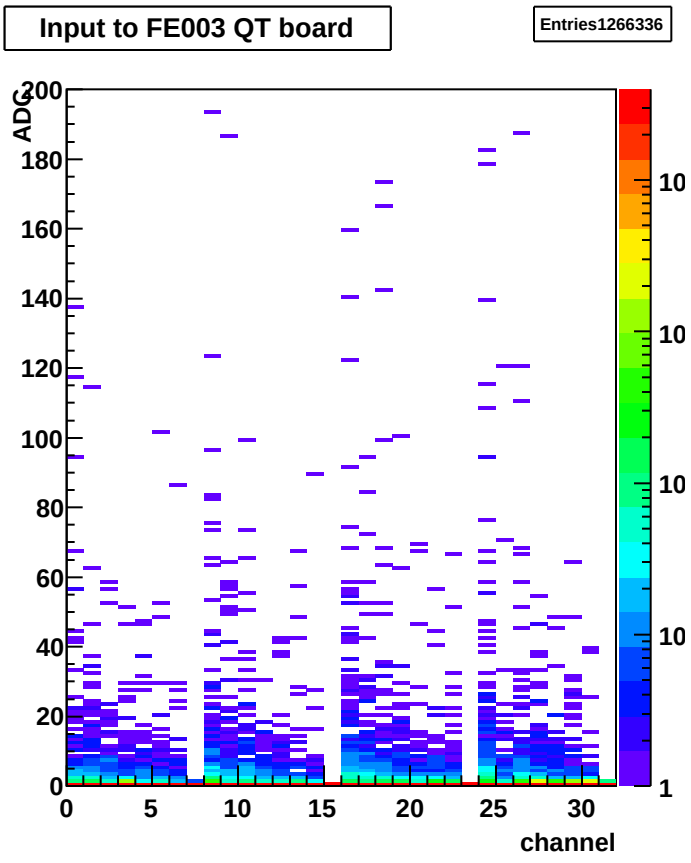

Input to FPD L3 DSM
Entries 633168
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

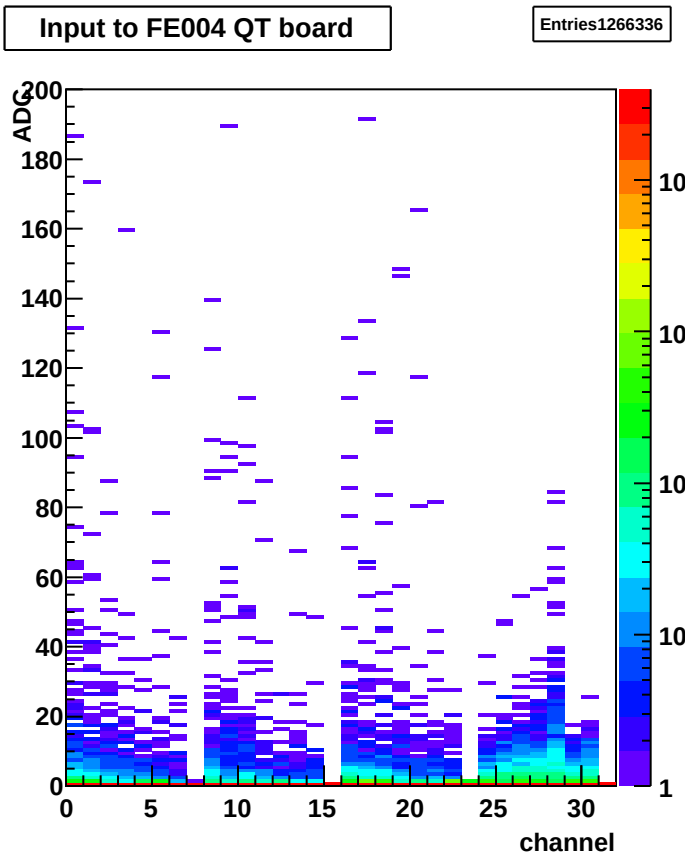

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

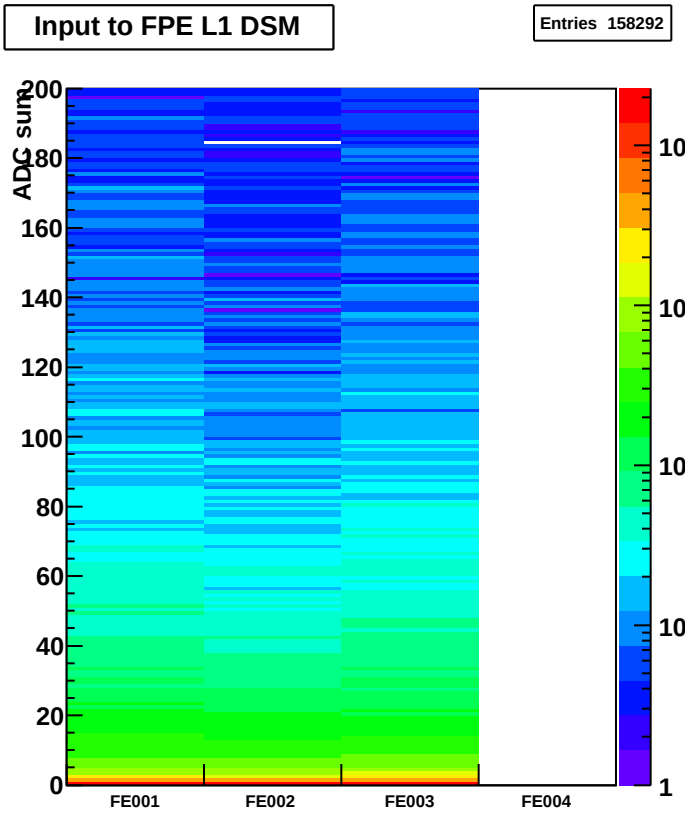

Input to FPD L2 DSM
Entries 158292
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

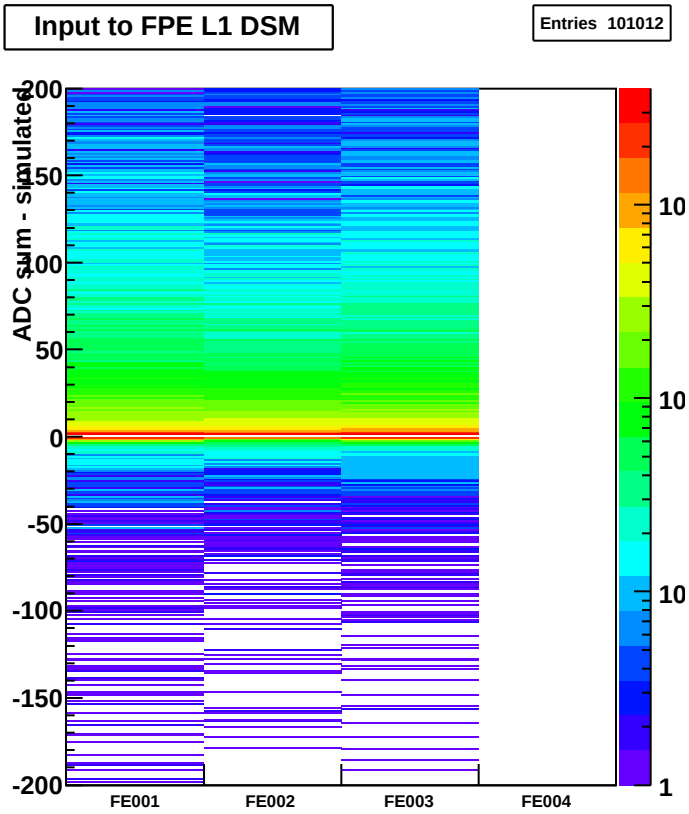

Input to FE001 QT board
Entries1266336
ADC
channel

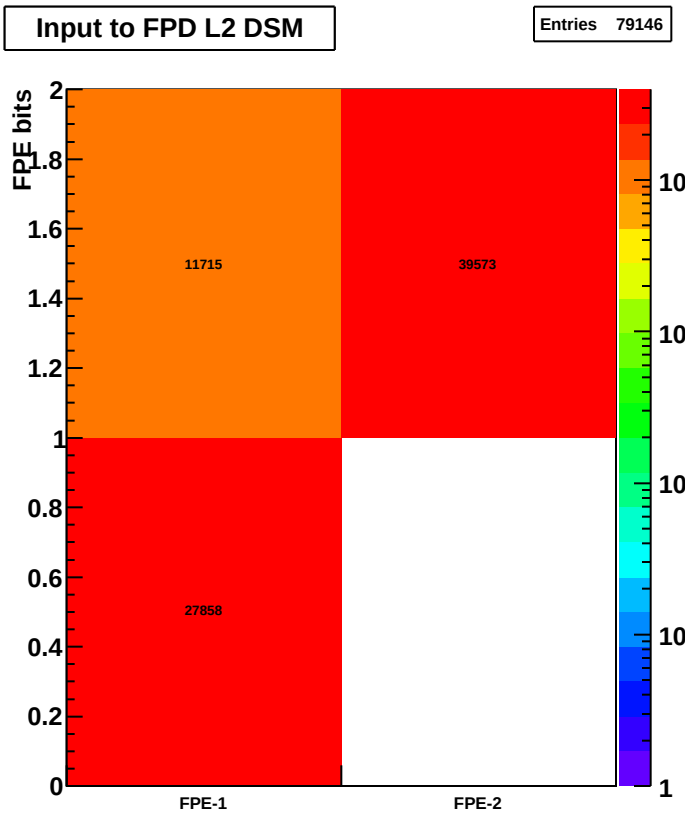

Input to FE002 QT board
Entries1266336
ADC
channel

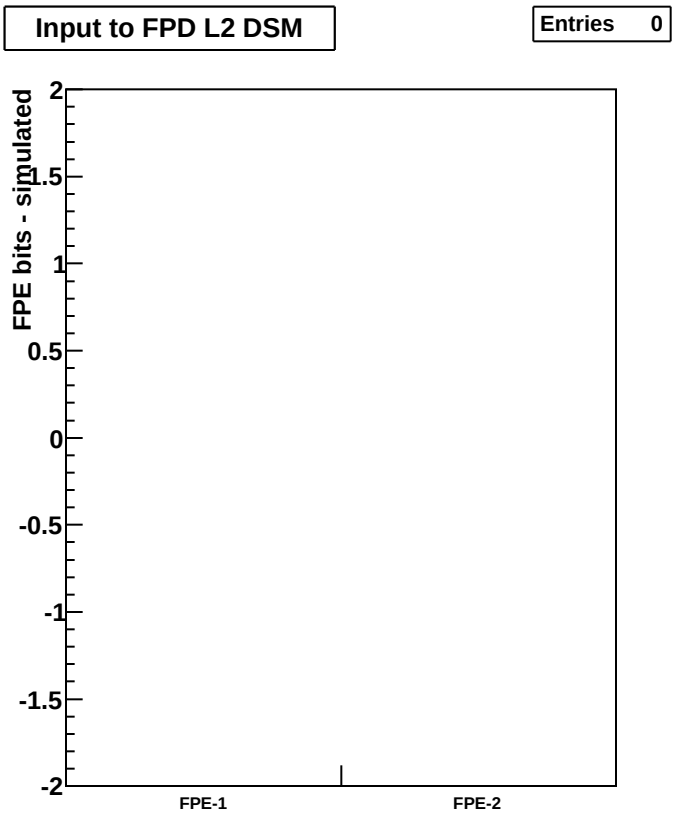

Input to FE003 QT board
Entries1266336
ADC
channel


Input to FE004 QT board
Entries1266336
ADC
channel


Input to FPE L1 DSM
Entries 158292
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 101012
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 79146
FPE bits
11715
39573
27858
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2