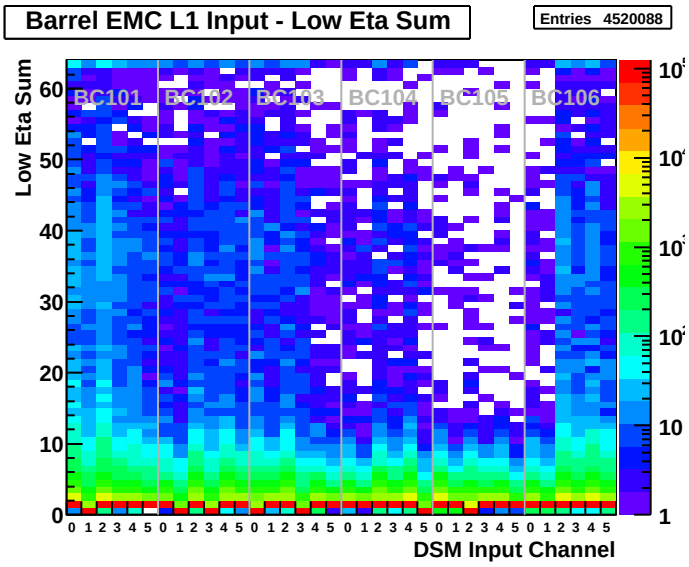
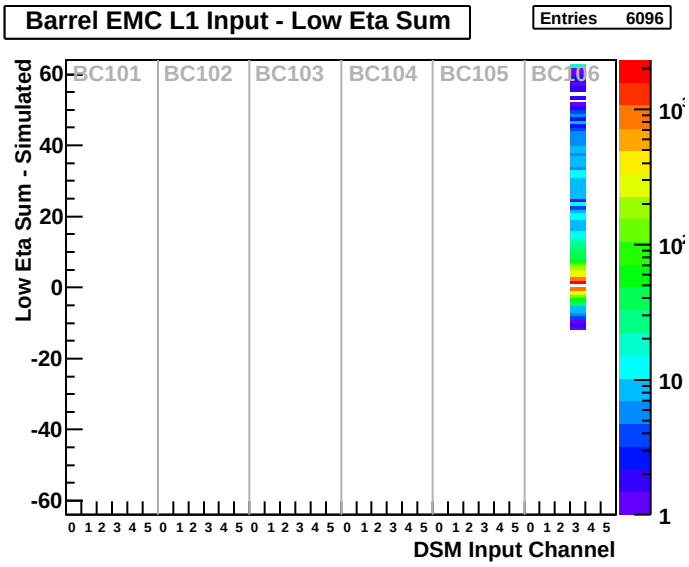
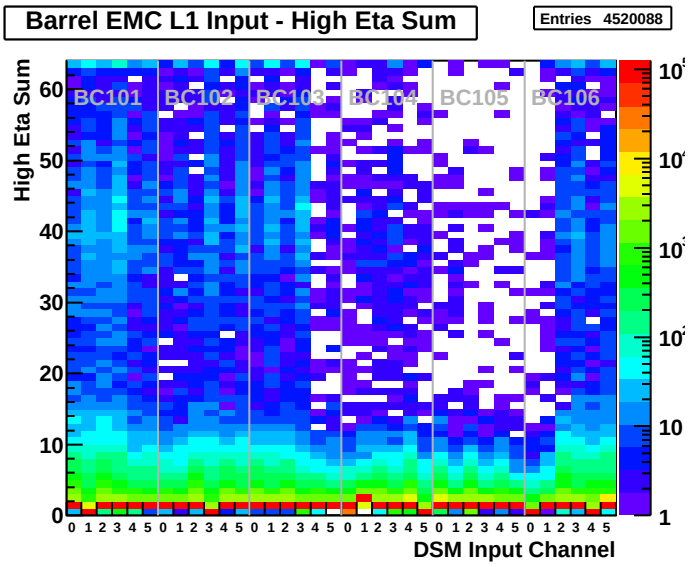
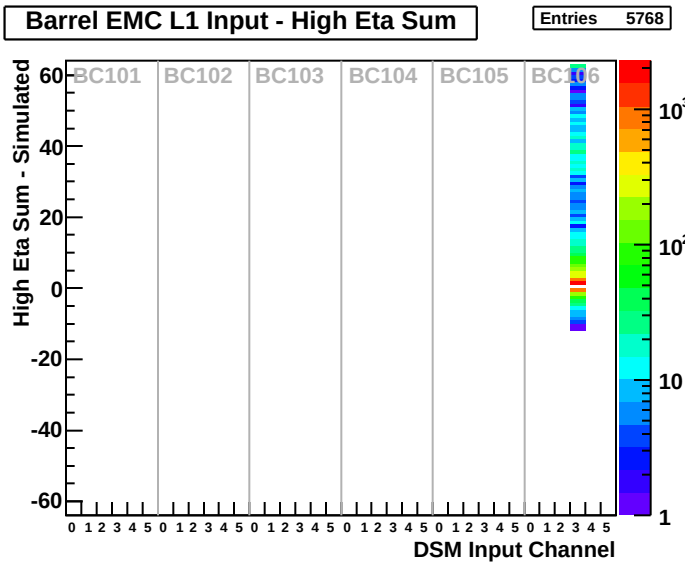
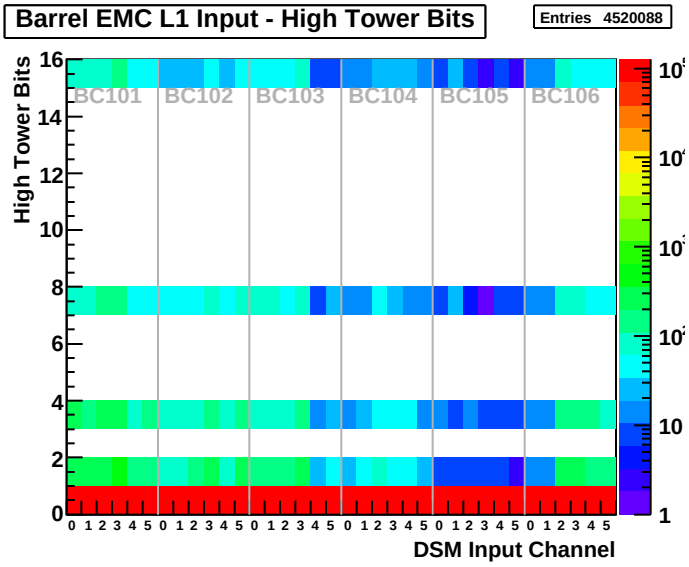
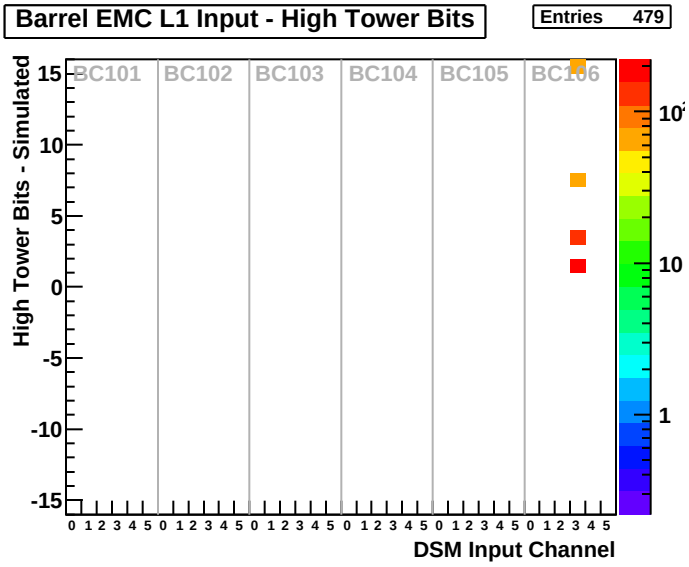

Barrel EMC L1 Input - Low Eta Sum
Entries 4520088
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

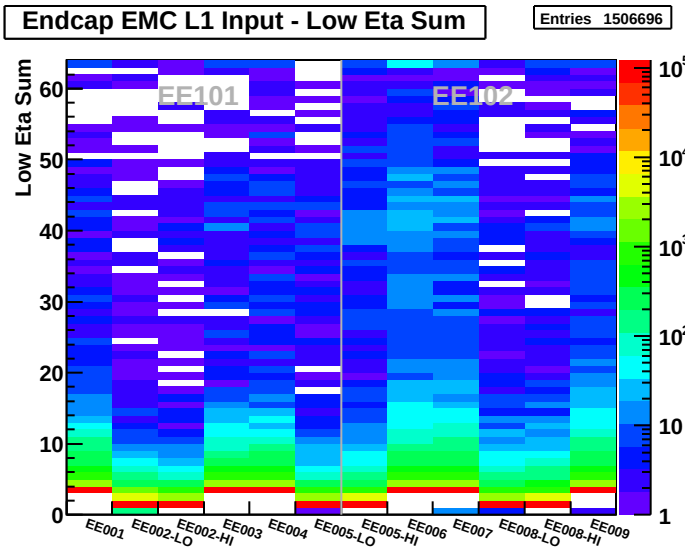

Barrel EMC L1 Input - Low Eta Sum
Entries 6096
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

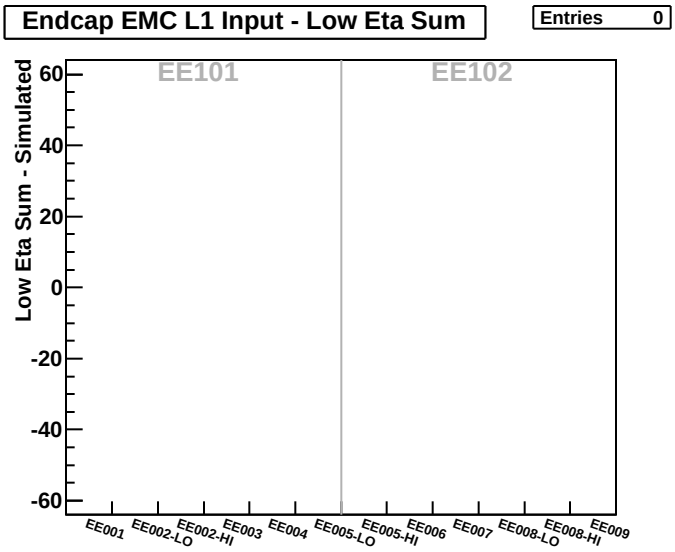

Barrel EMC L1 Input - High Eta Sum
Entries 4520088
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

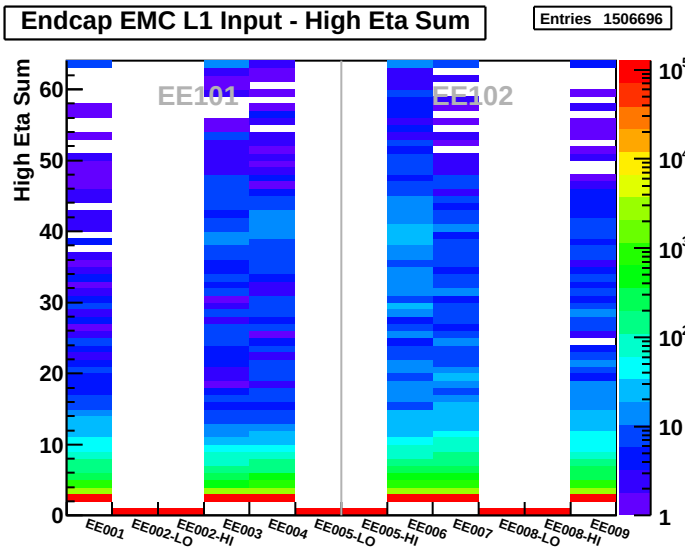

Barrel EMC L1 Input - High Eta Sum
Entries 5768
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

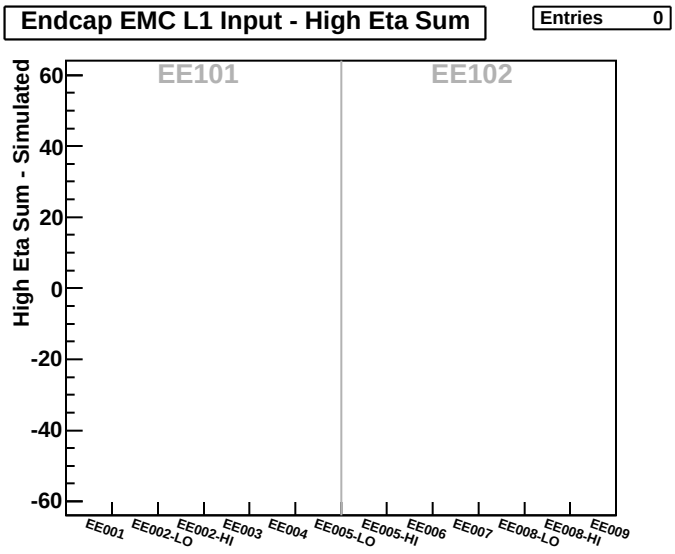

Barrel EMC L1 Input - High Tower Bits
Entries 4520088
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

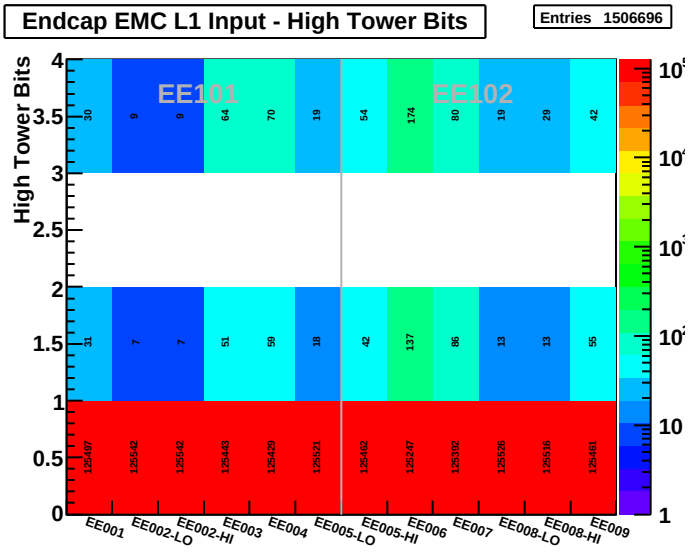

Barrel EMC L1 Input - High Tower Bits
Entries 479
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

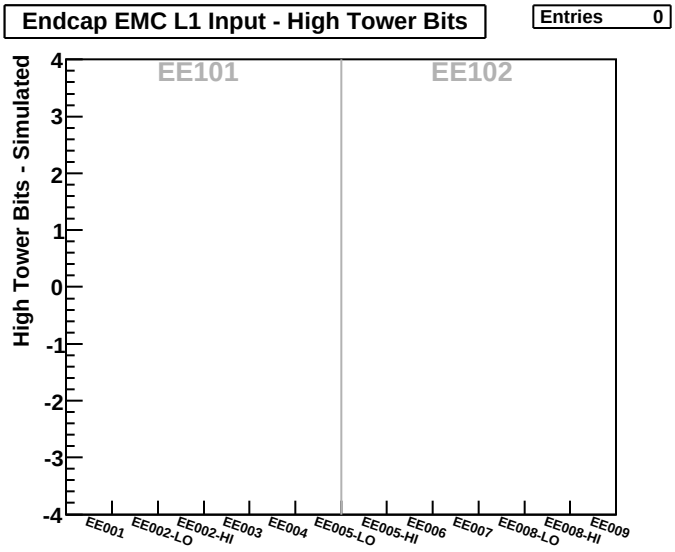

Endcap EMC L1 Input - Low Eta Sum
Entries 1506696
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

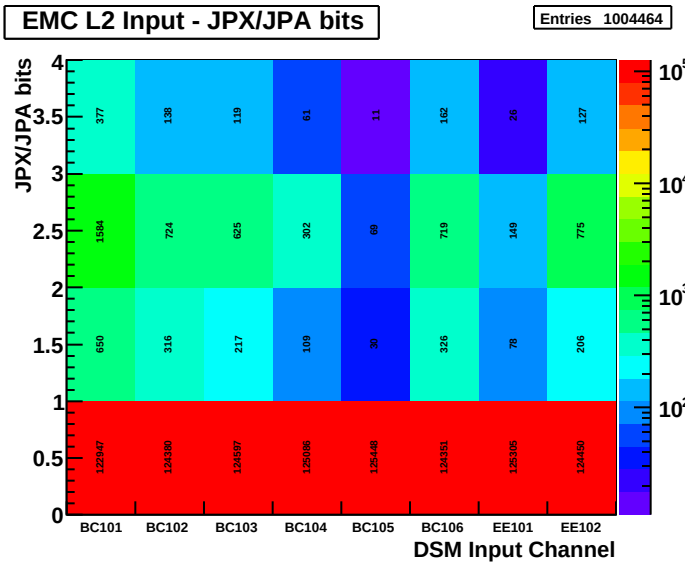

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

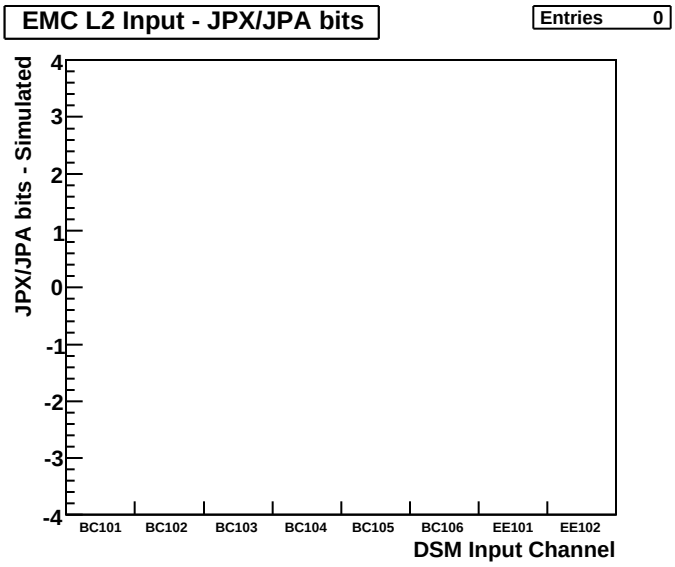

Endcap EMC L1 Input - High Eta Sum
Entries 1506696
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

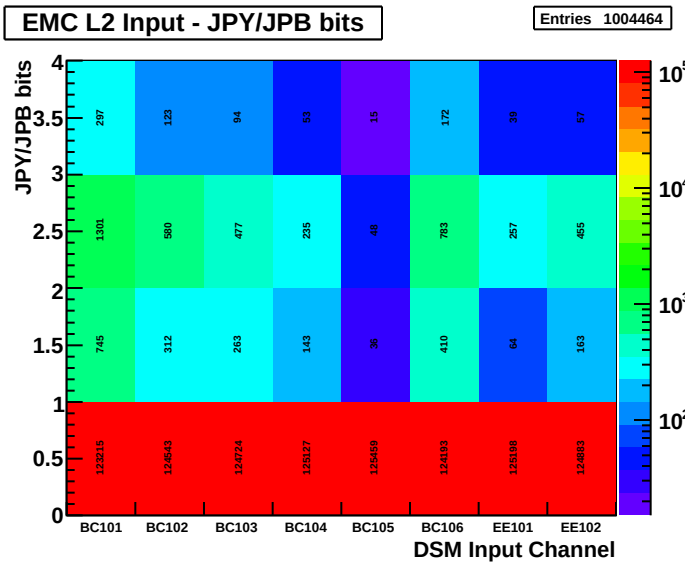

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

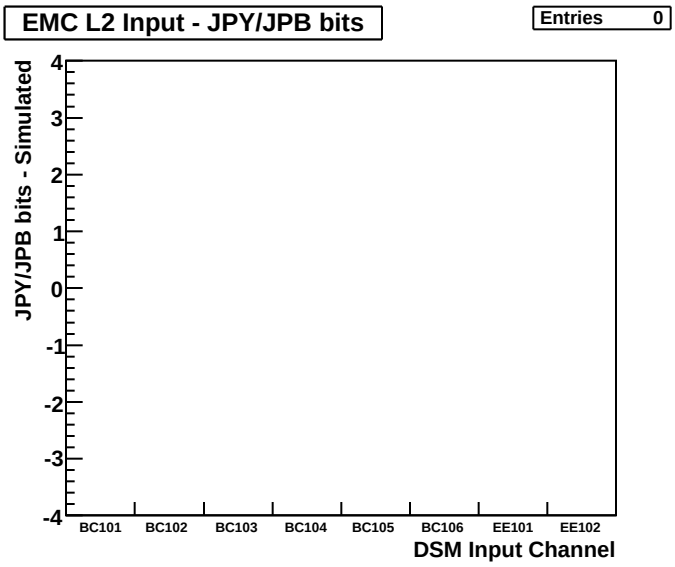

Endcap EMC L1 Input - High Tower Bits
Entries 1506696
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

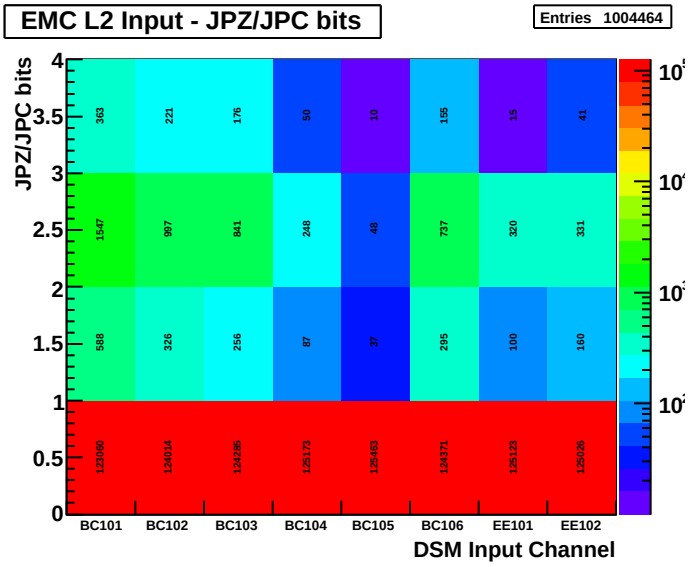

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

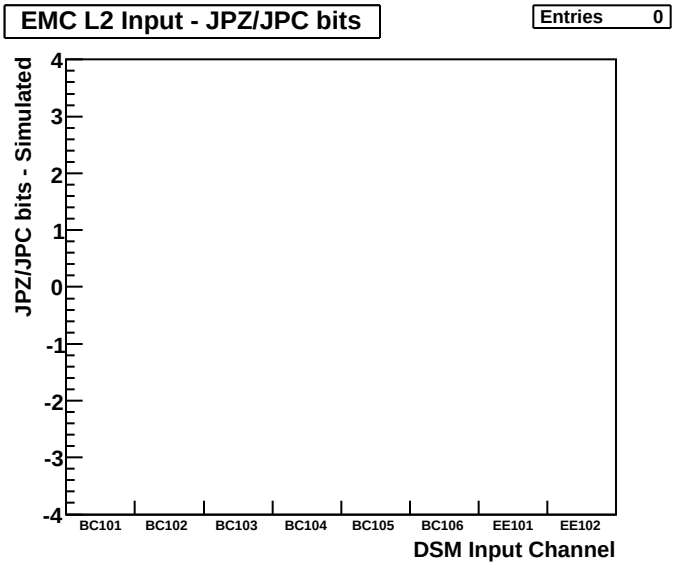

EMC L2 Input - JPX/JPA bits
Entries 1004464
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
377 138 119 61 11 162 26 127
1584 724 625 302 69 719 149 775
650 316 217 109 30 326 78 206
122947 124380 124597 125086 125448 124351 125305 124450

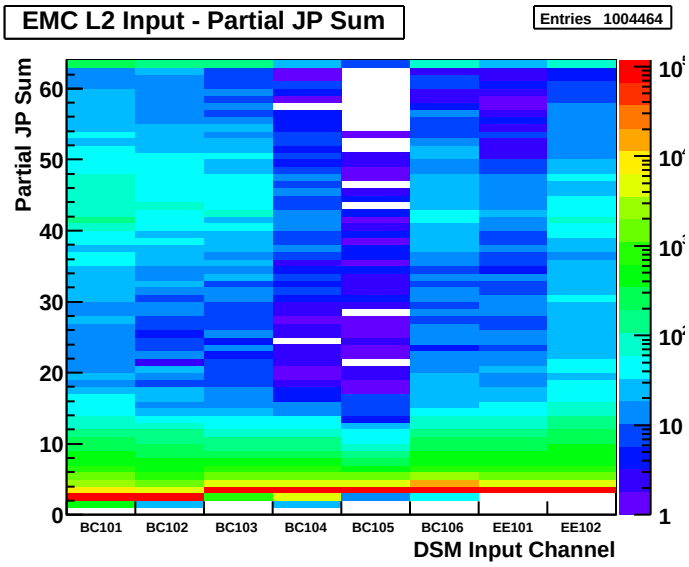

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

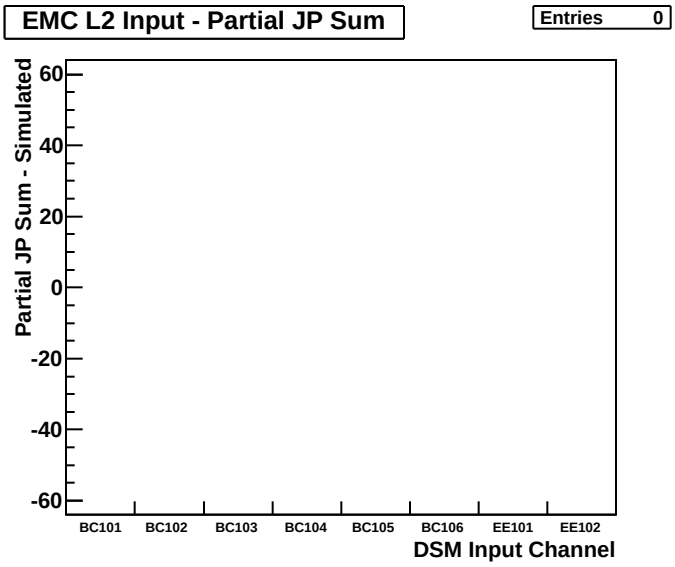

EMC L2 Input - JPY/JPB bits
Entries 1004464
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
297 123 94 53 15 172 39 57
1301 580 477 235 48 783 257 455
745 312 263 143 36 410 64 163
123215 124543 124724 125127 125459 124193 125198 124883

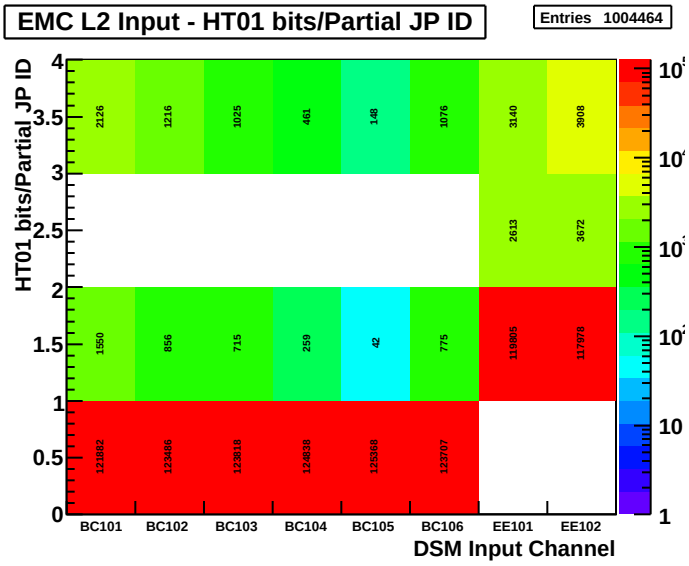

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

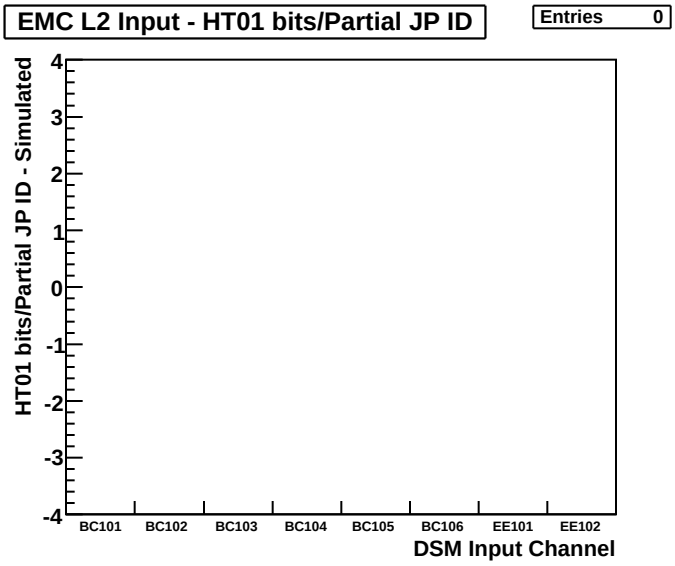

EMC L2 Input - JPZ/JPC bits
Entries 1004464
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
363 221 176 50 10 155 15 41
1547 997 841 248 48 737 320 331
588 326 256 87 37 295 100 160
123060 124014 124285 125173 125463 124371 125123 125035

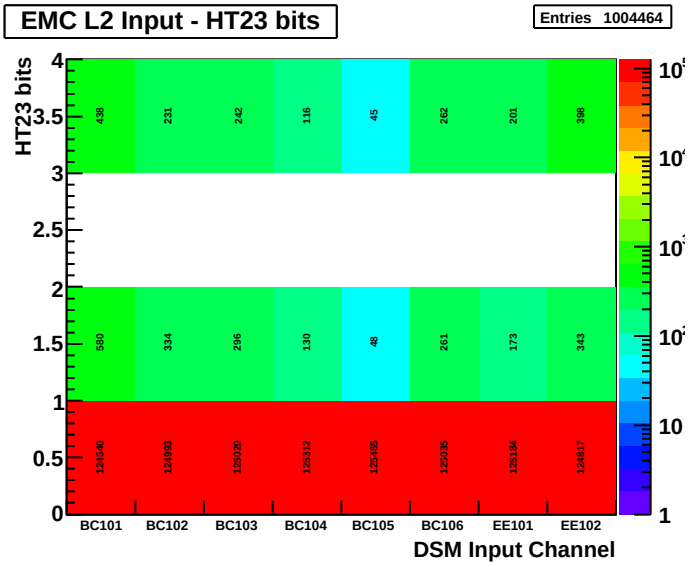

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

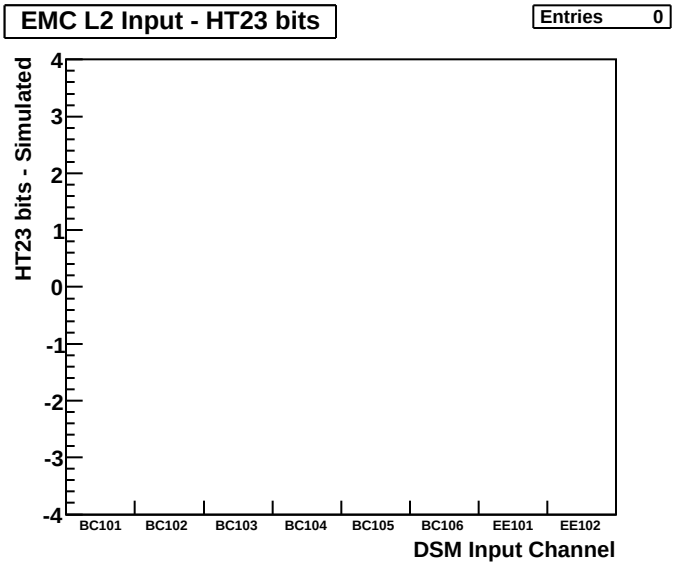

EMC L2 Input - Partial JP Sum
Entries 1004464
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

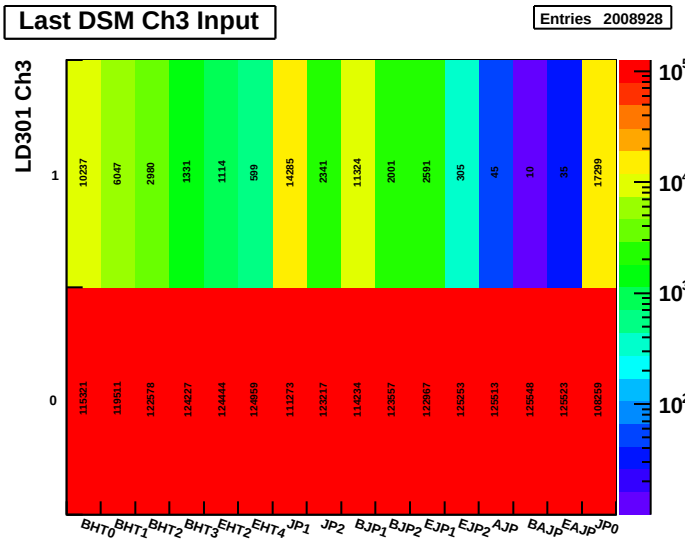

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

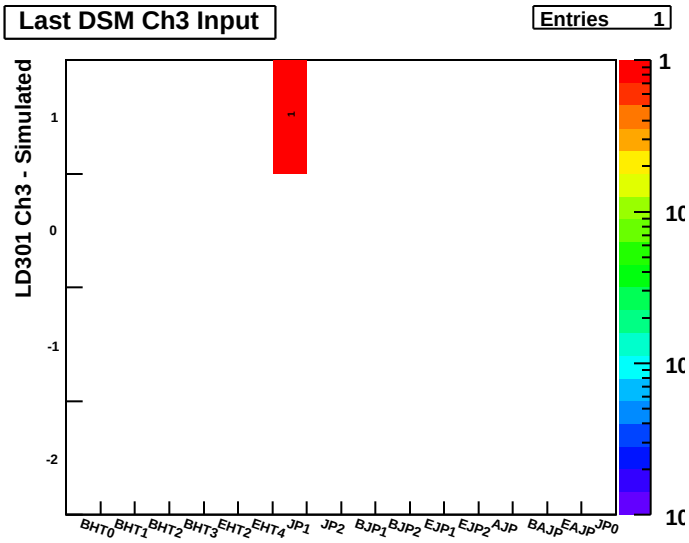

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1004464
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

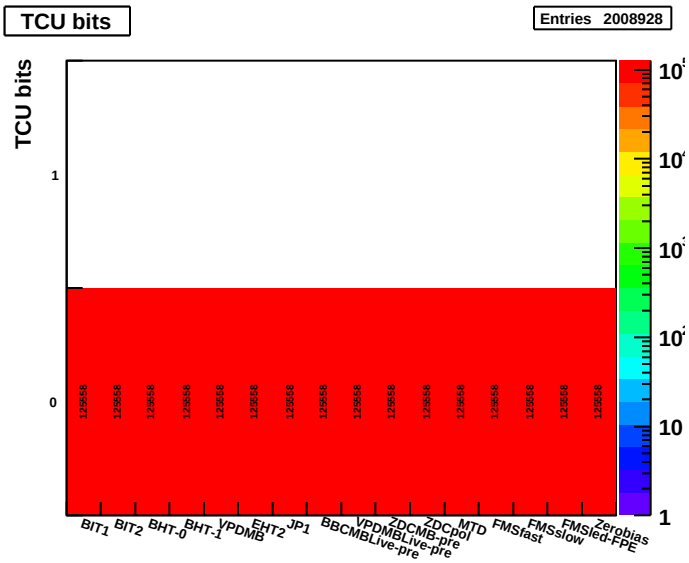

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

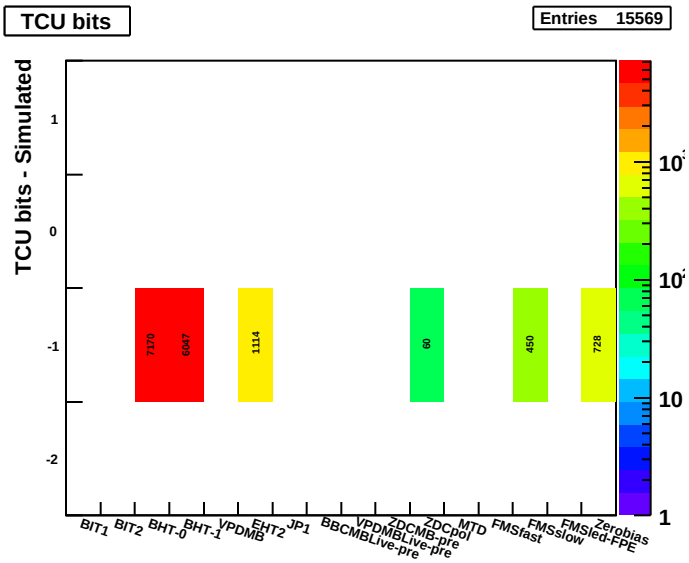

EMC L2 Input - HT23 bits
Entries 1004464
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

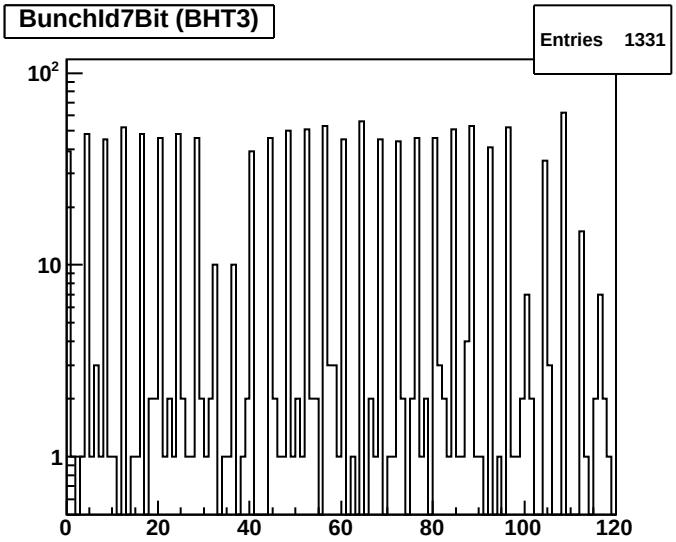

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

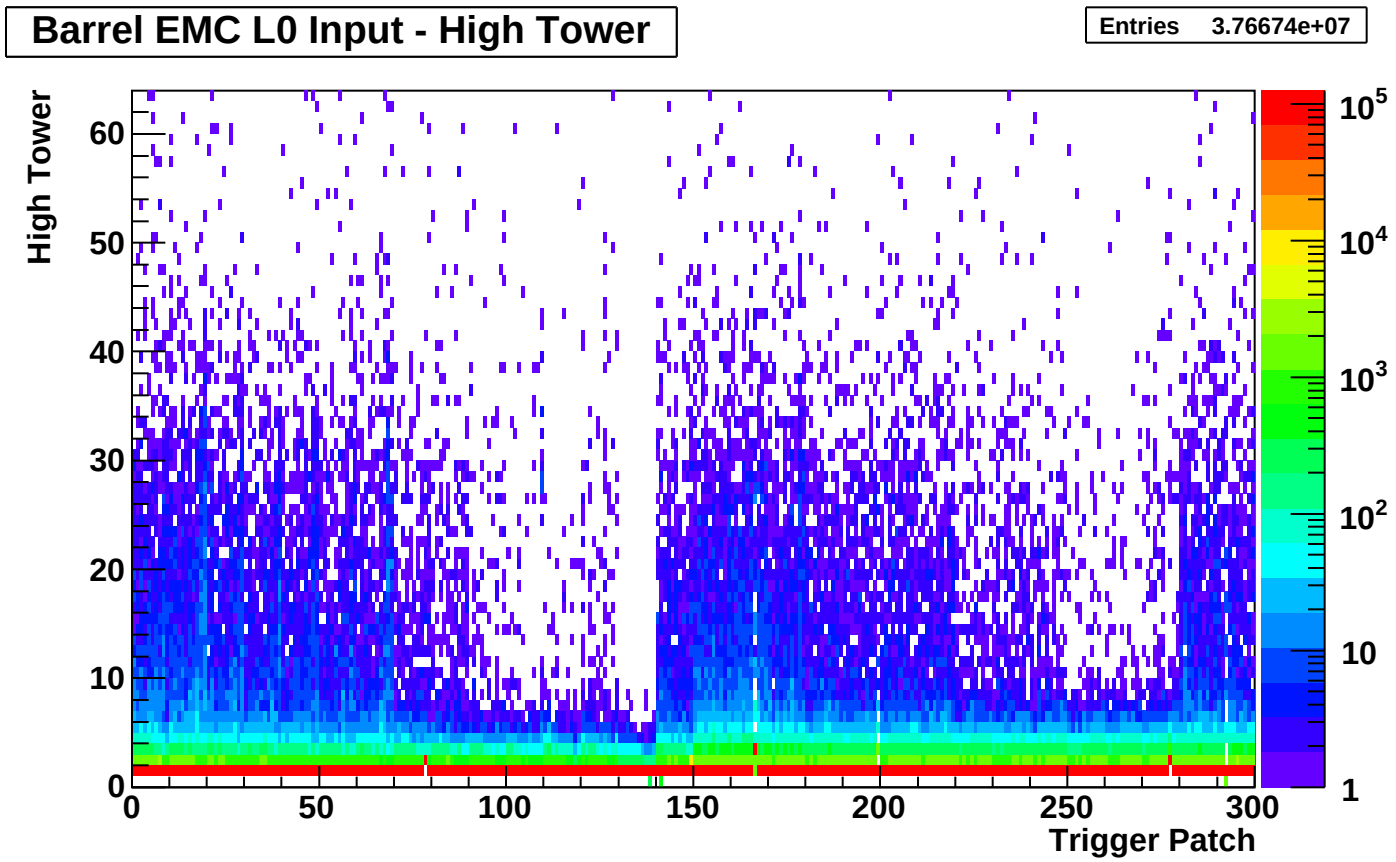

Last DSM Ch3 Input
Entries 2008928
LD301 Ch3
10237 6047 2980 1331 1114 599 14285 2341 11324 2001 2591 305 45 10 35 17299
115321 119511 122578 124227 124444 124959 111273 123217 114234 123557 122967 125253 125513 125548 125523 108259
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

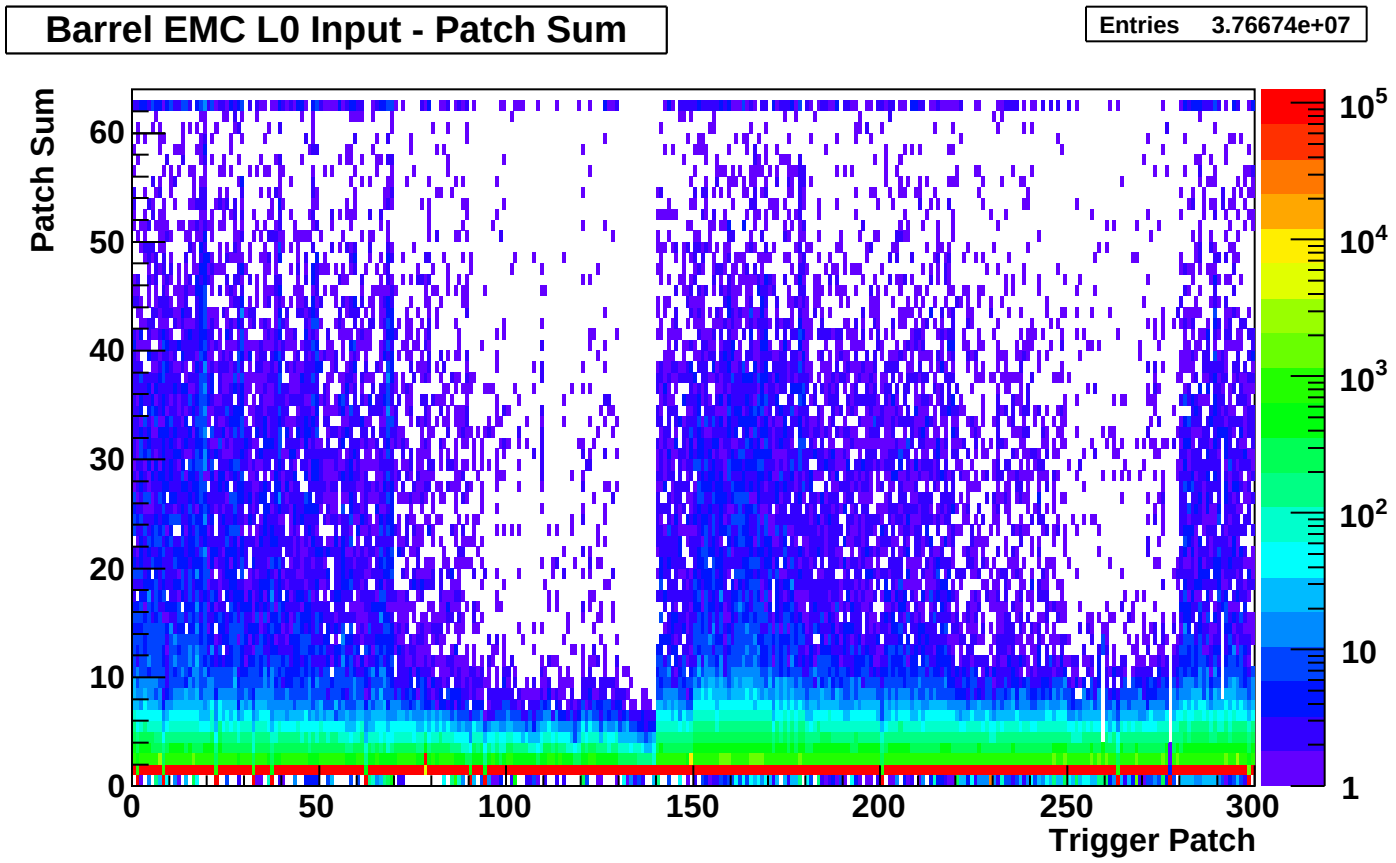

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

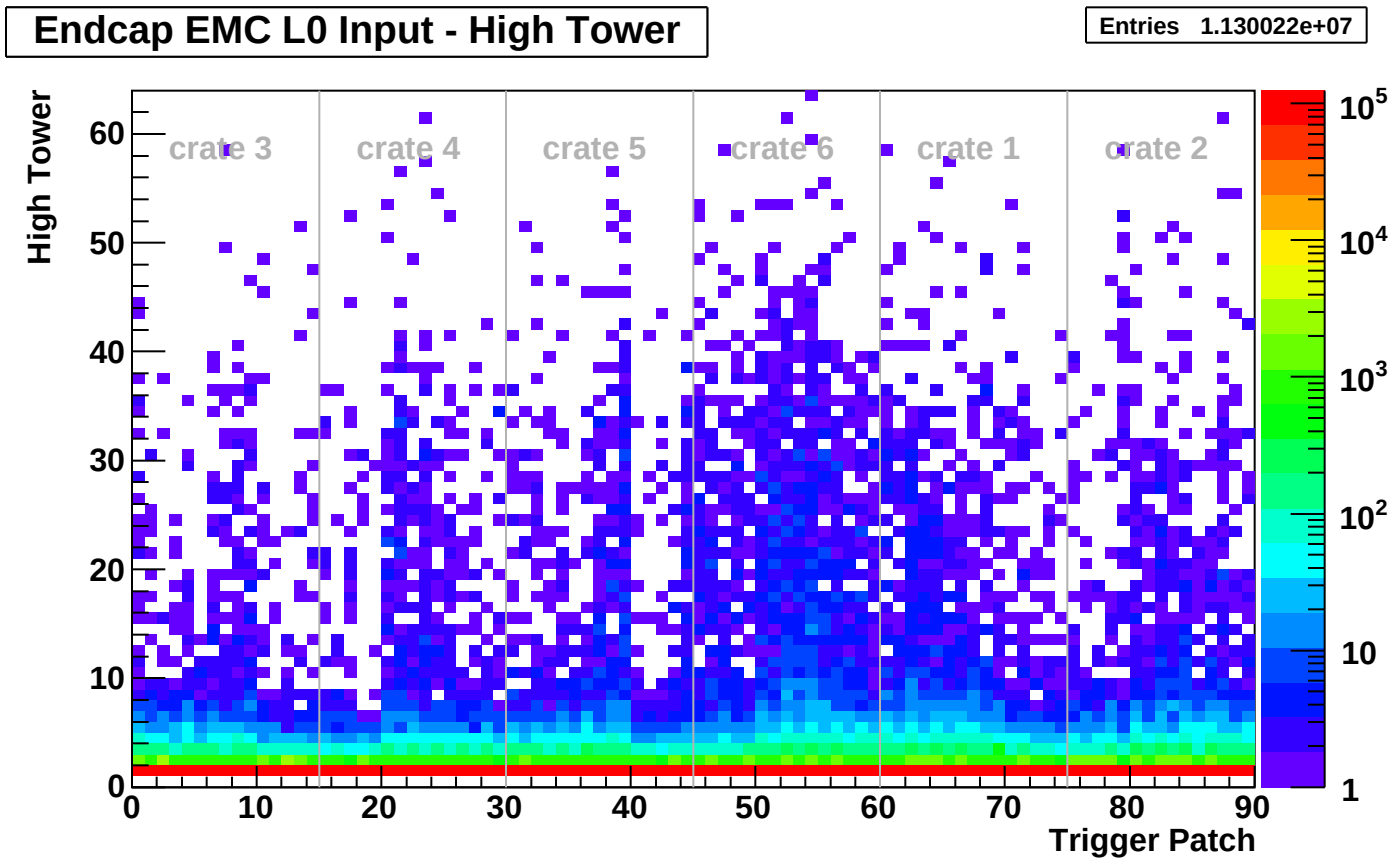

TCU bits
Entries 2008928
TCU bits
125558
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

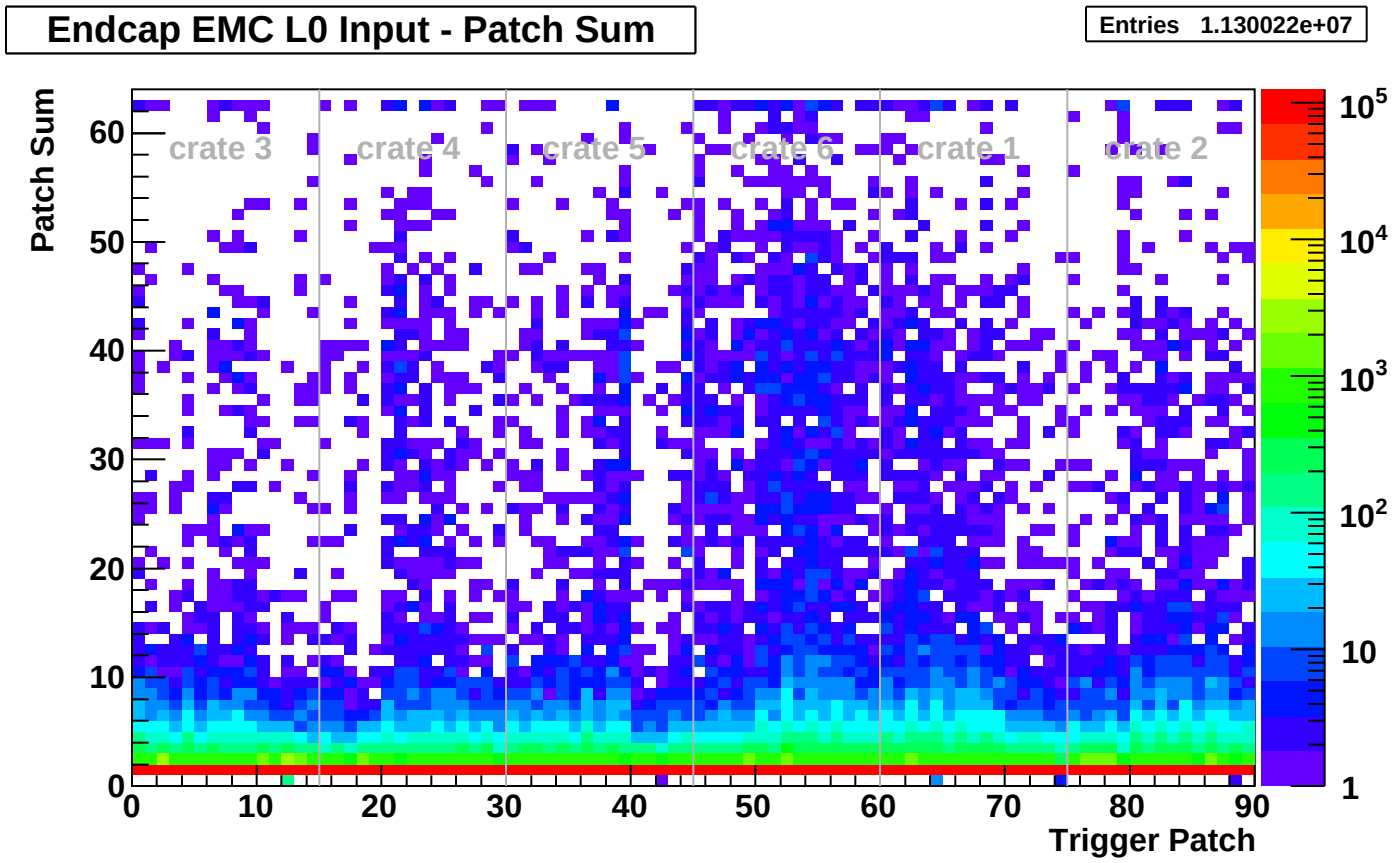

TCU bits
Entries 15569
TCU bits - Simulated
7370 6047 1114 60 450 728
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

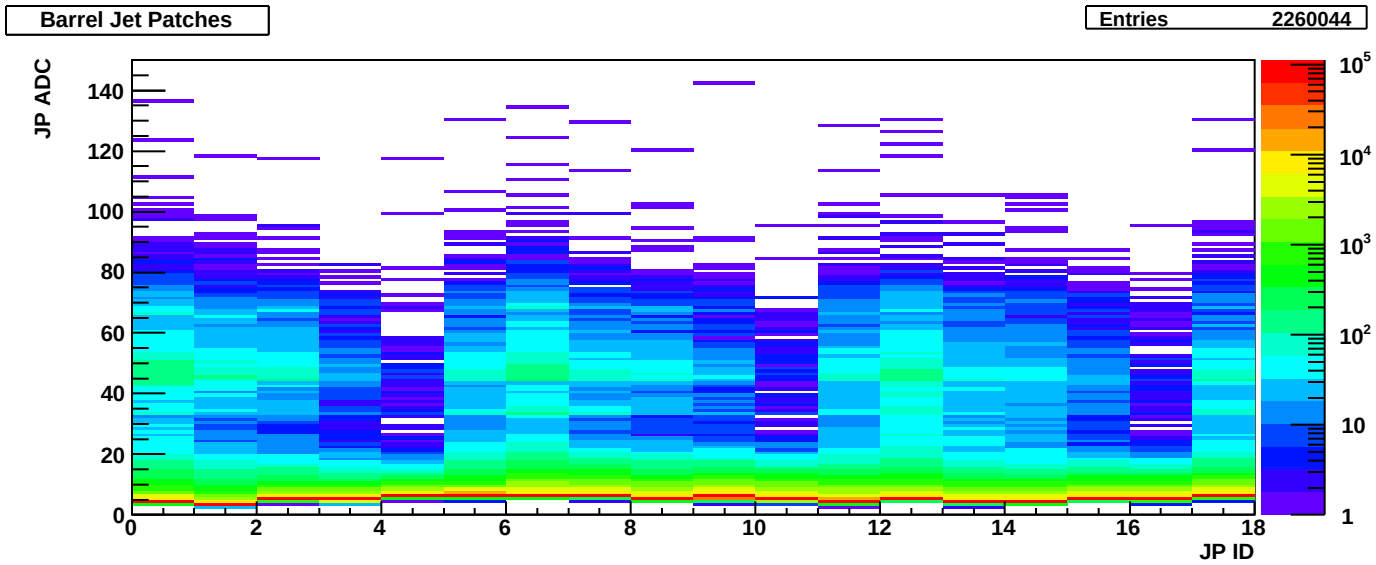

BunchId7Bit (BHT3)
Entries 1331
0 20 40 60 80 100 120

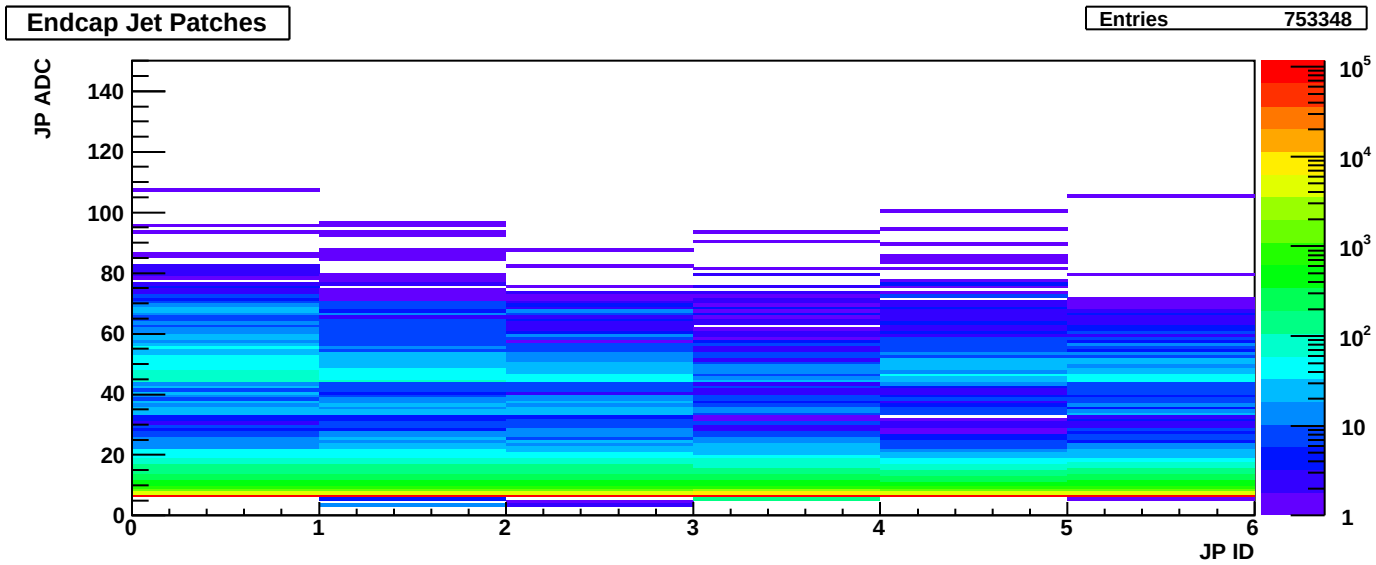

Barrel EMC L0 Input - High Tower
Entries 3.76674e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

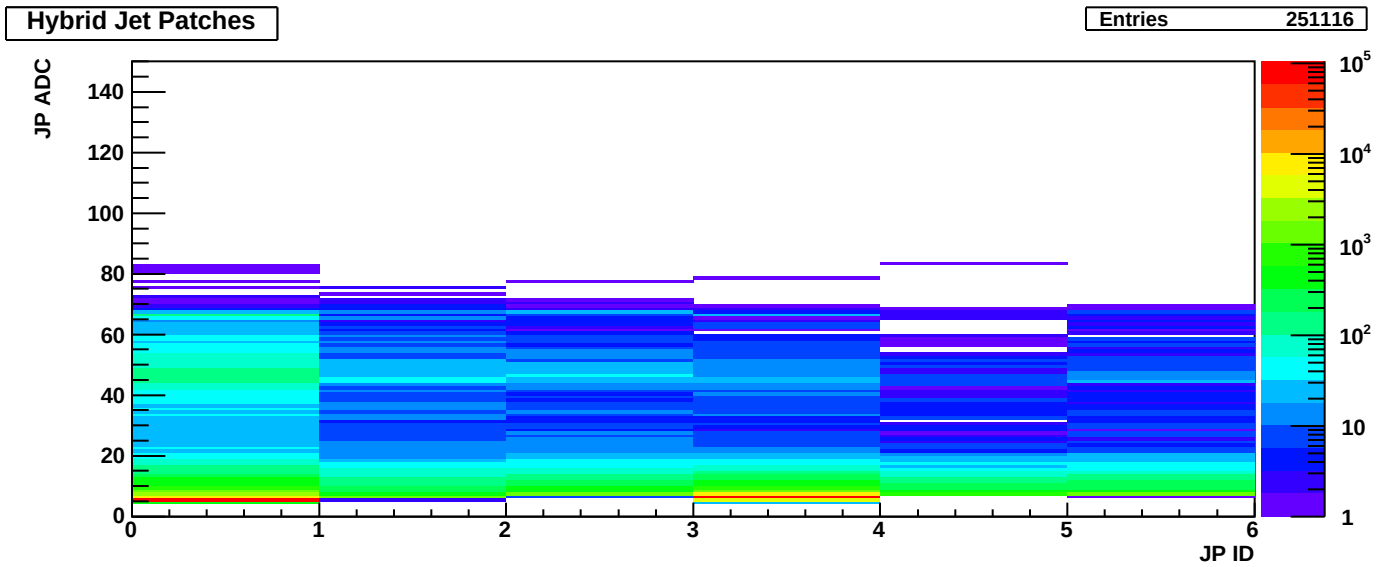

Barrel EMC L0 Input - Patch Sum
Entries 3.76674e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

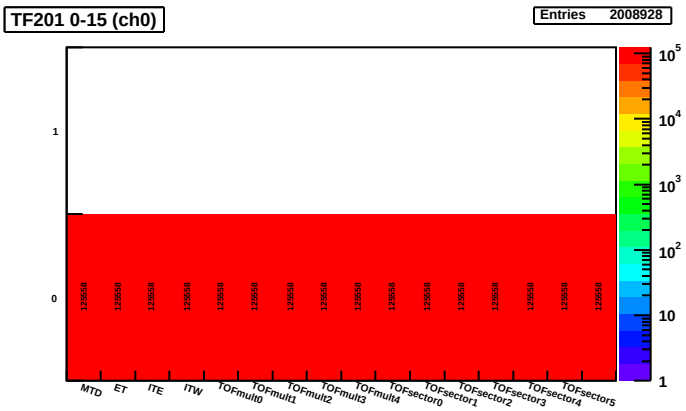

Endcap EMC L0 Input - High Tower
Entries 1.130022e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

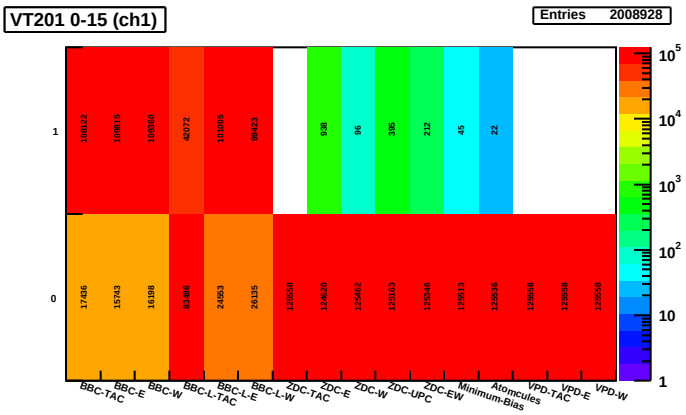

Endcap EMC L0 Input - Patch Sum
Entries 1.130022e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

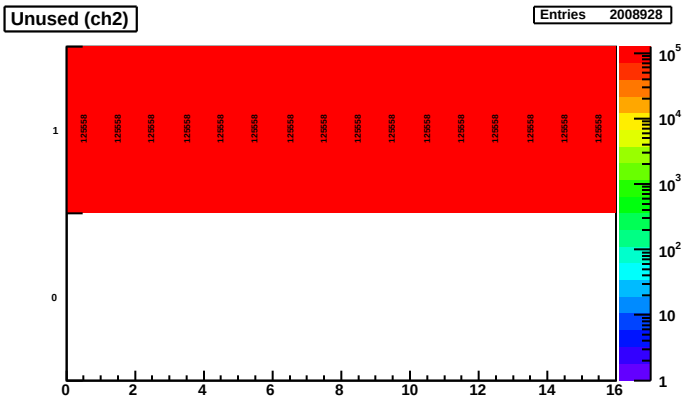

Barrel Jet Patches
Entries 2260044
JP ADC
JP ID

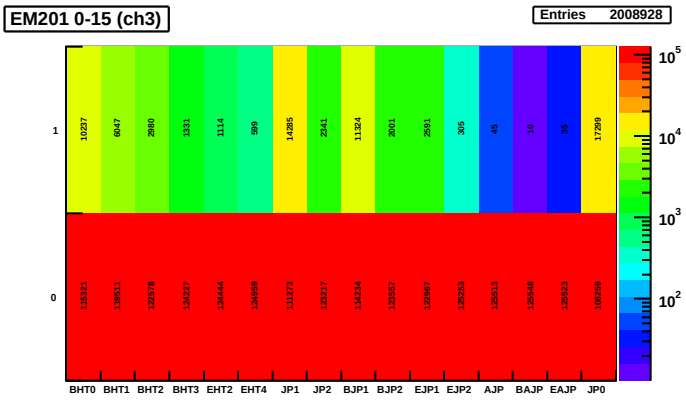

Endcap Jet Patches
Entries 753348
JP ADC
JP ID

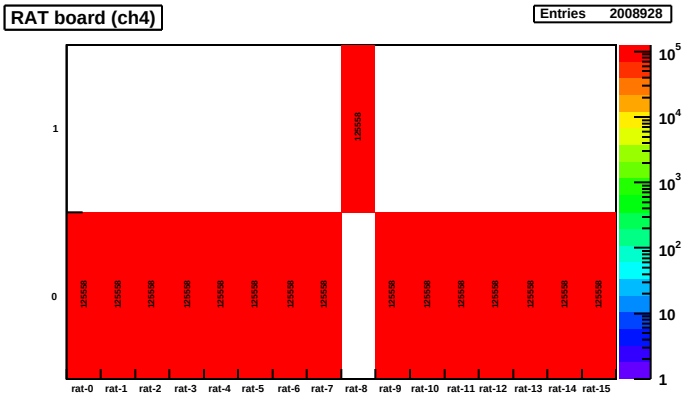

Hybrid Jet Patches
Entries 251116
JP ADC
JP ID

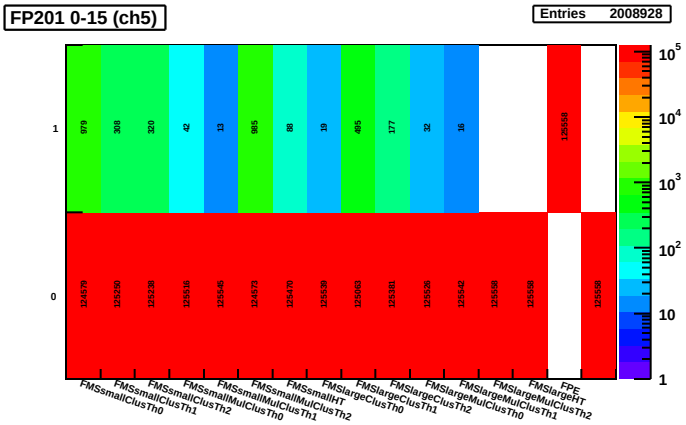

TF201 0-15 (ch0)
Entries 2008928
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

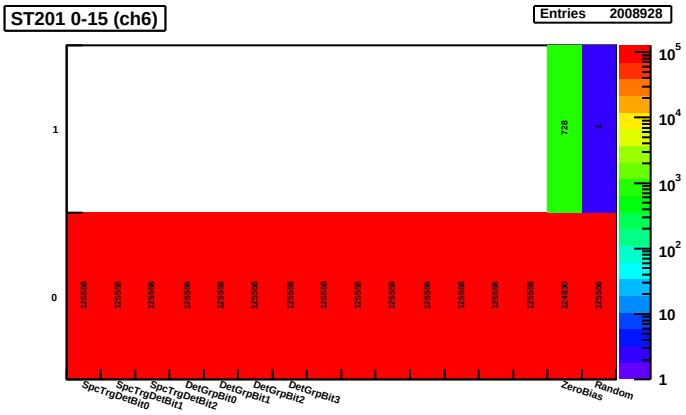

VT201 0-15 (ch1)
Entries 2008928
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

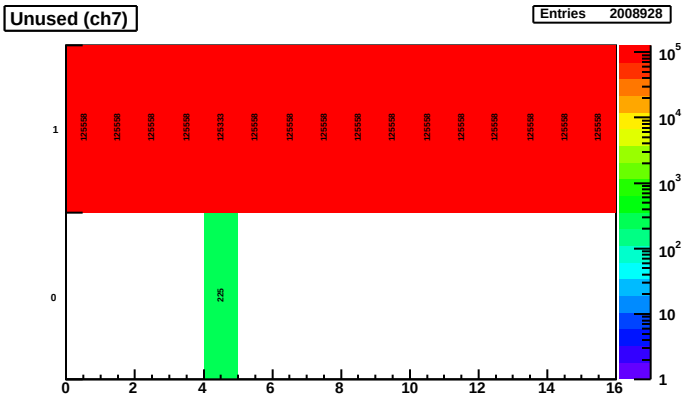

Unused (ch2)
Entries 2008928

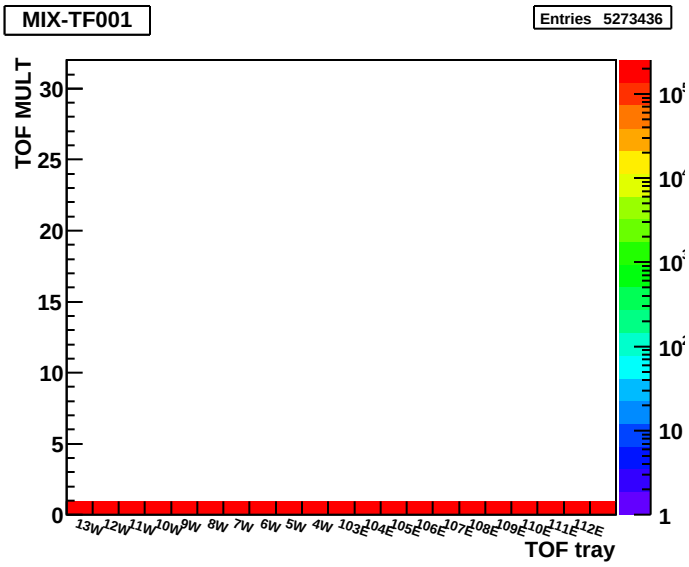

EM201 0-15 (ch3)
Entries 2008928
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

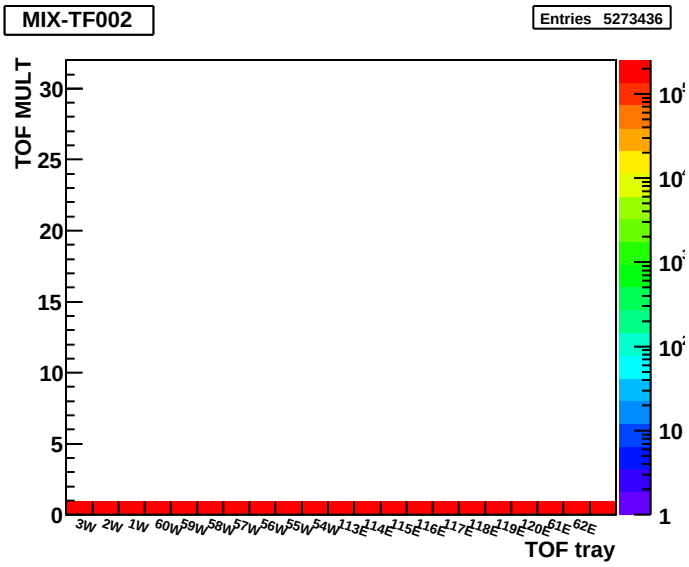

RAT board (ch4)
Entries 2008928
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

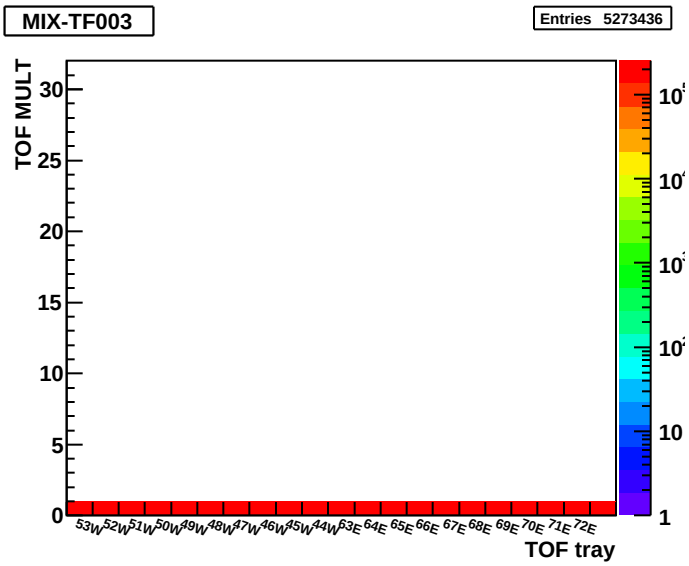

FP201 0-15 (ch5)
Entries 2008928
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

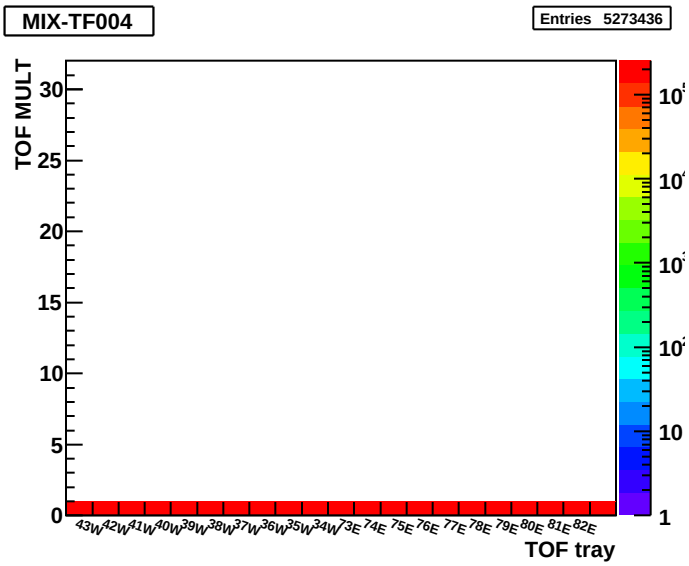

ST201 0-15 (ch6)
Entries 2008928
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

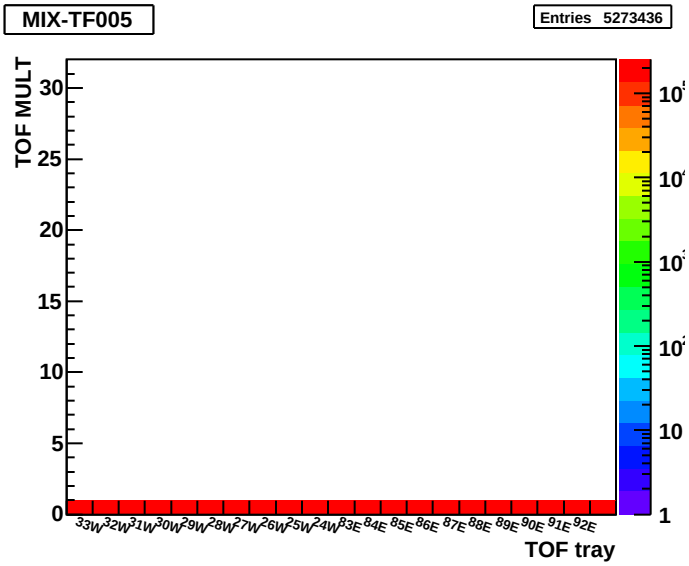

Unused (ch7)
Entries 2008928

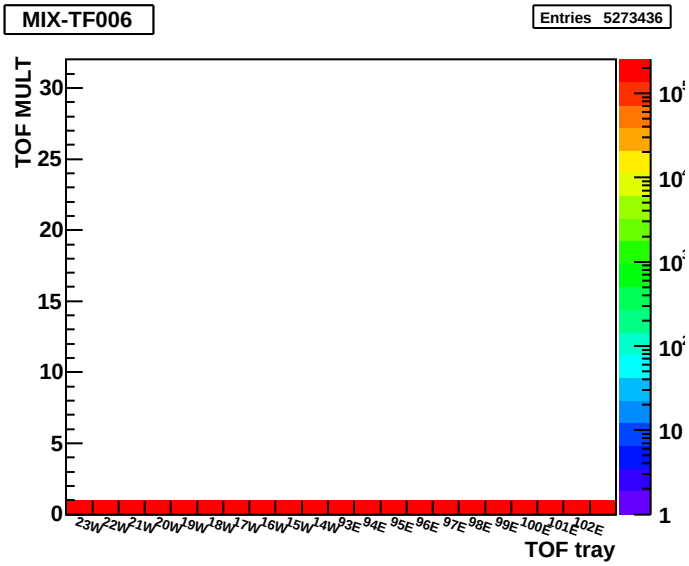

MIX-TF001
Entries 5273436
TOF MULT
TOF tray

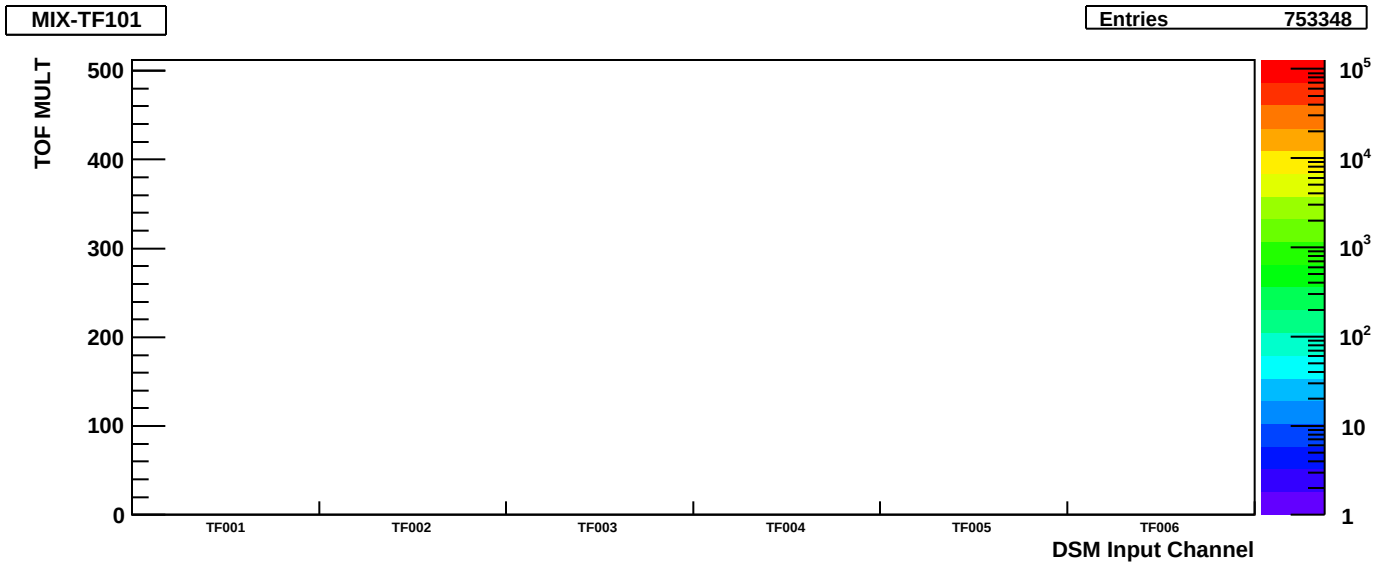

MIX-TF002
Entries 5273436
TOF MULT
TOF tray

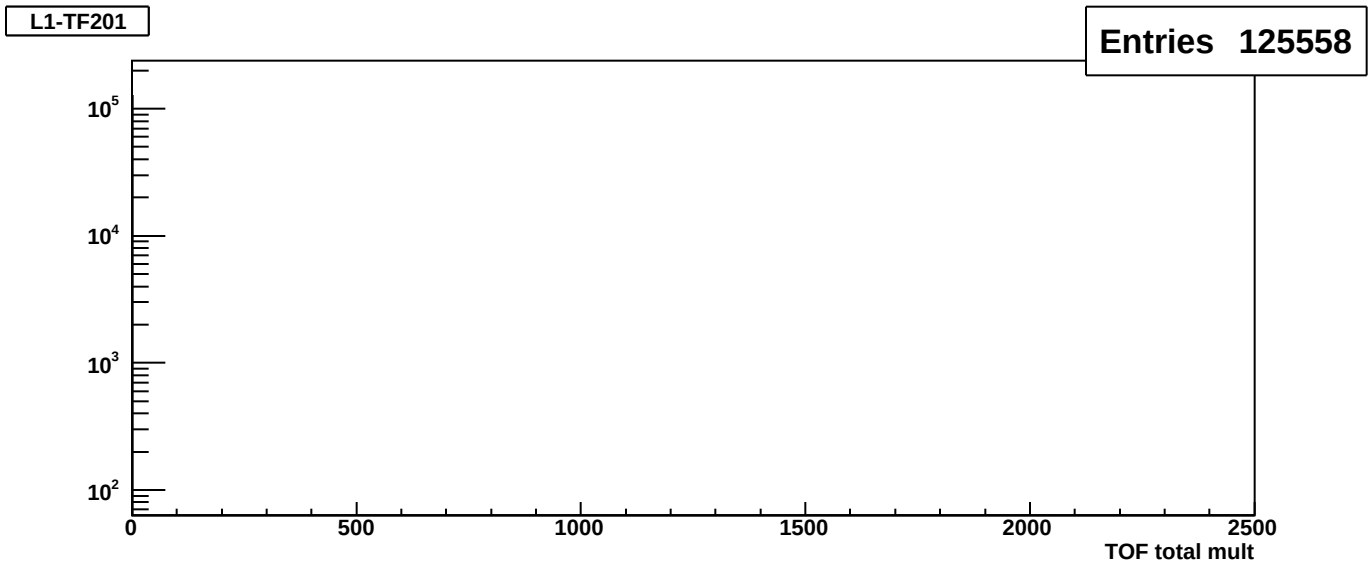

MIX-TF003
Entries 5273436
TOF MULT
TOF tray

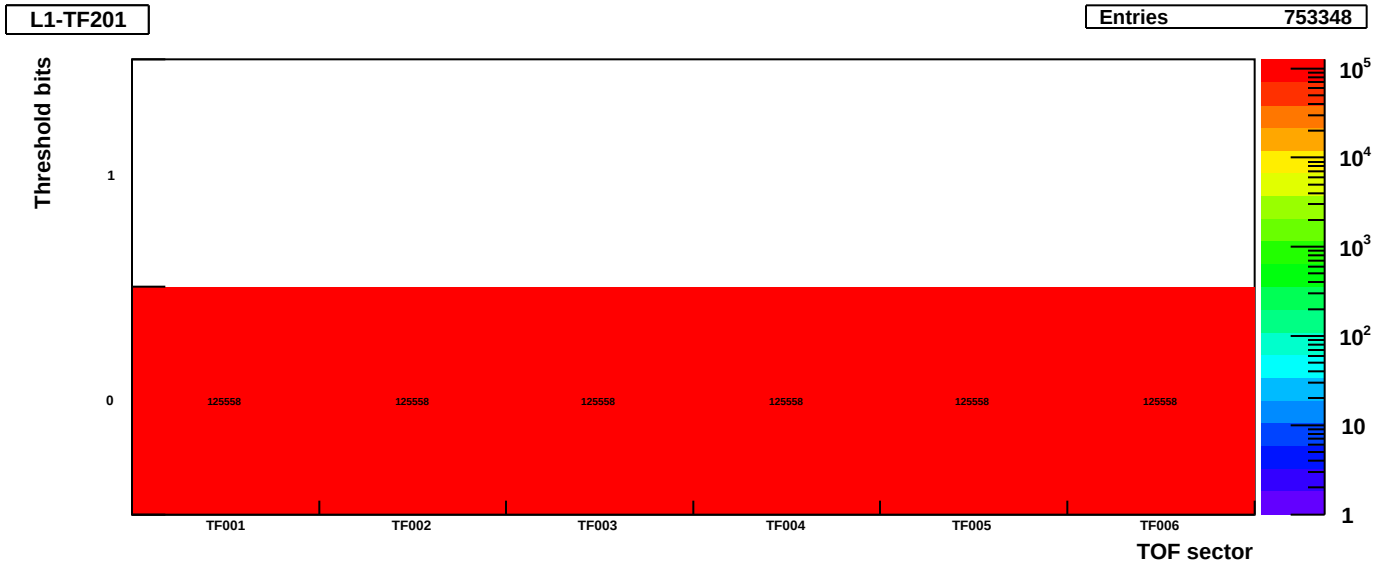

MIX-TF004
Entries 5273436
TOF MULT
TOF tray

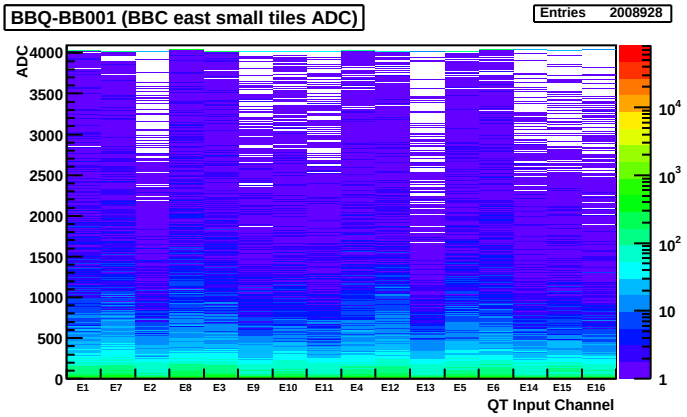

MIX-TF005
Entries 5273436
TOF MULT
TOF tray

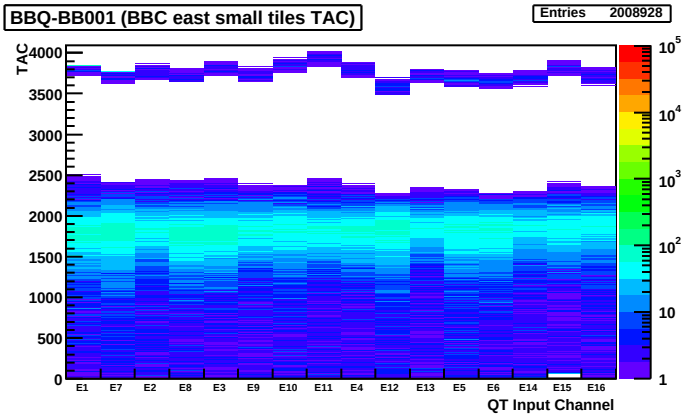

MIX-TF006
Entries 5273436
TOF MULT
TOF tray

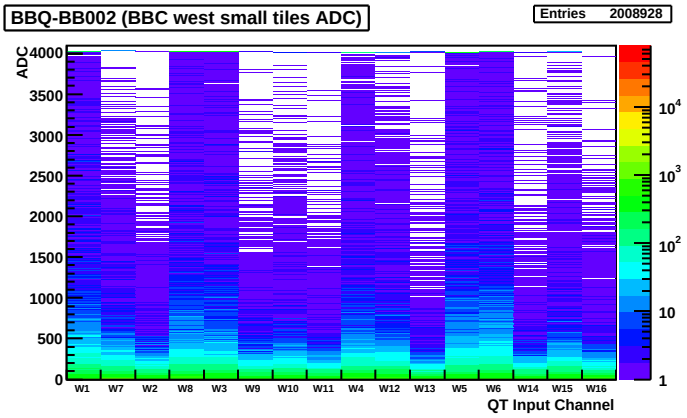

MIX-TF101
Entries 753348
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

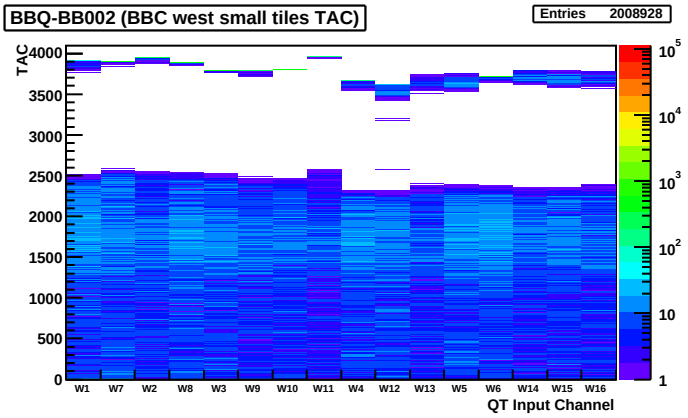

L1-TF201
Entries 125558
10^5
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

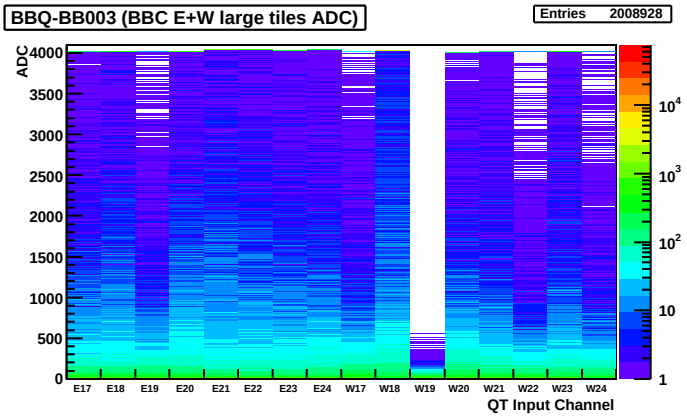

L1-TF201
Entries 753348
Threshold bits
1
0
125558
125558
125558
125558
125558
125558
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

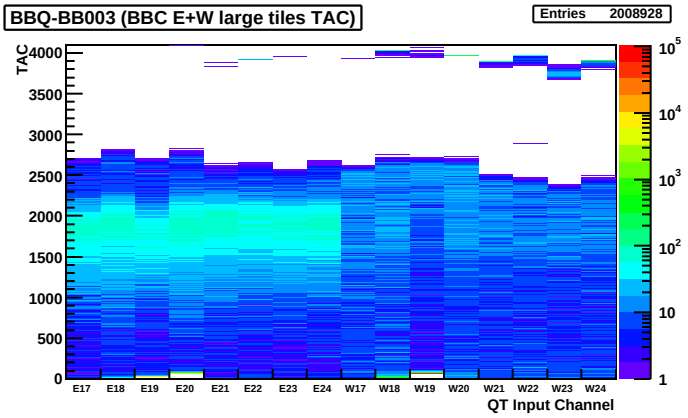

BBQ-BB001 (BBC east small tiles ADC)
Entries 2008928
ADC
QT Input Channel

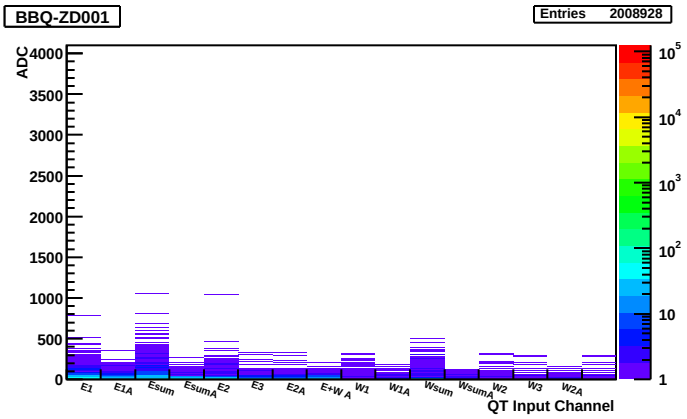

BBQ-BB001 (BBC east small tiles TAC)
Entries 2008928
TAC
QT Input Channel

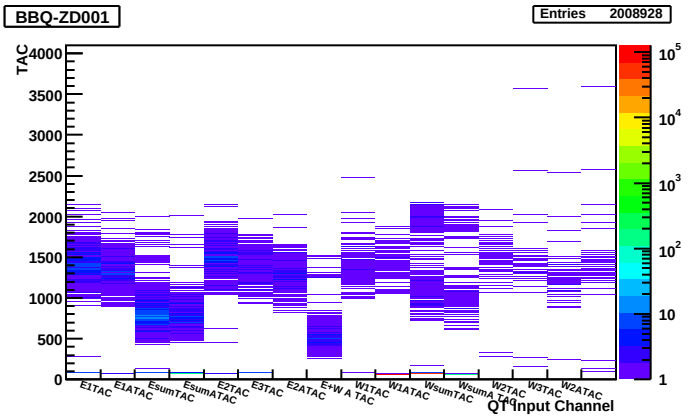

BBQ-BB002 (BBC west small tiles ADC)
Entries 2008928
ADC
QT Input Channel

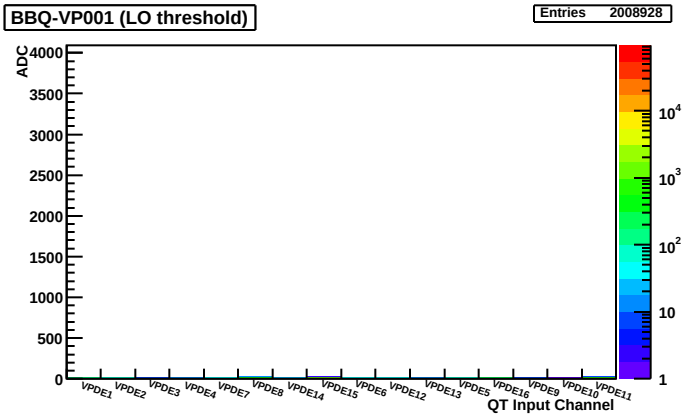

BBQ-BB002 (BBC west small tiles TAC)
Entries 2008928
TAC
QT Input Channel

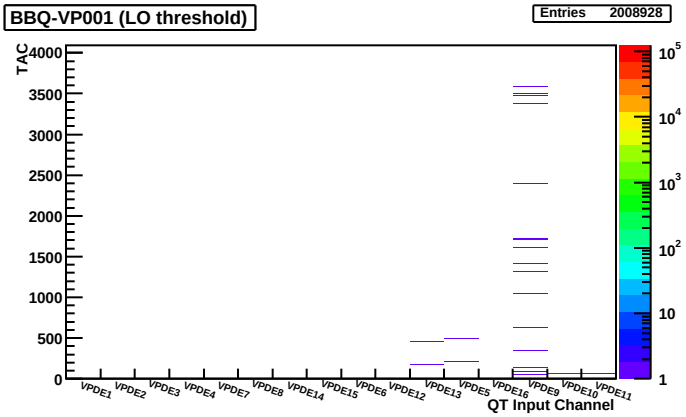

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2008928
ADC
QT Input Channel

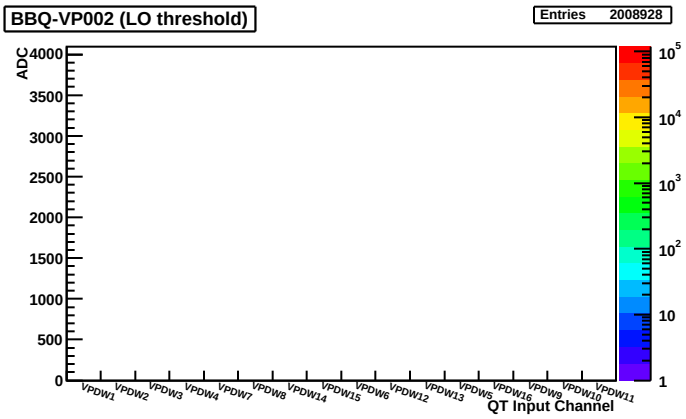

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2008928
TAC
QT Input Channel

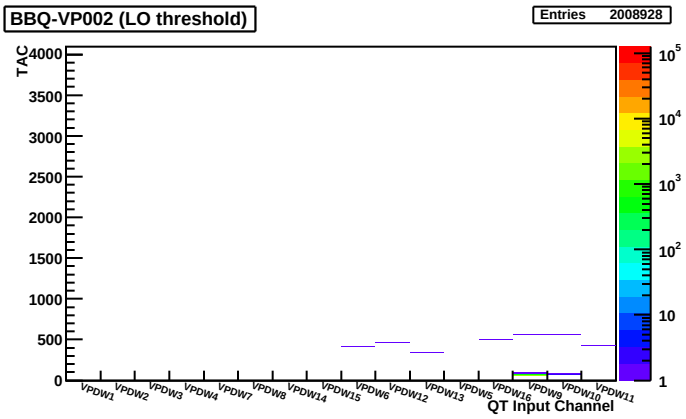

BBQ-ZD001
Entries 2008928
ADC
QT Input Channel

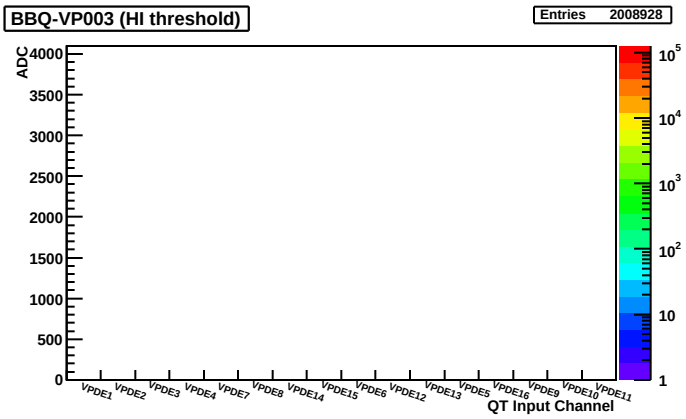

BBQ-ZD001
Entries 2008928
TAC
QT Input Channel

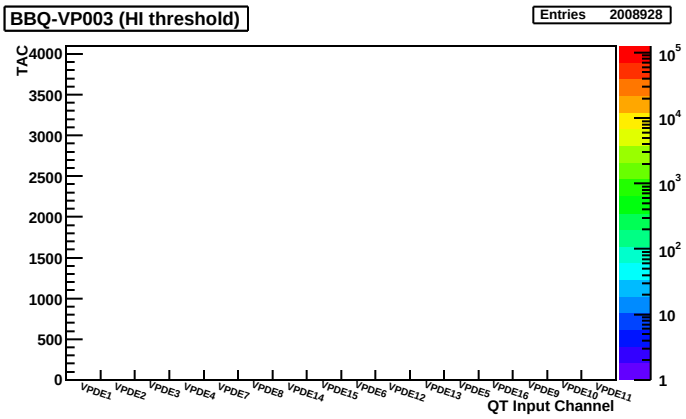

BBQ-VP001 (LO threshold)
Entries 2008928
ADC
QT Input Channel

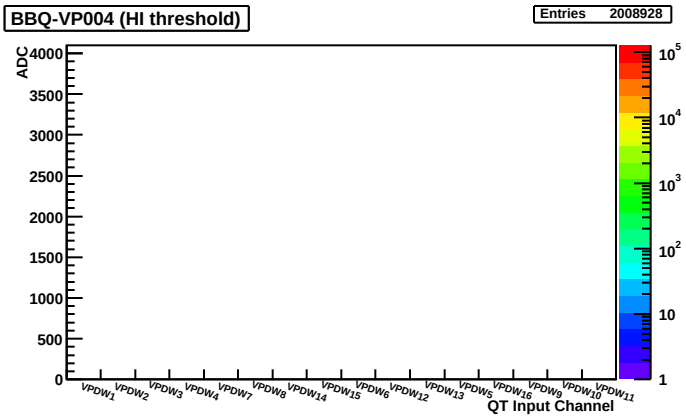

BBQ-VP001 (LO threshold)
Entries 2008928
TAC
QT Input Channel

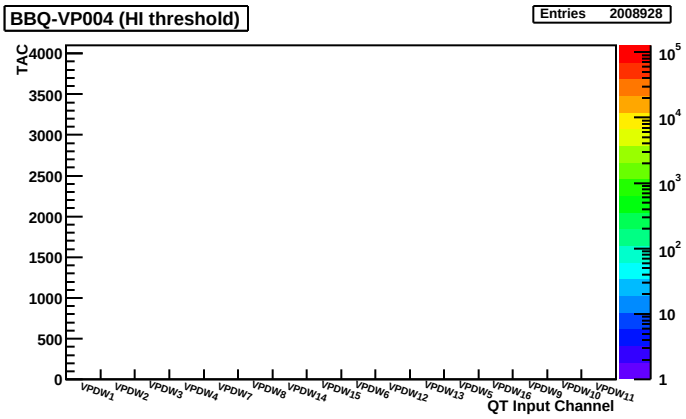

BBQ-VP002 (LO threshold)
Entries 2008928
ADC
QT Input Channel

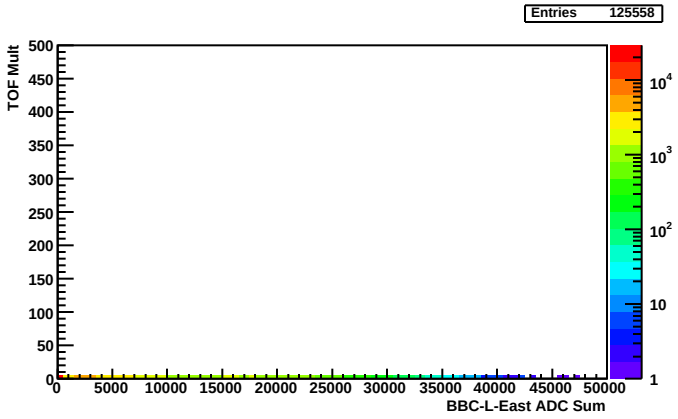

BBQ-VP002 (LO threshold)
Entries 2008928
TAC
QT Input Channel

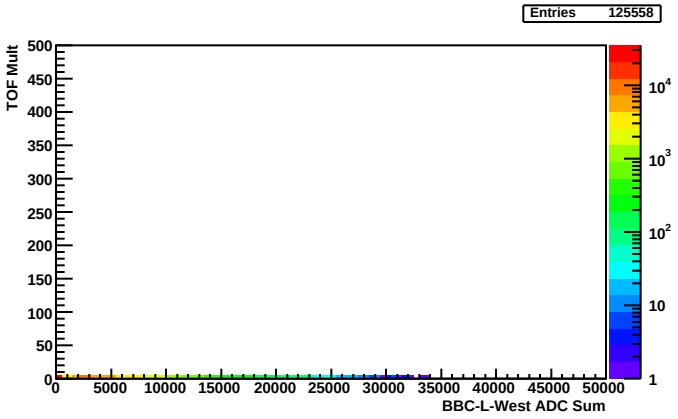

BBQ-VP003 (HI threshold)
Entries 2008928
ADC
QT Input Channel

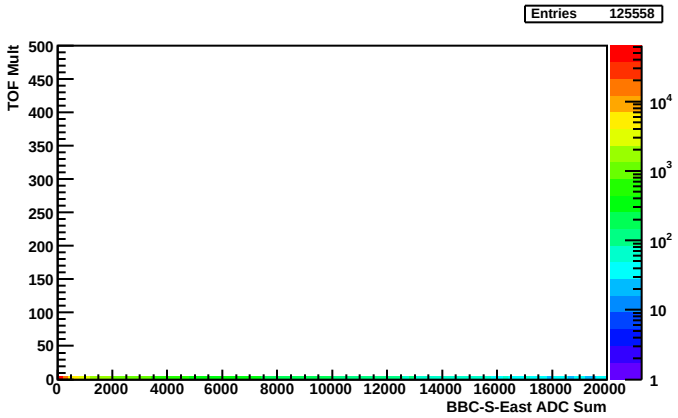

BBQ-VP003 (HI threshold)
Entries 2008928
TAC
QT Input Channel

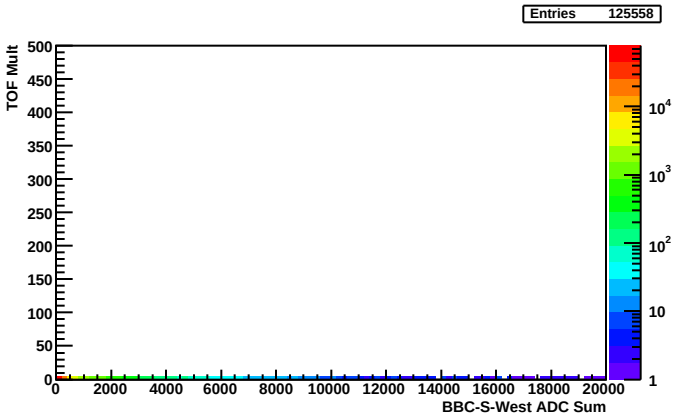

BBQ-VP004 (HI threshold)
Entries 2008928
ADC
QT Input Channel

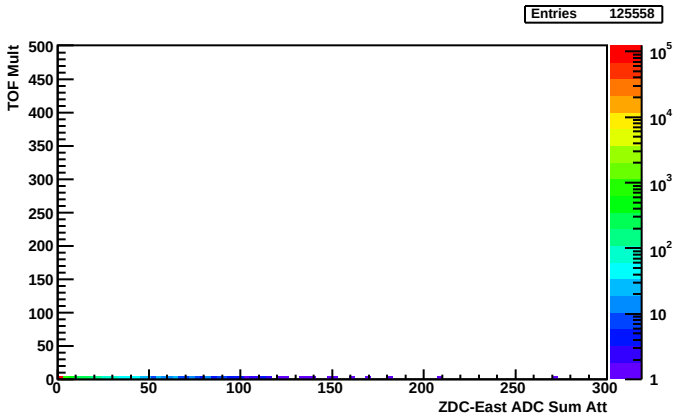

BBQ-VP004 (HI threshold)
Entries 2008928
TAC
QT Input Channel

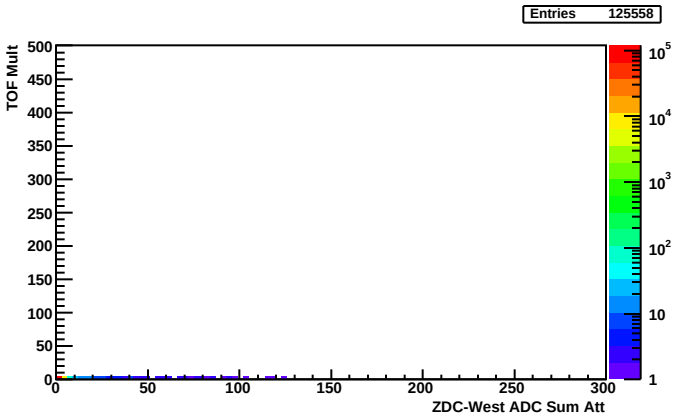

Entries 125558
TOF Mult
BBC-L-East ADC Sum

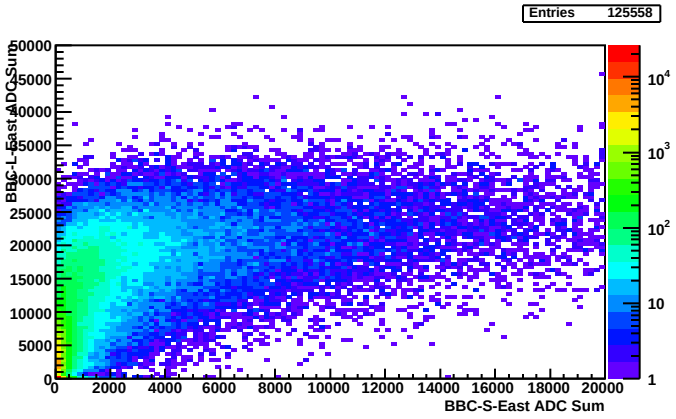

Entries 125558
TOF Mult
BBC-L-West ADC Sum

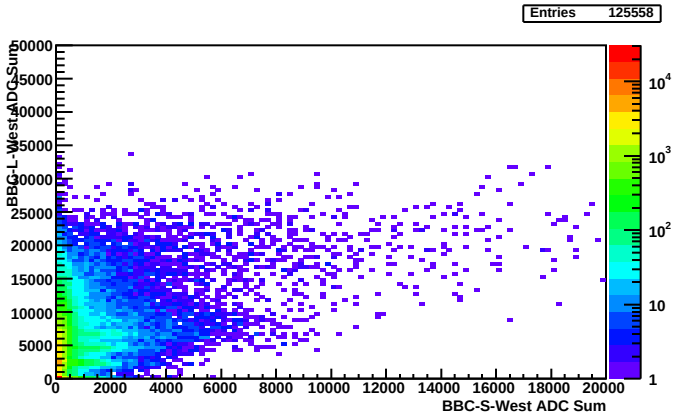

Entries 125558
TOF Mult
BBC-S-East ADC Sum

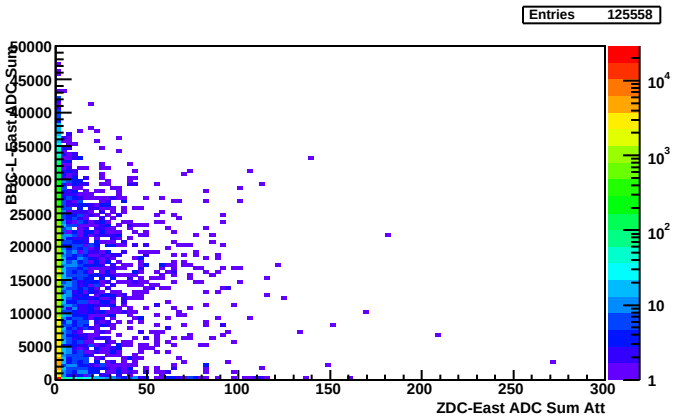

Entries 125558
TOF Mult
BBC-S-West ADC Sum

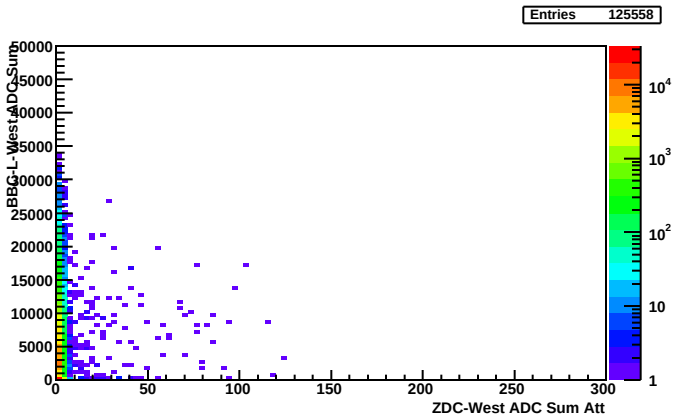

Entries 125558
TOF Mult
ZDC-East ADC Sum Att

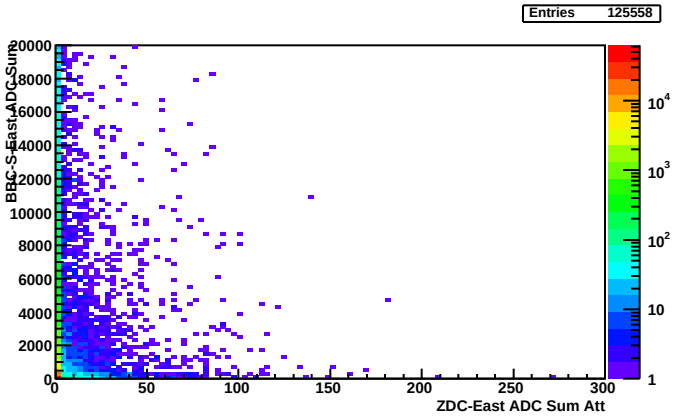

Entries 125558
TOF Mult
ZDC-West ADC Sum Att

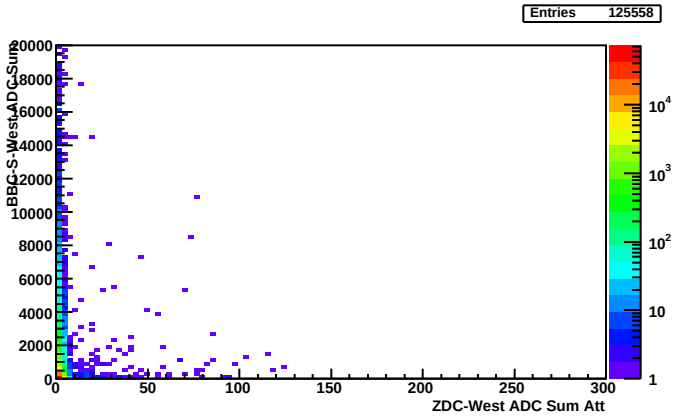

Entries 125558
BBC-L-East ADC Sum
BBC-S-East ADC Sum

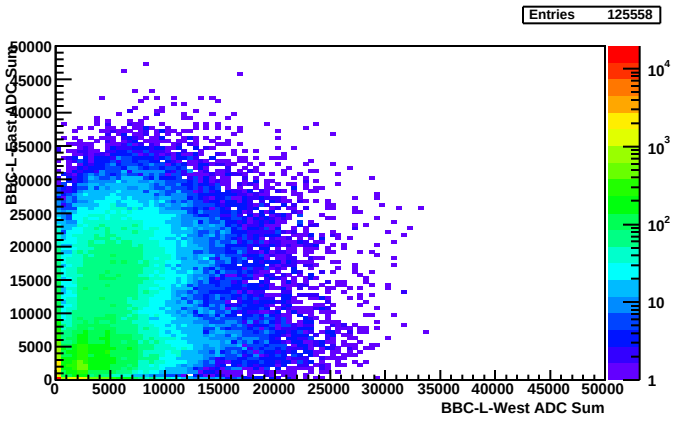

Entries 125558
BBC-L-West ADC Sum
BBC-S-West ADC Sum

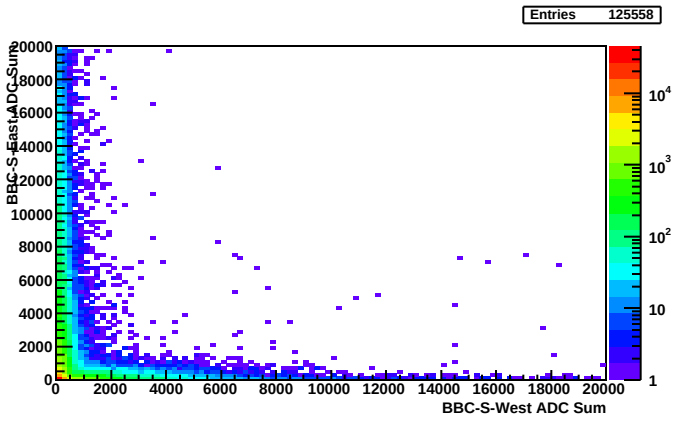

Entries 125558
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

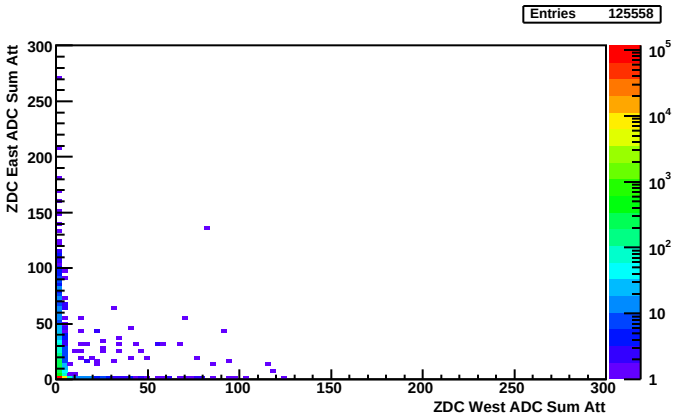

Entries 125558
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

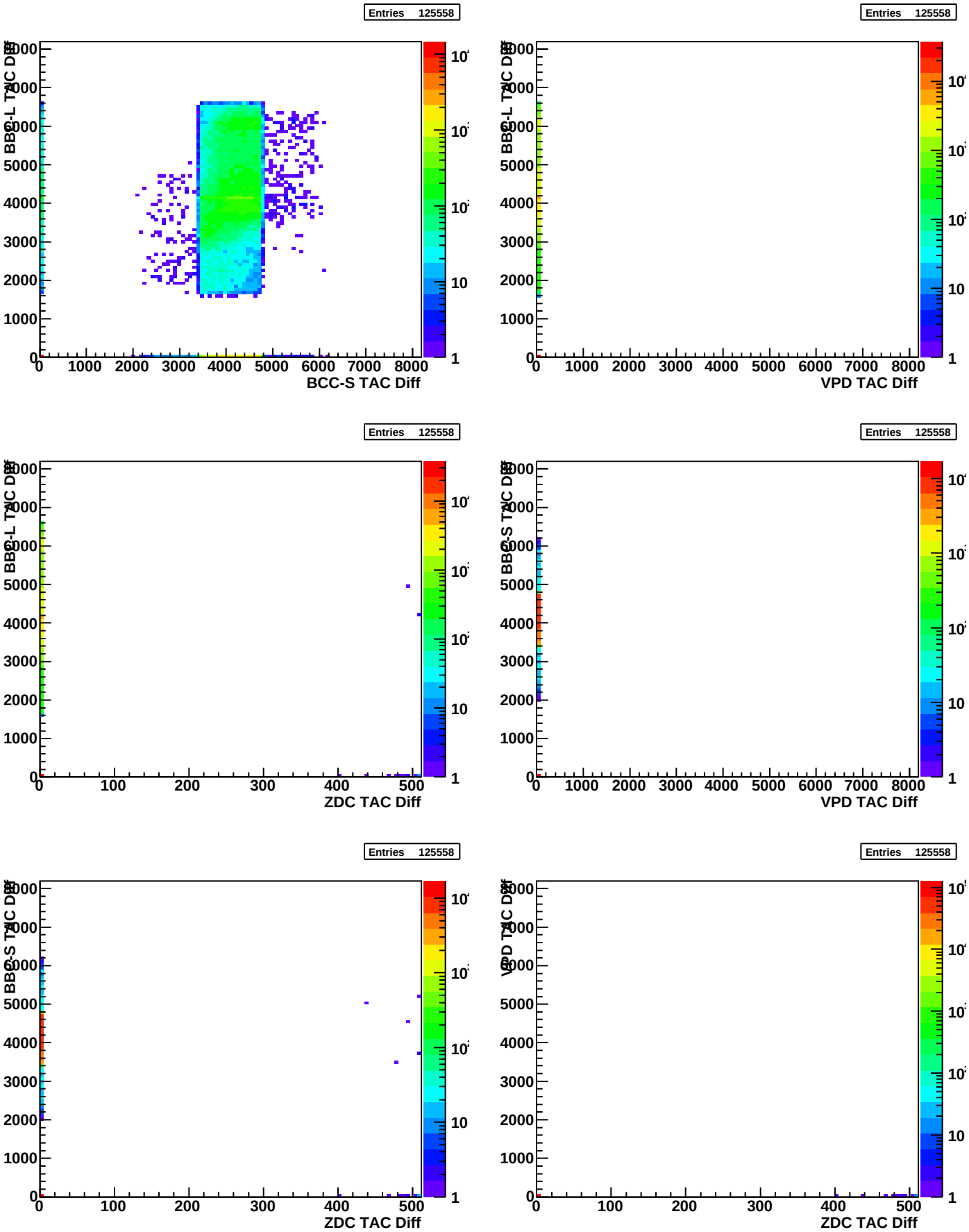

Entries 125558
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

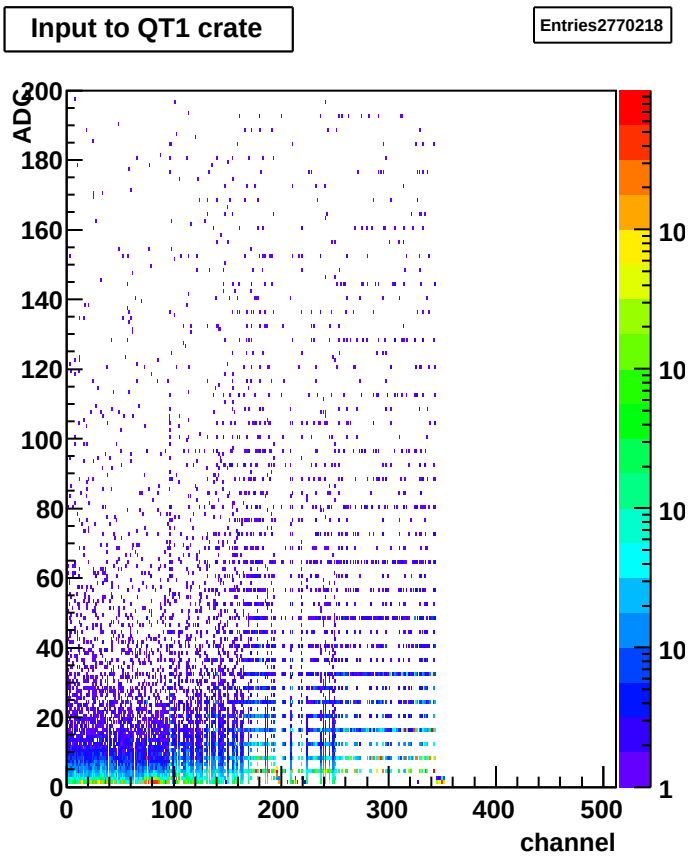

Entries 125558
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

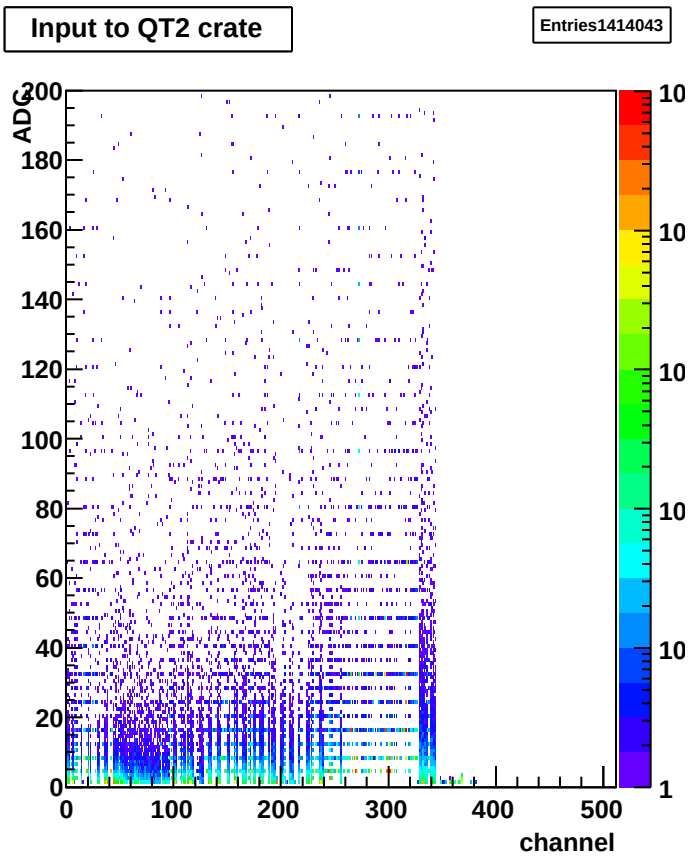

Entries 125558
BBC-L-East ADC Sum
BBC-L-West ADC Sum

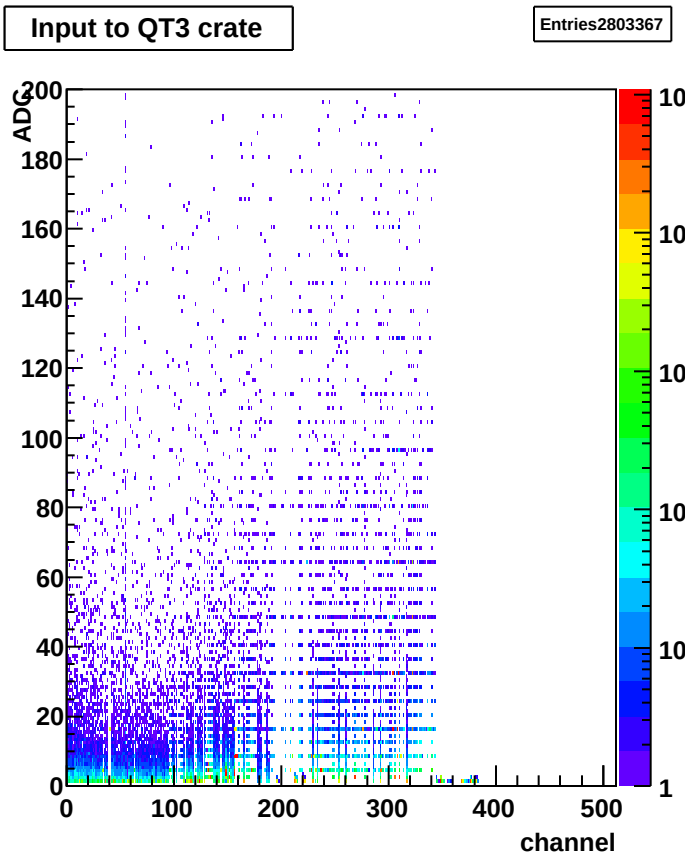

Entries 125558
BBC-S-East ADC Sum
BBC-S-West ADC Sum

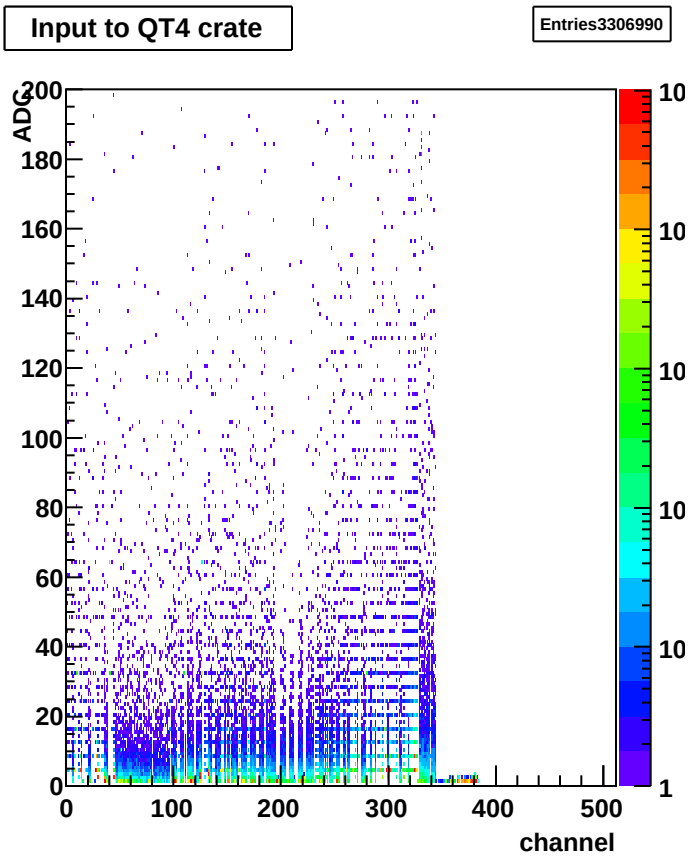

Entries 125558
ZDC East ADC Sum Att
ZDC West ADC Sum Att

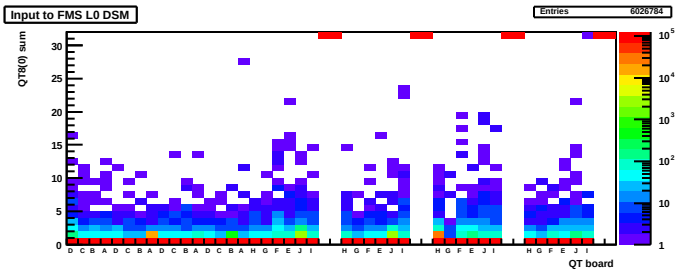

Entries 125558
BBC-L TAC Diff
BCC-S TAC Diff
Entries 125558
BBC-L TAC Diff
VPD TAC Diff
Entries 125558
BBC-L TAC Diff
ZDC TAC Diff
Entries 125558
BBC-S TAC Diff
VPD TAC Diff
Entries 125558
BBC-S TAC Diff
ZDC TAC Diff
Entries 125558
VPD TAC Diff
ZDC TAC Diff

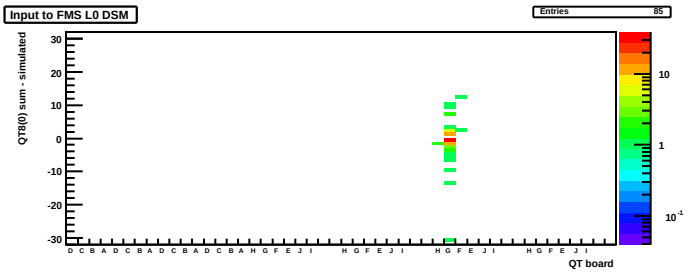

Input to QT1 crate
Entries2770218
ADC
channel

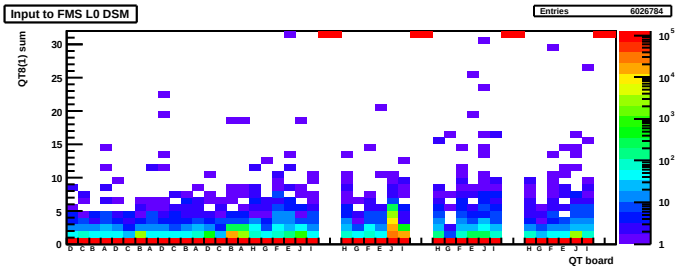

Input to QT2 crate
Entries1414043
ADC
channel

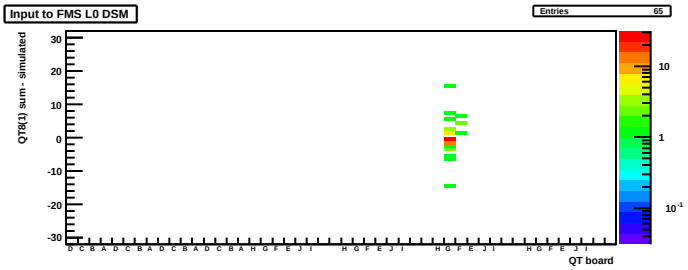

Input to QT3 crate
Entries2803367
ADC
channel

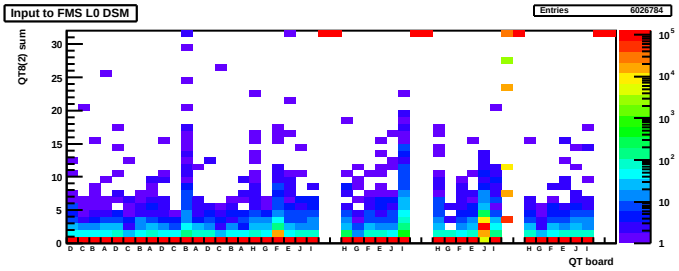

Input to QT4 crate
Entries3306990
ADC
channel

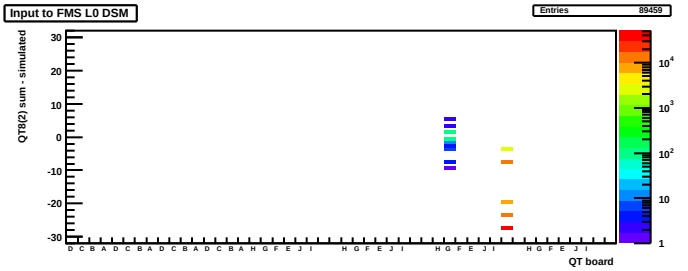

Input to FMS L0 DSM
Entries 6026784
QT8(0) sum
QT board

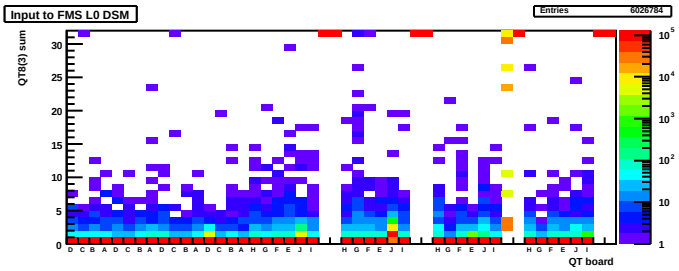

Input to FMS L0 DSM
Entries 85
QT8(0) sum - simulated
QT board

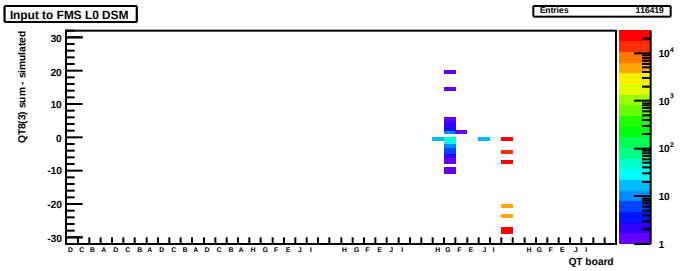

Input to FMS L0 DSM
Entries 6026784
QT8(1) sum
QT board

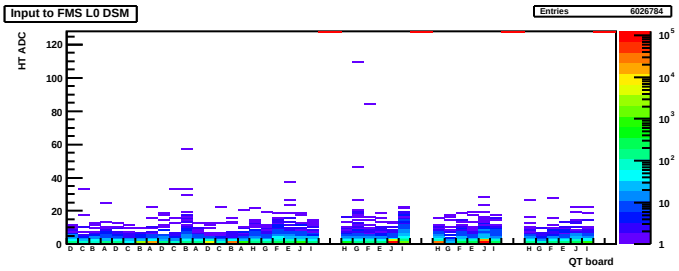

Input to FMS L0 DSM
Entries 65
QT8(1) sum - simulated
QT board

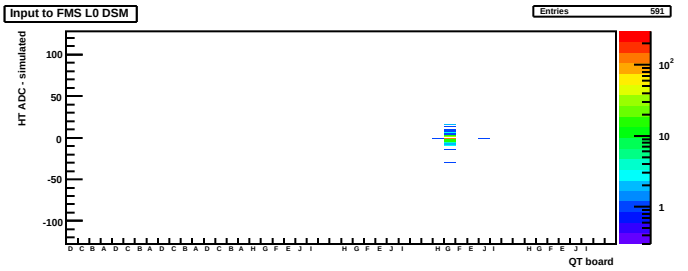

Input to FMS L0 DSM
Entries 6026784
QT8(2) sum
QT board

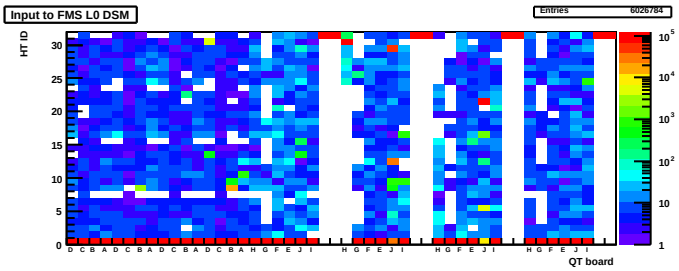

Input to FMS L0 DSM
Entries 89459
QT8(2) sum - simulated
QT board

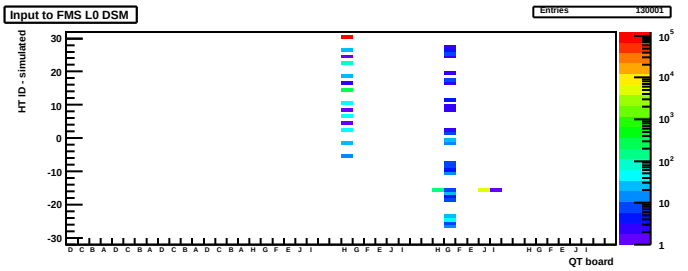

Input to FMS L0 DSM
Entries 6026784
QT8(3) sum
QT board

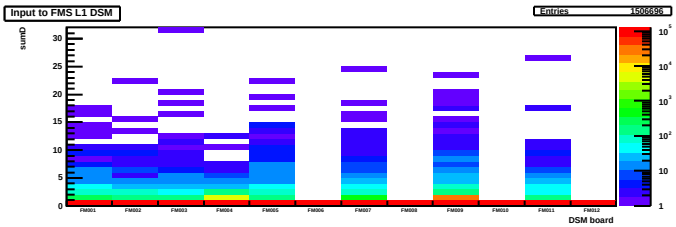

Input to FMS L0 DSM
Entries 116419
QT8(3) sum - simulated
QT board

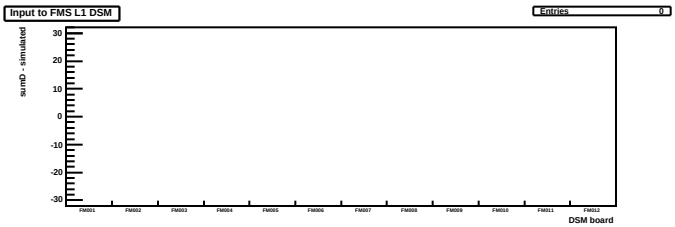

Input to FMS L0 DSM
Entries 6026784
HT ADC
QT board

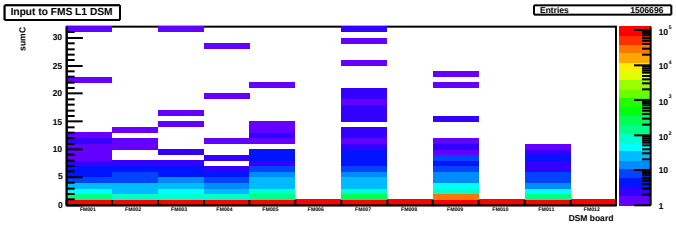

Input to FMS L0 DSM
Entries 591
HT ADC - simulated
QT board

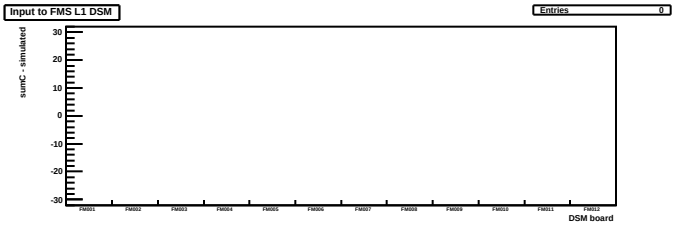

Input to FMS L0 DSM
Entries 6026784
HT ID
QT board

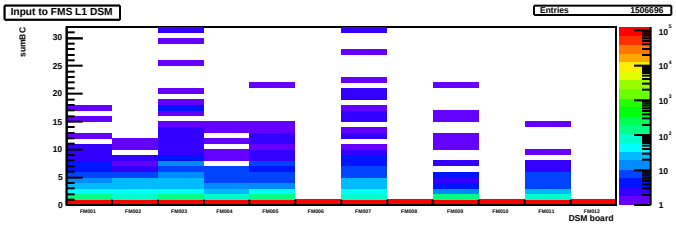

Input to FMS L0 DSM
Entries 130001
HT ID - simulated
QT board

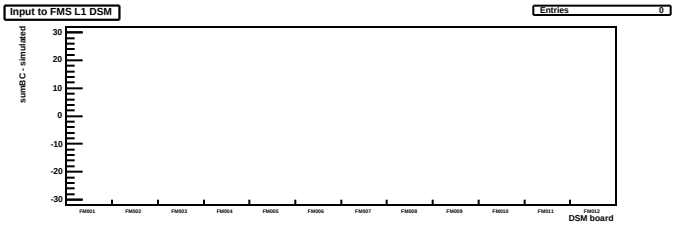

Input to FMS L1 DSM
Entries 1506696
sumD
DSM board

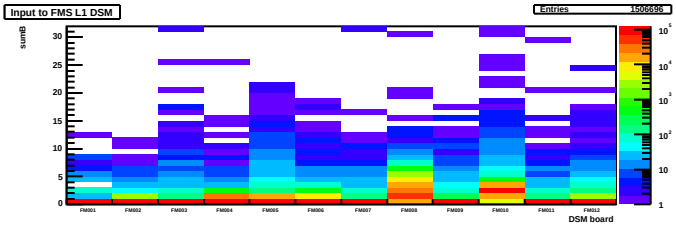

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

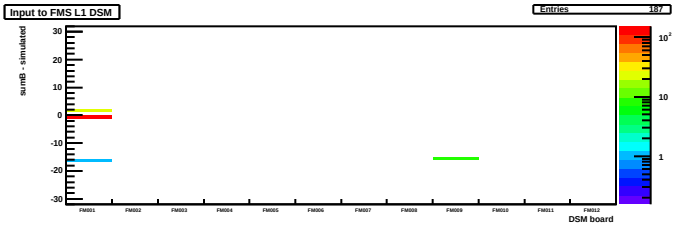

Input to FMS L1 DSM
Entries 1506696
sumC
DSM board

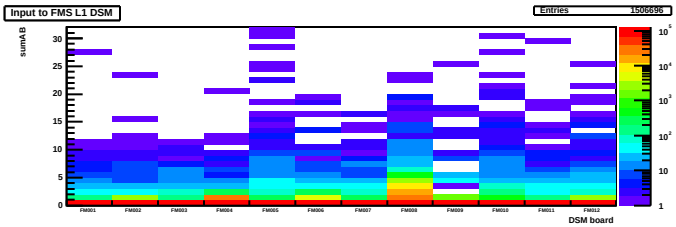

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

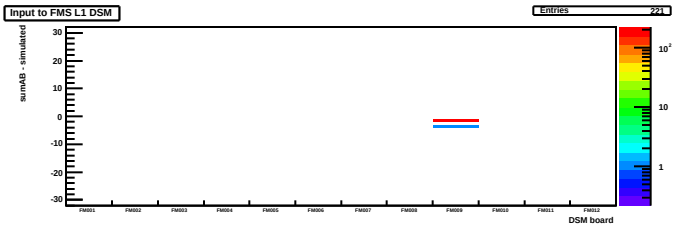

Input to FMS L1 DSM
Entries 1506696
sumBC
DSM board

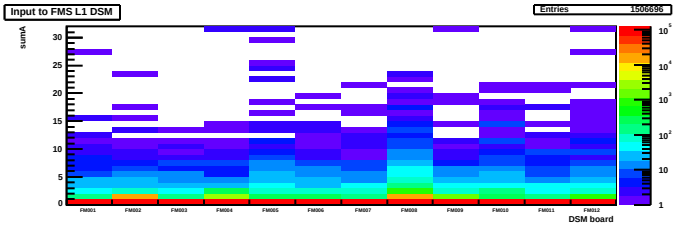

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

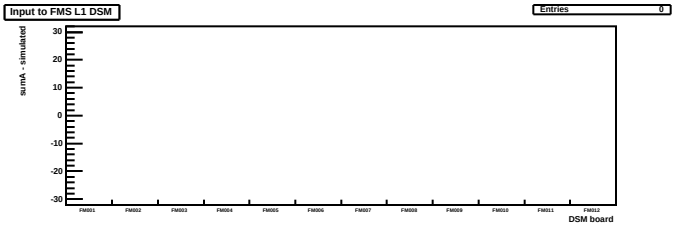

Input to FMS L1 DSM
Entries 1506696
sumB
DSM board

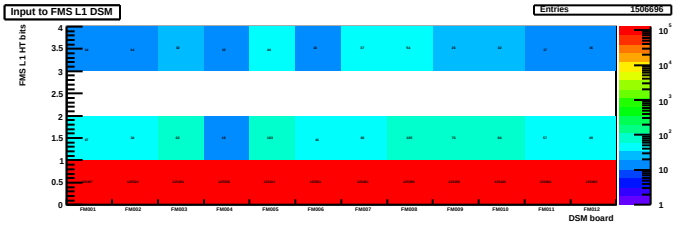

Input to FMS L1 DSM
Entries 187
sumB - simulated
DSM board

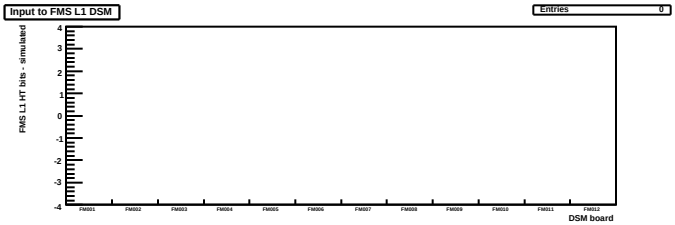

Input to FMS L1 DSM
Entries 1506696
sumAB
DSM board

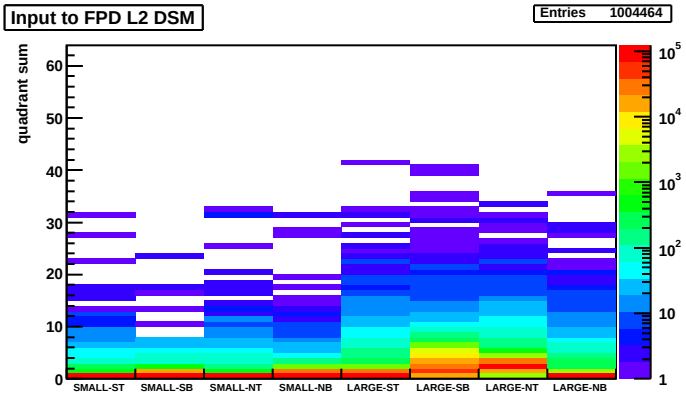

Input to FMS L1 DSM
Entries 221
sumAB - simulated
DSM board

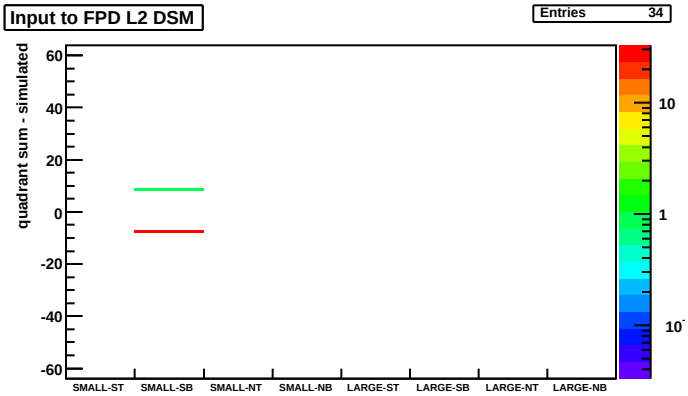

Input to FMS L1 DSM
Entries 1506696
sumA
DSM board

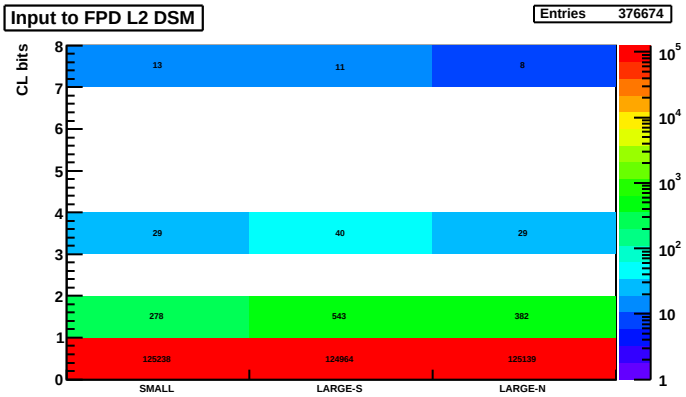

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

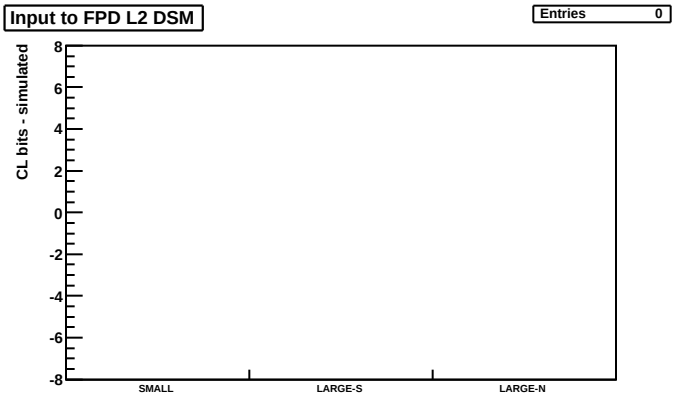

Input to FMS L1 DSM
Entries 1506696
FMS L1 HT bits
DSM board

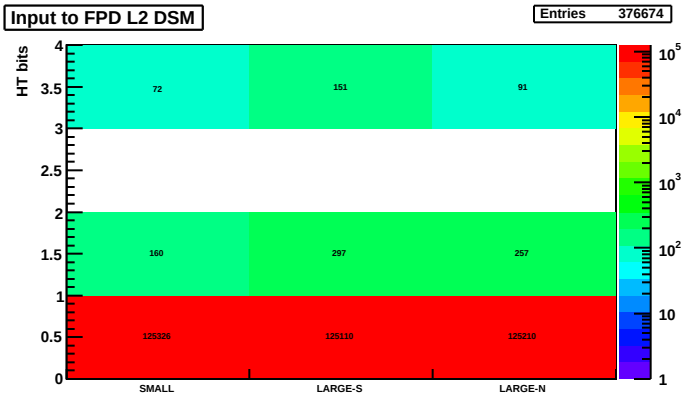

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

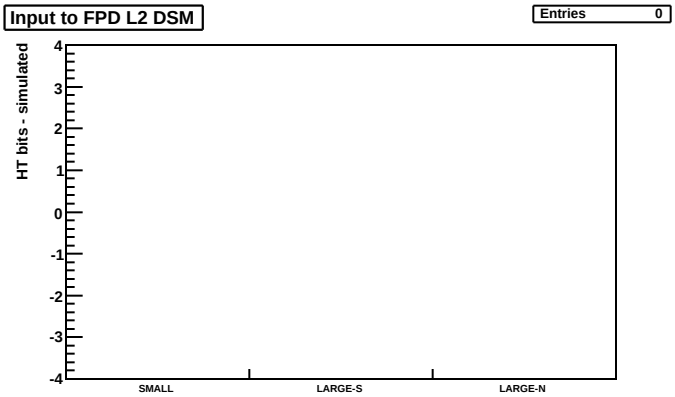

Input to FPD L2 DSM
Entries 1004464
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

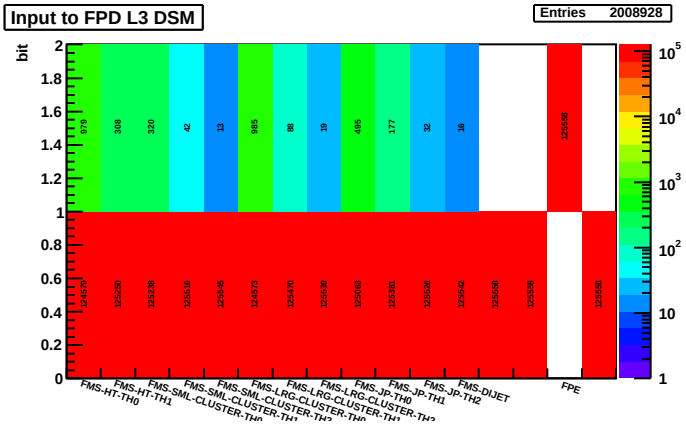

Input to FPD L2 DSM
Entries 34
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

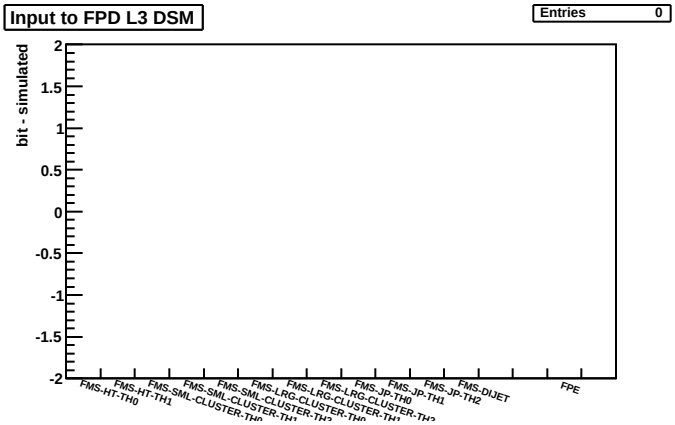

Input to FPD L2 DSM
Entries 376674
CL bits
13
11
8
29
40
29
278
543
382
125238
124964
125139
SMALL
LARGE-S
LARGE-N

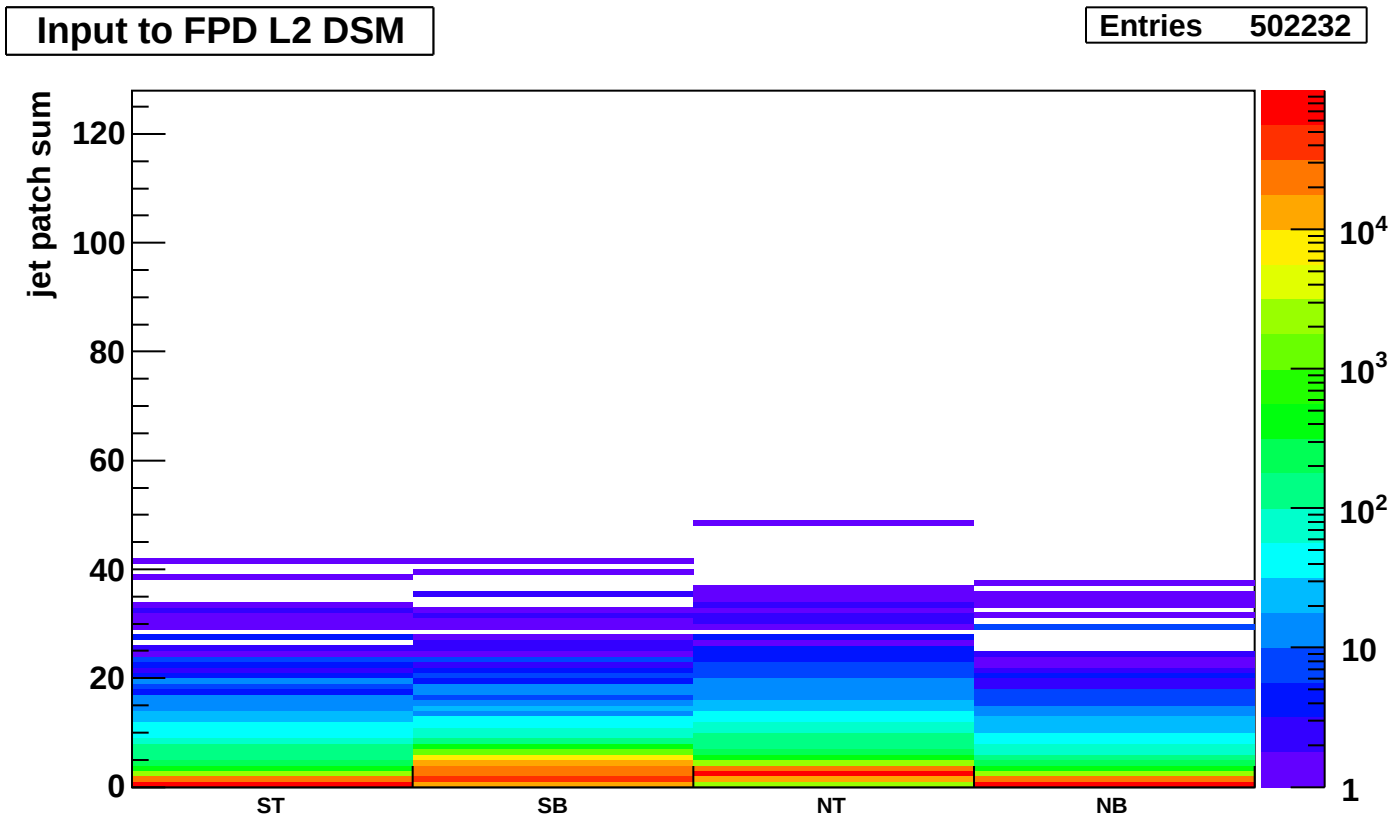

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

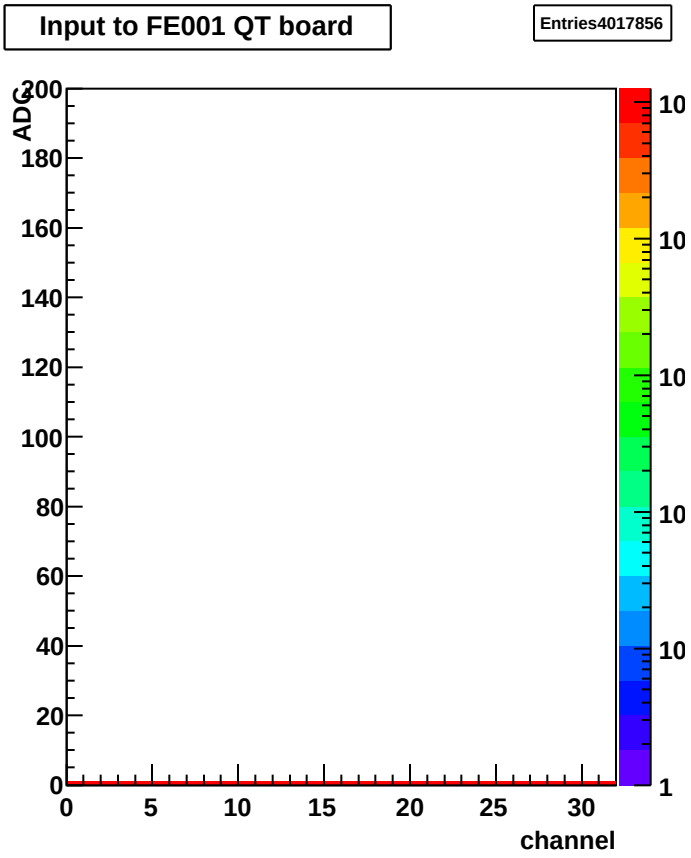

Input to FPD L2 DSM
Entries 376674
HT bits
72
151
91
160
297
257
125326
125110
125210
SMALL
LARGE-S
LARGE-N

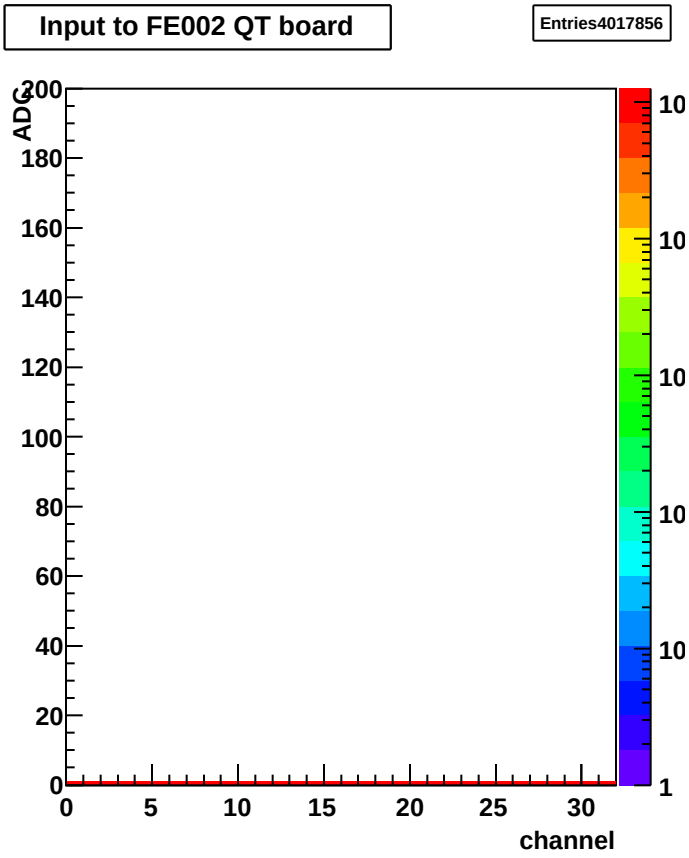

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

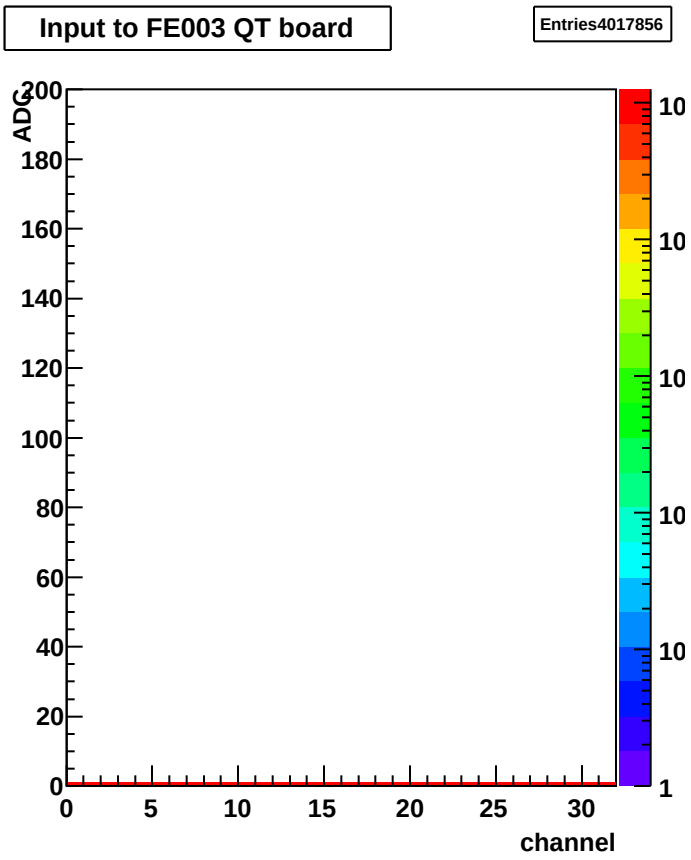

Input to FPD L3 DSM
Entries 2008928
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

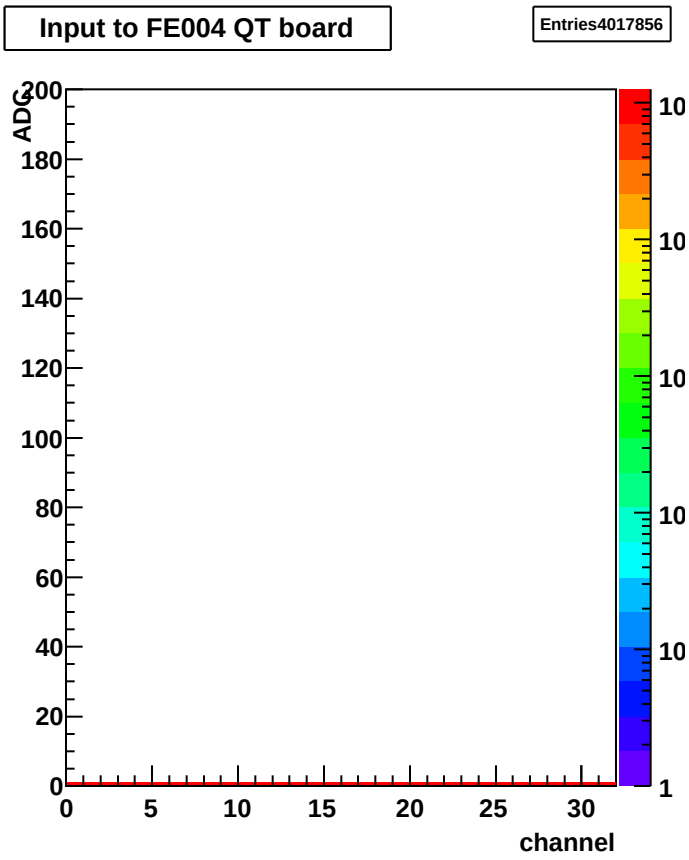

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

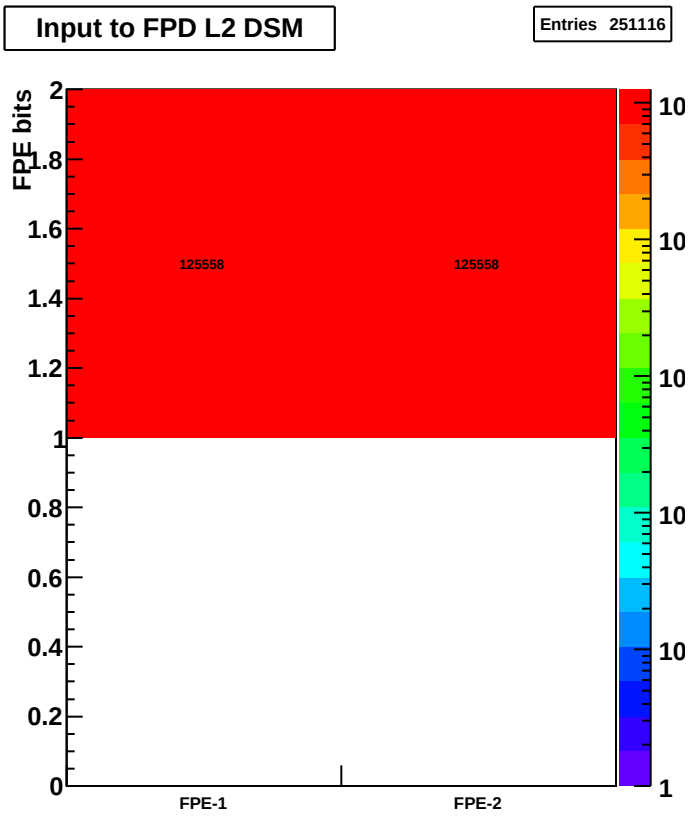

Input to FPD L2 DSM
Entries 502232
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

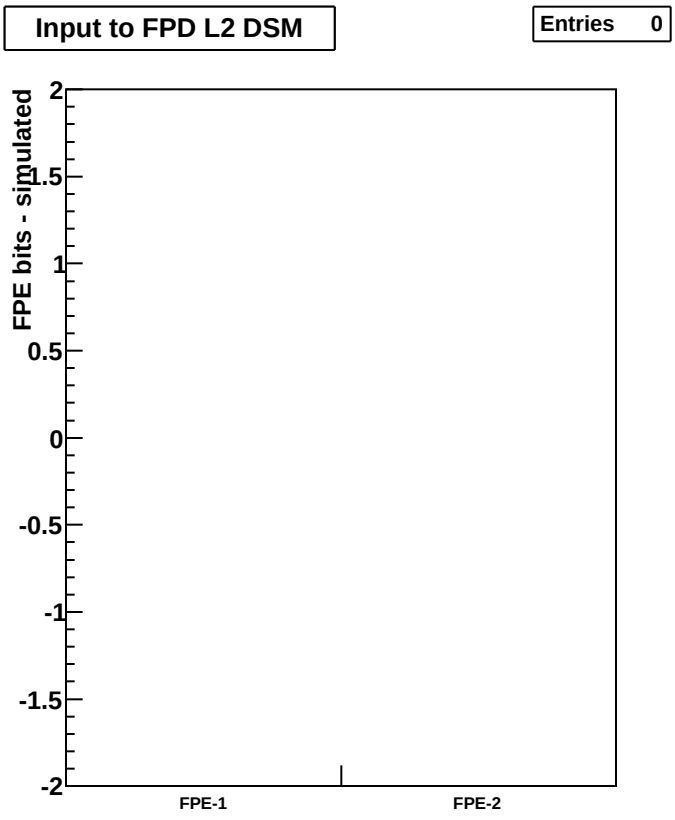

Input to FE001 QT board
Entries4017856
ADC
channel


Input to FE002 QT board
Entries4017856
ADC
channel


Input to FE003 QT board
Entries4017856
ADC
channel


Input to FE004 QT board
Entries4017856
ADC
channel


Input to FPD L2 DSM
Entries 251116
FPE bits
125558
125558
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2