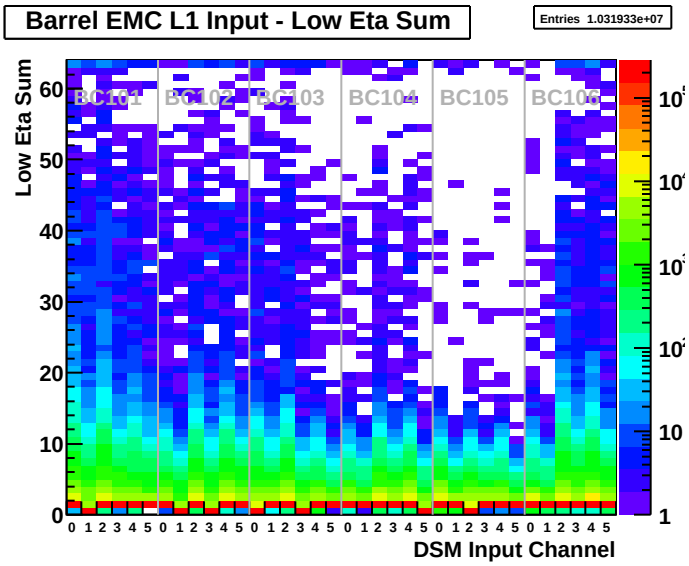
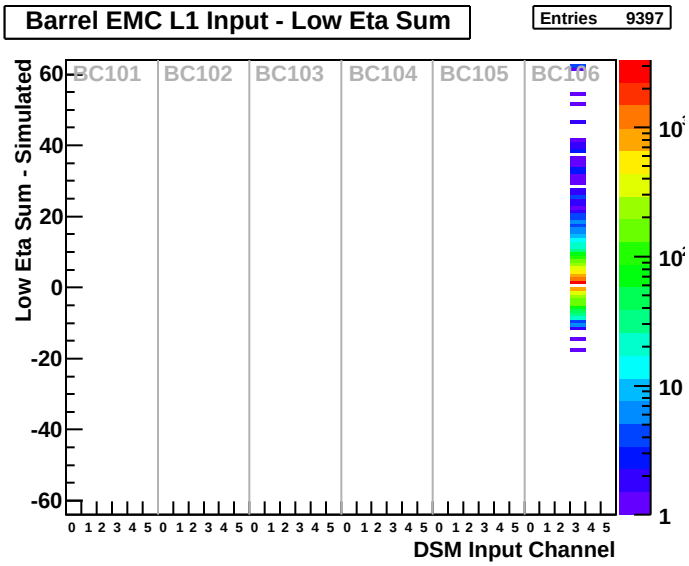
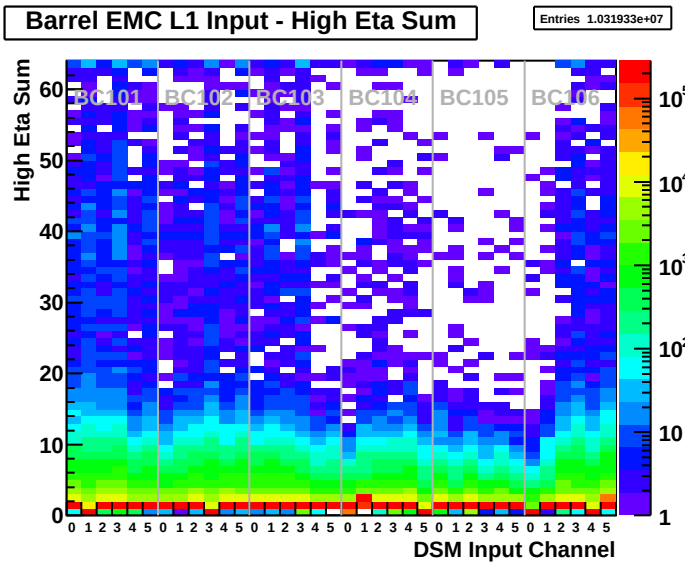
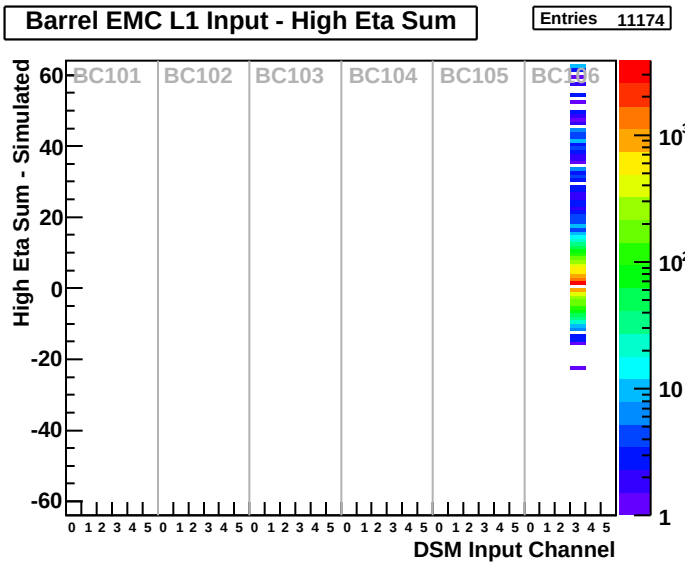
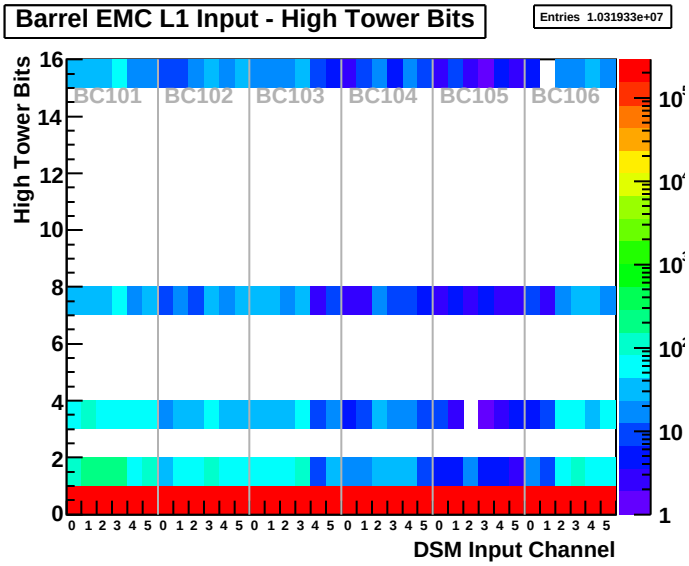
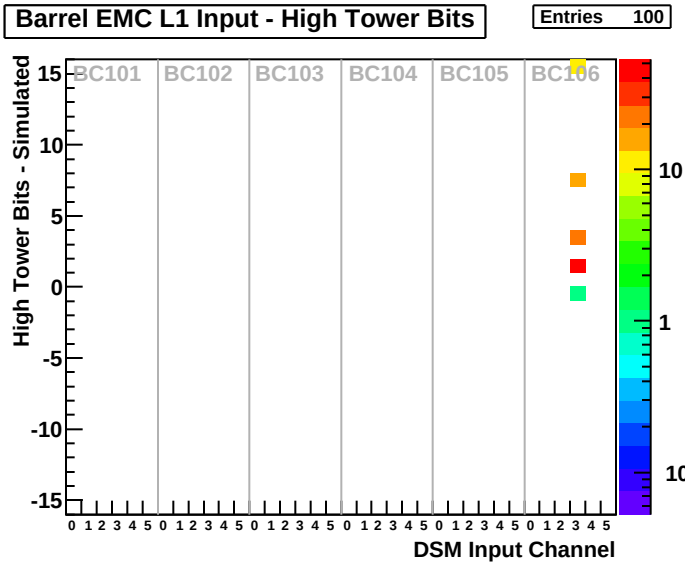

Barrel EMC L1 Input - Low Eta Sum
Entries 1.031933e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

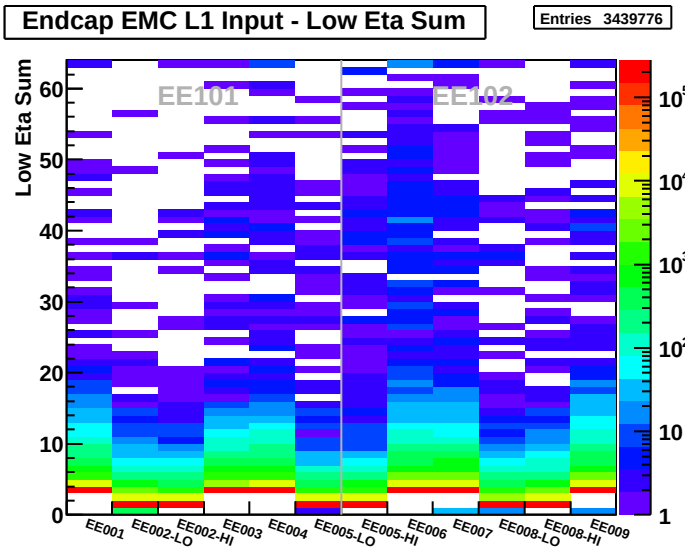

Barrel EMC L1 Input - Low Eta Sum
Entries 9397
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

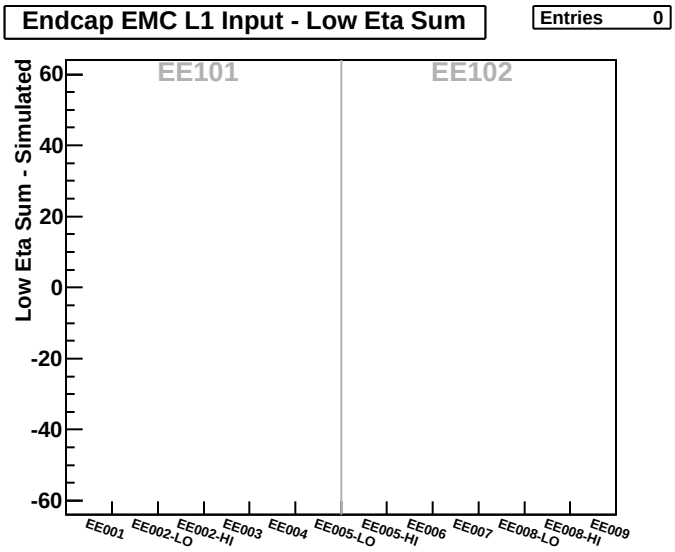

Barrel EMC L1 Input - High Eta Sum
Entries 1.031933e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

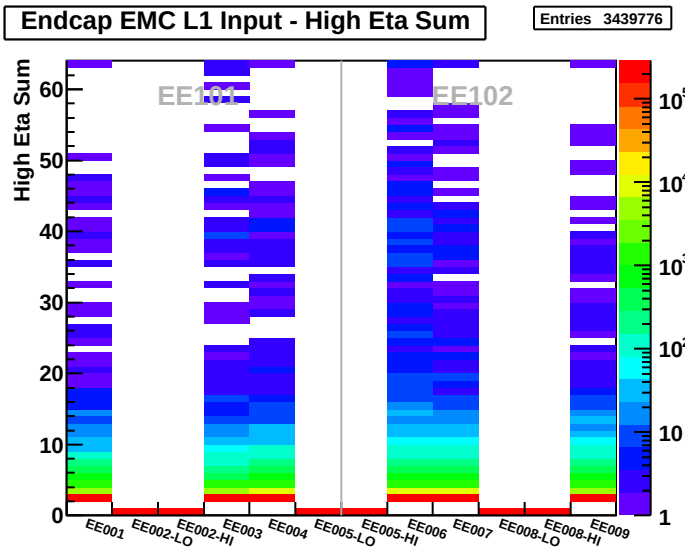

Barrel EMC L1 Input - High Eta Sum
Entries 11174
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

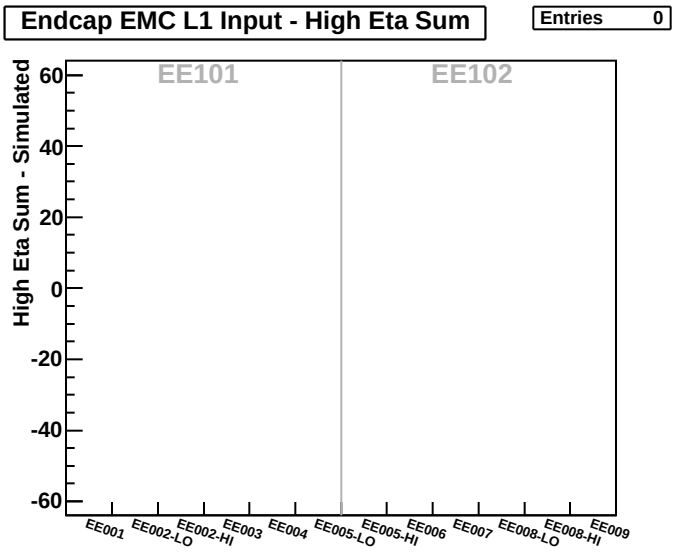

Barrel EMC L1 Input - High Tower Bits
Entries 1.031933e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

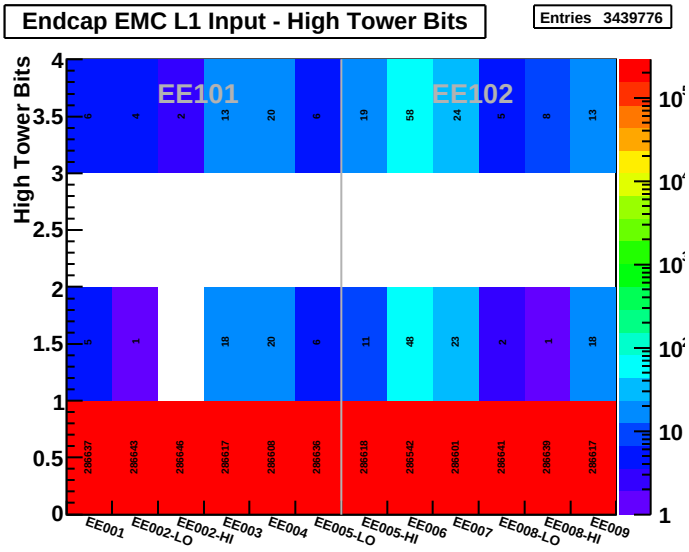

Barrel EMC L1 Input - High Tower Bits
Entries 100
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

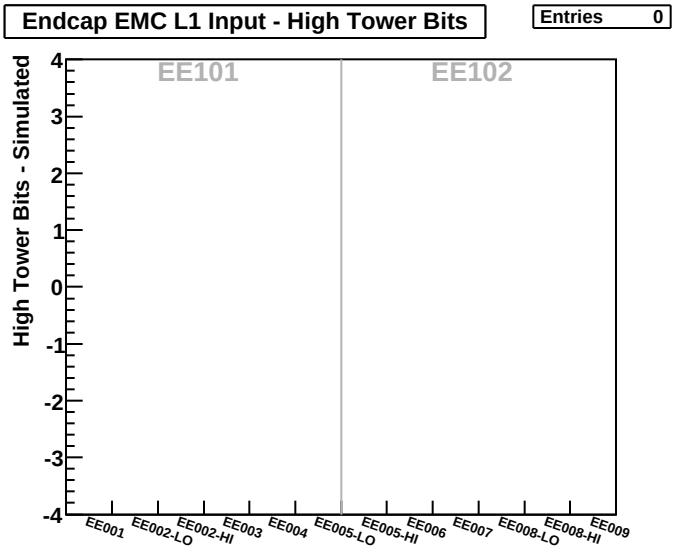

Endcap EMC L1 Input - Low Eta Sum
Entries 3439776
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

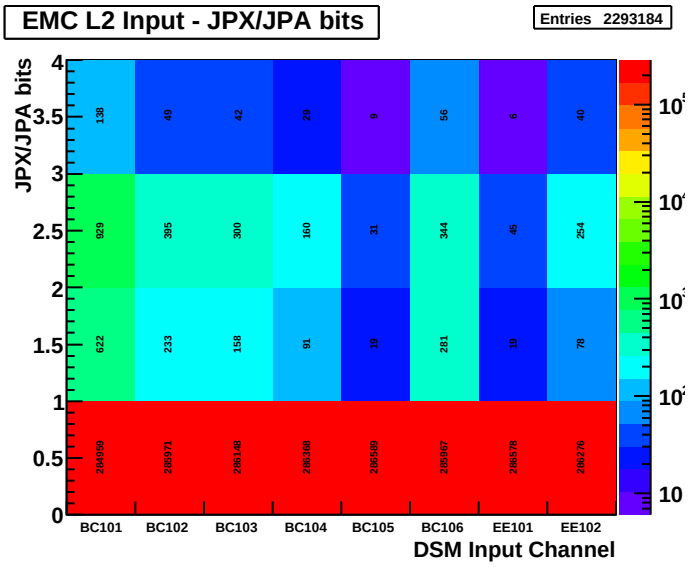

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

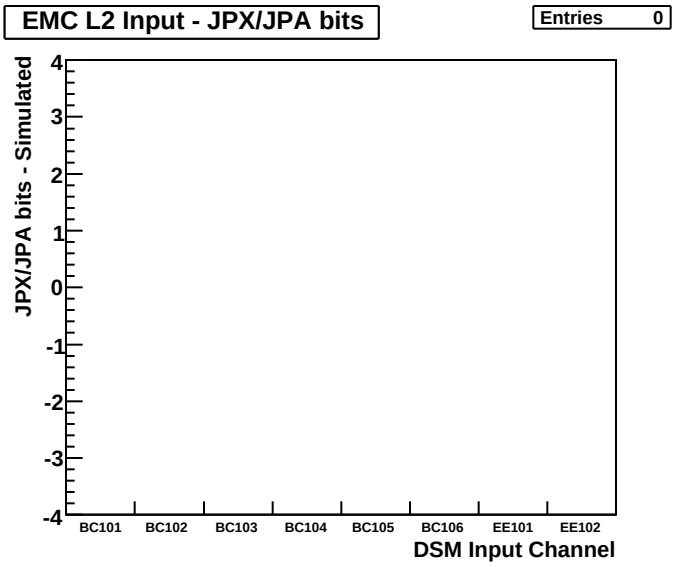

Endcap EMC L1 Input - High Eta Sum
Entries 3439776
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

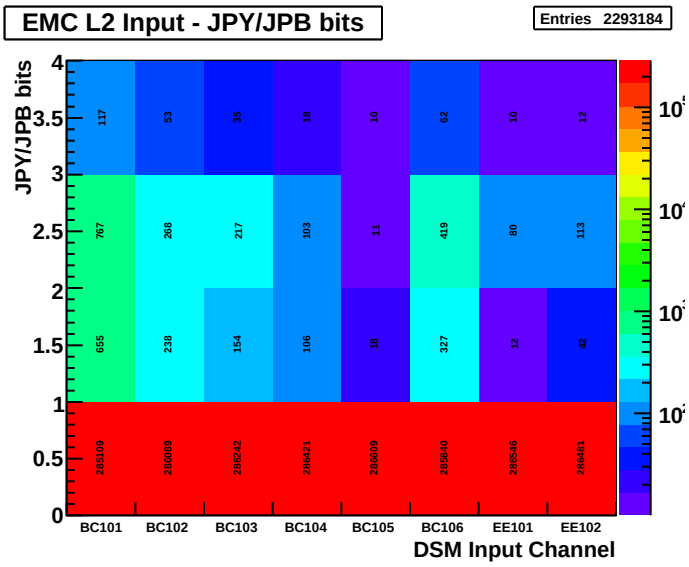

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

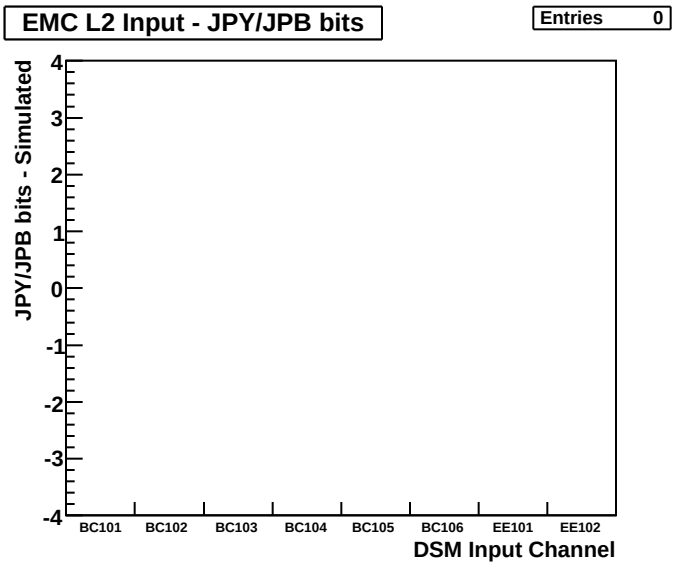

Endcap EMC L1 Input - High Tower Bits
Entries 3439776
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

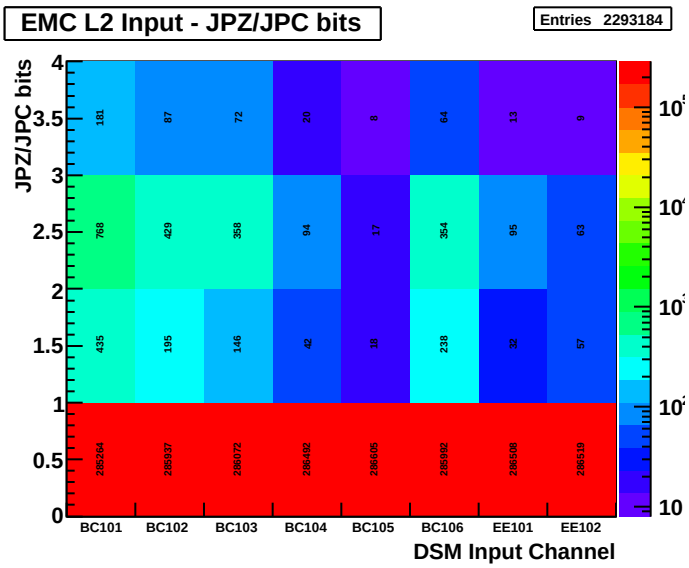

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

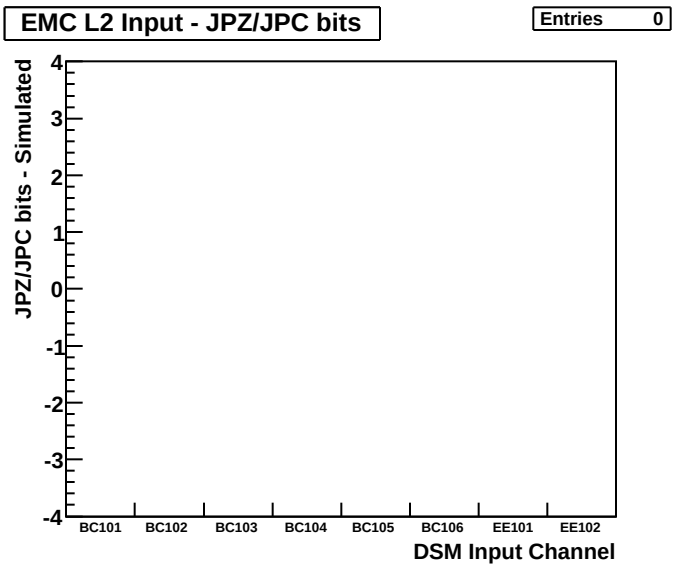

EMC L2 Input - JPX/JPA bits
Entries 2293184
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
138 49 42 29 9 56 6 40
929 395 300 160 31 344 45 254
622 233 158 91 19 281 19 78
284999 285971 286148 286368 286589 285967 286578 286276

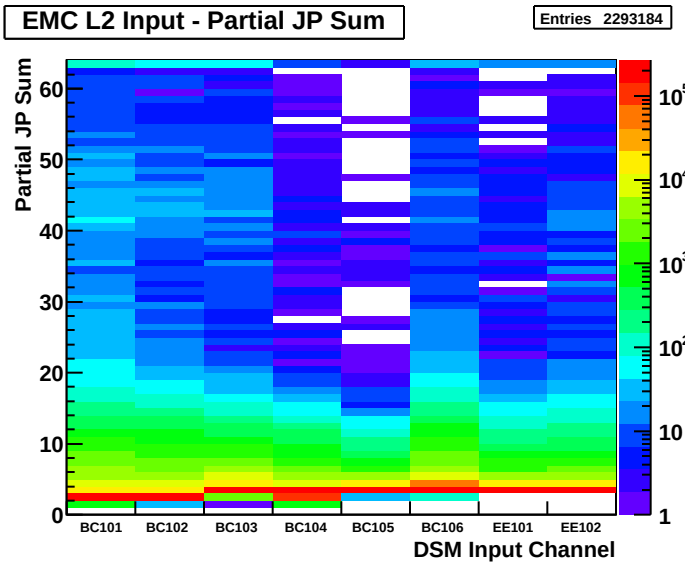

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

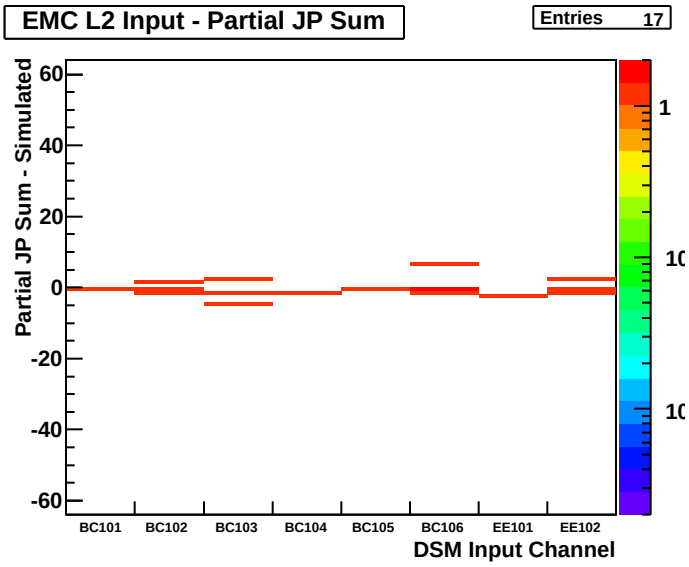

EMC L2 Input - JPY/JPB bits
Entries 2293184
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
117 53 35 18 10 62 10 12
767 268 217 103 11 419 80 113
655 238 154 106 18 327 12 42
285109 286089 286242 286421 286609 285940 286546 286481

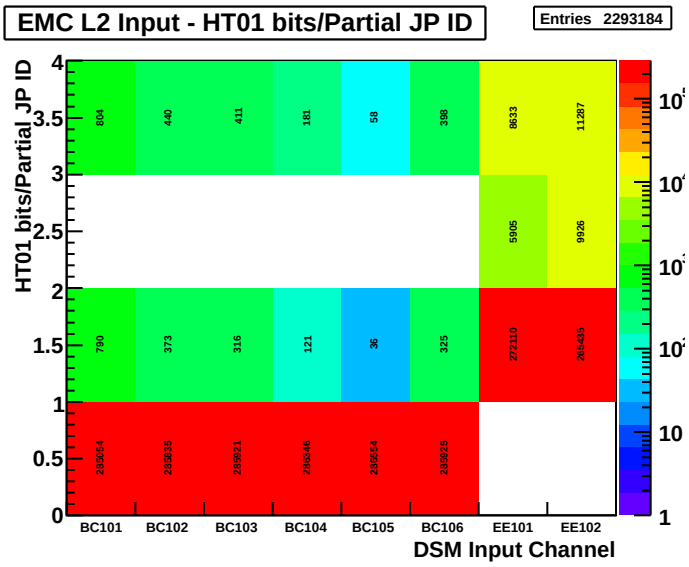

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

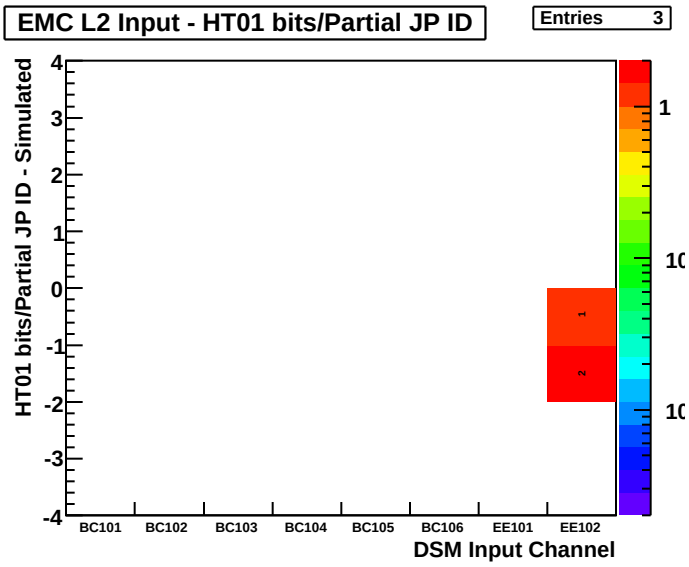

EMC L2 Input - JPZ/JPC bits
Entries 2293184
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
181 87 72 20 8 64 13 9
768 429 358 94 17 354 95 63
435 195 146 42 18 238 32 57
285264 285937 286072 286492 286605 285992 286508 286519

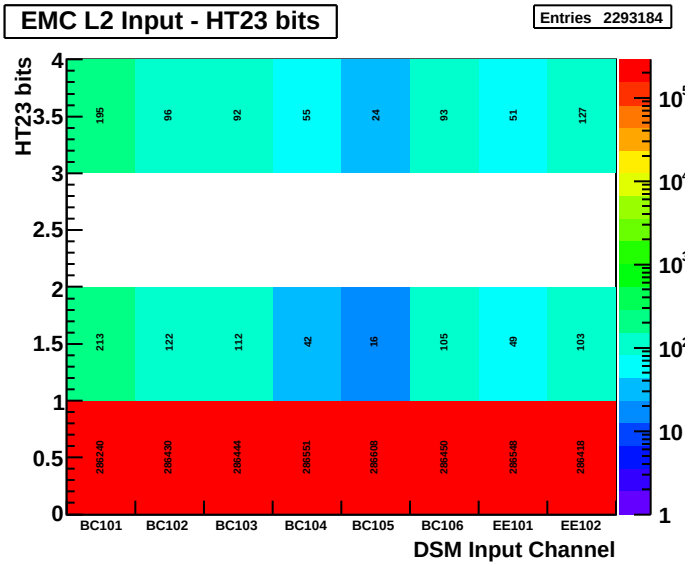

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

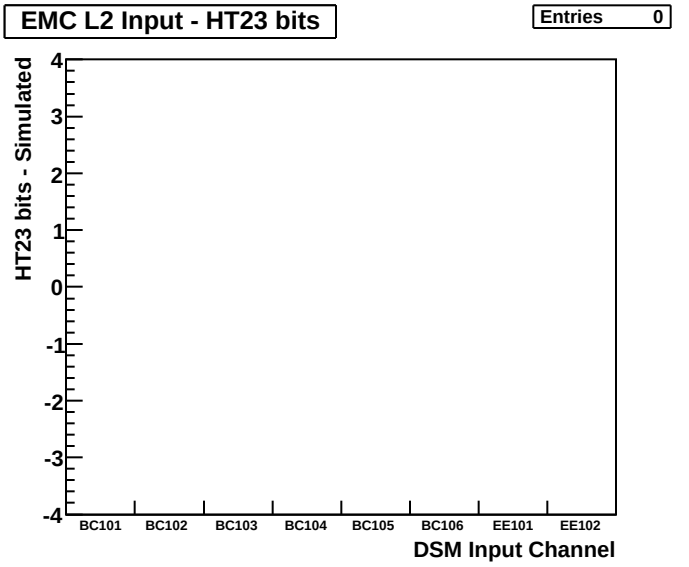

EMC L2 Input - Partial JP Sum
Entries 2293184
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

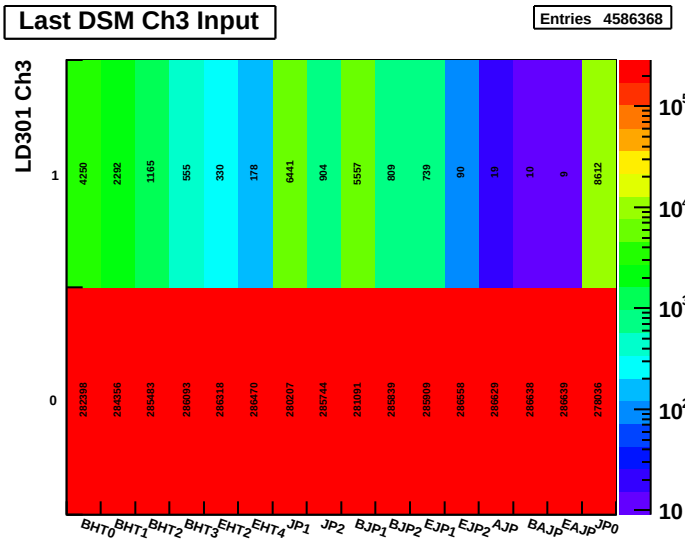

EMC L2 Input - Partial JP Sum
Entries 17
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

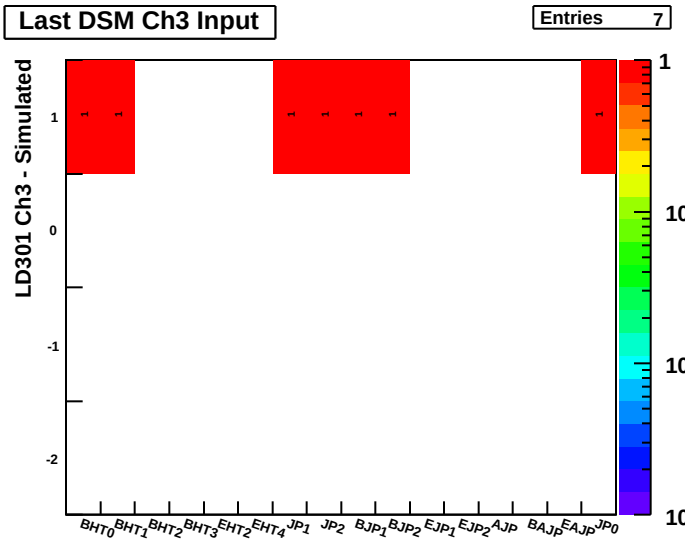

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2293184
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

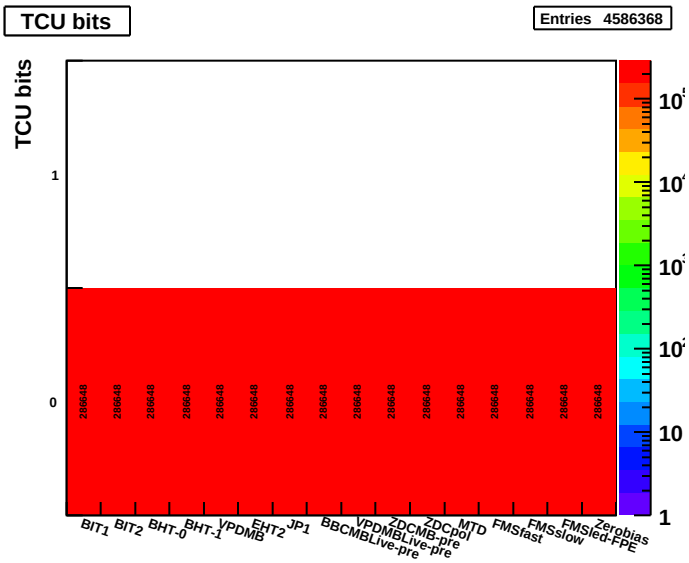

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

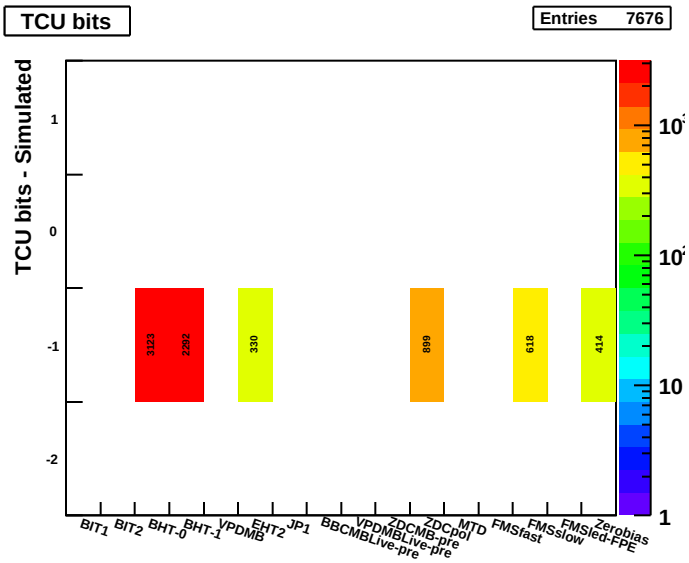

EMC L2 Input - HT23 bits
Entries 2293184
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

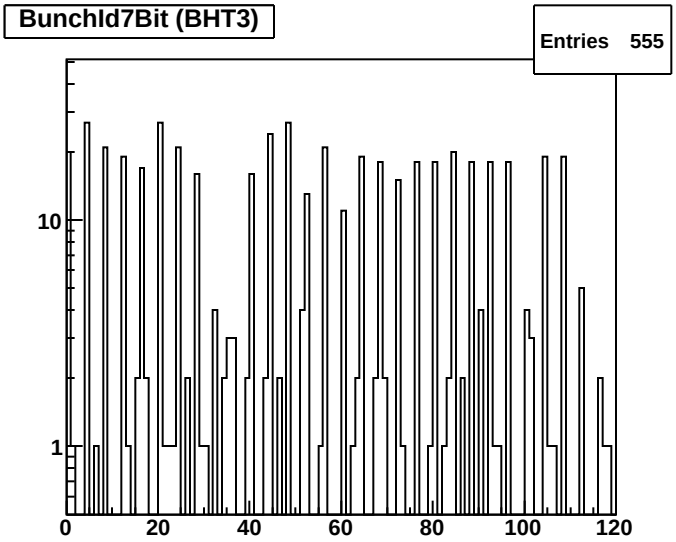

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

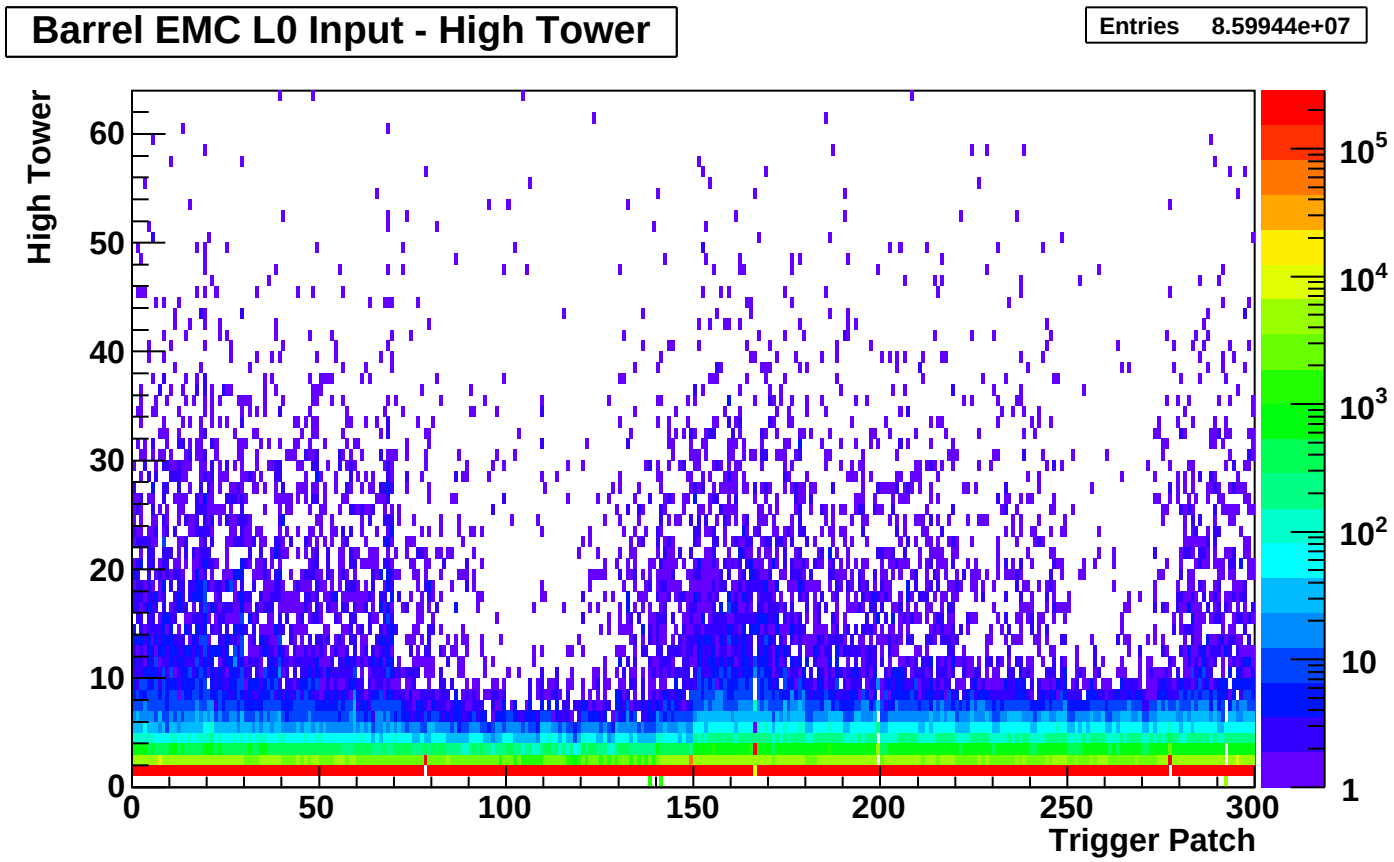

Last DSM Ch3 Input
Entries 4586368
LD301 Ch3
4250
2292
1165
555
330
178
6441
904
5557
809
739
90
19
10
9
8612
282398
284356
285483
286093
286318
286470
280207
285744
281091
285839
285909
286558
286629
286638
286639
278036
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

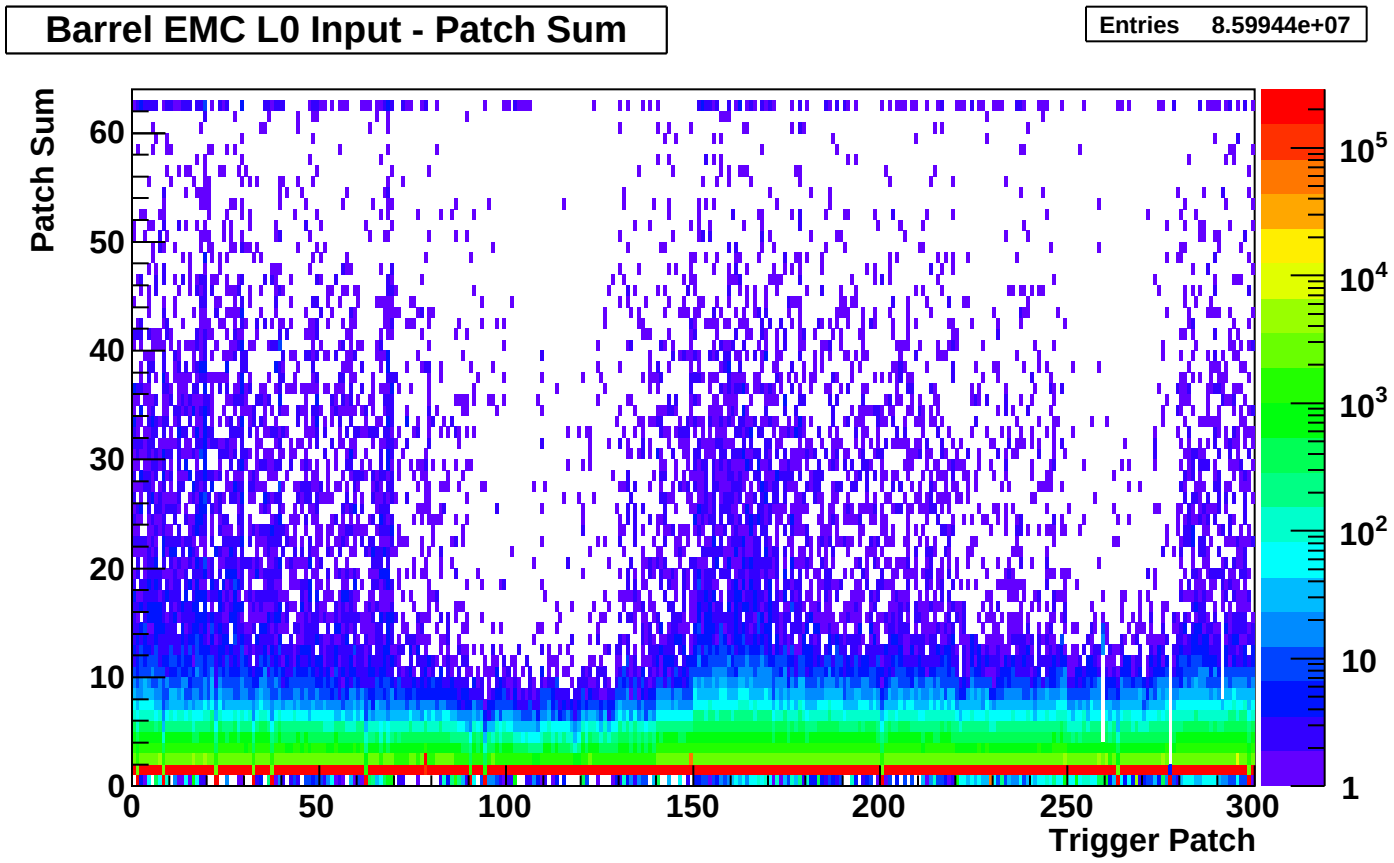

Last DSM Ch3 Input
Entries 7
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

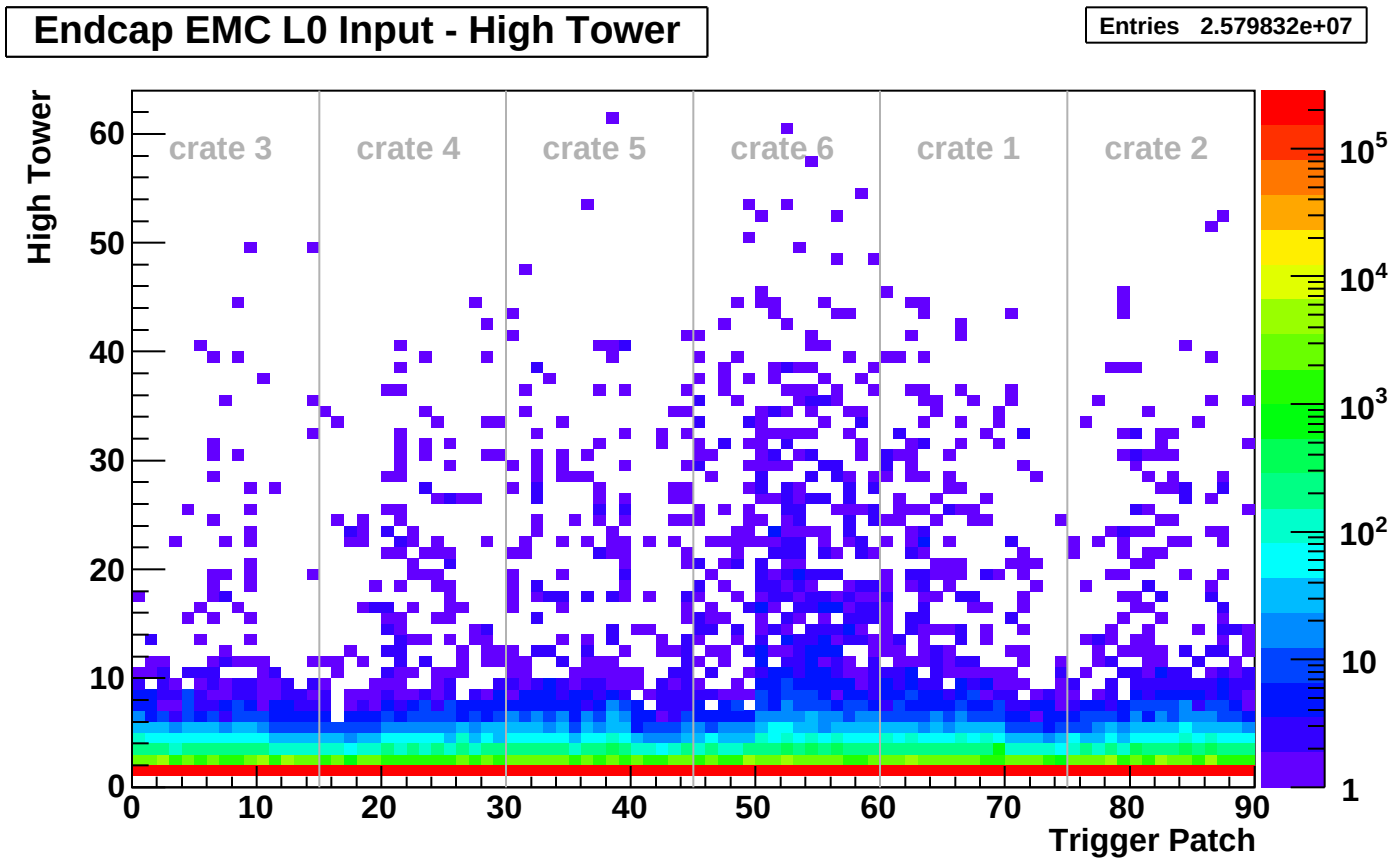

TCU bits
Entries 4586368
286648
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

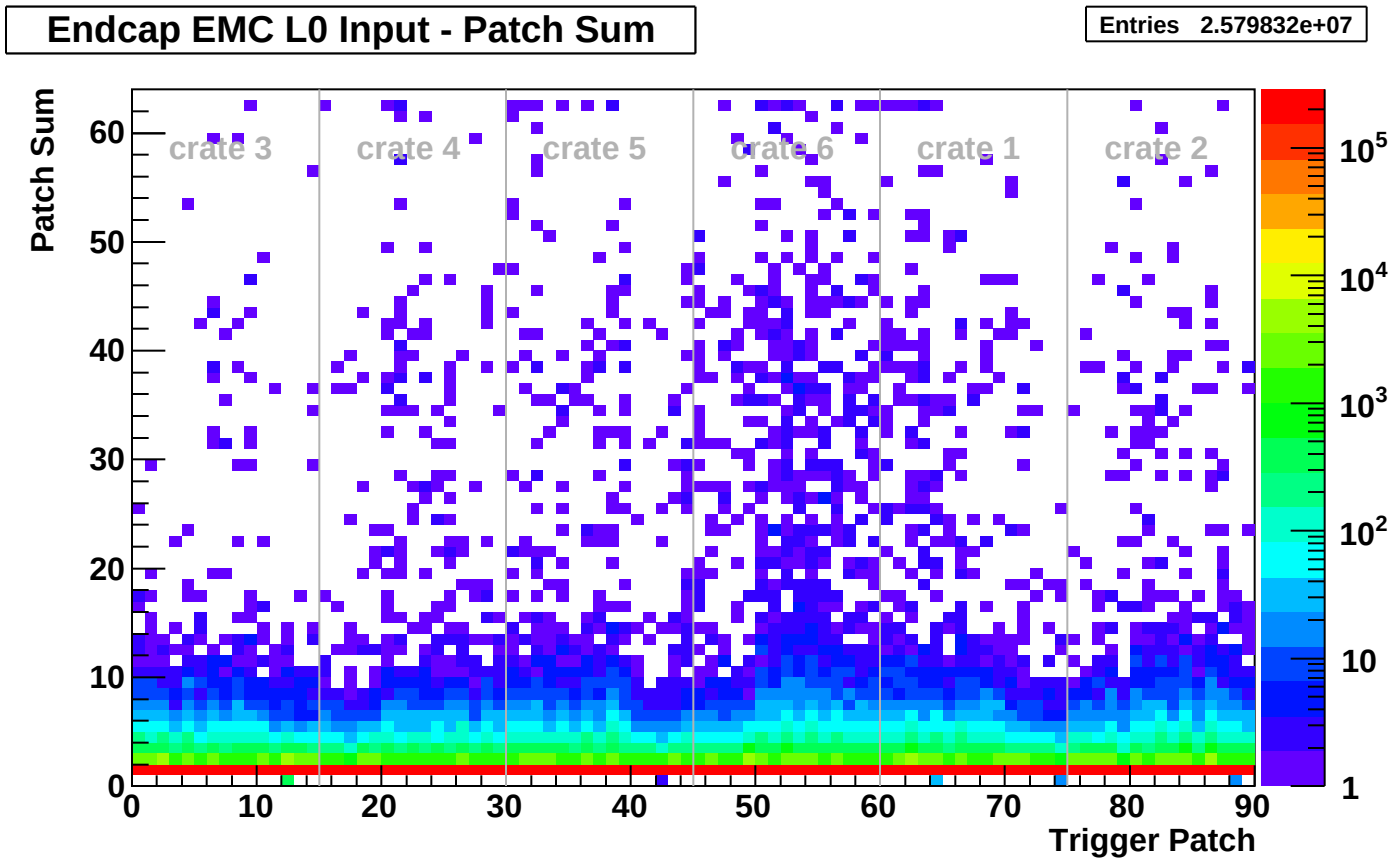

TCU bits
Entries 7676
TCU bits - Simulated
3123
2292
330
899
618
414
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

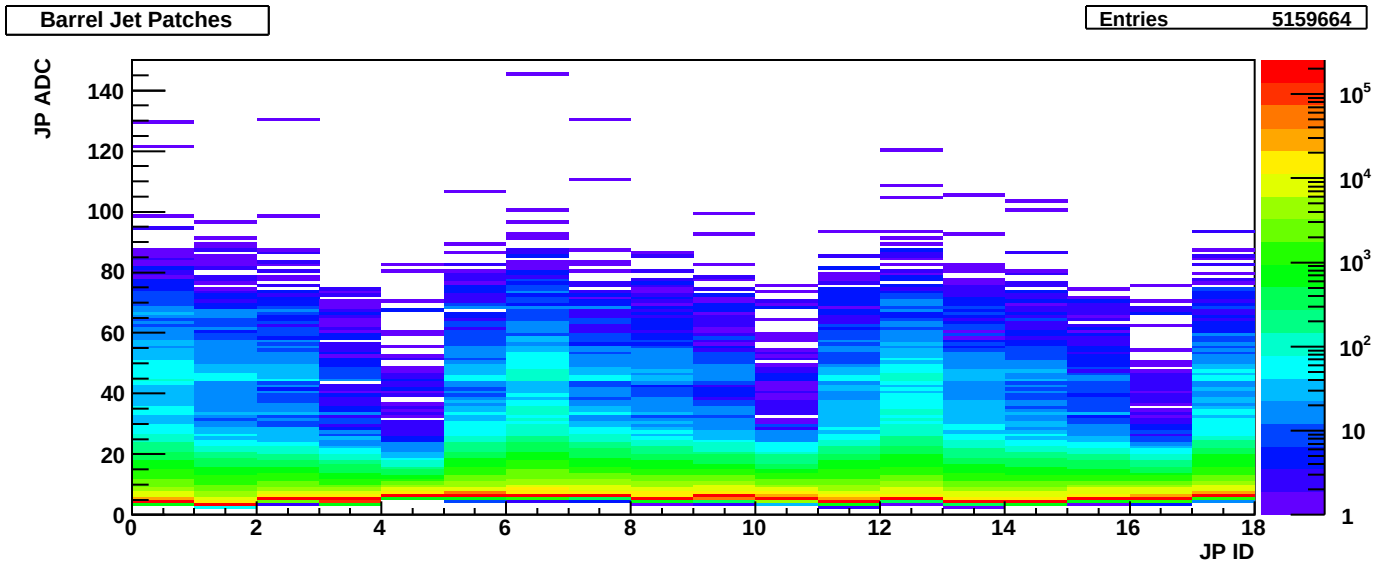

BunchId7Bit (BHT3)
Entries 555
0 20 40 60 80 100 120

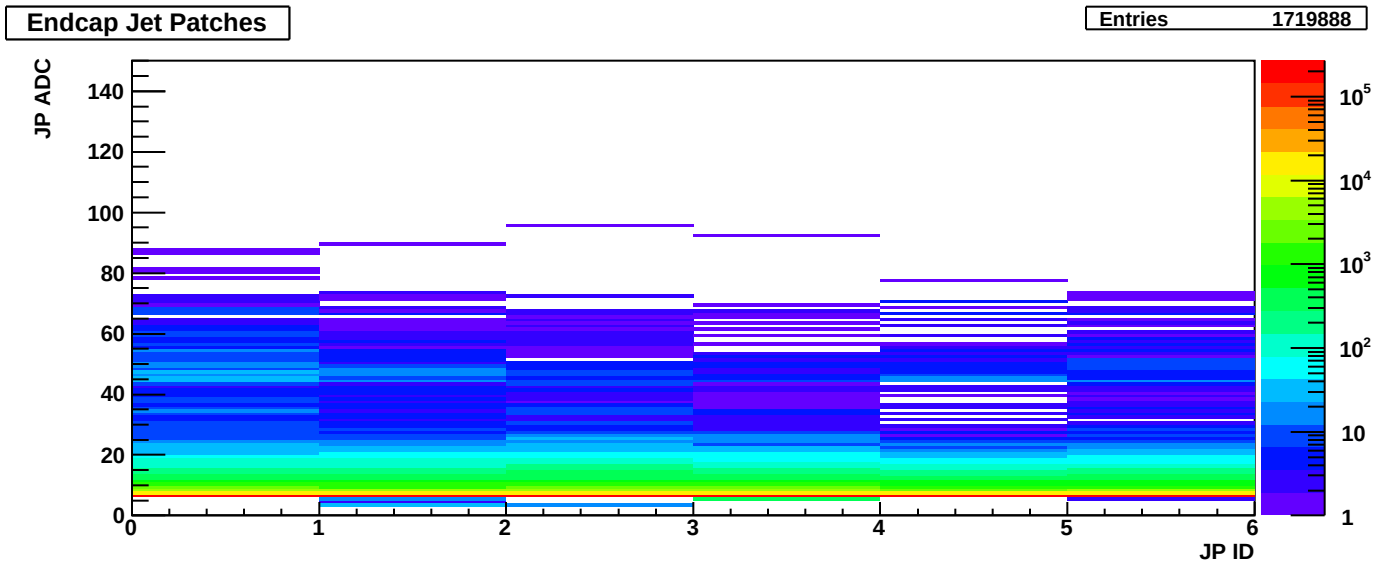

Barrel EMC L0 Input - High Tower
Entries 8.59944e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

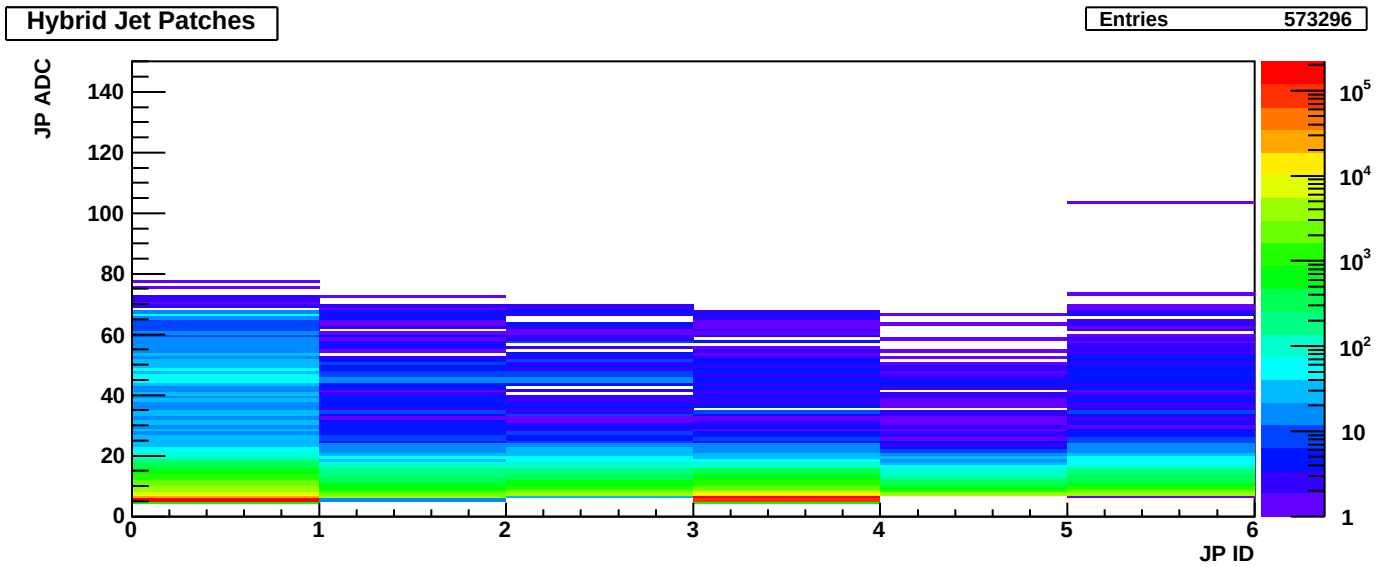

Barrel EMC L0 Input - Patch Sum
Entries 8.59944e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

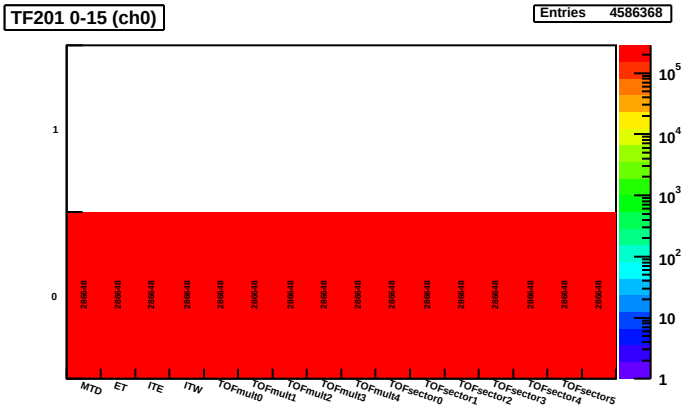

Endcap EMC L0 Input - High Tower
Entries 2.579832e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

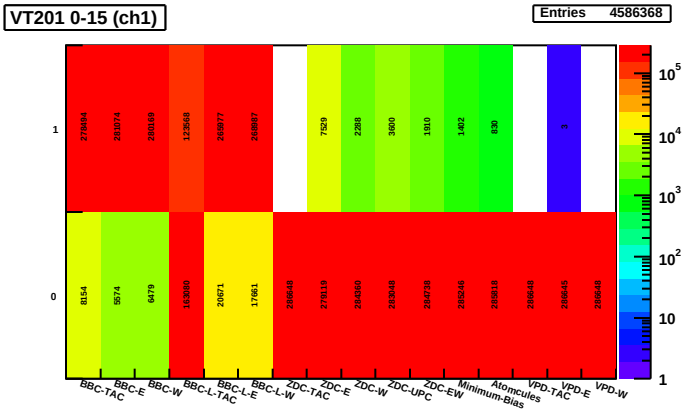

Endcap EMC L0 Input - Patch Sum
Entries 2.579832e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

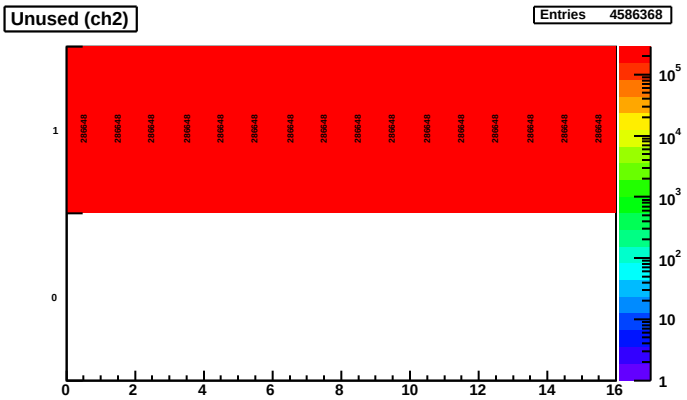

Barrel Jet Patches
Entries 5159664
JP ADC
JP ID

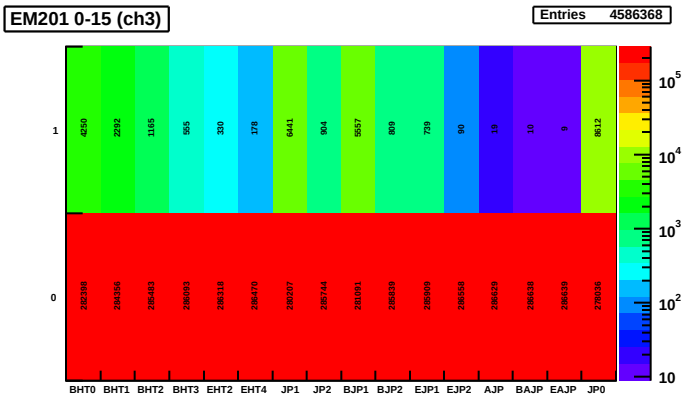

Endcap Jet Patches
Entries 1719888
JP ADC
JP ID

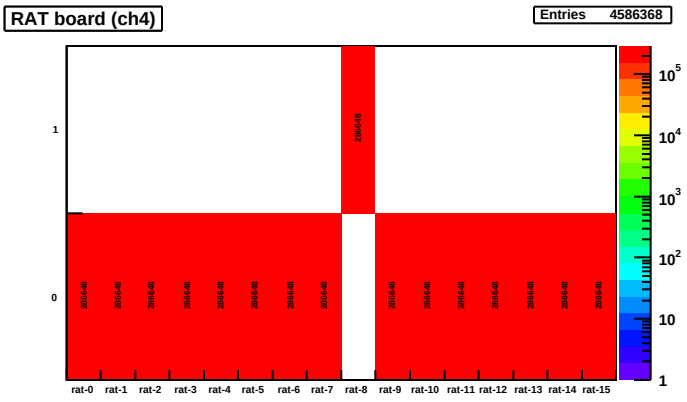

Hybrid Jet Patches
Entries 573296
JP ADC
JP ID

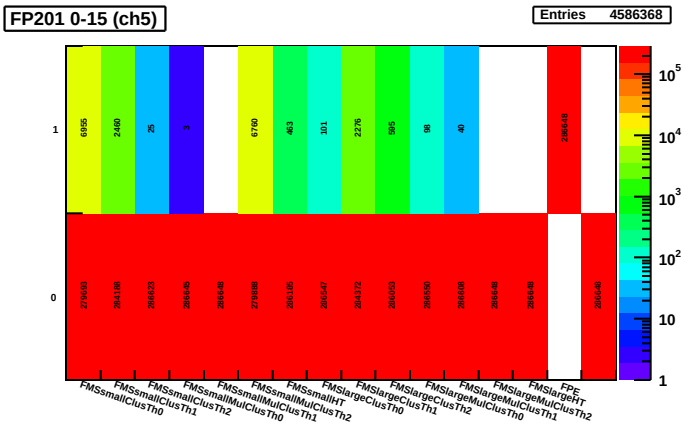

TF201 0-15 (ch0)
Entries 4586368
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

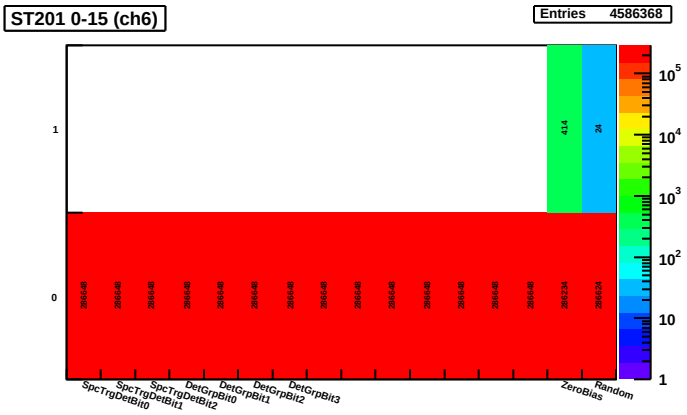

VT201 0-15 (ch1)
Entries 4586368
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

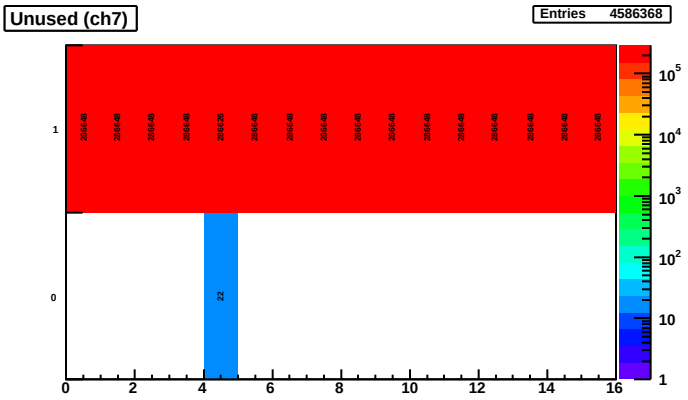

Unused (ch2)
Entries 4586368

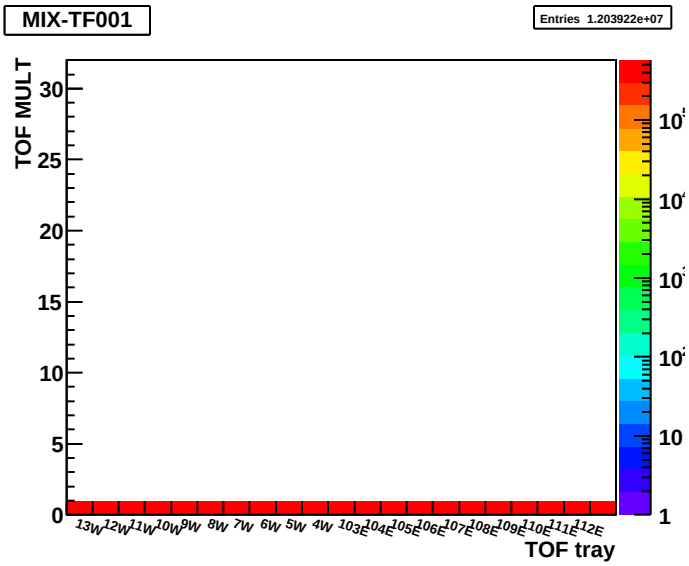

EM201 0-15 (ch3)
Entries 4586368
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

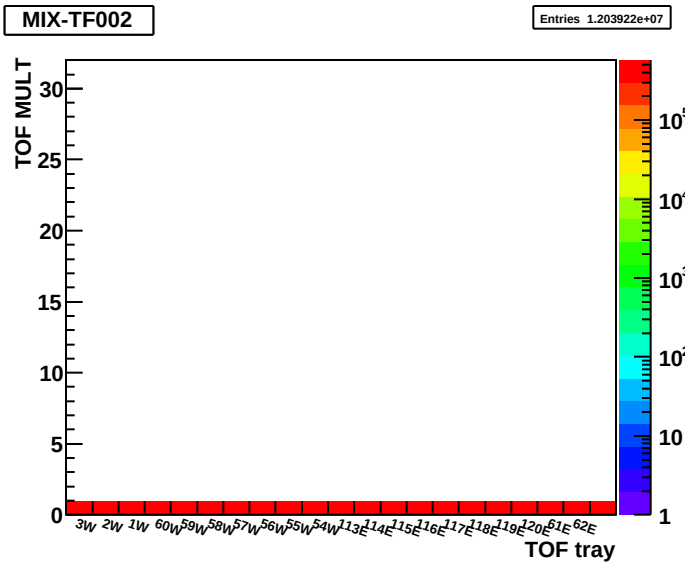

RAT board (ch4)
Entries 4586368
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

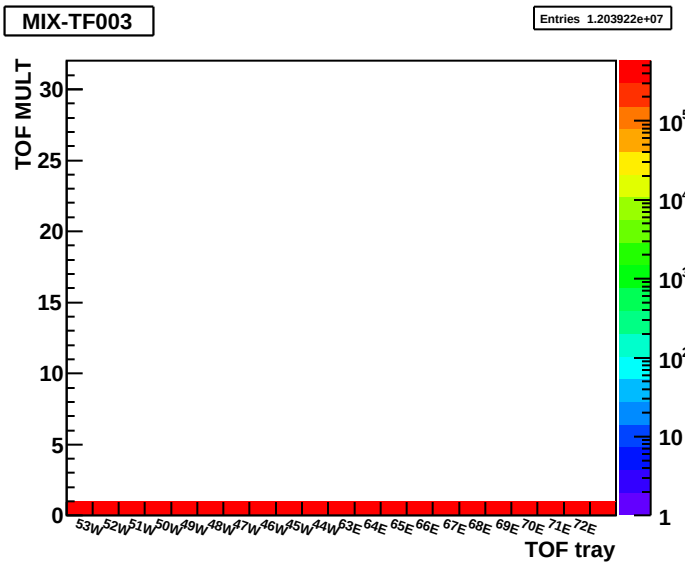

FP201 0-15 (ch5)
Entries 4586368
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

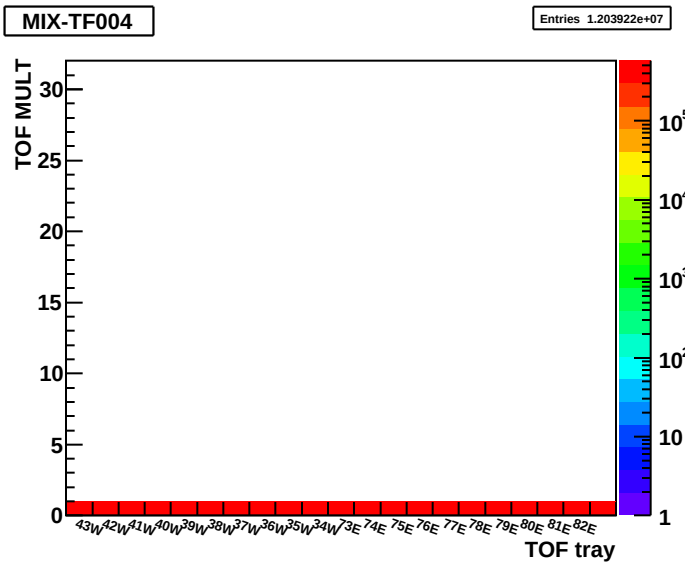

ST201 0-15 (ch6)
Entries 4586368
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

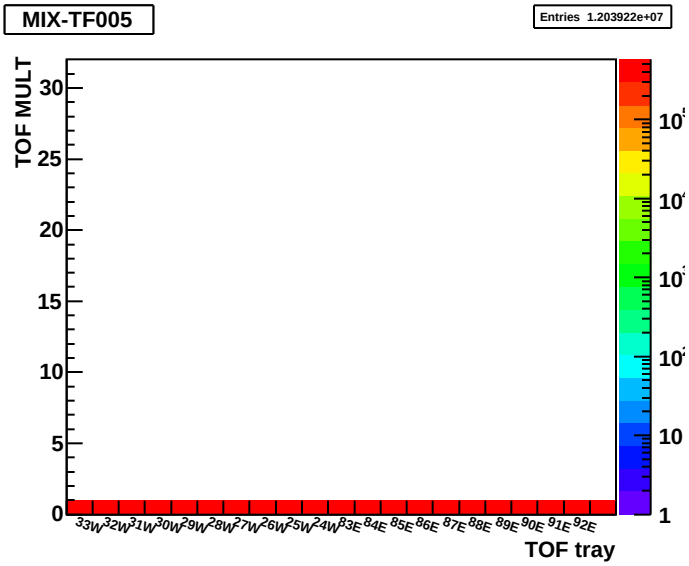

Unused (ch7)
Entries 4586368

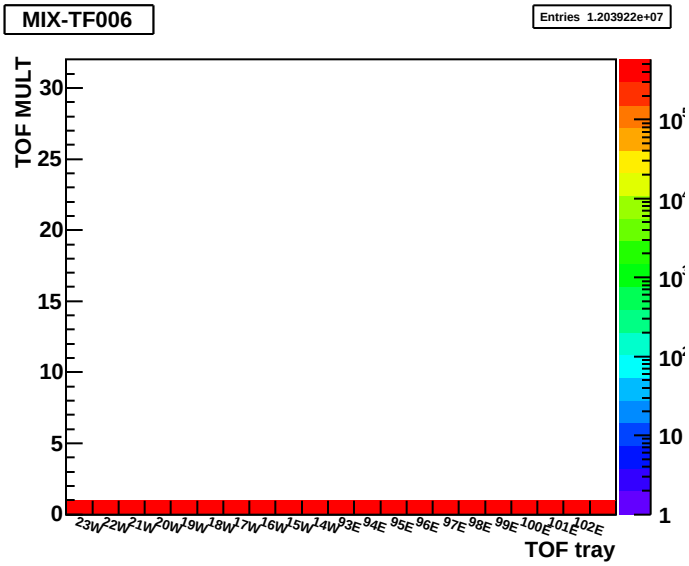

MIX-TF001
Entries 1.203922e+07
TOF MULT
TOF tray

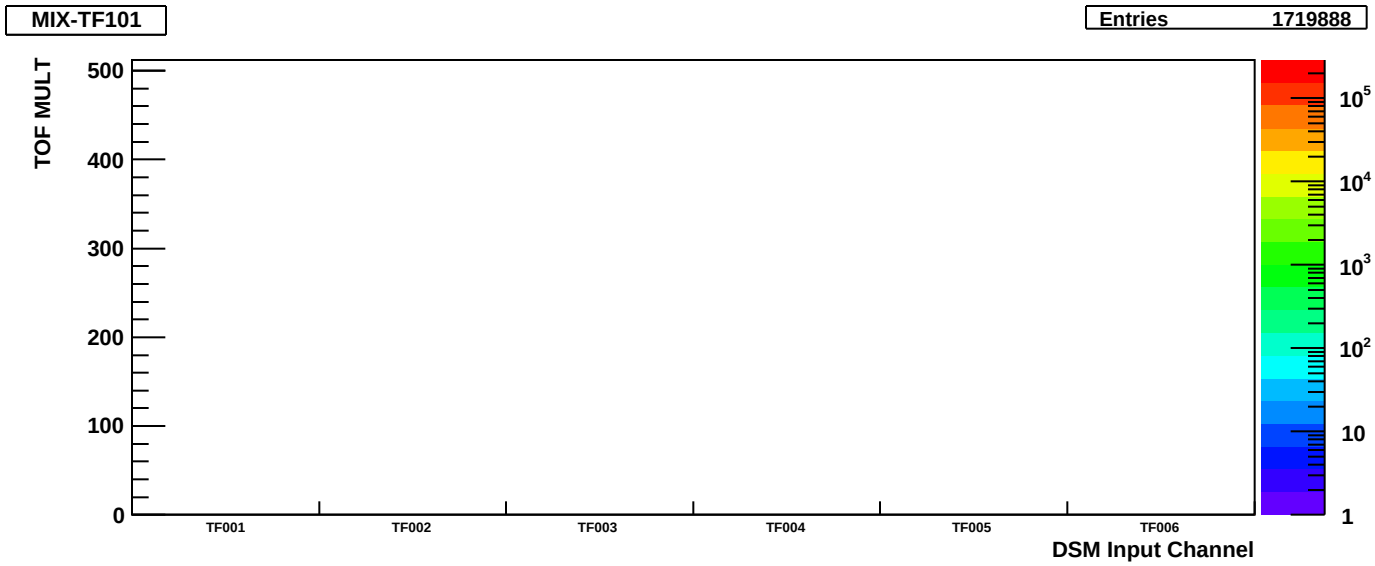

MIX-TF002
Entries 1.203922e+07
TOF MULT
TOF tray

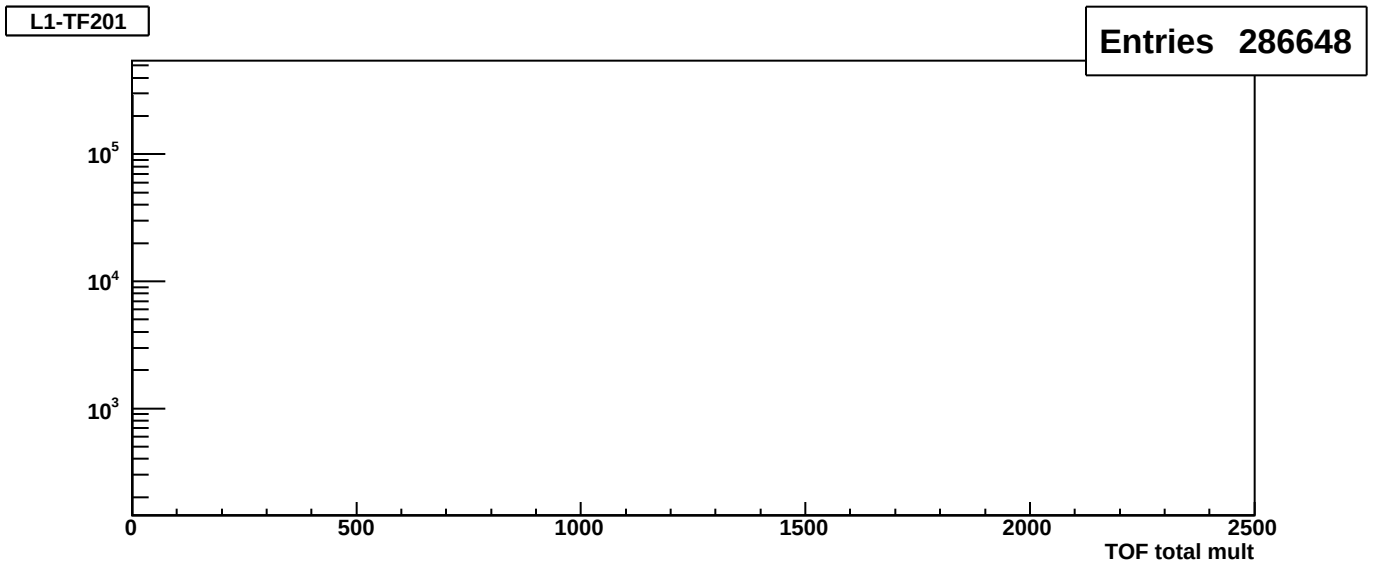

MIX-TF003
Entries 1.203922e+07
TOF MULT
TOF tray

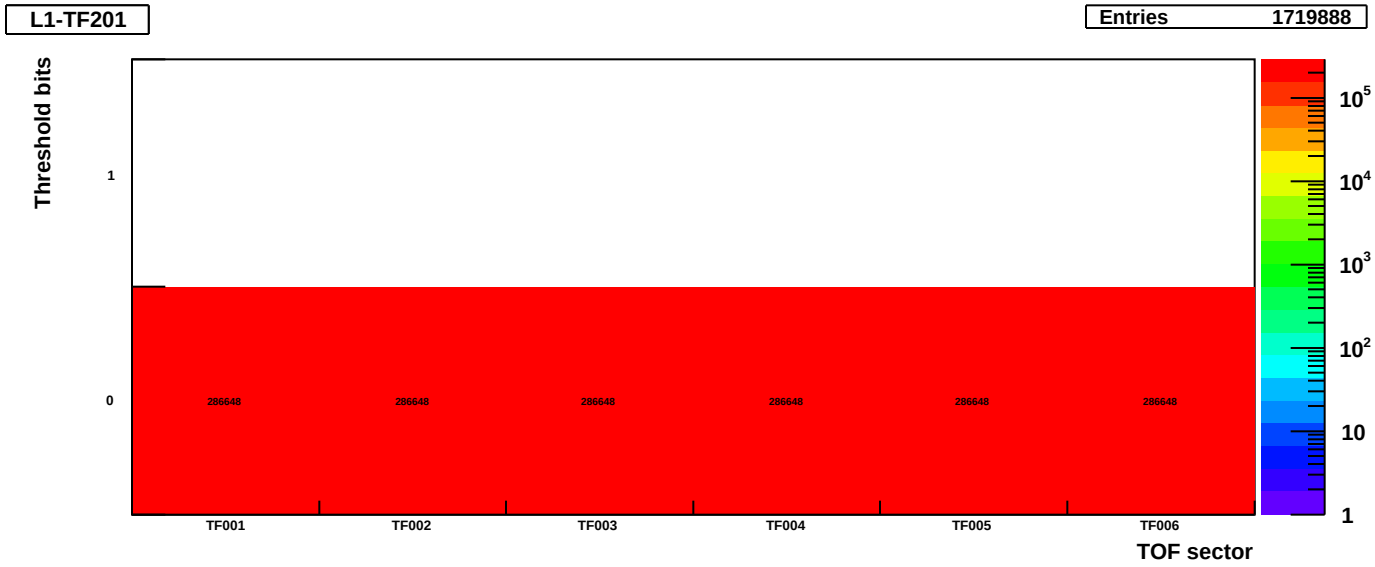

MIX-TF004
Entries 1.203922e+07
TOF MULT
TOF tray

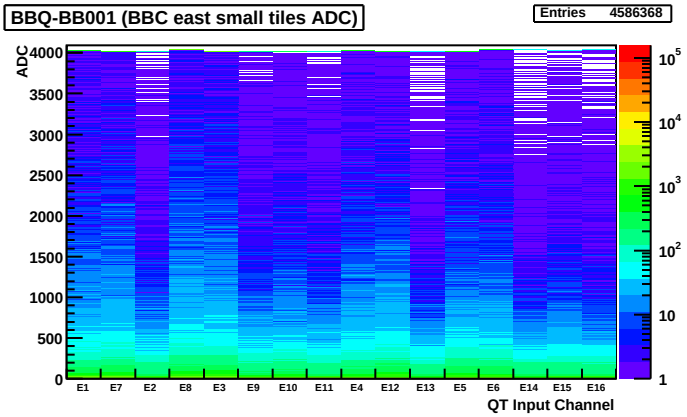

MIX-TF005
Entries 1.203922e+07
TOF MULT
TOF tray

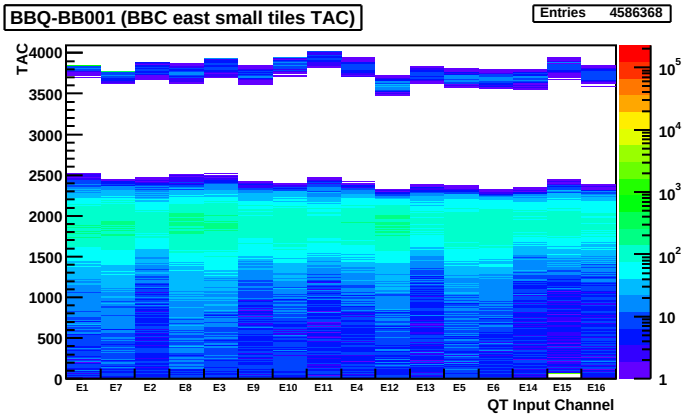

MIX-TF006
Entries 1.203922e+07
TOF MULT
TOF tray

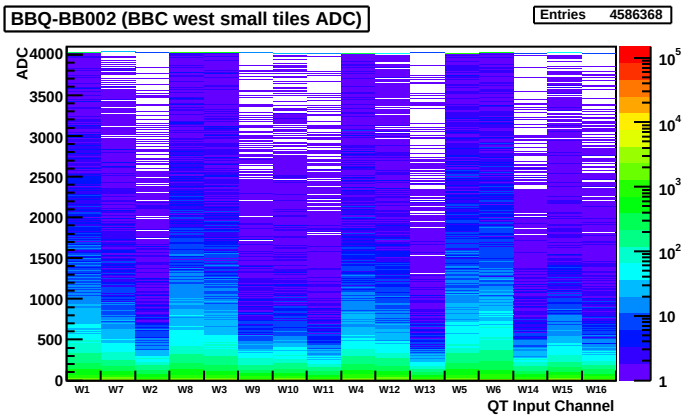

MIX-TF101
Entries 1719888
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

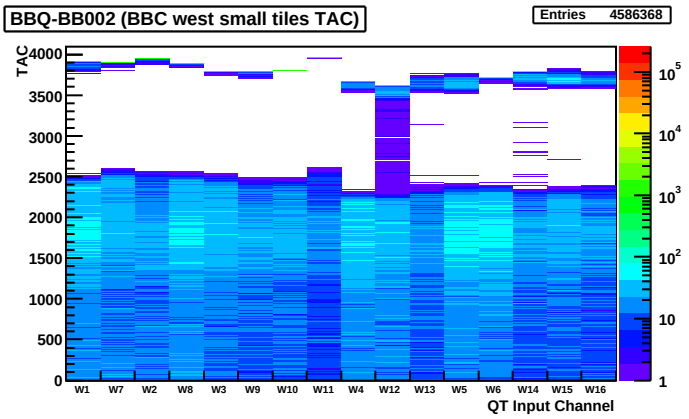

L1-TF201
Entries 286648
10^5
10^4
10^3
0
500
1000
1500
2000
2500
TOF total mult

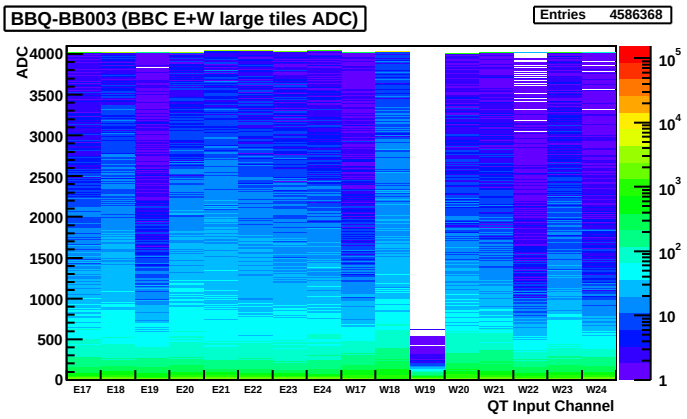

L1-TF201
Entries 1719888
Threshold bits
1
0
286648
286648
286648
286648
286648
286648
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

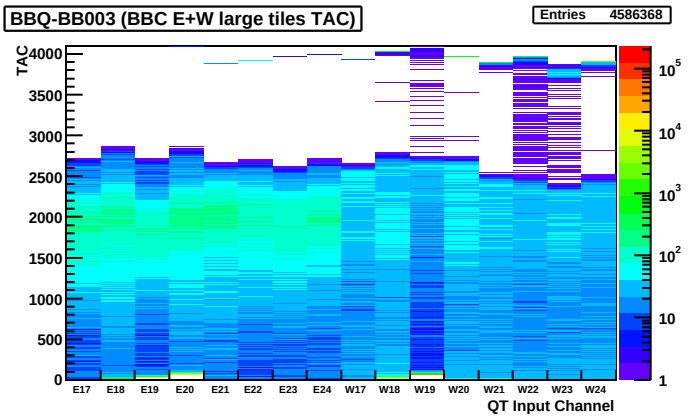

BBQ-BB001 (BBC east small tiles ADC)
Entries 4586368
ADC
QT Input Channel

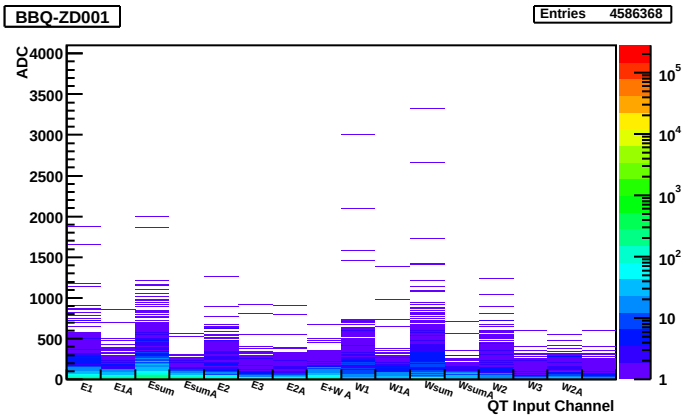

BBQ-BB001 (BBC east small tiles TAC)
Entries 4586368
TAC
QT Input Channel

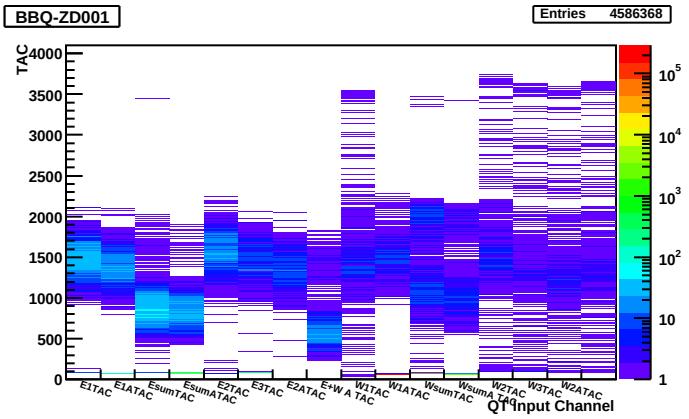

BBQ-BB002 (BBC west small tiles ADC)
Entries 4586368
ADC
QT Input Channel

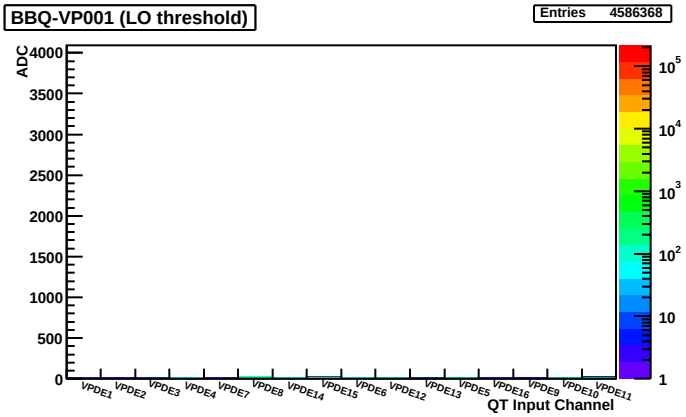

BBQ-BB002 (BBC west small tiles TAC)
Entries 4586368
TAC
QT Input Channel

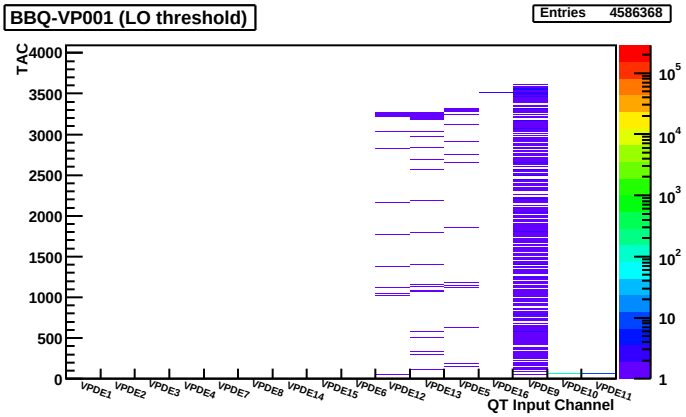

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4586368
ADC
QT Input Channel

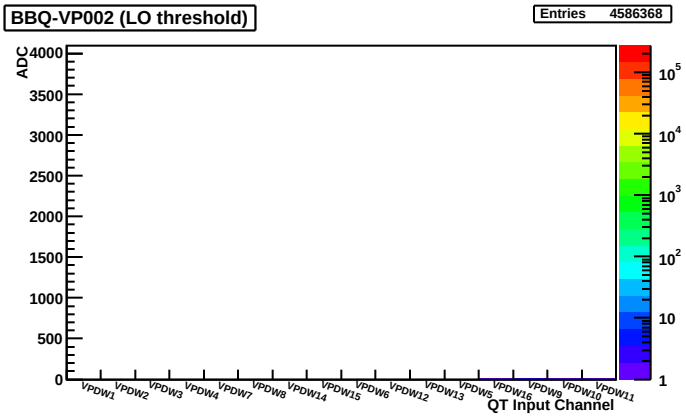

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4586368
TAC
QT Input Channel

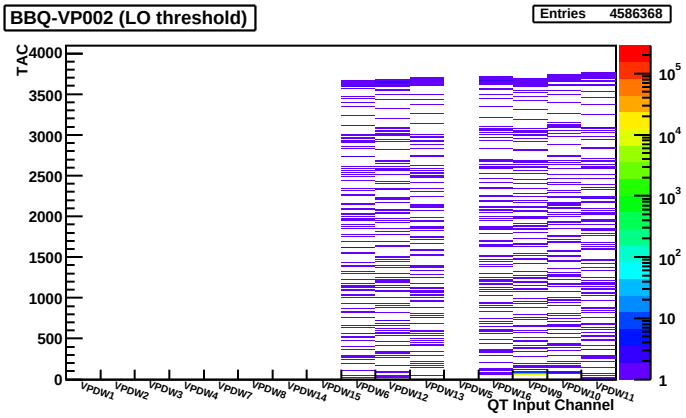

BBQ-ZD001
Entries 4586368
ADC
QT Input Channel

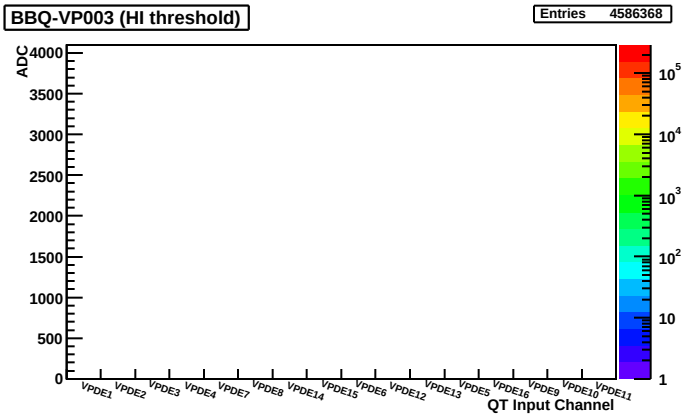

BBQ-ZD001
Entries 4586368
TAC
QT Input Channel

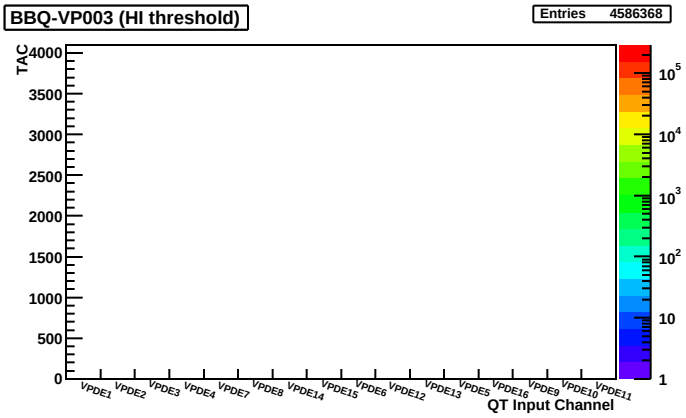

BBQ-VP001 (LO threshold)
Entries 4586368
ADC
QT Input Channel

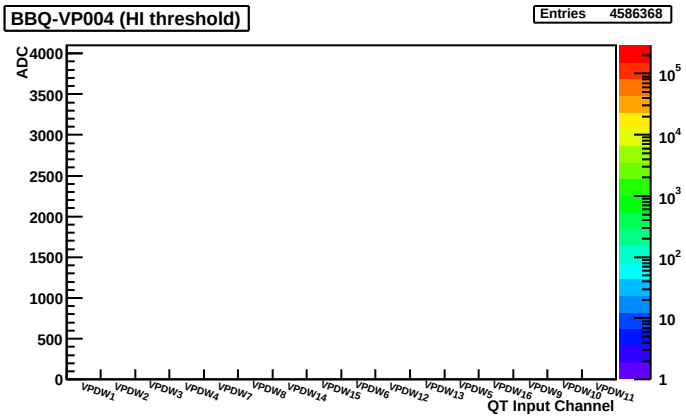

BBQ-VP001 (LO threshold)
Entries 4586368
TAC
QT Input Channel

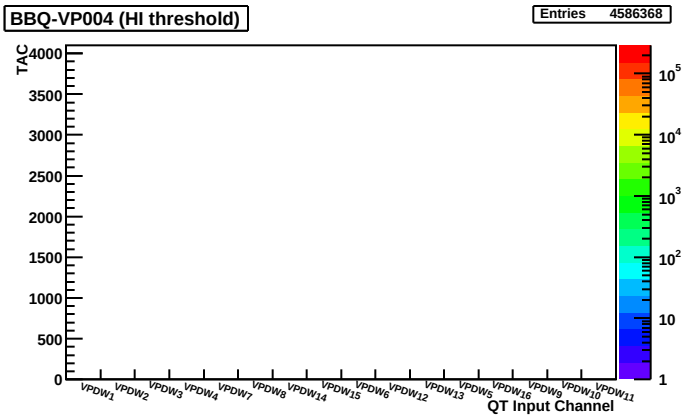

BBQ-VP002 (LO threshold)
Entries 4586368
ADC
QT Input Channel

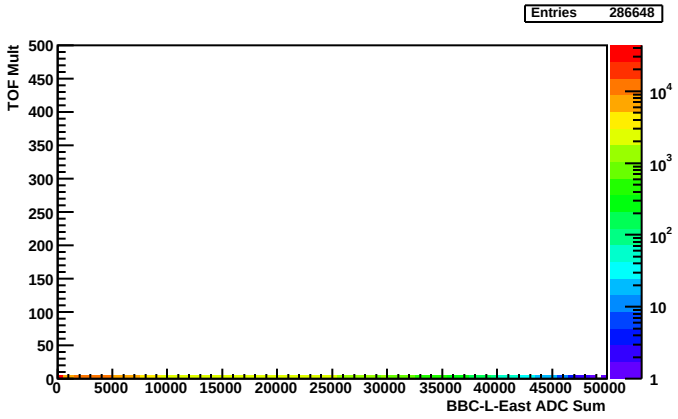

BBQ-VP002 (LO threshold)
Entries 4586368
TAC
QT Input Channel

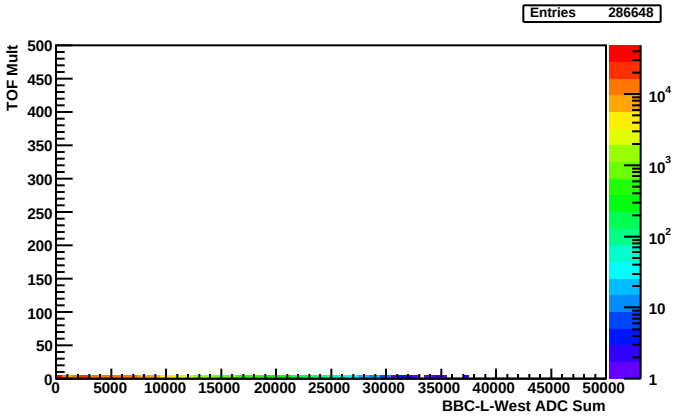

BBQ-VP003 (HI threshold)
Entries 4586368
ADC
QT Input Channel

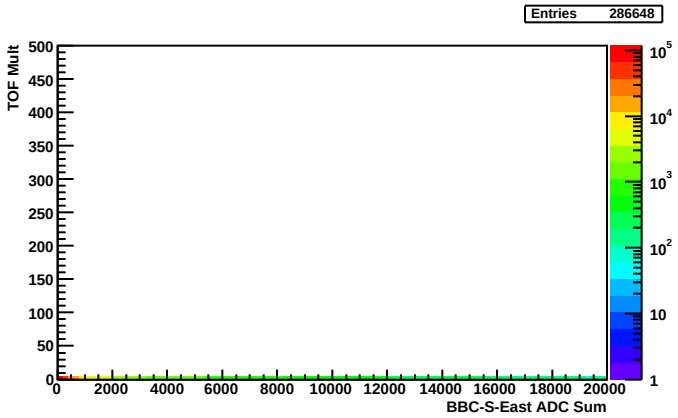

BBQ-VP003 (HI threshold)
Entries 4586368
TAC
QT Input Channel

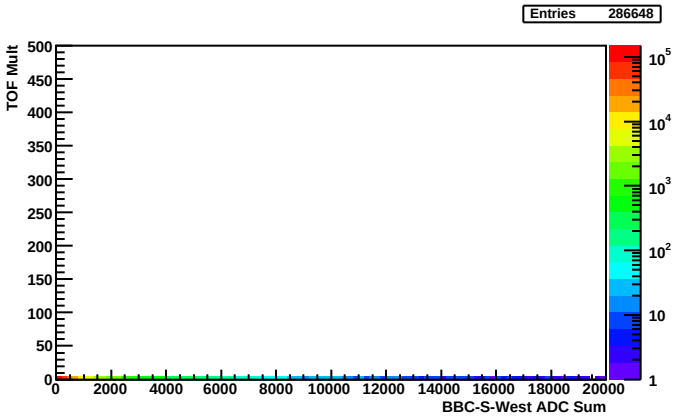

BBQ-VP004 (HI threshold)
Entries 4586368
ADC
QT Input Channel

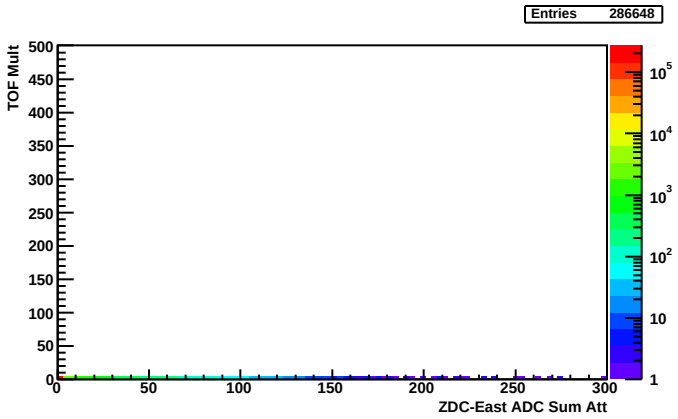

BBQ-VP004 (HI threshold)
Entries 4586368
TAC
QT Input Channel

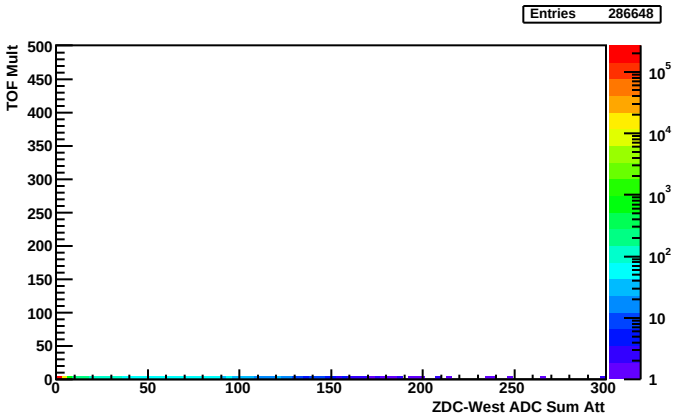

Entries 286648
TOF Mult
BBC-L-East ADC Sum

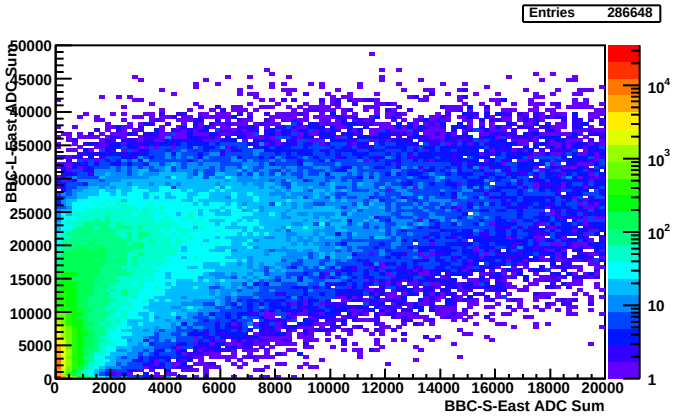

Entries 286648
TOF Mult
BBC-L-West ADC Sum

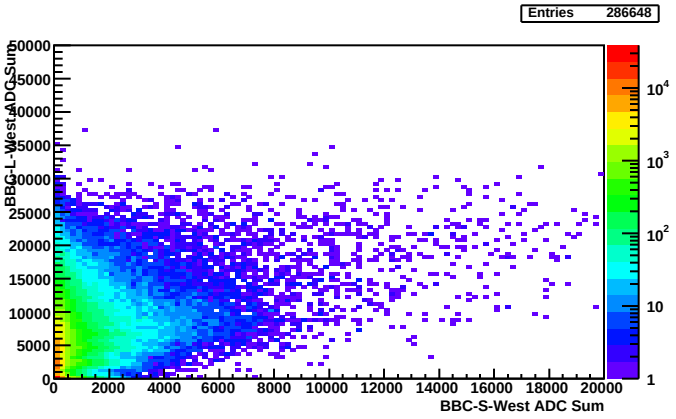

Entries 286648
TOF Mult
BBC-S-East ADC Sum

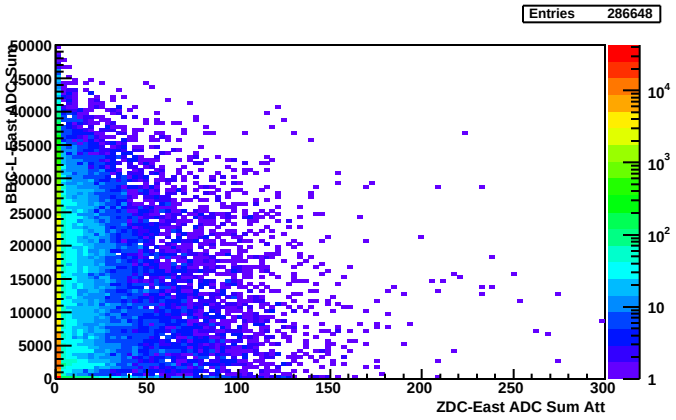

Entries 286648
TOF Mult
BBC-S-West ADC Sum

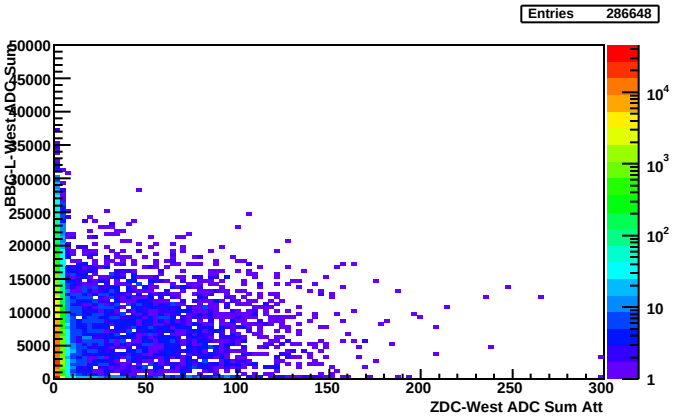

Entries 286648
TOF Mult
ZDC-East ADC Sum Att

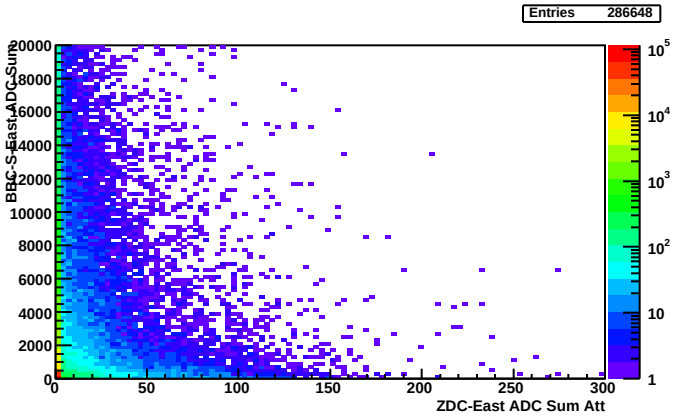

Entries 286648
TOF Mult
ZDC-West ADC Sum Att

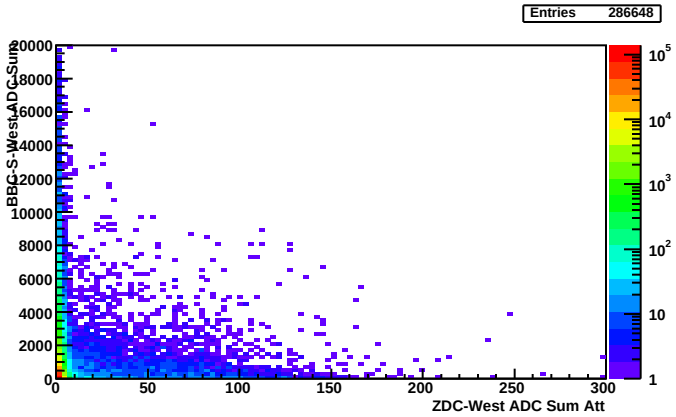

Entries 286648
BBC-L-East ADC Sum
BBC-S-East ADC Sum

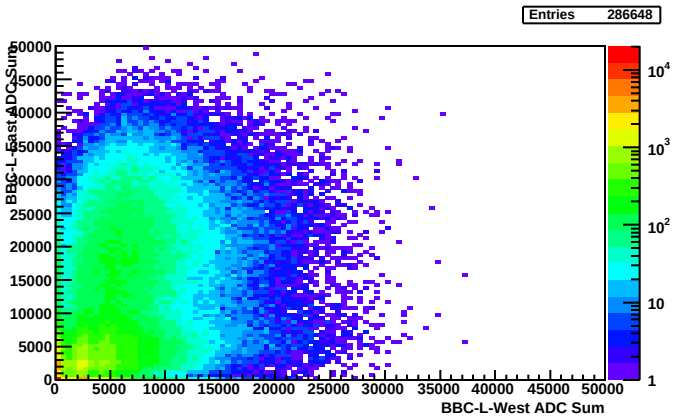

Entries 286648
BBC-L-West ADC Sum
BBC-S-West ADC Sum

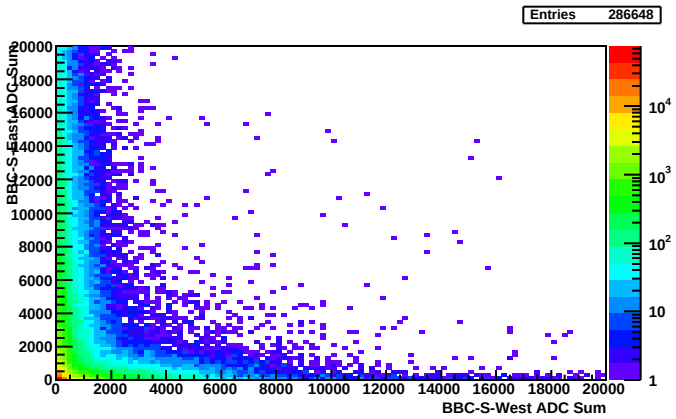

Entries 286648
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

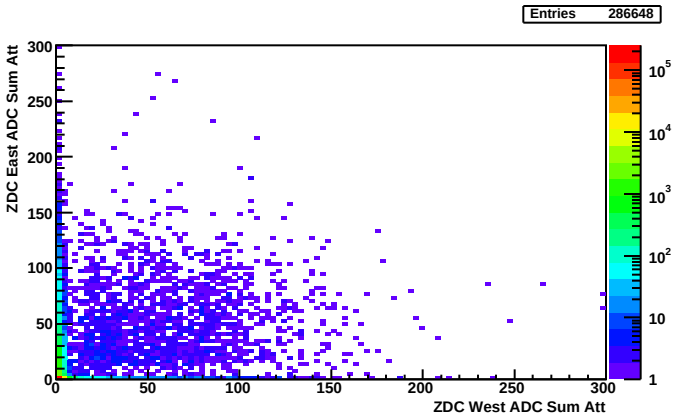

Entries 286648
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

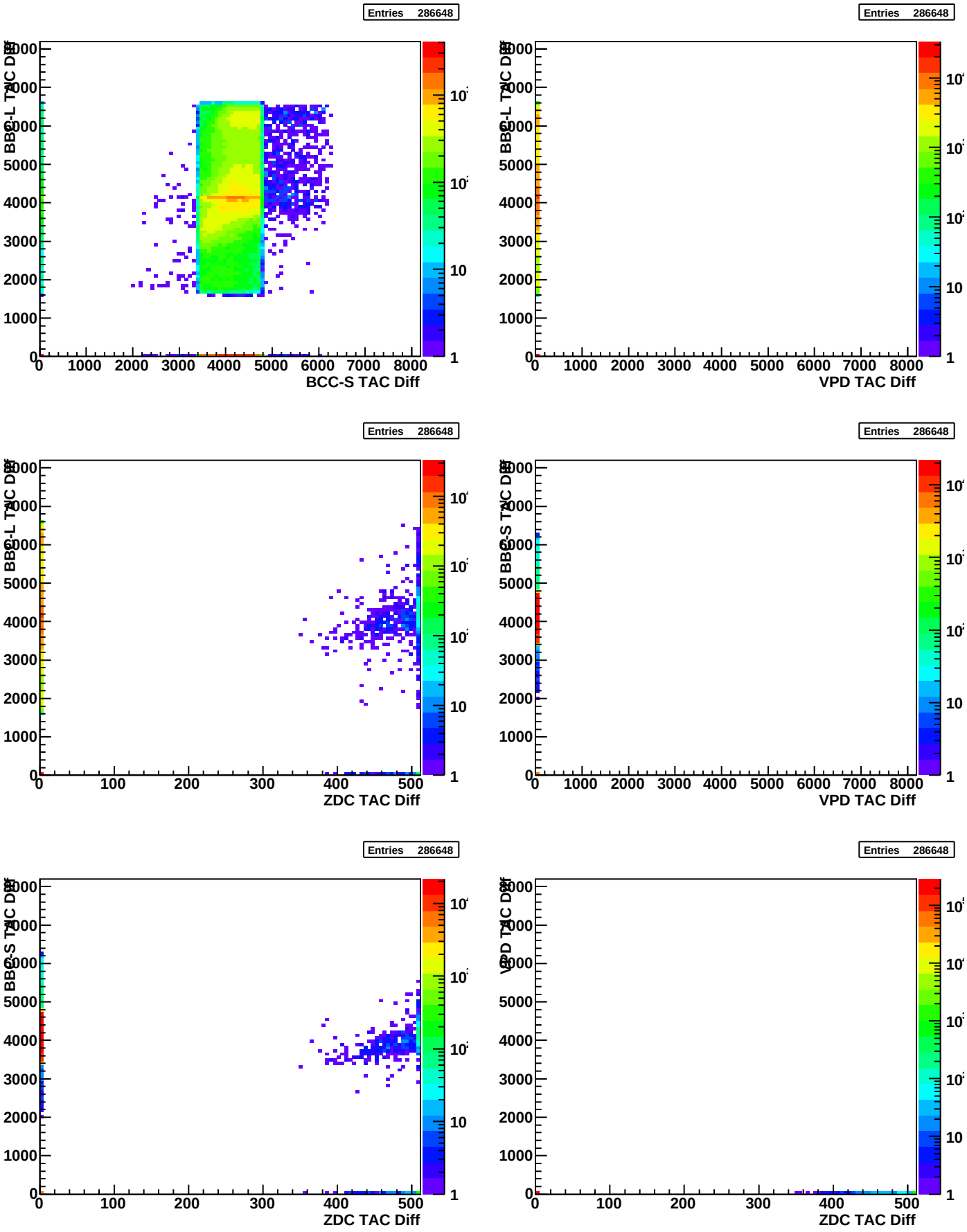

Entries 286648
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

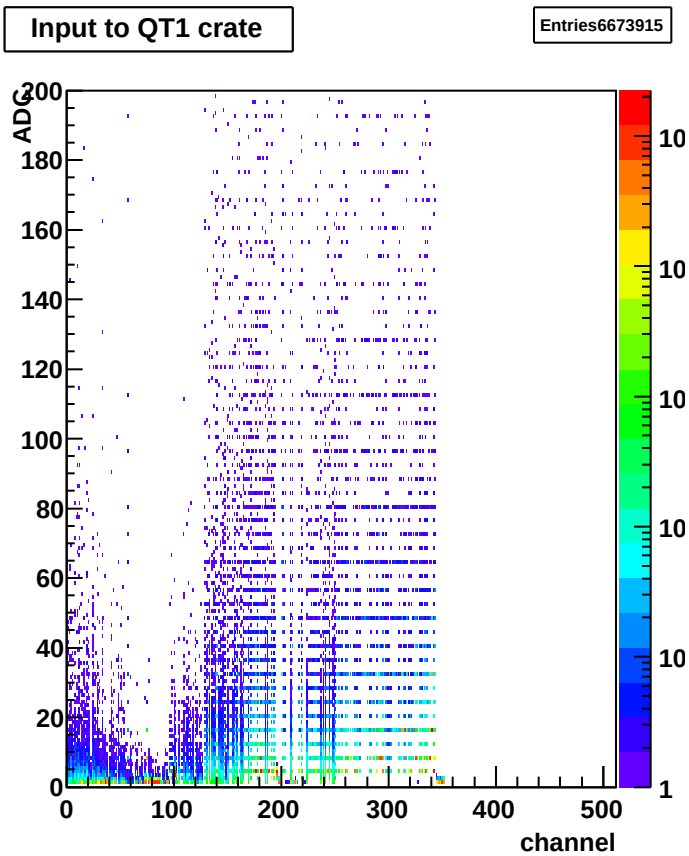

Entries 286648
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

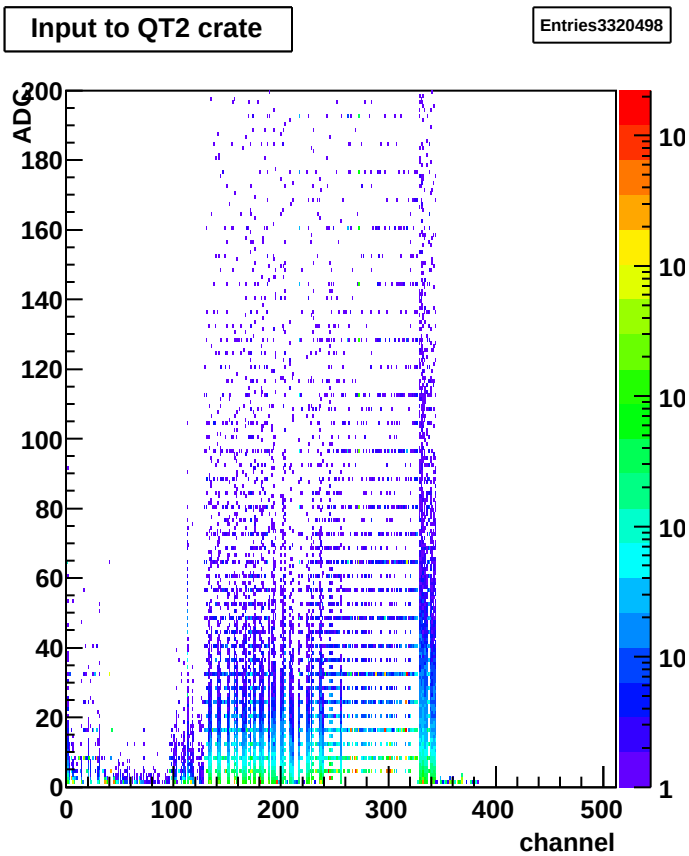

Entries 286648
BBC-L-East ADC Sum
BBC-L-West ADC Sum

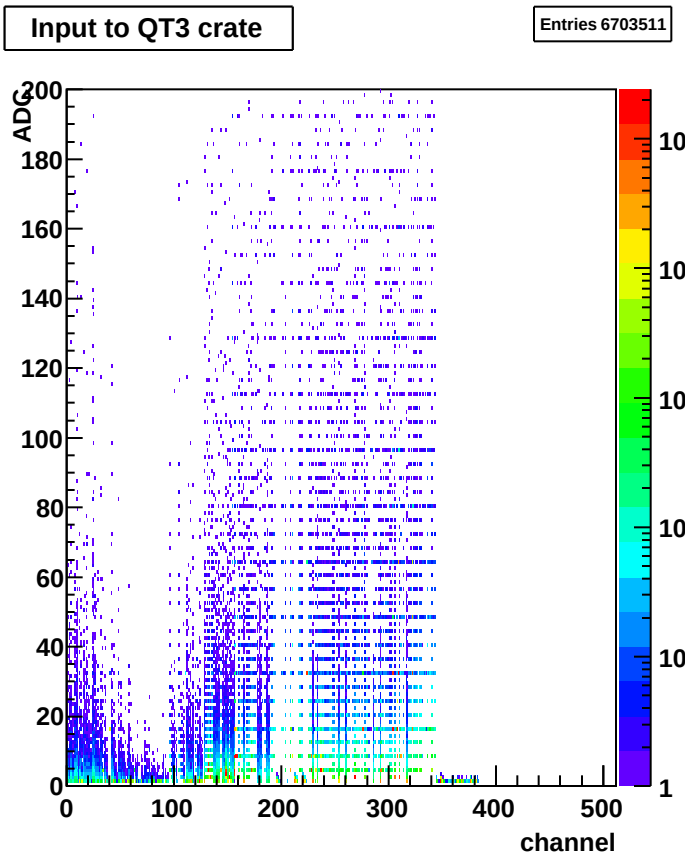

Entries 286648
BBC-S-East ADC Sum
BBC-S-West ADC Sum

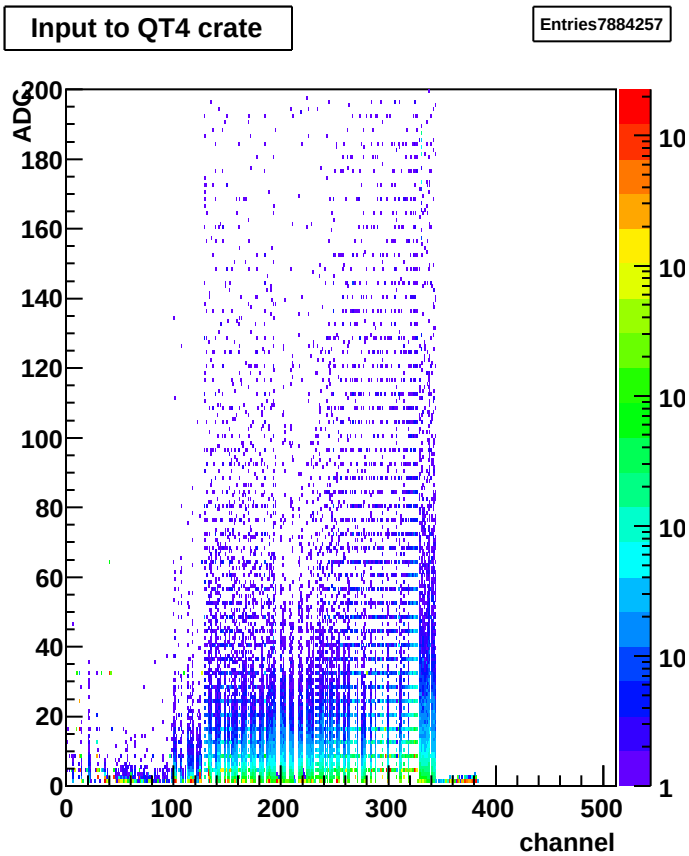

Entries 286648
ZDC East ADC Sum Att
ZDC West ADC Sum Att

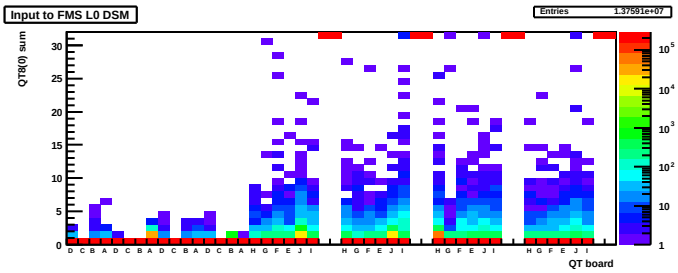

Entries 286648
BBC-L TAC Diff
BCC-S TAC Diff
Entries 286648
BBC-L TAC Diff
VPD TAC Diff
Entries 286648
BBC-L TAC Diff
ZDC TAC Diff
Entries 286648
BBC-S TAC Diff
VPD TAC Diff
Entries 286648
BBC-S TAC Diff
ZDC TAC Diff
Entries 286648
VPD TAC Diff
ZDC TAC Diff

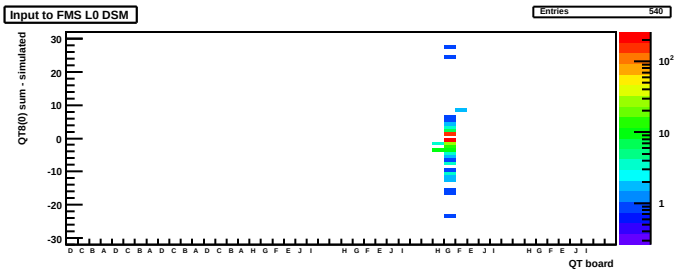

Input to QT1 crate
Entries6673915
ADC
channel

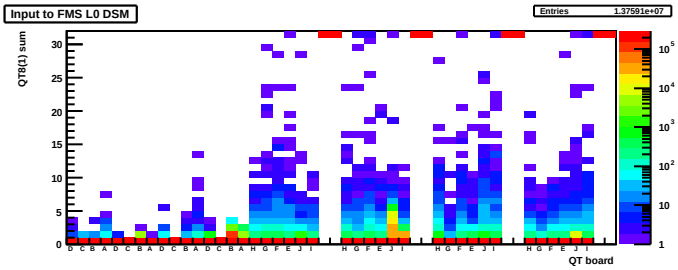

Input to QT2 crate
Entries3320498
ADC
channel

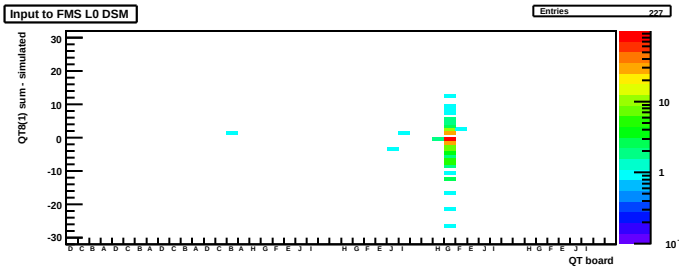

Input to QT3 crate
Entries 6703511
ADC
channel

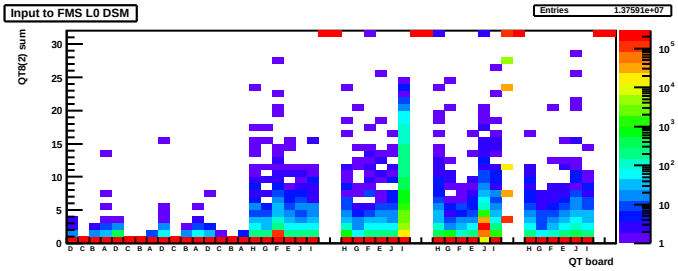

Input to QT4 crate
Entries7884257
ADC
channel

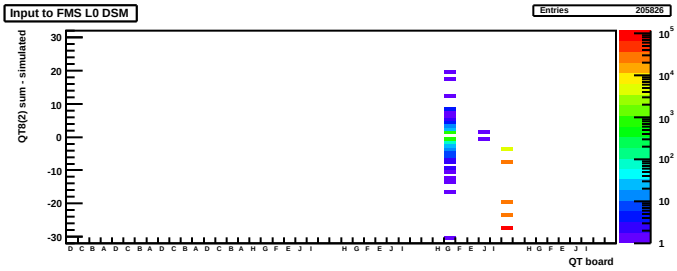

Input to FMS L0 DSM
Entries 1.37591e+07
QT8(0) sum
QT board

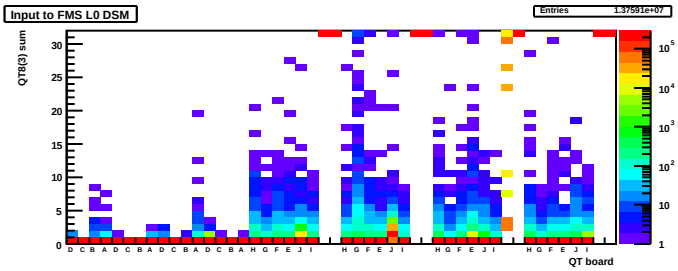

Input to FMS L0 DSM
Entries 540
QT8(0) sum - simulated
QT board

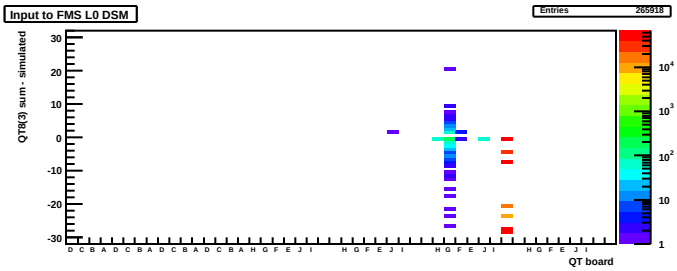

Input to FMS L0 DSM
Entries 1.37591e+07
QT8(1) sum
QT board

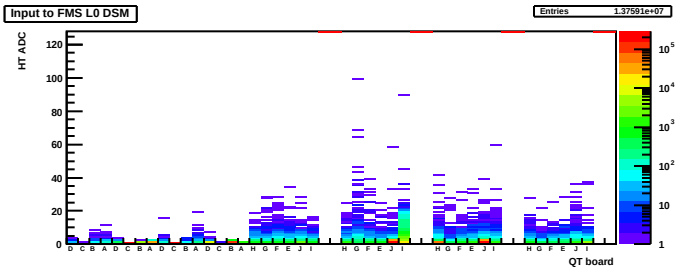

Input to FMS L0 DSM
Entries 227
QT8(1) sum - simulated
QT board

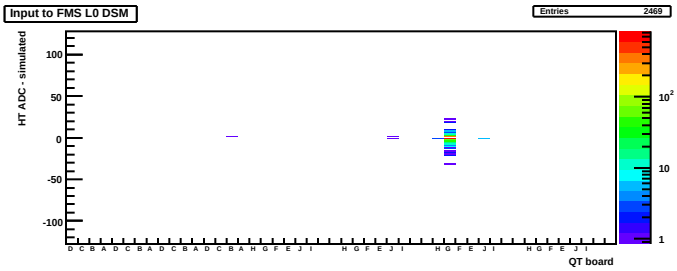

Input to FMS L0 DSM
Entries 1.37591e+07
QT8(2) sum
QT board

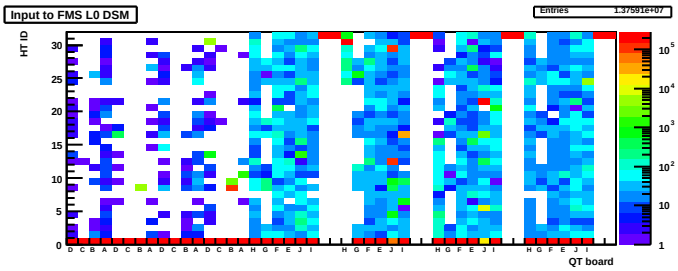

Input to FMS L0 DSM
Entries 205826
QT8(2) sum - simulated
QT board

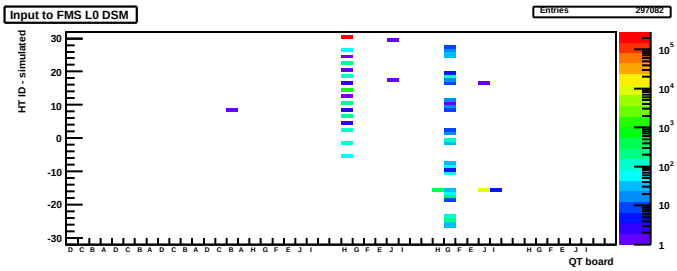

Input to FMS L0 DSM
Entries 1.37591e+07
QT8(3) sum
QT board

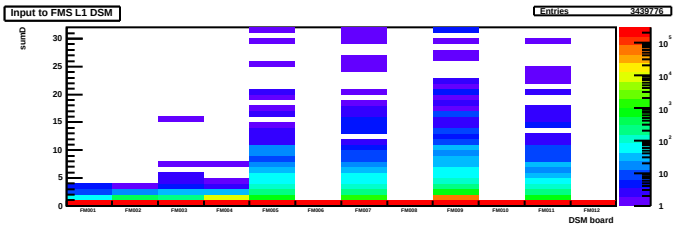

Input to FMS L0 DSM
Entries 265918
QT8(3) sum - simulated
QT board

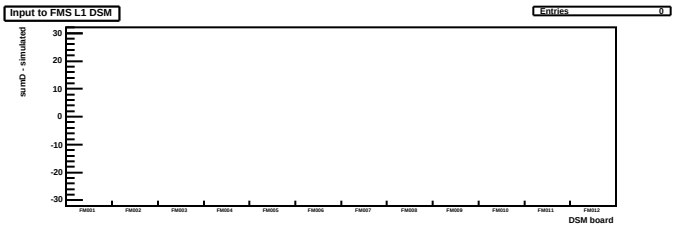

Input to FMS L0 DSM
Entries 1.37591e+07
HT ADC
QT board

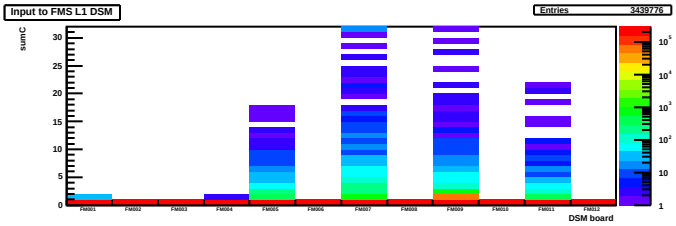

Input to FMS L0 DSM
Entries 2469
HT ADC - simulated
QT board

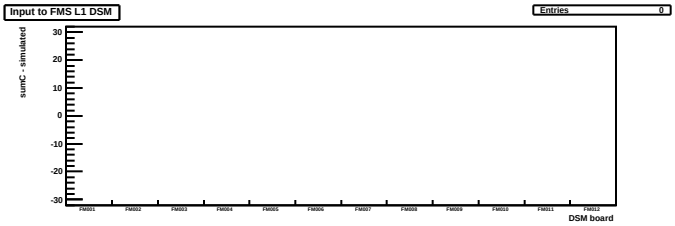

Input to FMS L0 DSM
Entries 1.37591e+07
HT ID
QT board

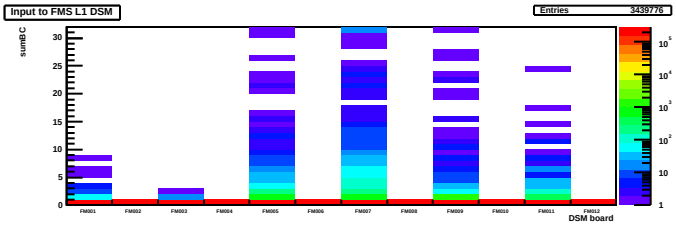

Input to FMS L0 DSM
Entries 297082
HT ID - simulated
QT board

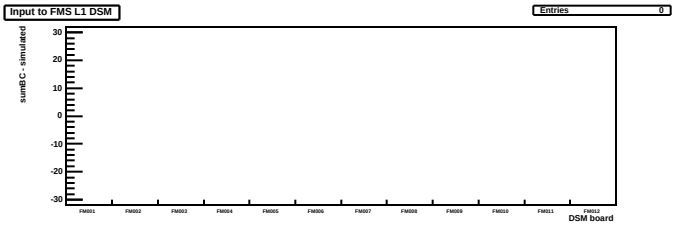

Input to FMS L1 DSM
Entries 3439776
sumD
DSM board

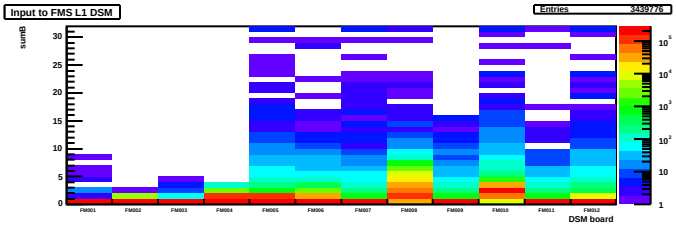

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

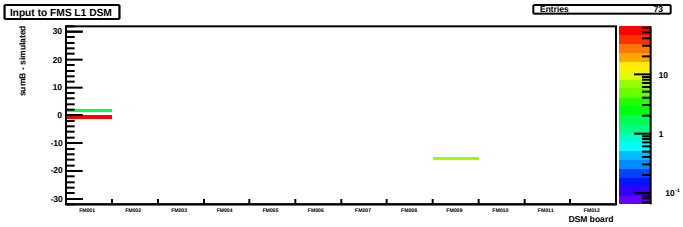

Input to FMS L1 DSM
Entries 3439776
sumC
DSM board

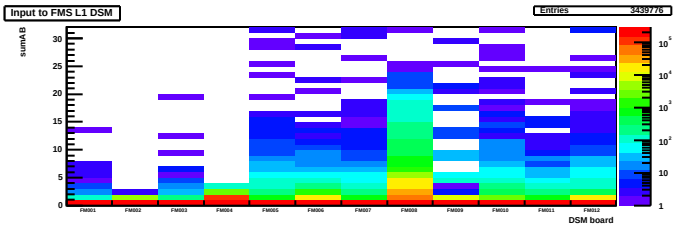

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

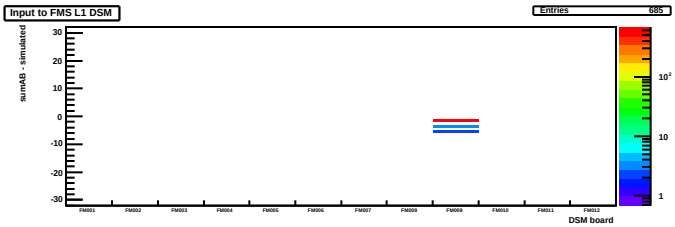

Input to FMS L1 DSM
Entries 3439776
sumBC
DSM board

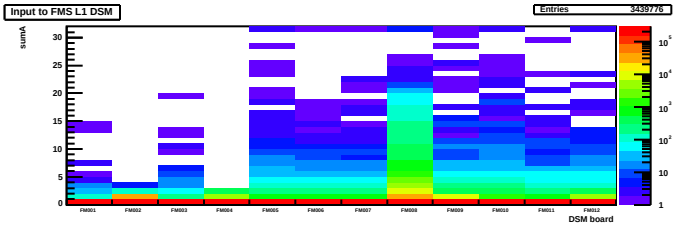

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

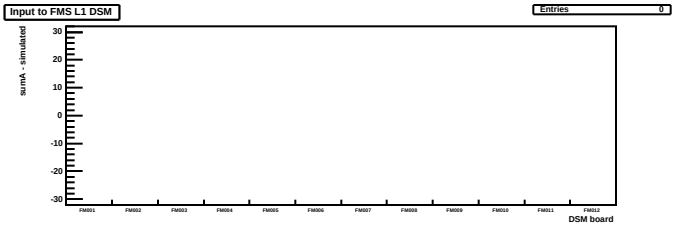

Input to FMS L1 DSM
Entries 3439776
sumB
DSM board

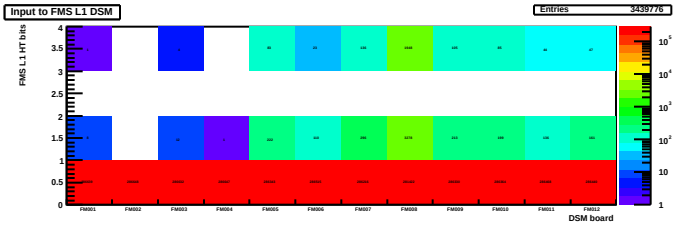

Input to FMS L1 DSM
Entries 73
sumB - simulated
DSM board

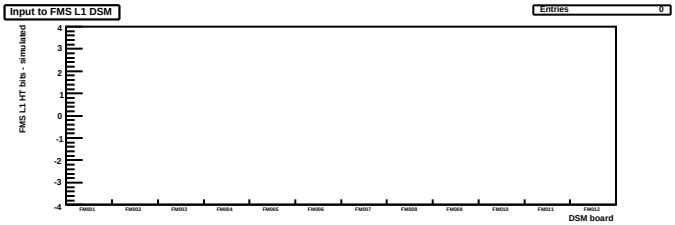

Input to FMS L1 DSM
Entries 3439776
sumAB
DSM board

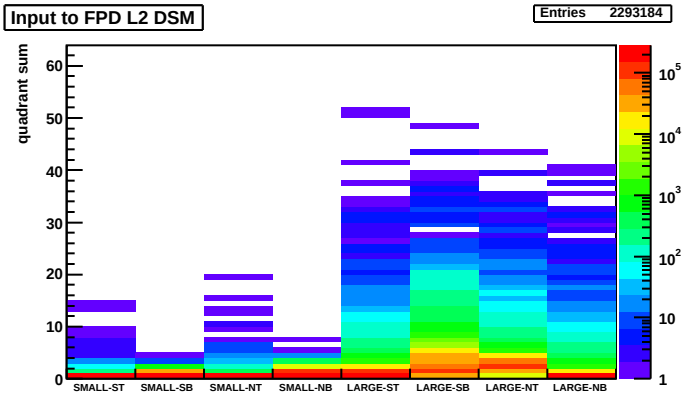

Input to FMS L1 DSM
Entries 685
sumAB - simulated
DSM board

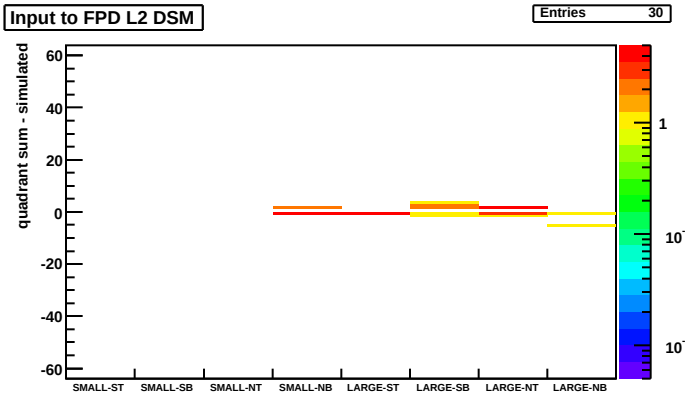

Input to FMS L1 DSM
Entries 3439776
sumA
DSM board

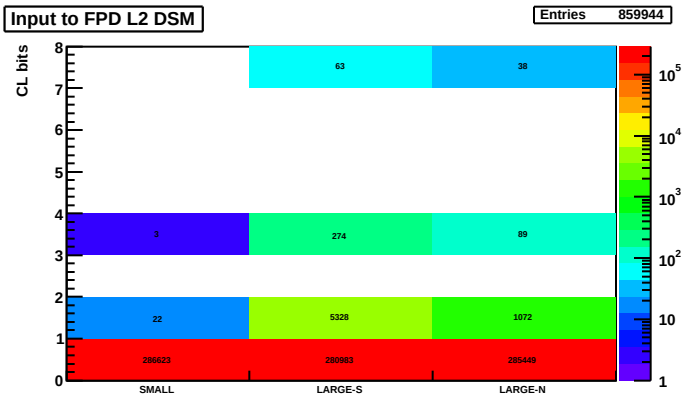

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

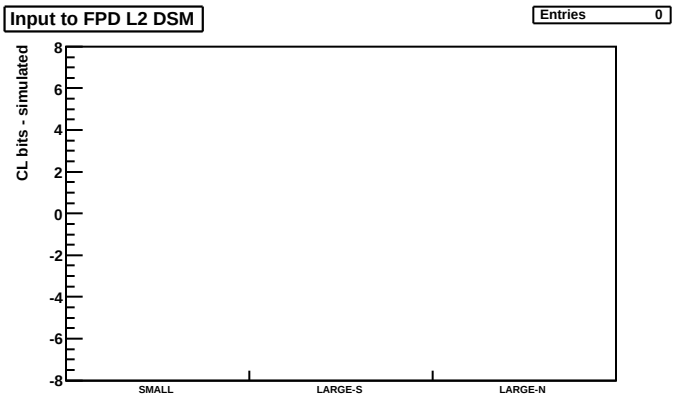

Input to FMS L1 DSM
Entries 3439776
FMS L1 HT bits
DSM board

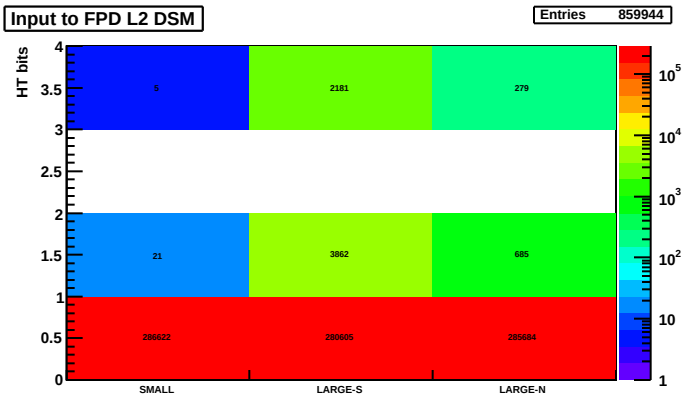

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

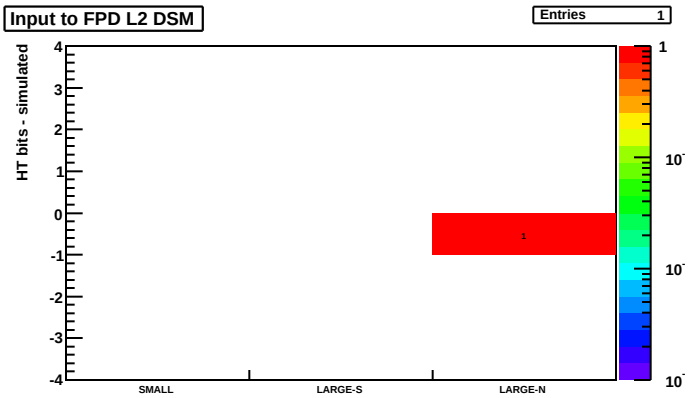

Input to FPD L2 DSM
Entries 2293184
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

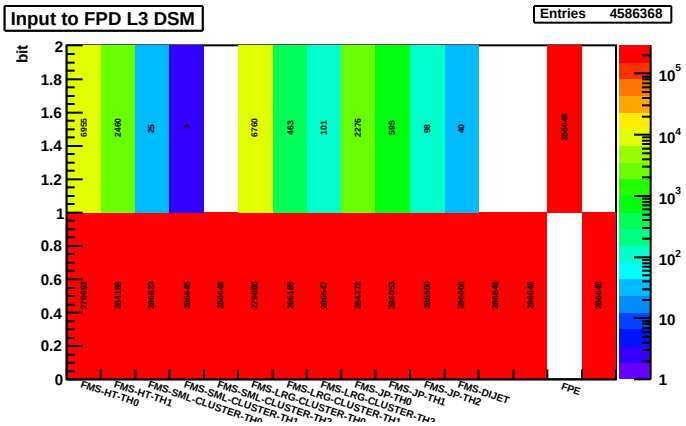

Input to FPD L2 DSM
Entries 30
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

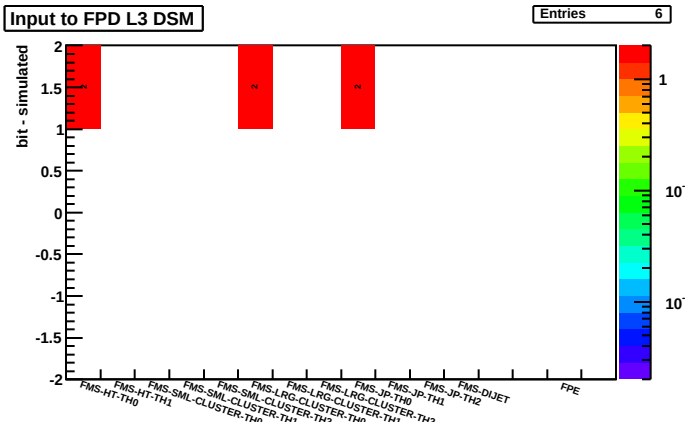

Input to FPD L2 DSM
Entries 859944
CL bits
63
38
3
274
89
22
5328
1072
286623
280983
285449
SMALL
LARGE-S
LARGE-N

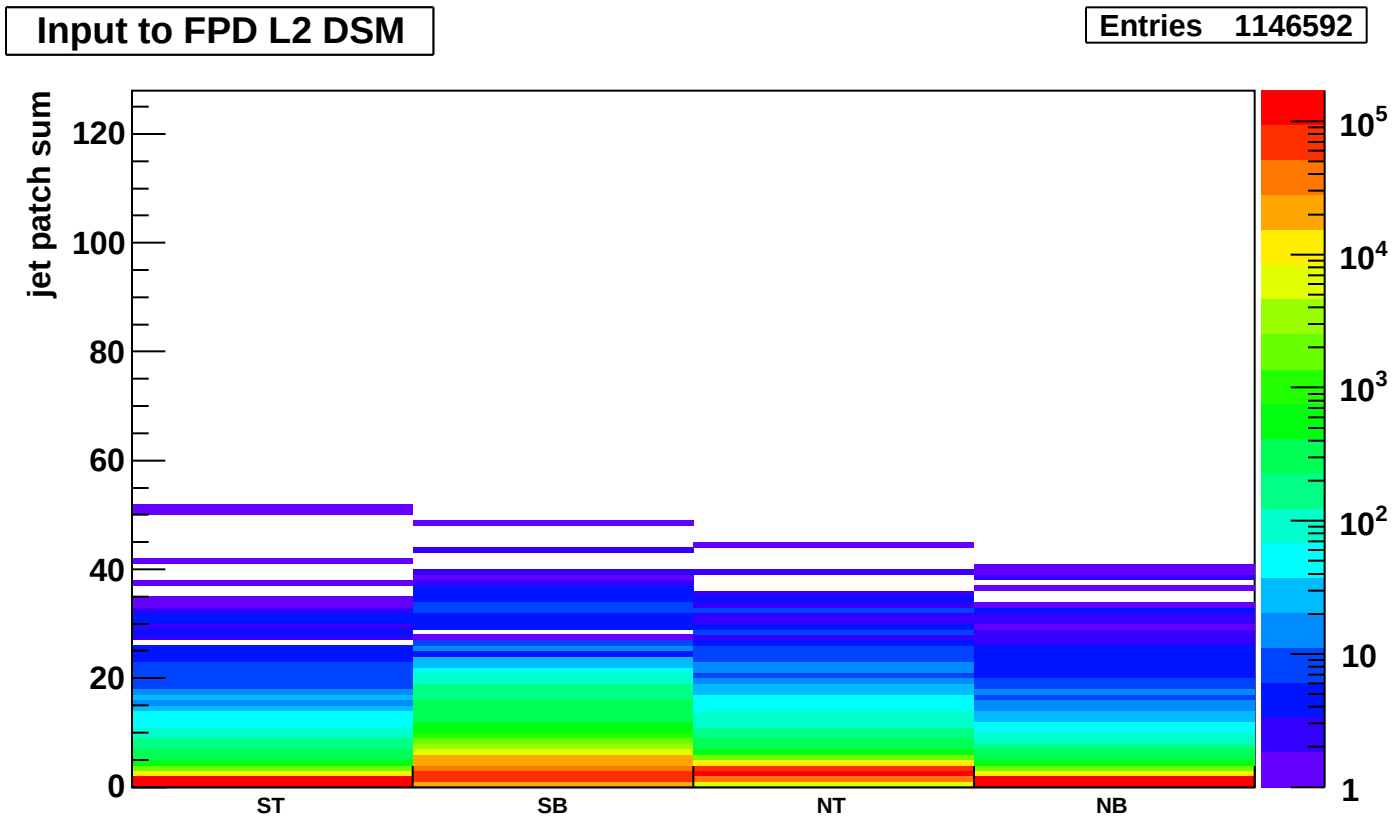

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

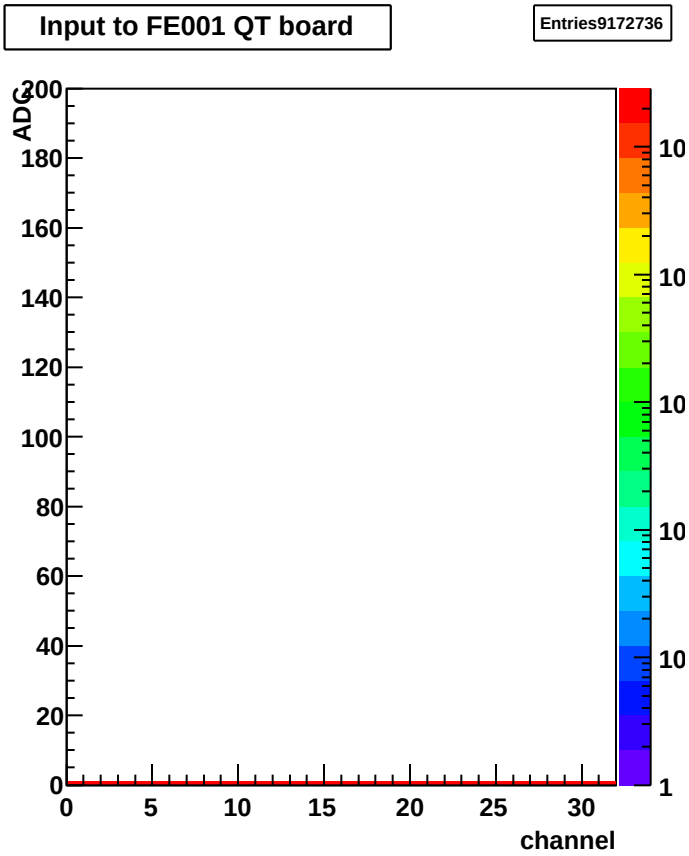

Input to FPD L2 DSM
Entries 859944
HT bits
5
2181
279
21
3862
685
286622
280605
285684
SMALL
LARGE-S
LARGE-N

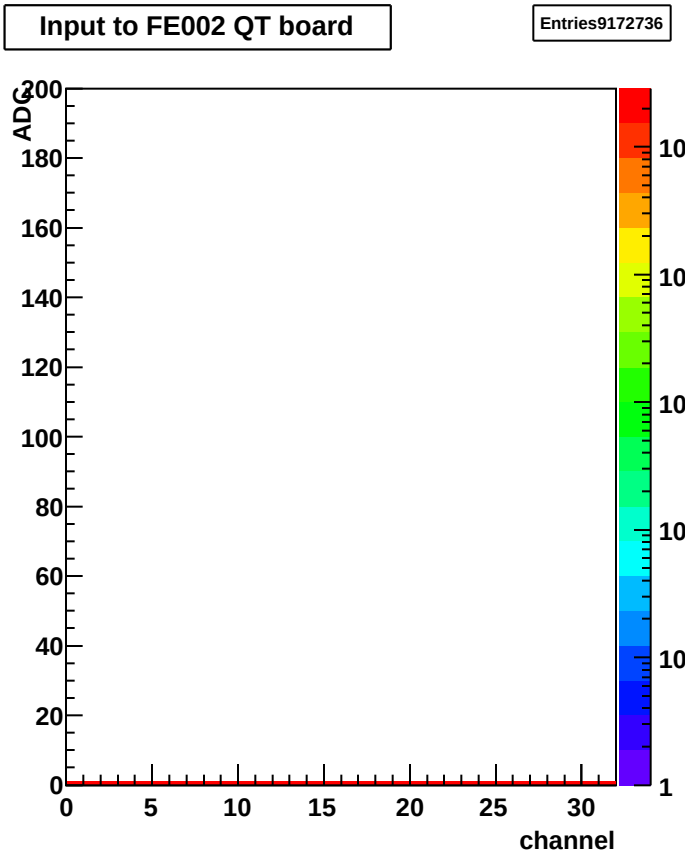

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

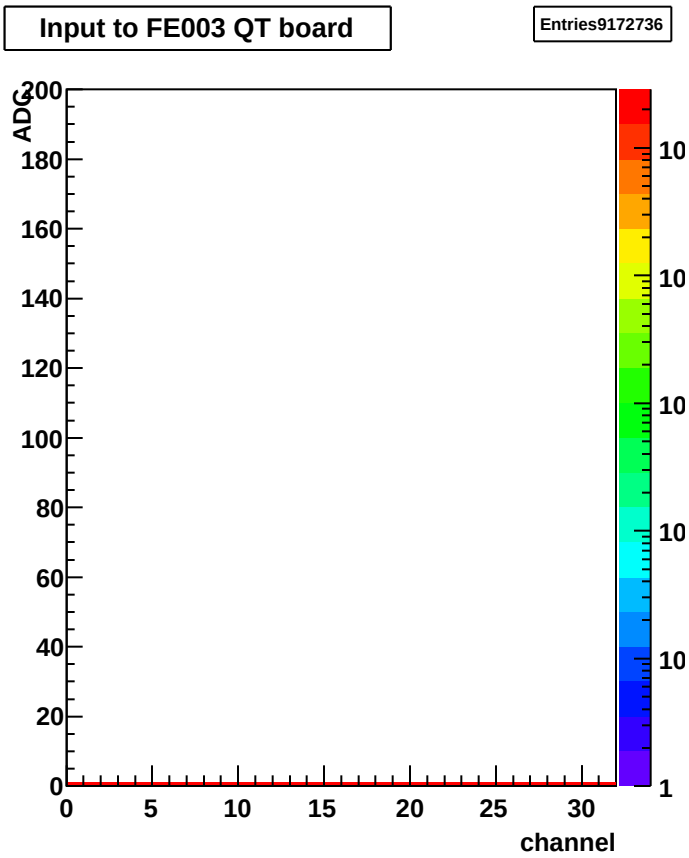

Input to FPD L3 DSM
Entries 4586368
bit
6935
2460
25
3
6700
463
101
2276
595
98
40
286648
279683
284188
286623
286645
286648
279988
286185
286647
284372
286053
286550
286608
286648
286648
286648
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

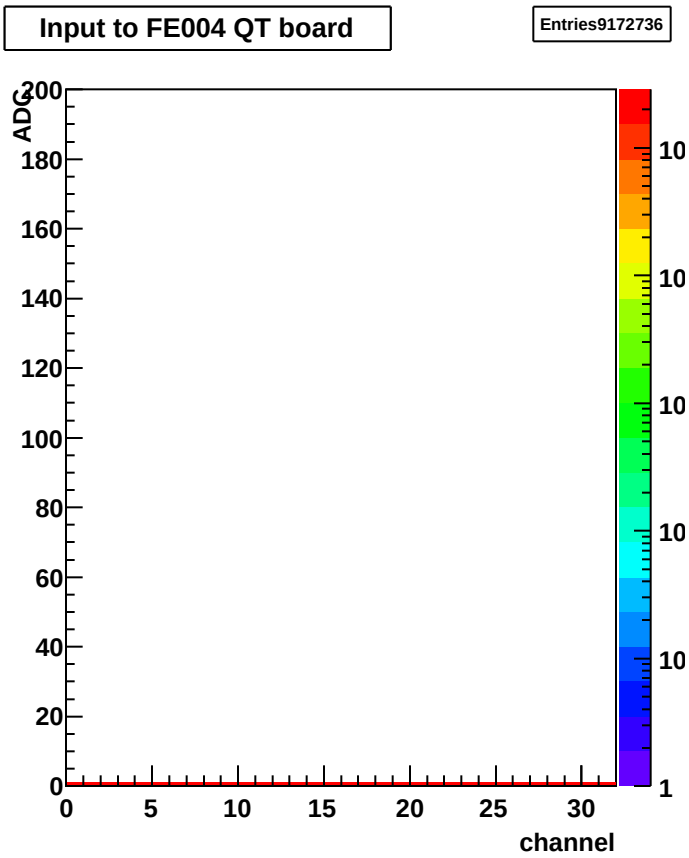

Input to FPD L3 DSM
Entries 6
bit - simulated
2
2
2
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

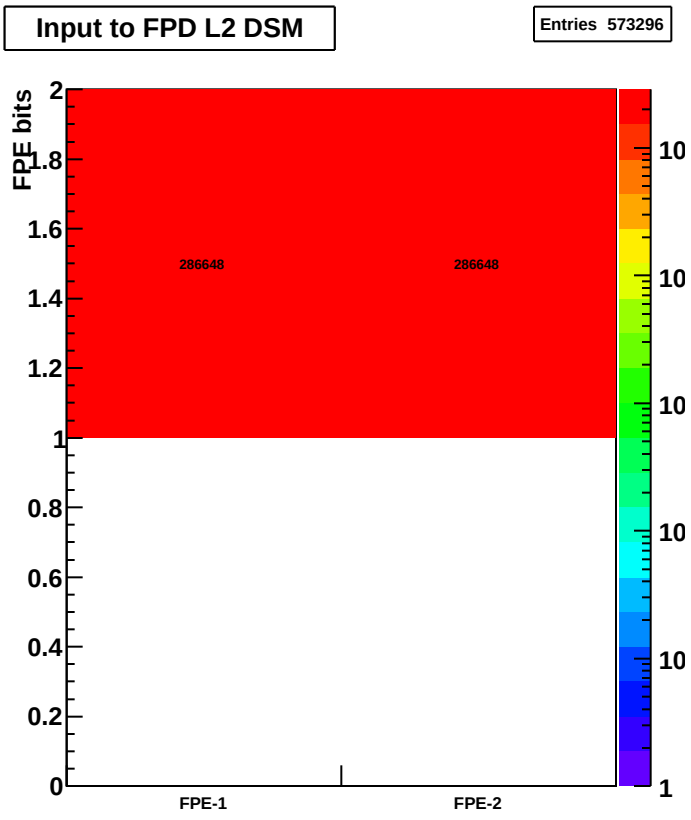

Input to FPD L2 DSM
Entries 1146592
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

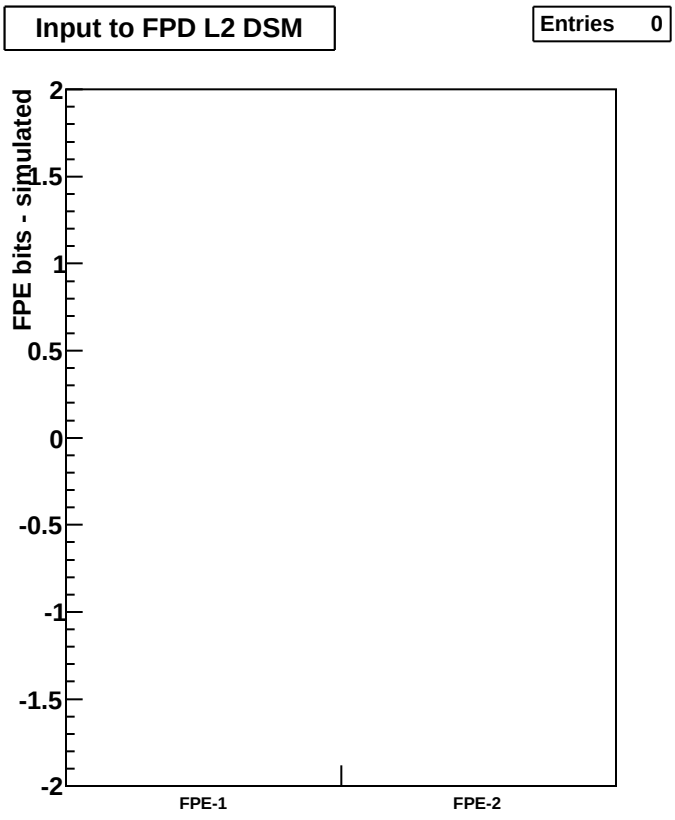

Input to FE001 QT board
Entries9172736
ADC
channel


Input to FE002 QT board
Entries9172736
ADC
channel


Input to FE003 QT board
Entries9172736
ADC
channel


Input to FE004 QT board
Entries9172736
ADC
channel


Input to FPD L2 DSM
Entries 573296
FPE bits
286648
286648
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2