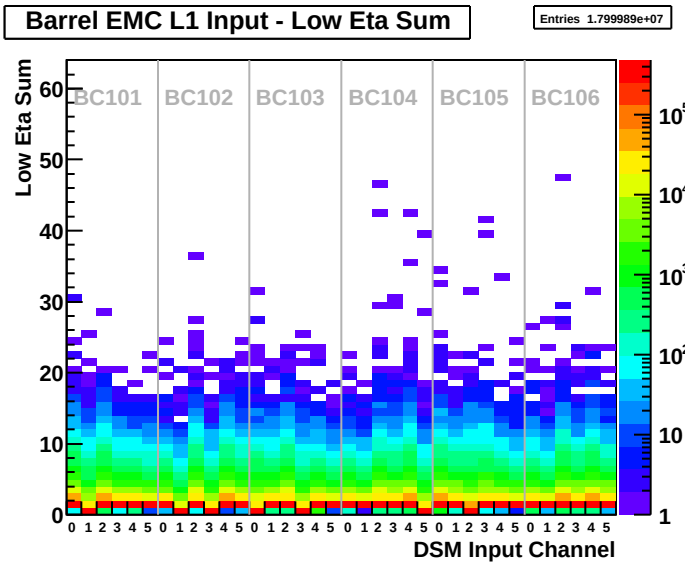
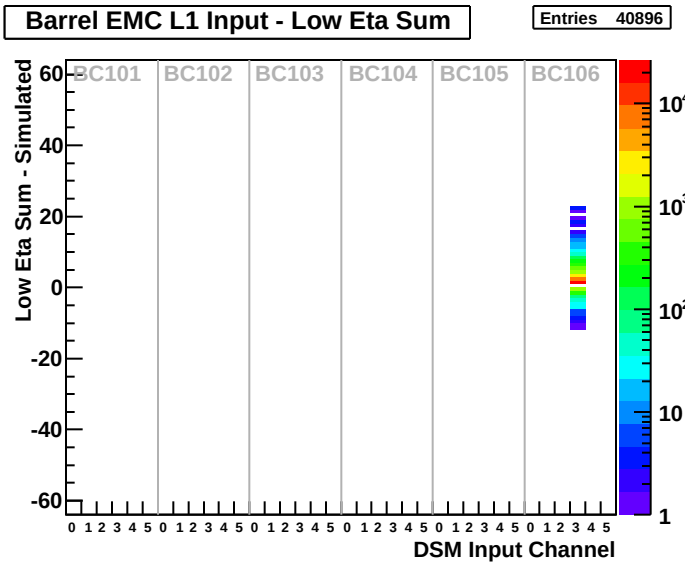
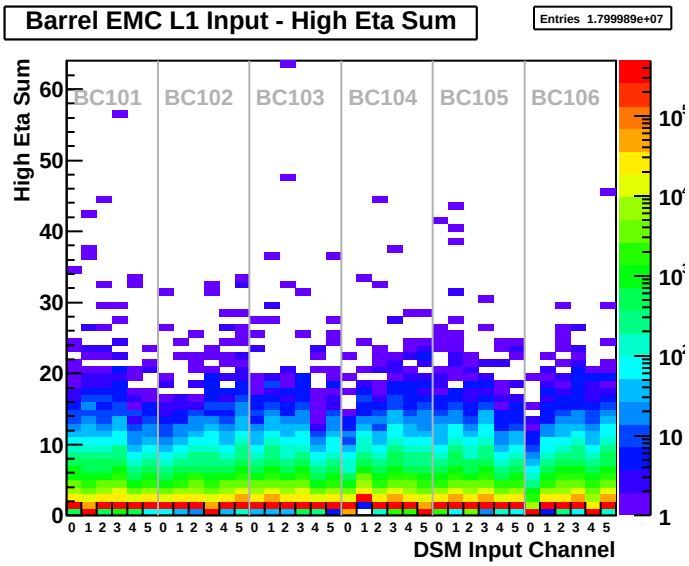
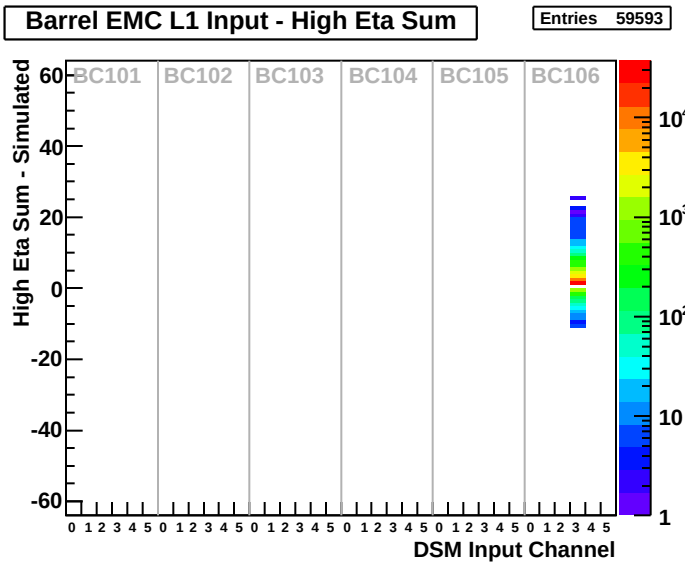
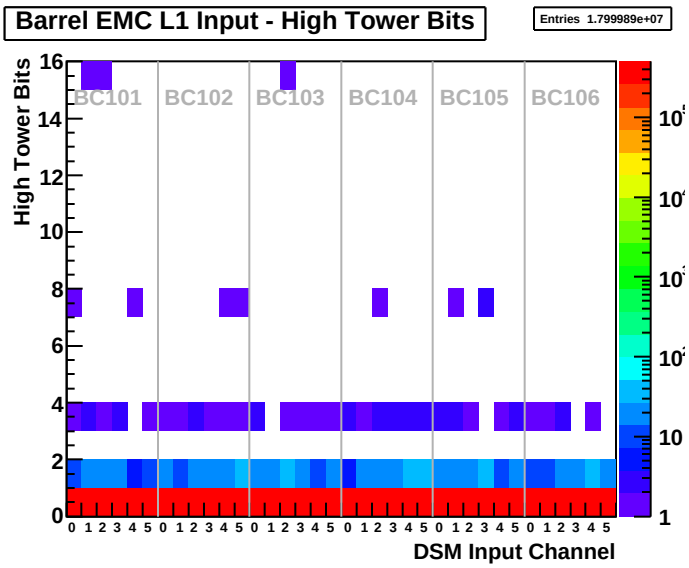
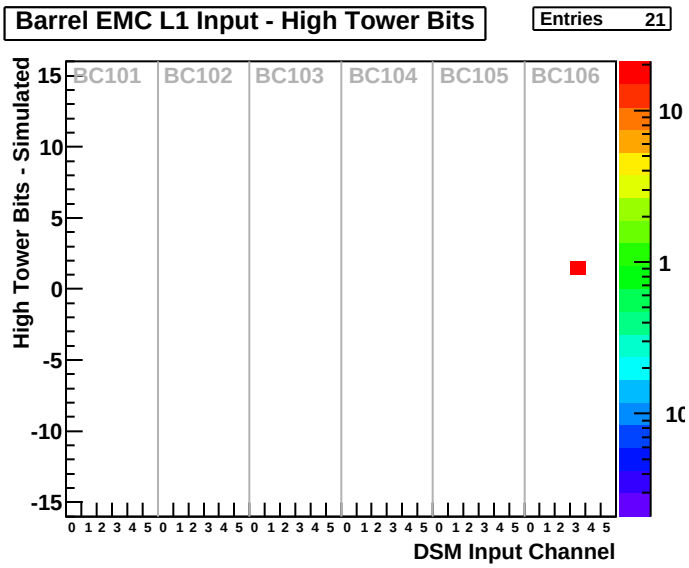

Barrel EMC L1 Input - Low Eta Sum
Entries 1.799989e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

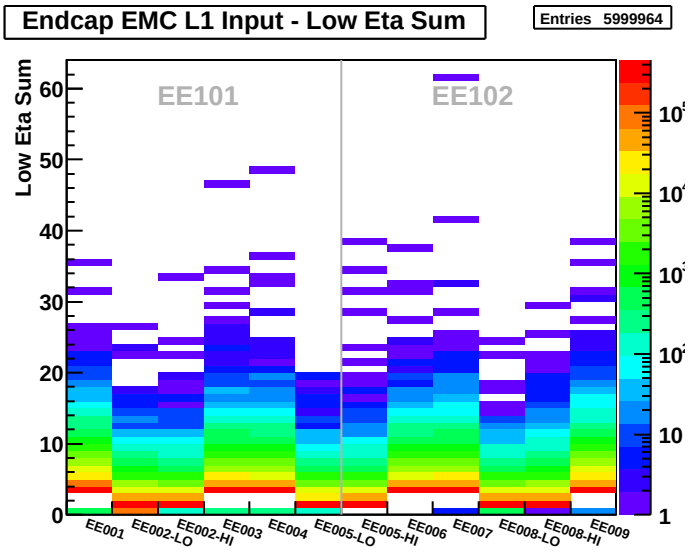

Barrel EMC L1 Input - Low Eta Sum
Entries 40896
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

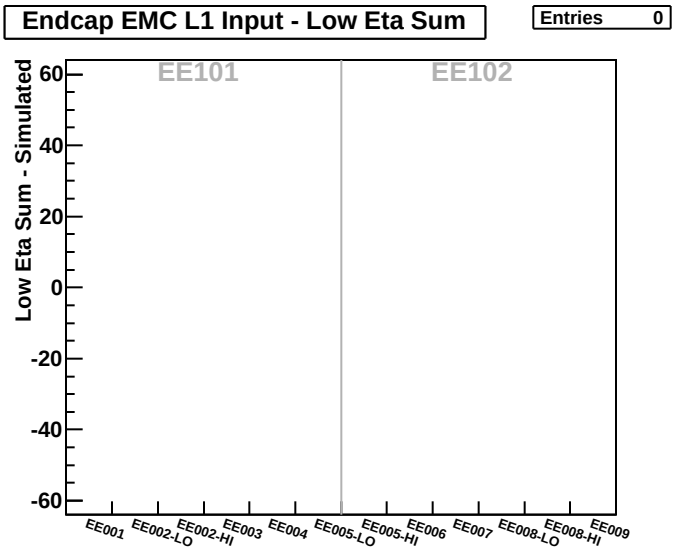

Barrel EMC L1 Input - High Eta Sum
Entries 1.799989e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

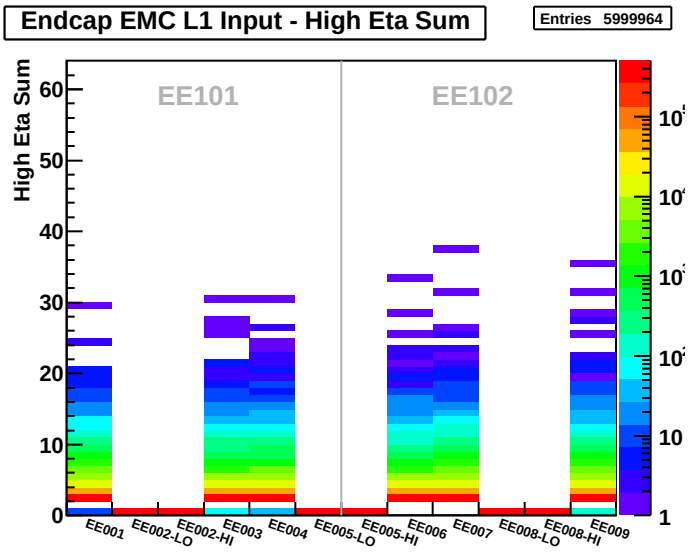

Barrel EMC L1 Input - High Eta Sum
Entries 59593
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

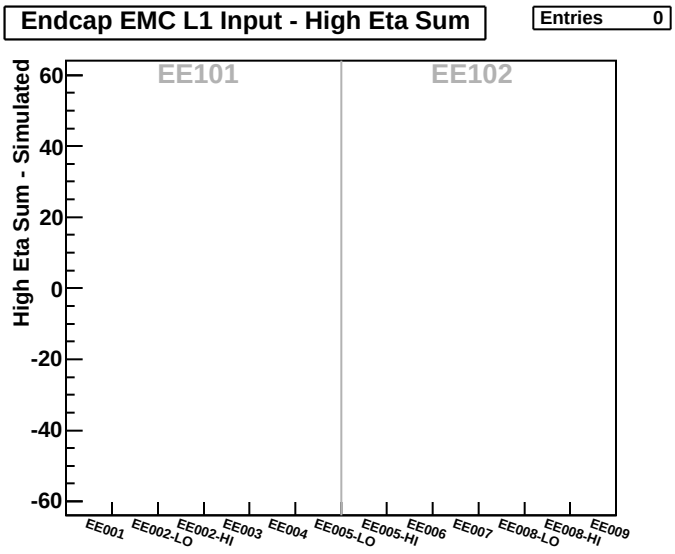

Barrel EMC L1 Input - High Tower Bits
Entries 1.799989e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

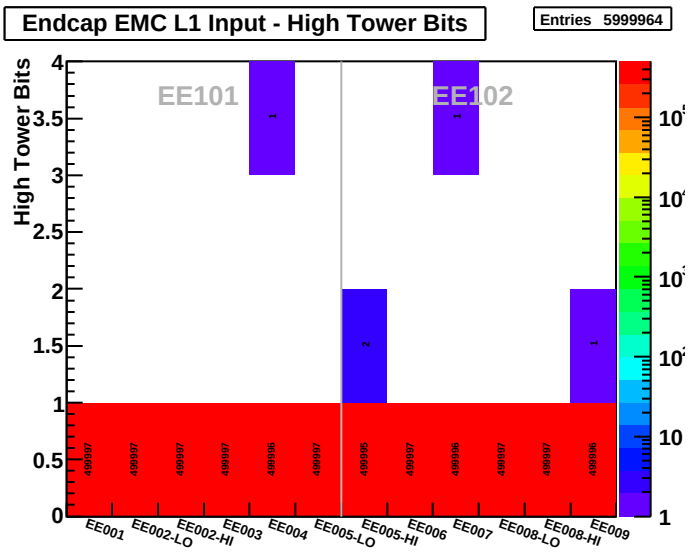

Barrel EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

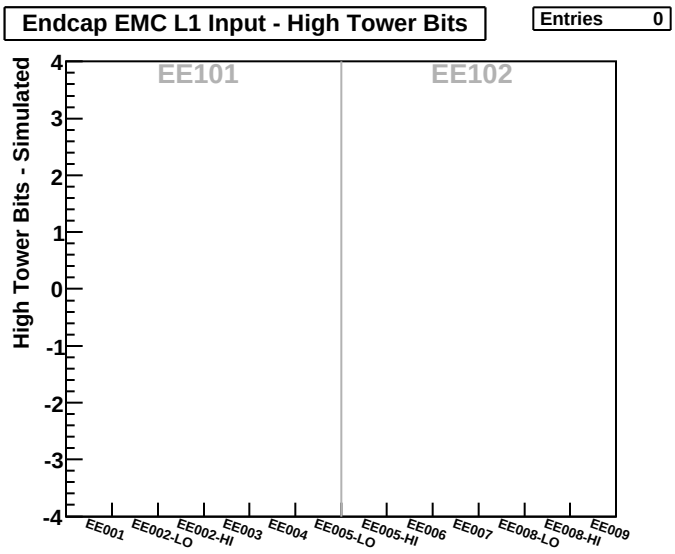

Endcap EMC L1 Input - Low Eta Sum
Entries 5999964
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

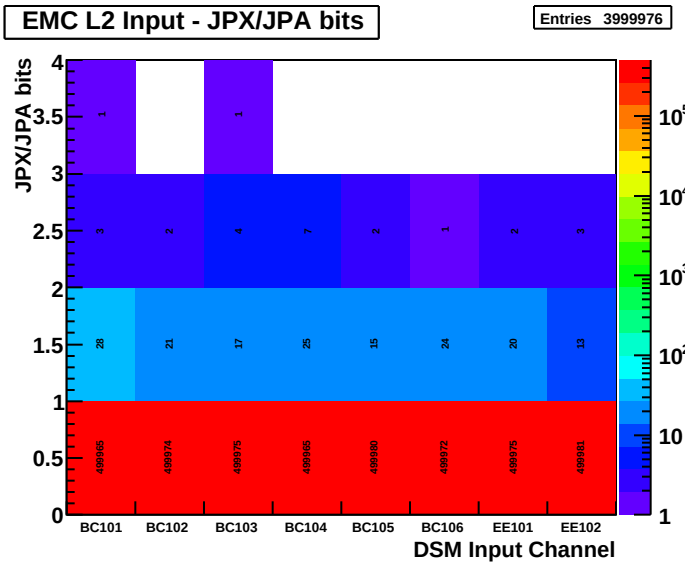

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

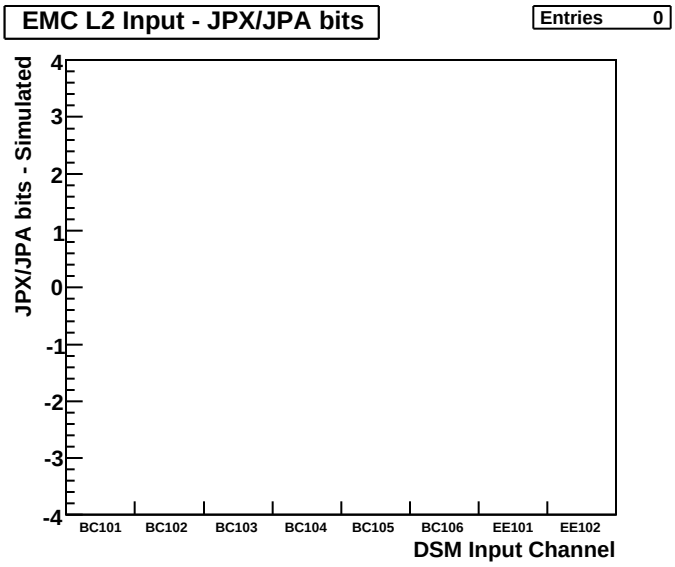

Endcap EMC L1 Input - High Eta Sum
Entries 5999964
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

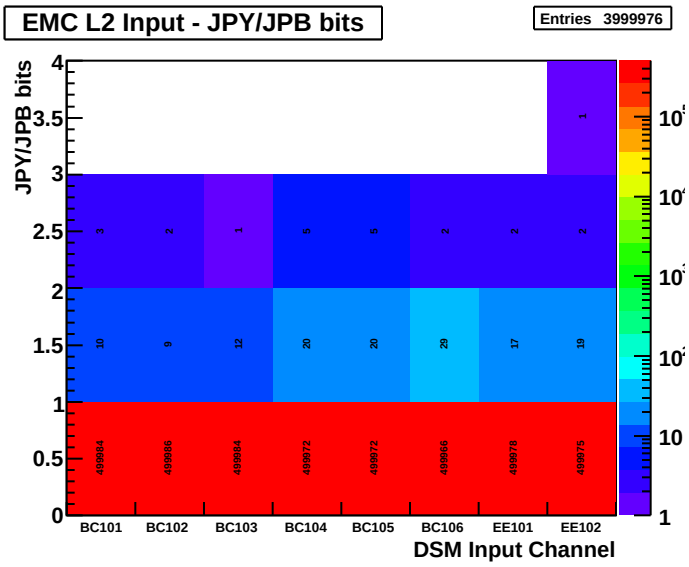

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

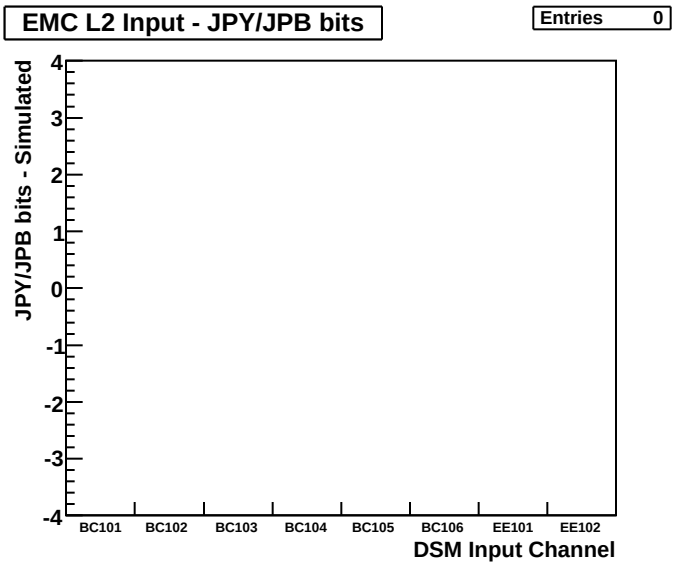

Endcap EMC L1 Input - High Tower Bits
Entries 5999964
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

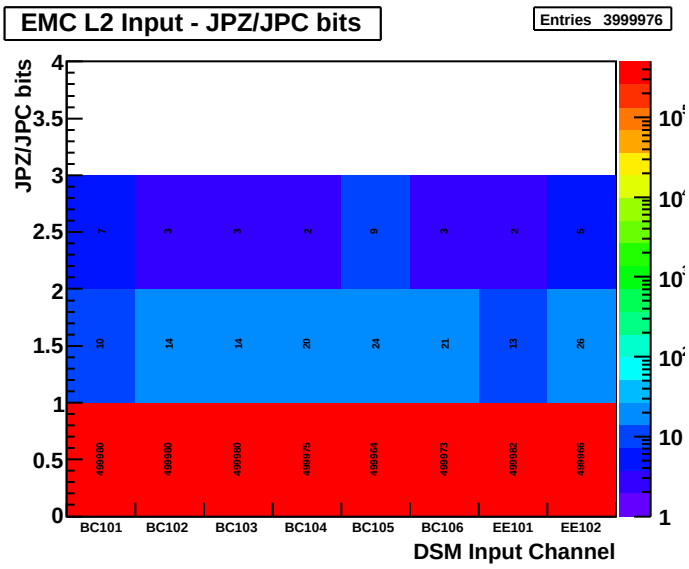

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

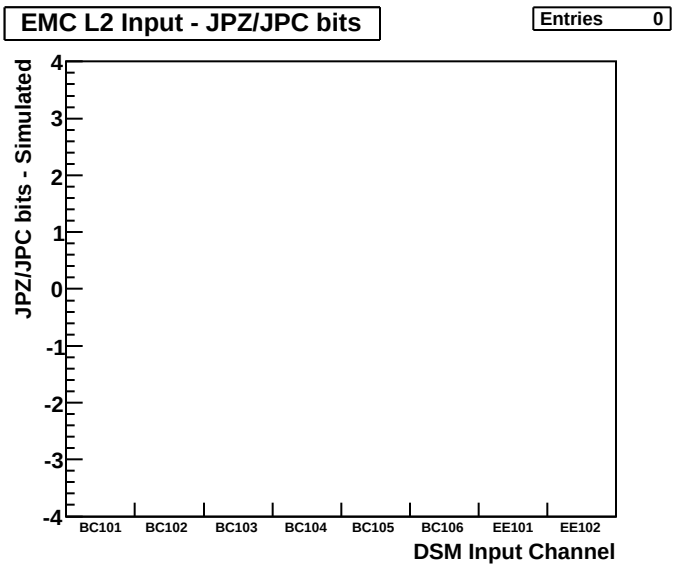

EMC L2 Input - JPX/JPA bits
Entries 3999976
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

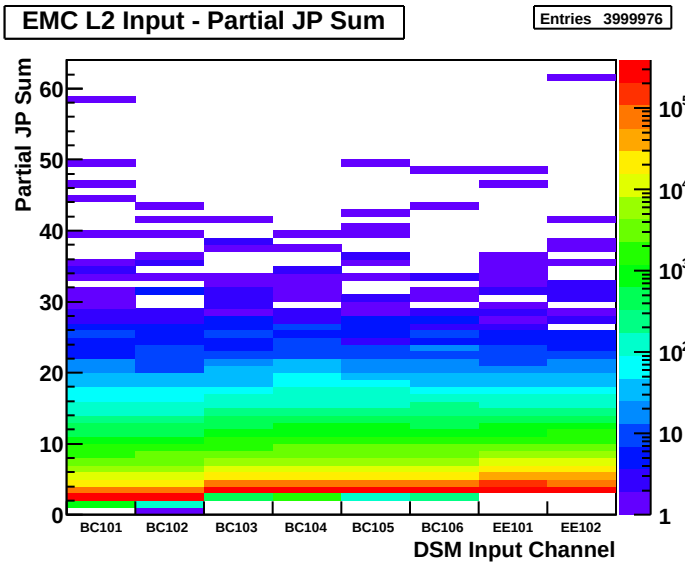

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

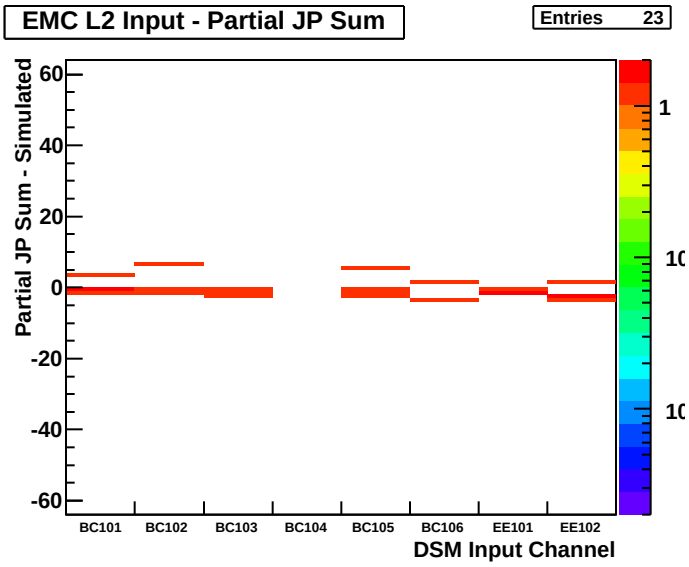

EMC L2 Input - JPY/JPB bits
Entries 3999976
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

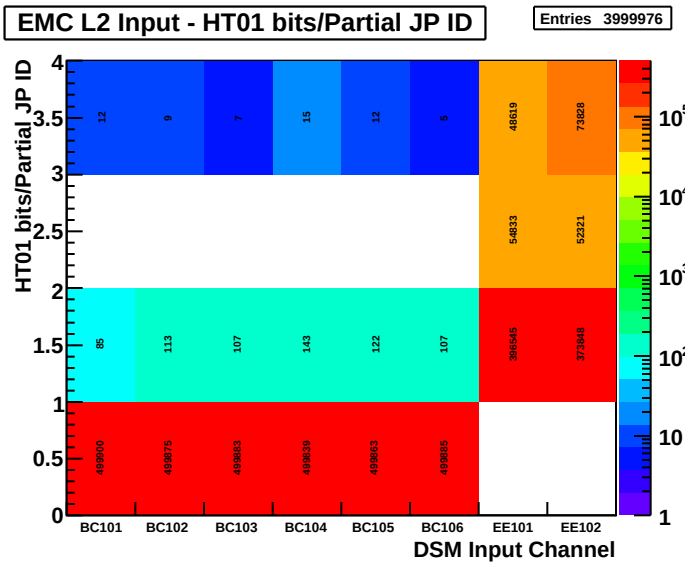

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

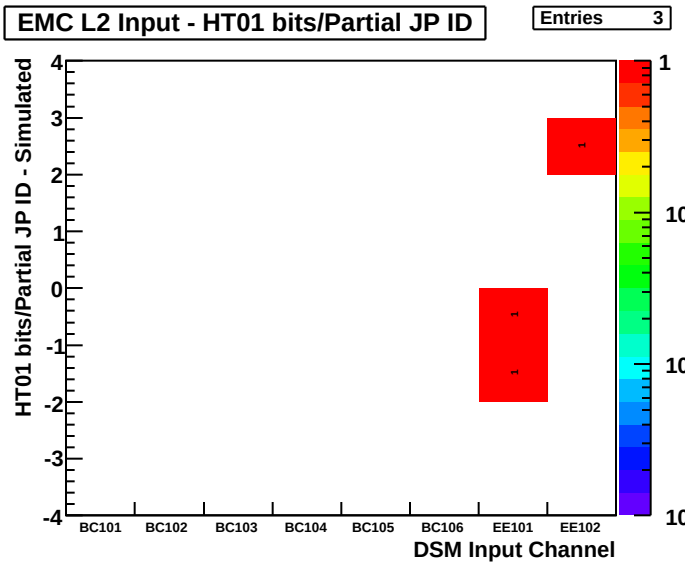

EMC L2 Input - JPZ/JPC bits
Entries 3999976
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

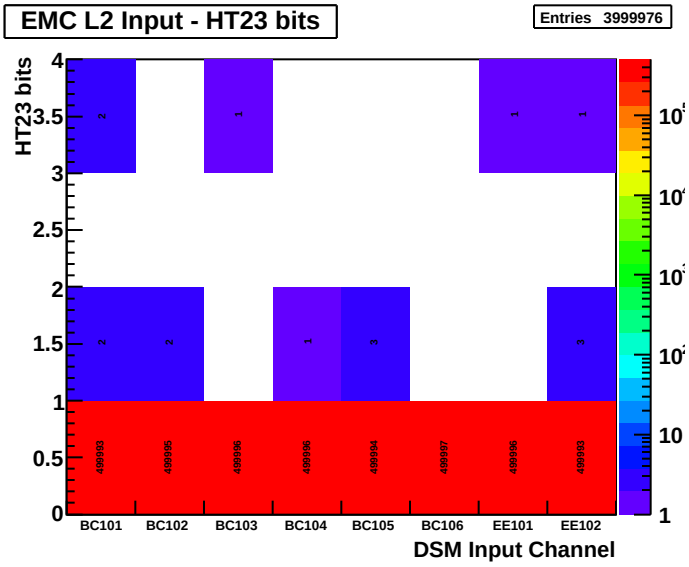

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

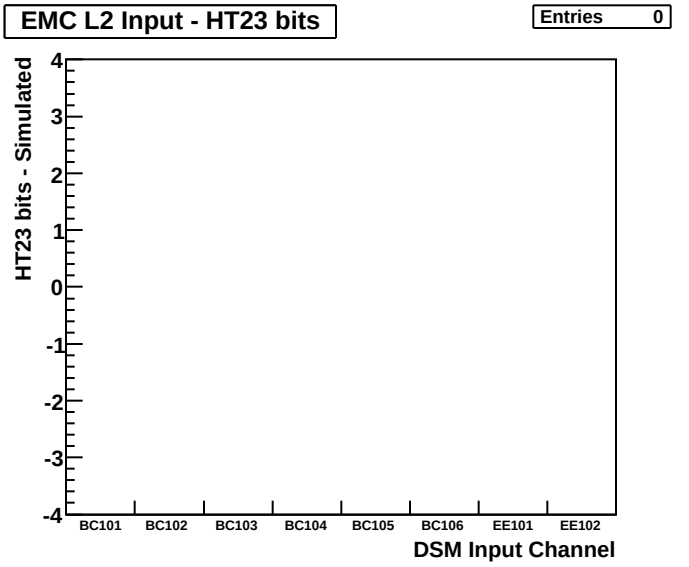

EMC L2 Input - Partial JP Sum
Entries 3999976
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

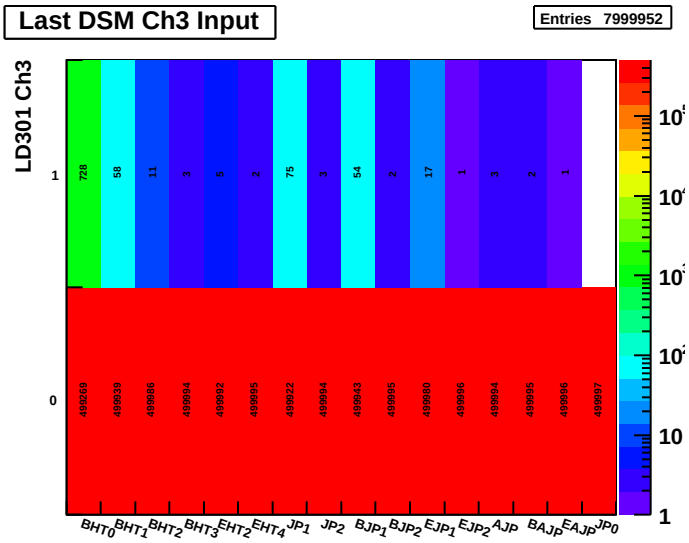

EMC L2 Input - Partial JP Sum
Entries 23
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

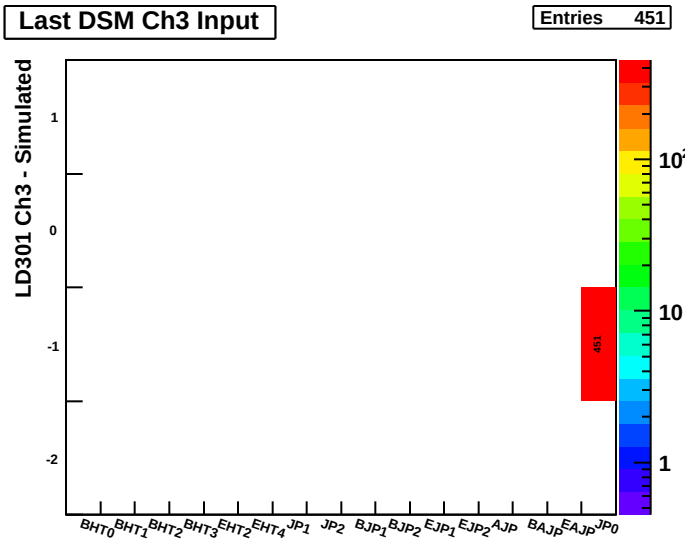

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3999976
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

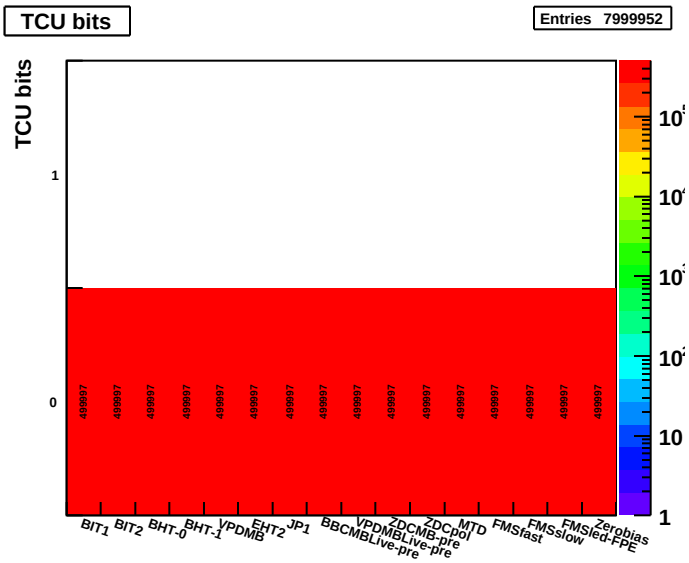

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

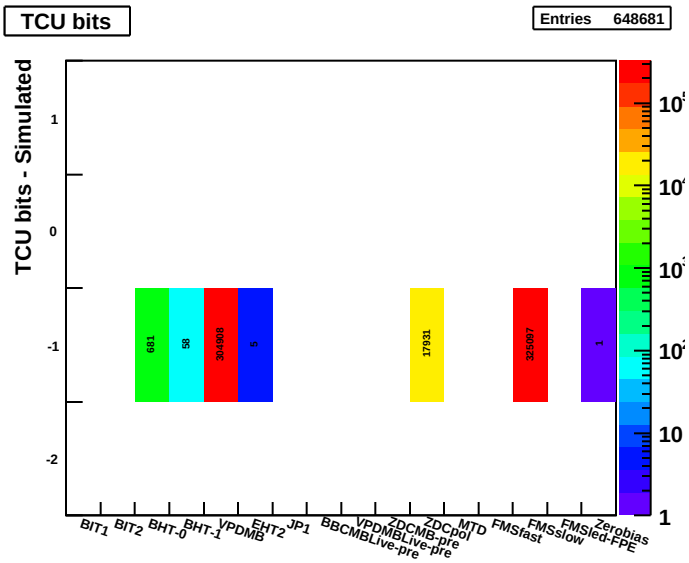

EMC L2 Input - HT23 bits
Entries 3999976
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

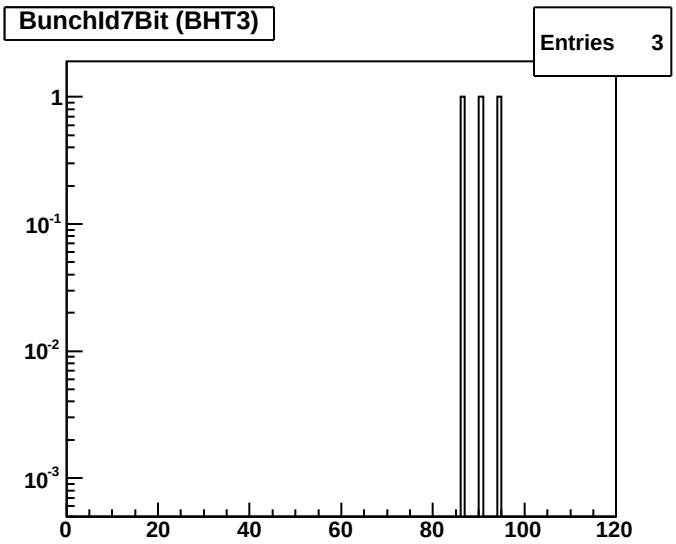

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

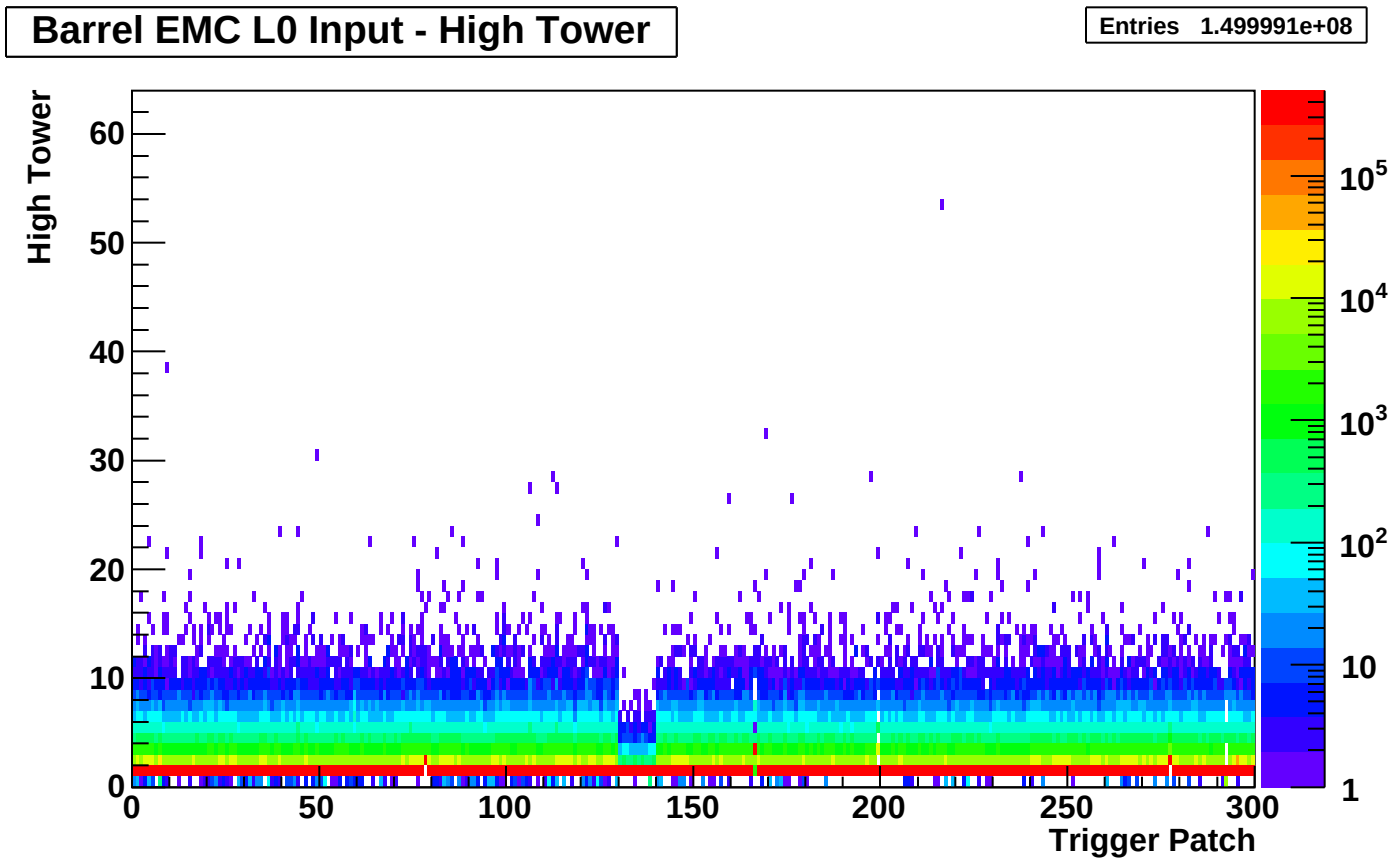

Last DSM Ch3 Input
Entries 7999952
LD301 Ch3
728 58 11 3 5 2 75 3 54 2 17 1 3 2 1
499269 499939 499986 499994 499992 499995 499922 499994 499943 499995 499980 499996 499994 499995 499996 499997
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

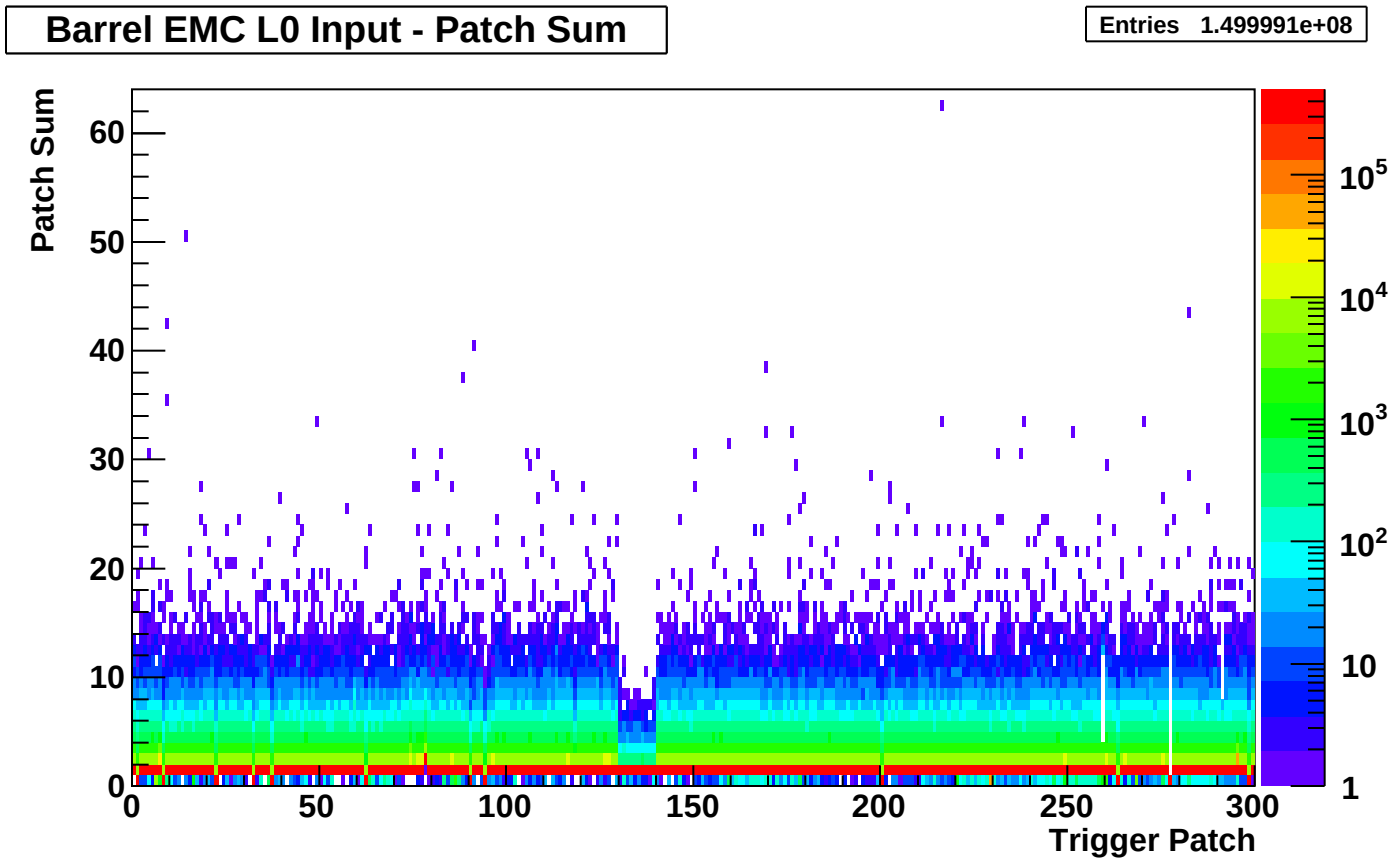

Last DSM Ch3 Input
Entries 451
LD301 Ch3 - Simulated
451
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

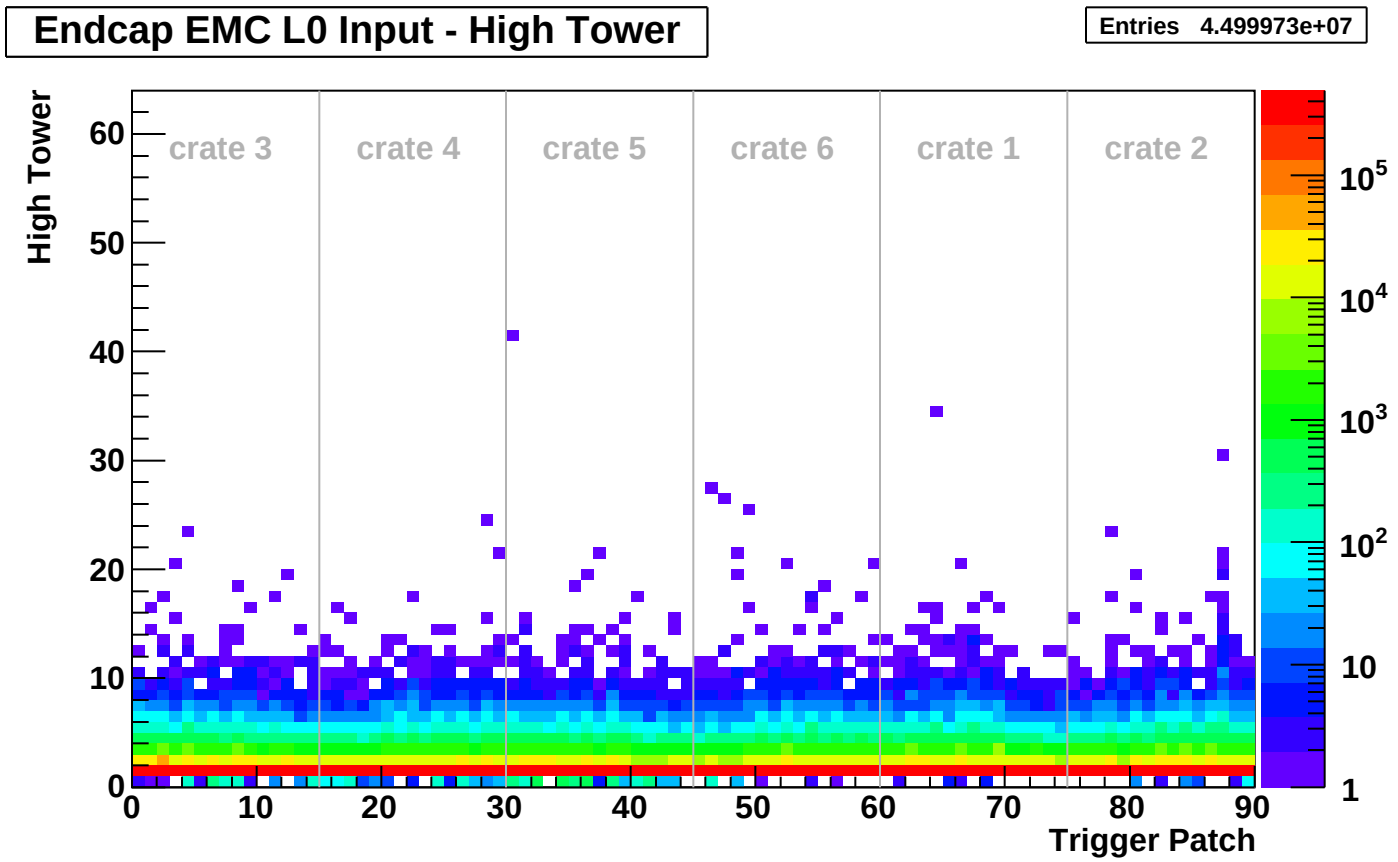

TCU bits
Entries 7999952
TCU bits
499997 499997 499997 499997 499997 499997 499997 499997 499997 499997 499997 499997 499997 499997 499997 499997
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

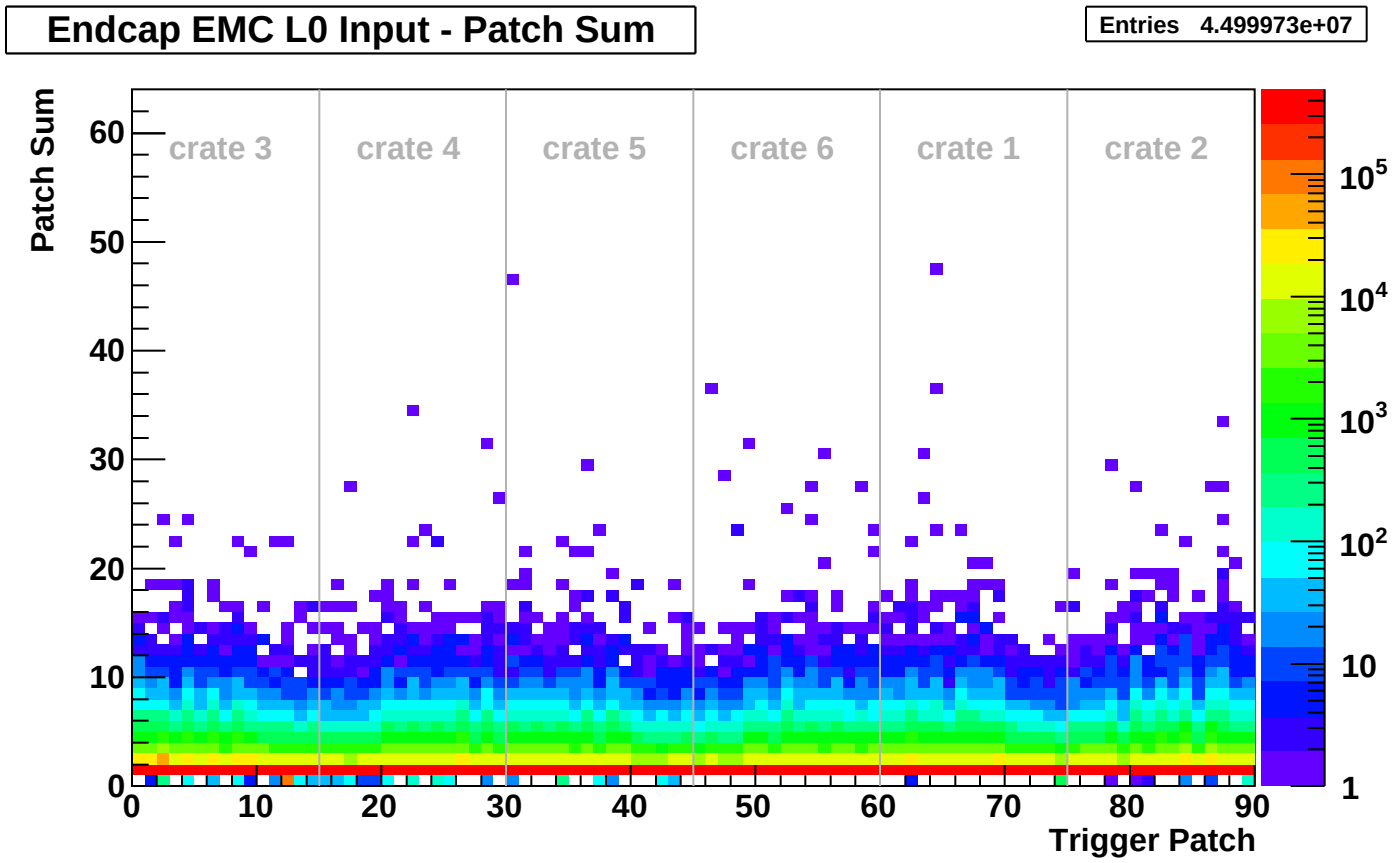

TCU bits
Entries 648681
TCU bits - Simulated
681 58 304908 5 17931 325087 1
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

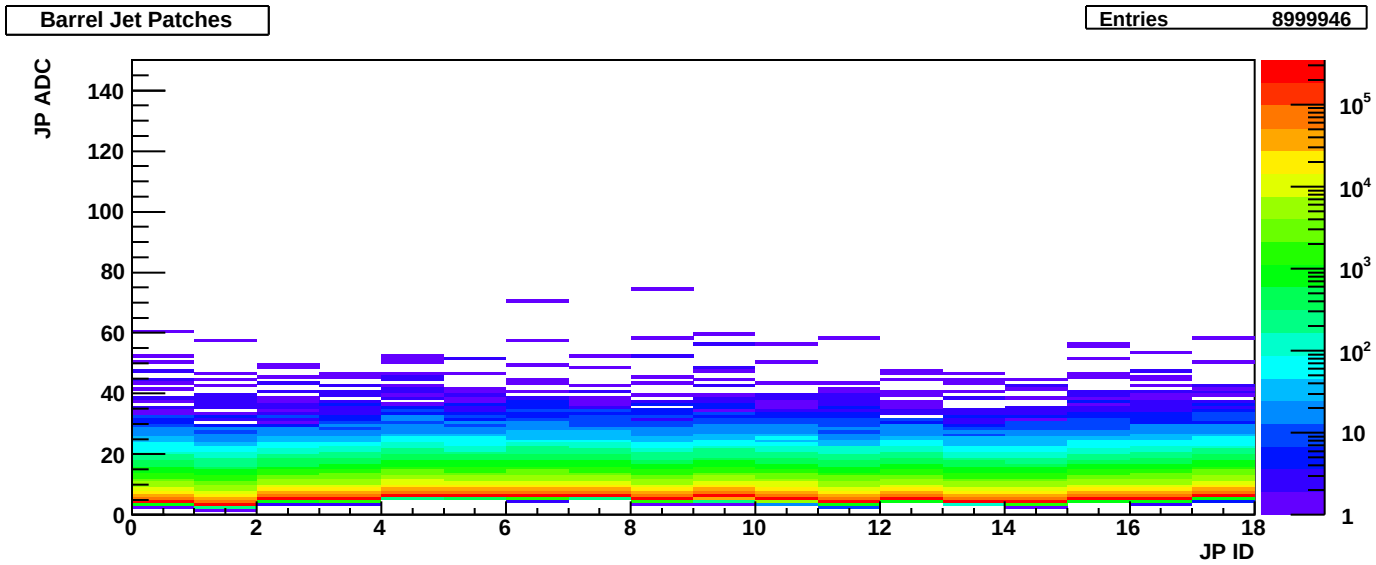

BunchId7Bit (BHT3)
Entries 3

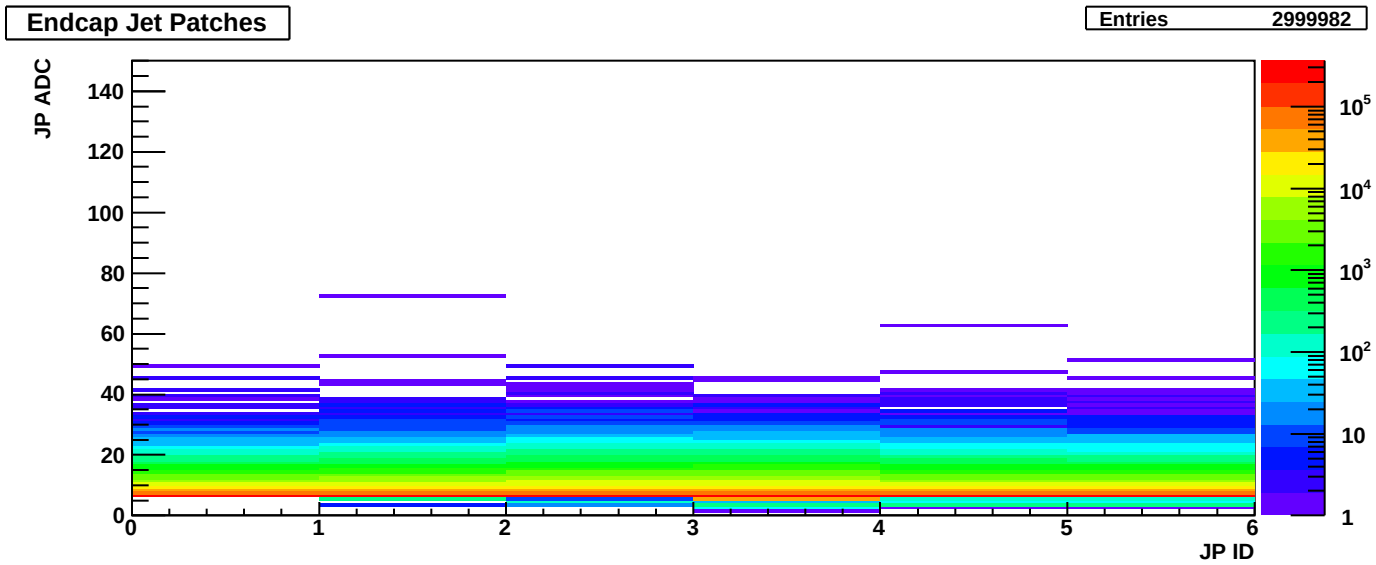

Barrel EMC L0 Input - High Tower
Entries 1.499991e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

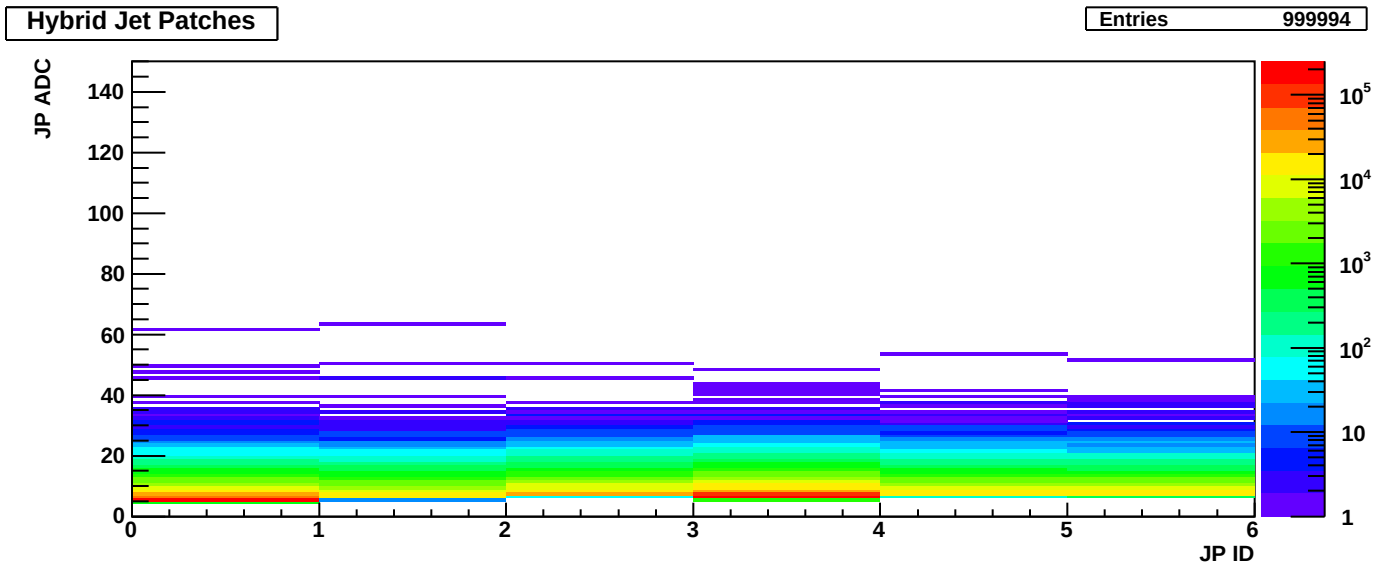

Barrel EMC L0 Input - Patch Sum
Entries 1.499991e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

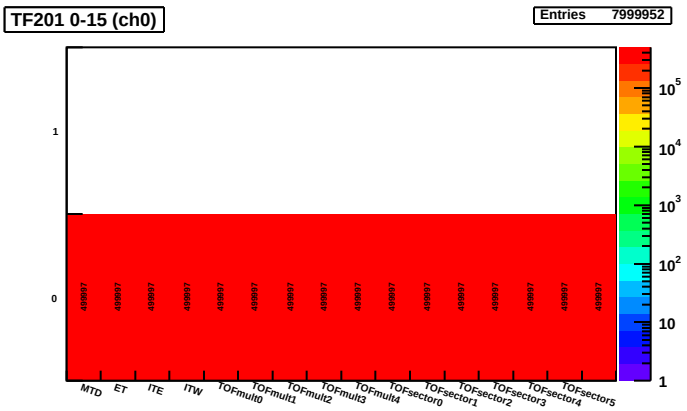

Endcap EMC L0 Input - High Tower
Entries 4.499973e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

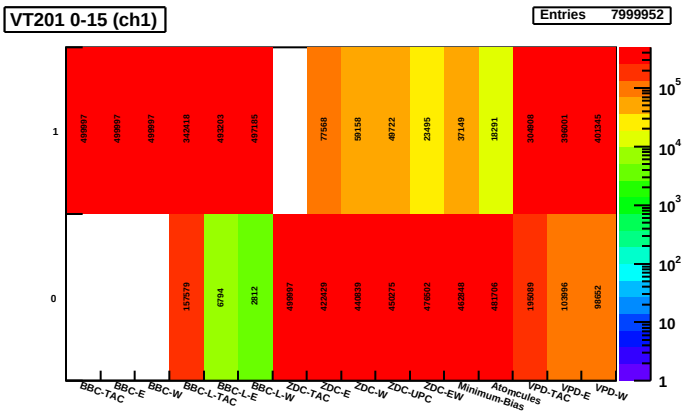

Endcap EMC L0 Input - Patch Sum
Entries 4.499973e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

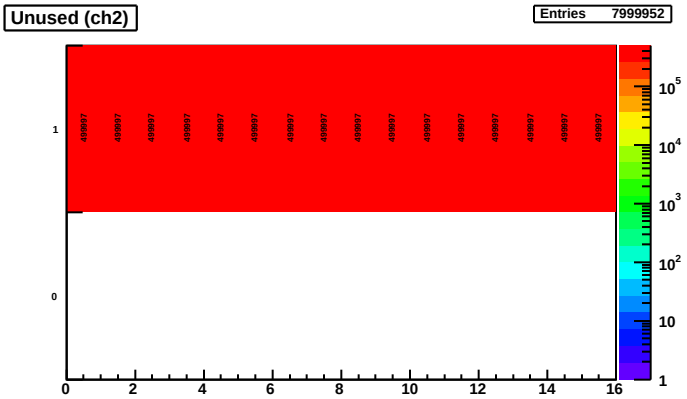

Barrel Jet Patches
Entries 8999946
JP ADC
JP ID

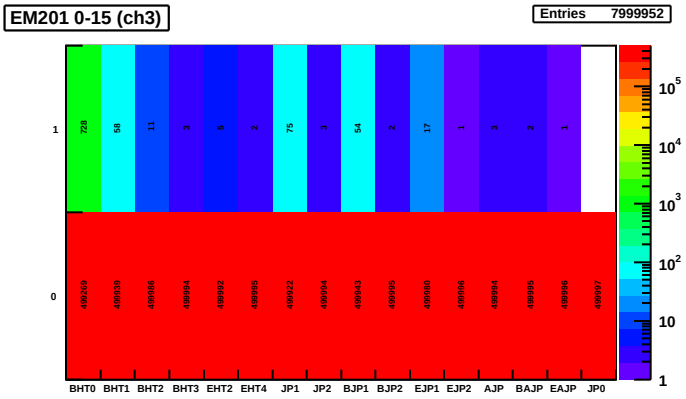

Endcap Jet Patches
Entries 2999982
JP ADC
JP ID

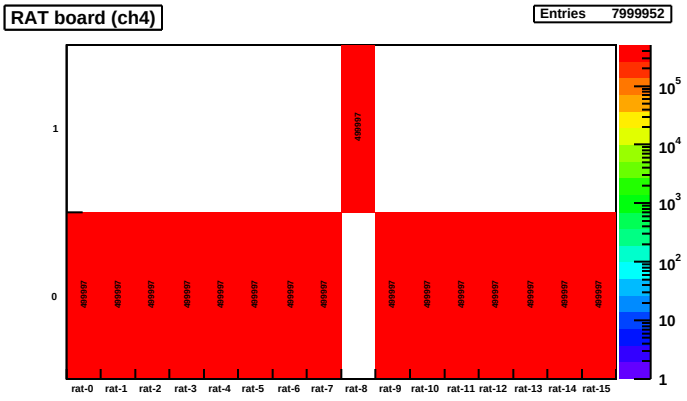

Hybrid Jet Patches
Entries 999994
JP ADC
JP ID

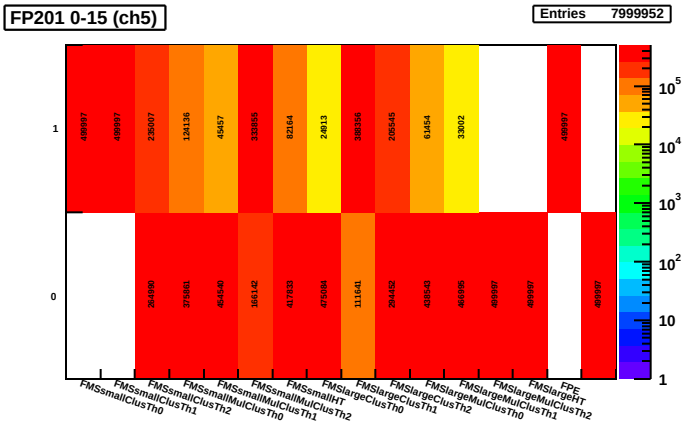

TF201 0-15 (ch0)
Entries 7999952
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

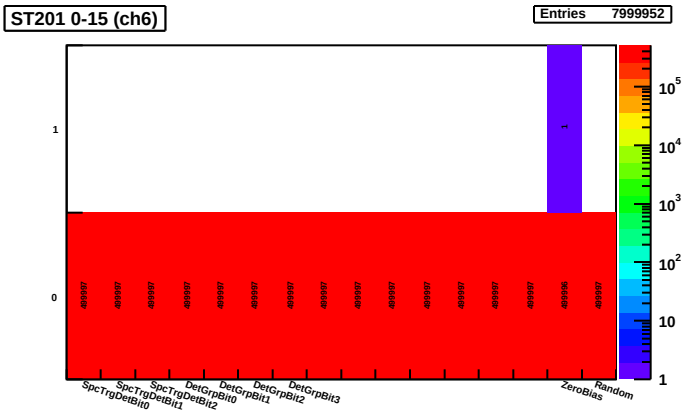

VT201 0-15 (ch1)
Entries 7999952
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

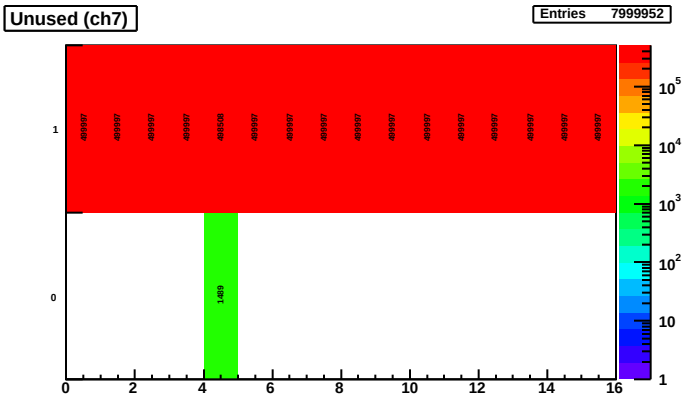

Unused (ch2)
Entries 7999952

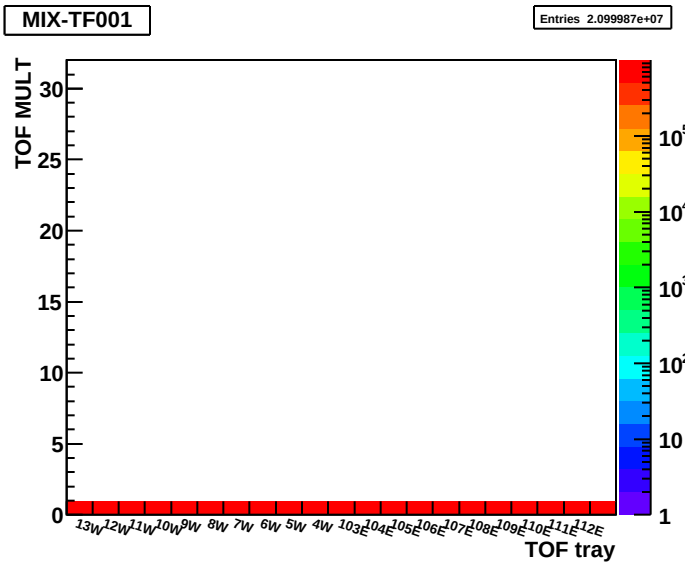

EM201 0-15 (ch3)
Entries 7999952
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

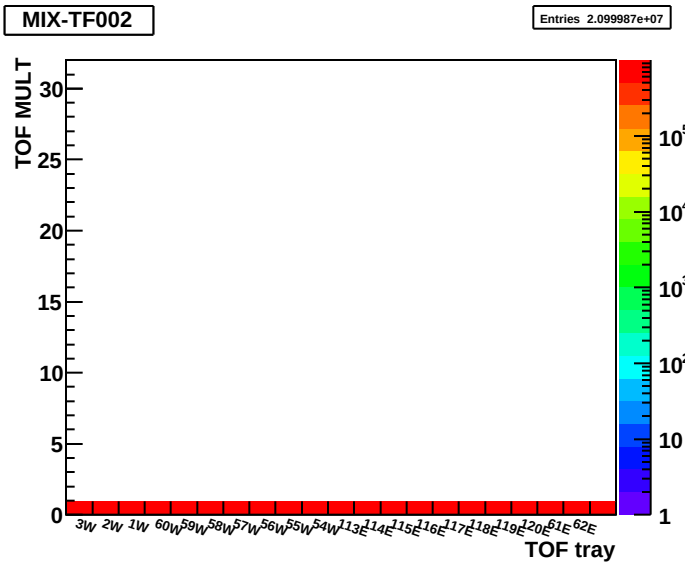

RAT board (ch4)
Entries 7999952
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

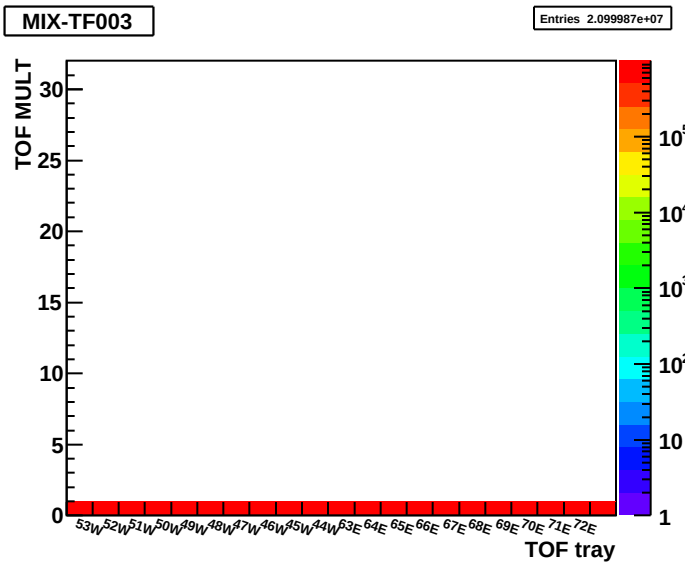

FP201 0-15 (ch5)
Entries 7999952
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

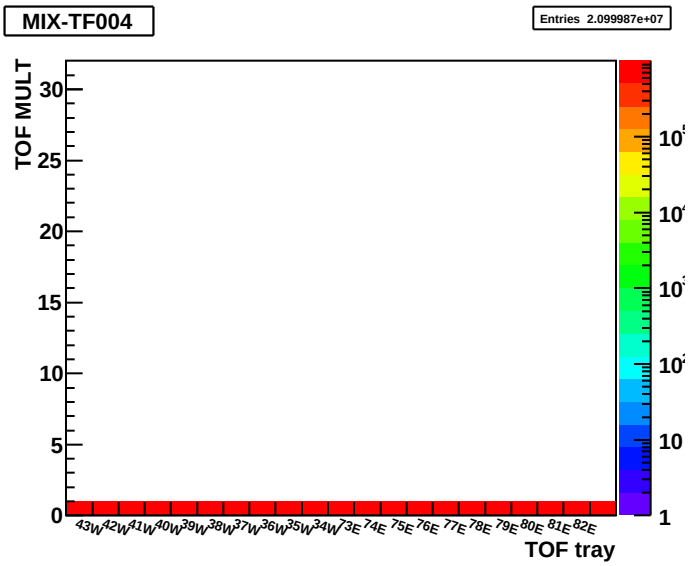

ST201 0-15 (ch6)
Entries 7999952
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

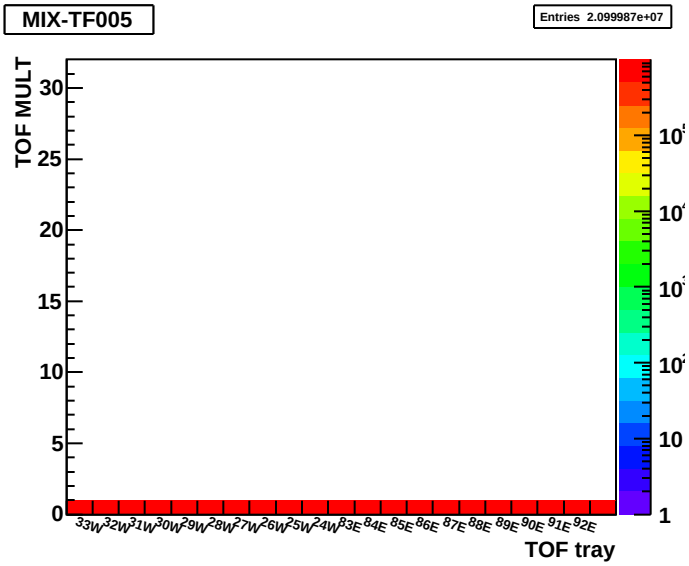

Unused (ch7)
Entries 7999952

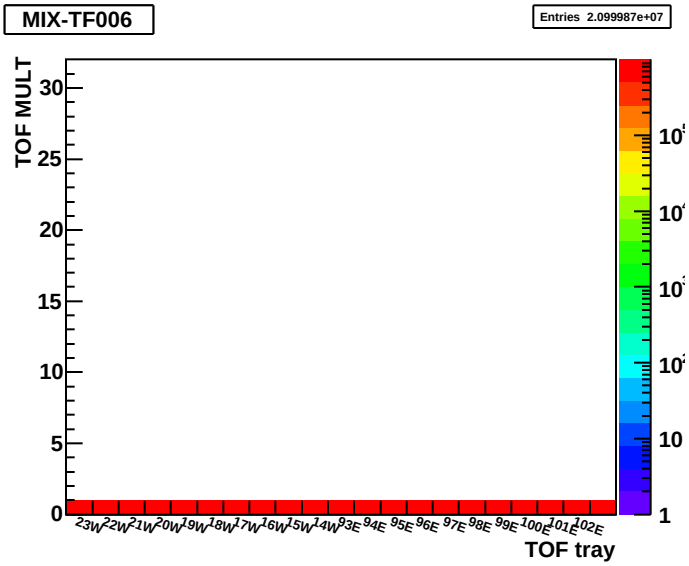

MIX-TF001
Entries 2.099987e+07
TOF MULT
TOF tray

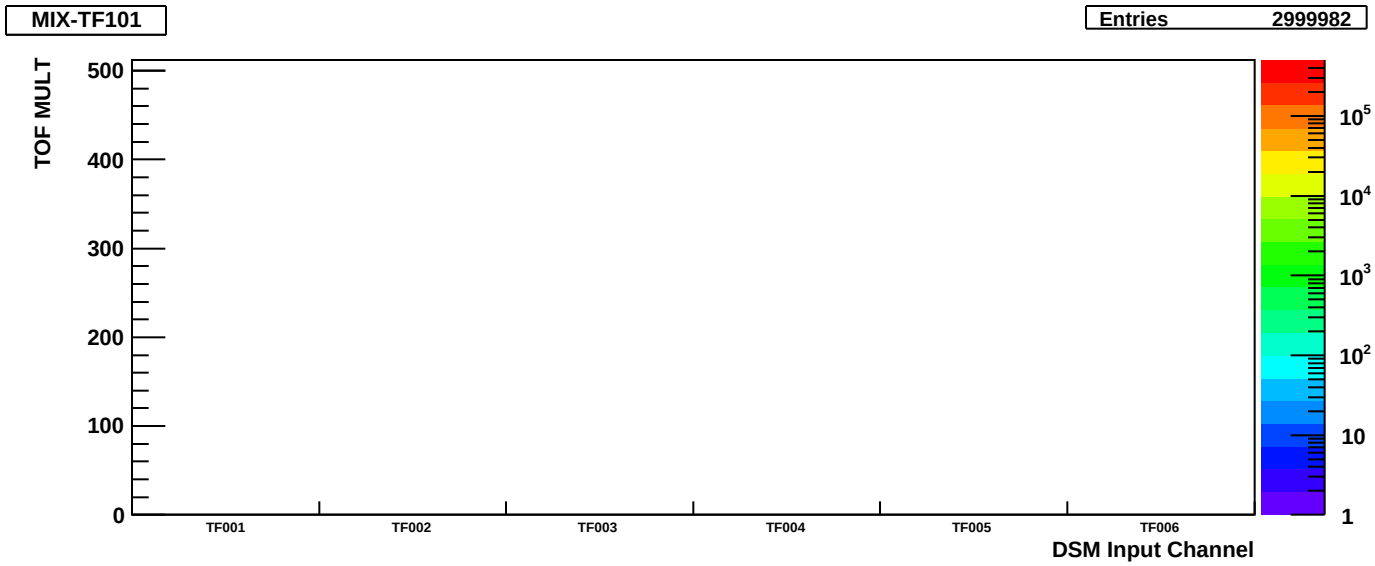

MIX-TF002
Entries 2.099987e+07
TOF MULT
TOF tray

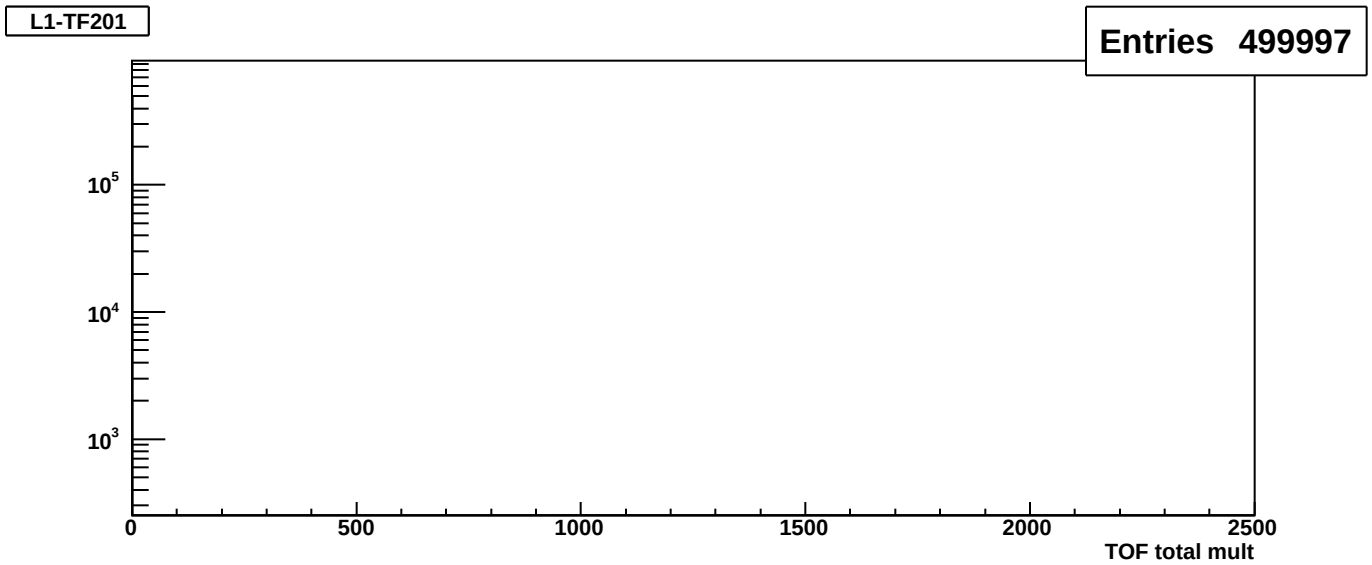

MIX-TF003
Entries 2.099987e+07
TOF MULT
TOF tray

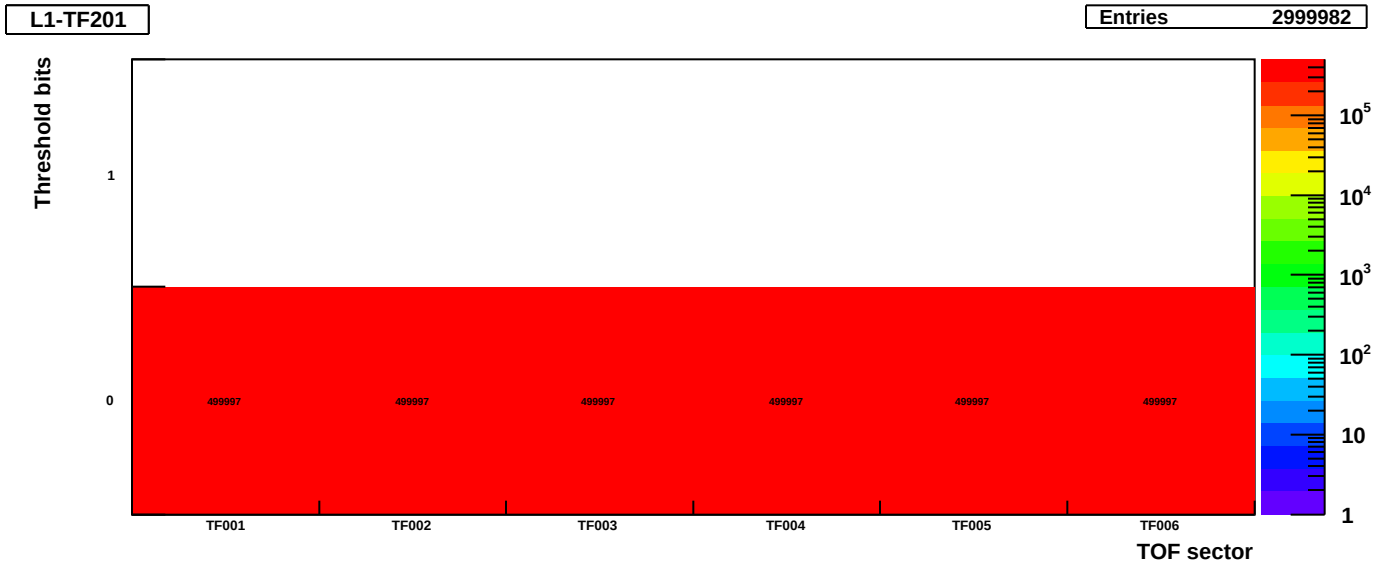

MIX-TF004
Entries 2.099987e+07
TOF MULT
TOF tray

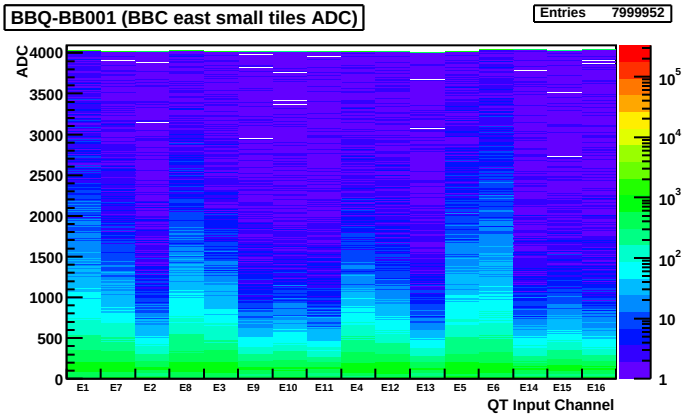

MIX-TF005
Entries 2.099987e+07
TOF MULT
TOF tray

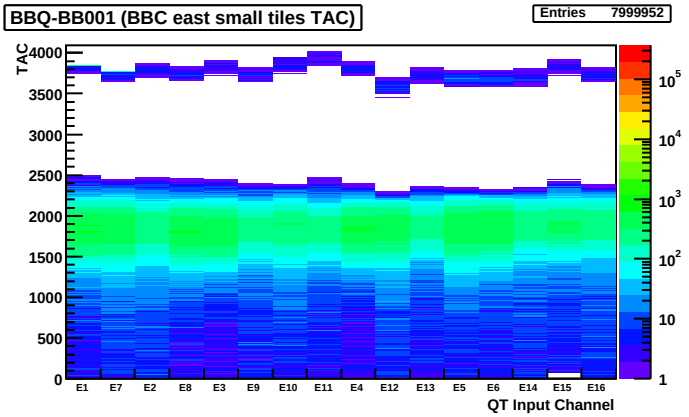

MIX-TF006
Entries 2.099987e+07
TOF MULT
TOF tray

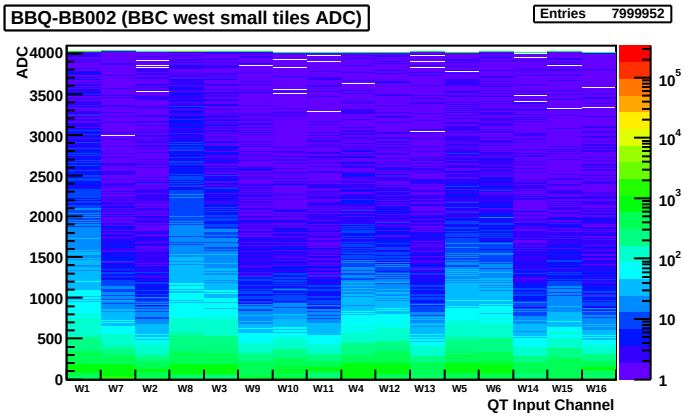

MIX-TF101
Entries 2999982
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

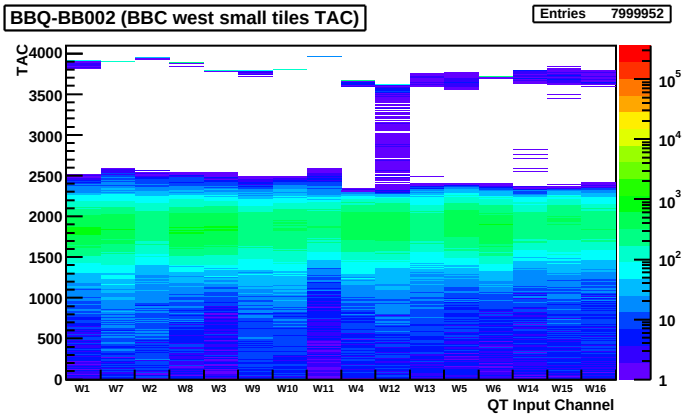

L1-TF201
Entries 499997
10^5
10^4
10^3
0
500
1000
1500
2000
2500
TOF total mult

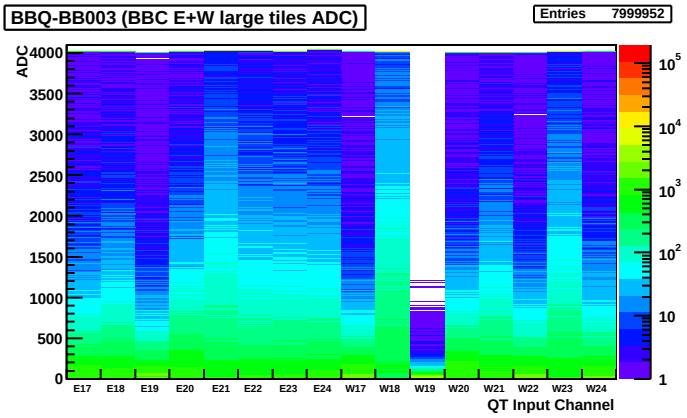

L1-TF201
Entries 2999982
Threshold bits
1
0
499997
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

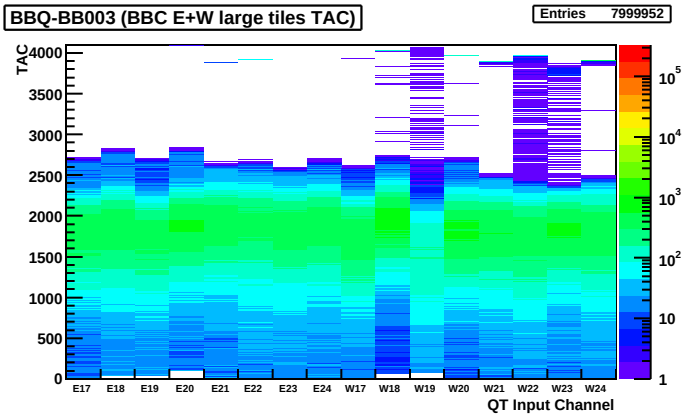

BBQ-BB001 (BBC east small tiles ADC)
Entries 7999952
ADC
QT Input Channel

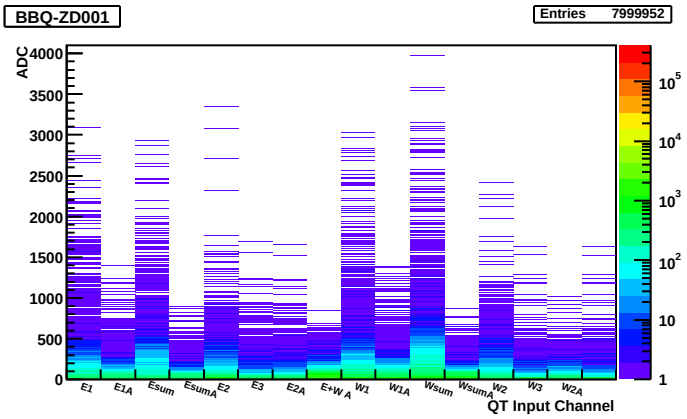

BBQ-BB001 (BBC east small tiles TAC)
Entries 7999952
TAC
QT Input Channel

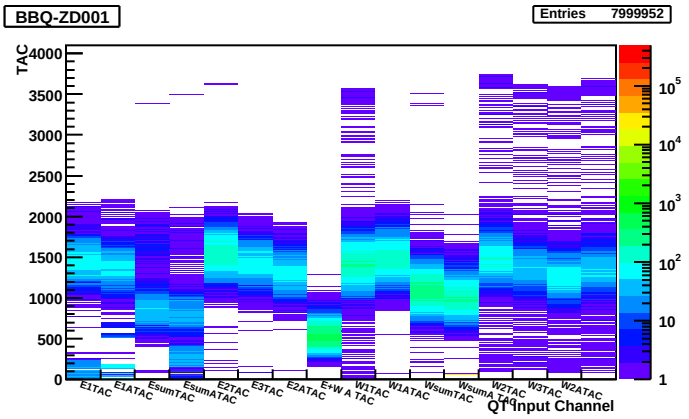

BBQ-BB002 (BBC west small tiles ADC)
Entries 7999952
ADC
QT Input Channel

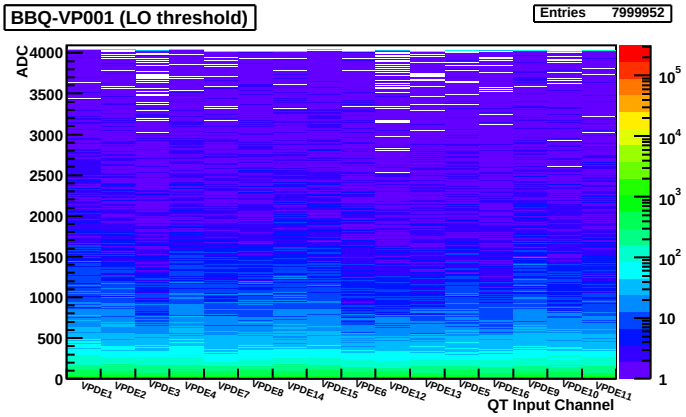

BBQ-BB002 (BBC west small tiles TAC)
Entries 7999952
TAC
QT Input Channel

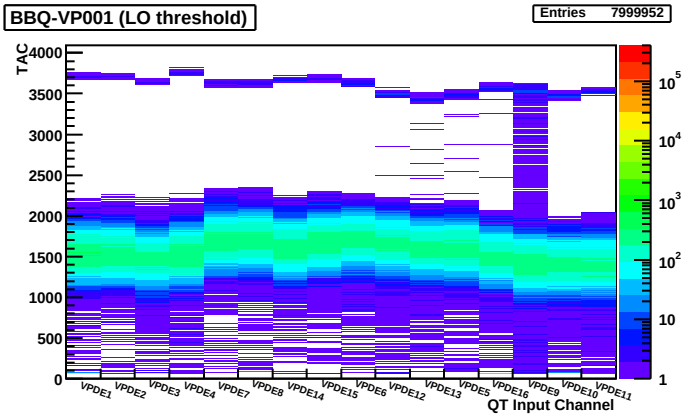

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7999952
ADC
QT Input Channel

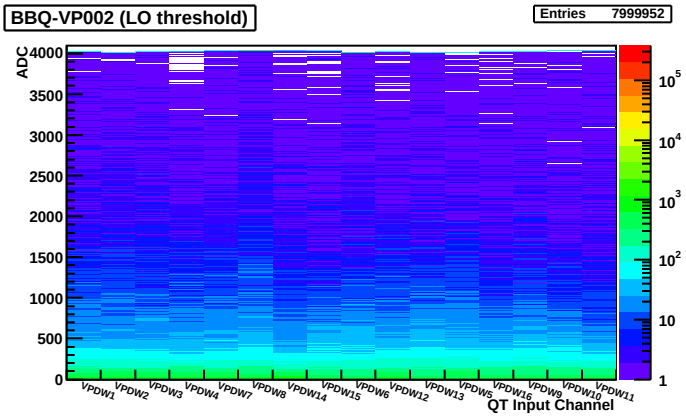

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7999952
TAC
QT Input Channel

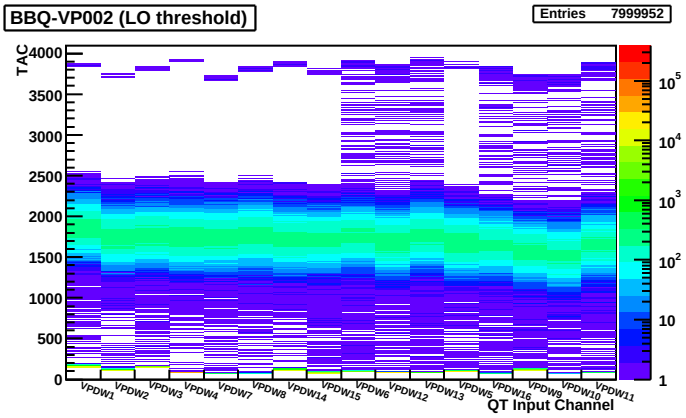

BBQ-ZD001
Entries 7999952
ADC
QT Input Channel

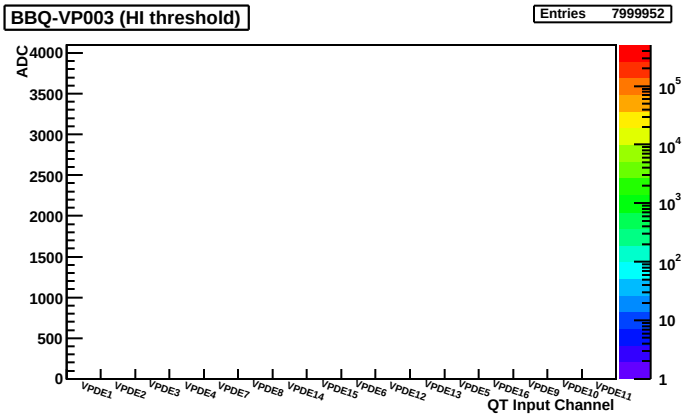

BBQ-ZD001
Entries 7999952
TAC
QT Input Channel

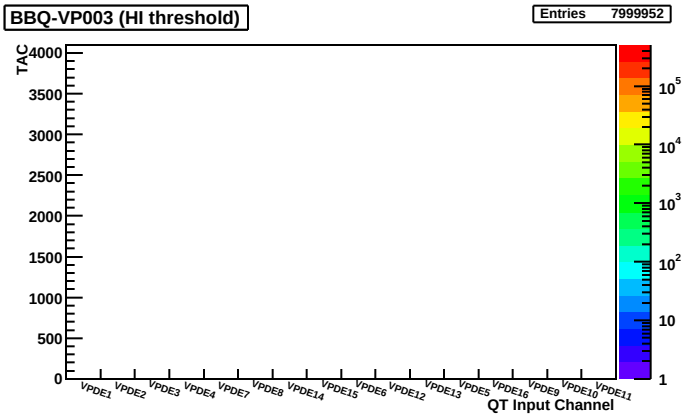

BBQ-VP001 (LO threshold)
Entries 7999952
ADC
QT Input Channel

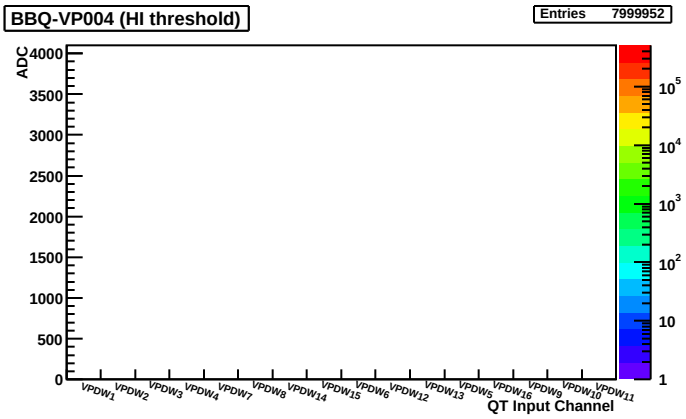

BBQ-VP001 (LO threshold)
Entries 7999952
TAC
QT Input Channel

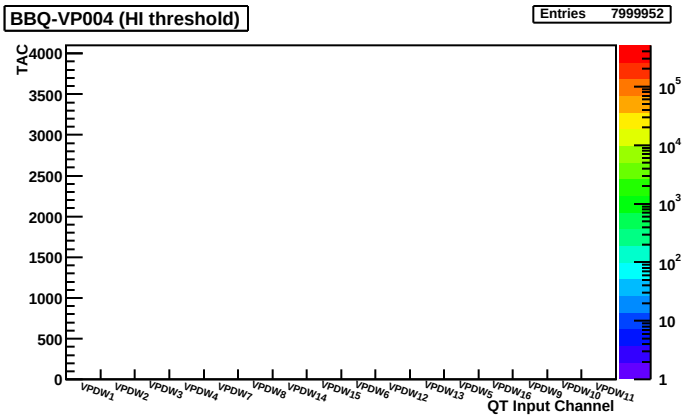

BBQ-VP002 (LO threshold)
Entries 7999952
ADC
QT Input Channel

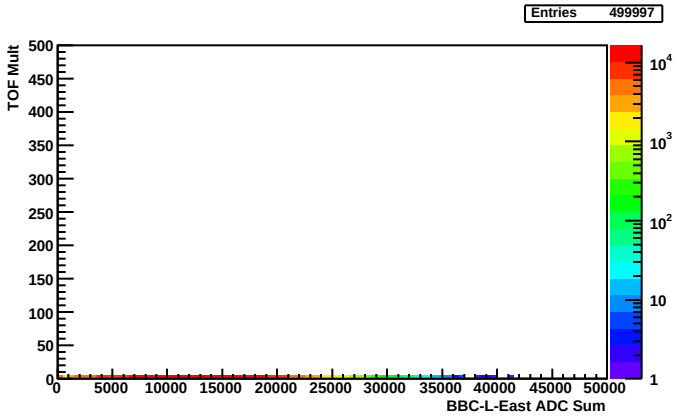

BBQ-VP002 (LO threshold)
Entries 7999952
TAC
QT Input Channel

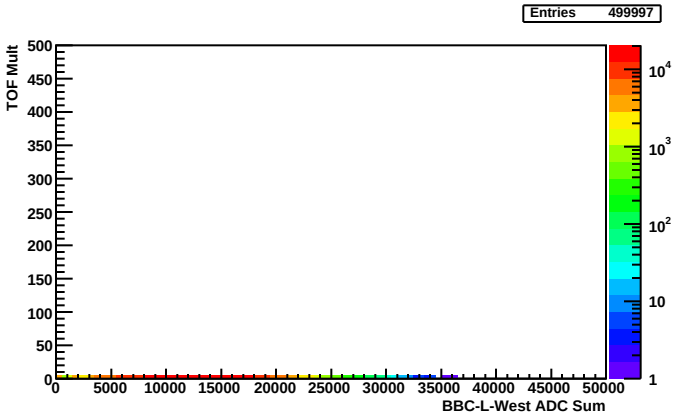

BBQ-VP003 (HI threshold)
Entries 7999952
ADC
QT Input Channel

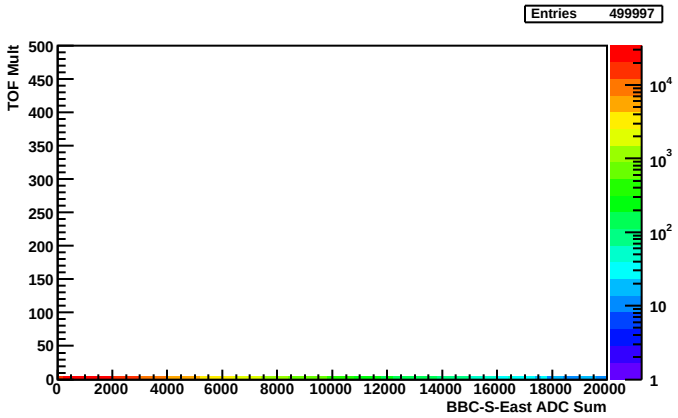

BBQ-VP003 (HI threshold)
Entries 7999952
TAC
QT Input Channel

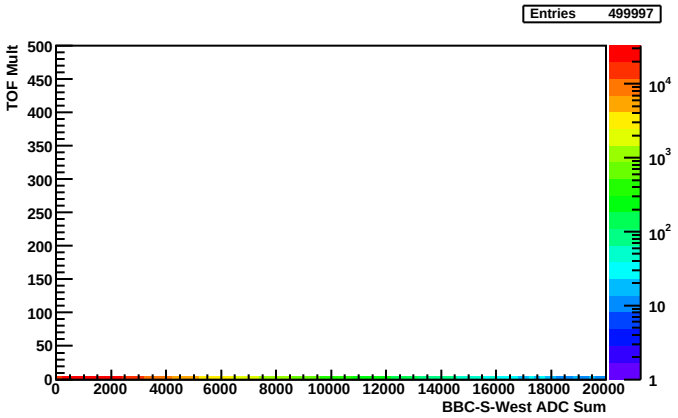

BBQ-VP004 (HI threshold)
Entries 7999952
ADC
QT Input Channel

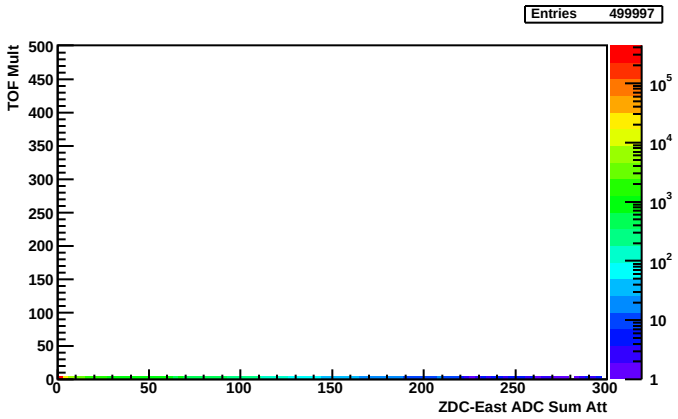

BBQ-VP004 (HI threshold)
Entries 7999952
TAC
QT Input Channel

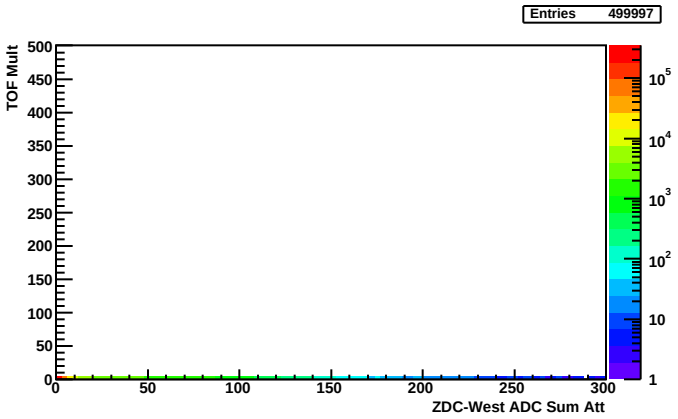

Entries 499997
TOF Mult
BBC-L-East ADC Sum

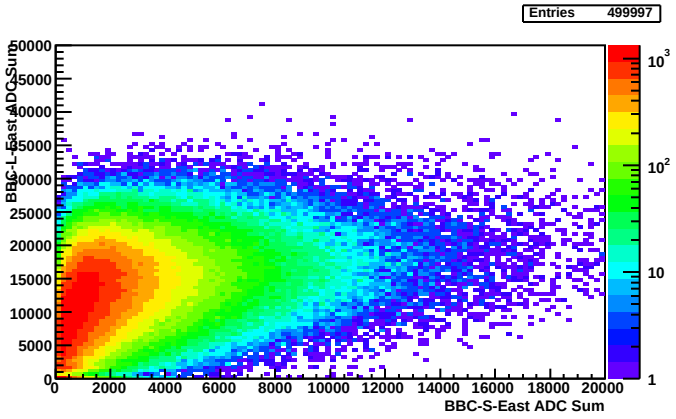

Entries 499997
TOF Mult
BBC-L-West ADC Sum

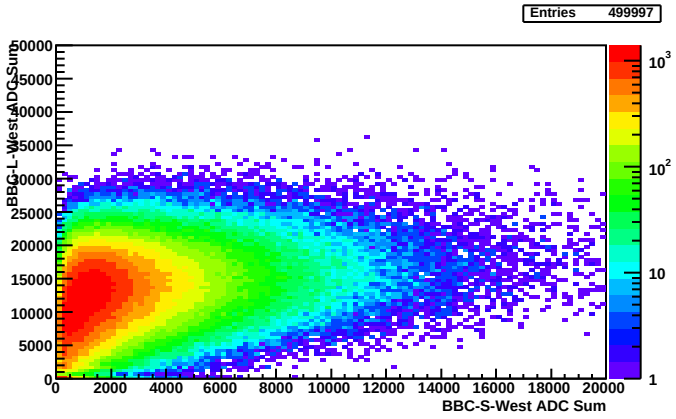

Entries 499997
TOF Mult
BBC-S-East ADC Sum

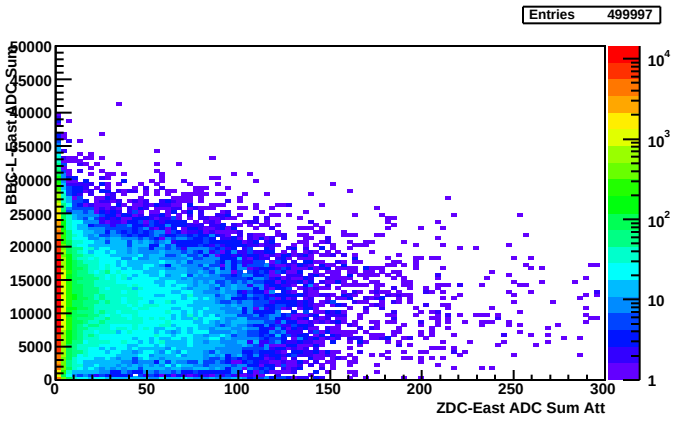

Entries 499997
TOF Mult
BBC-S-West ADC Sum

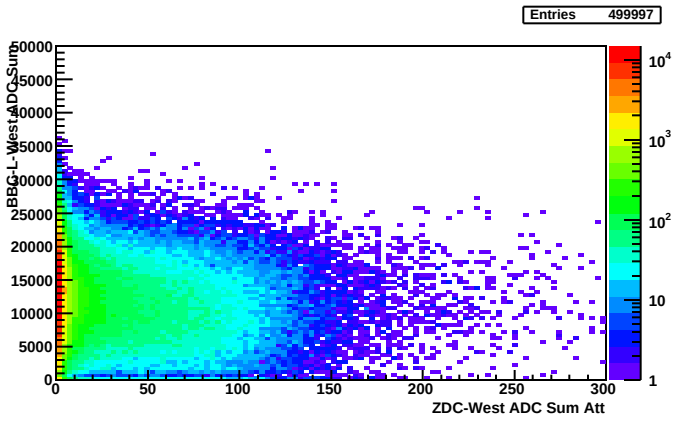

Entries 499997
TOF Mult
ZDC-East ADC Sum Att

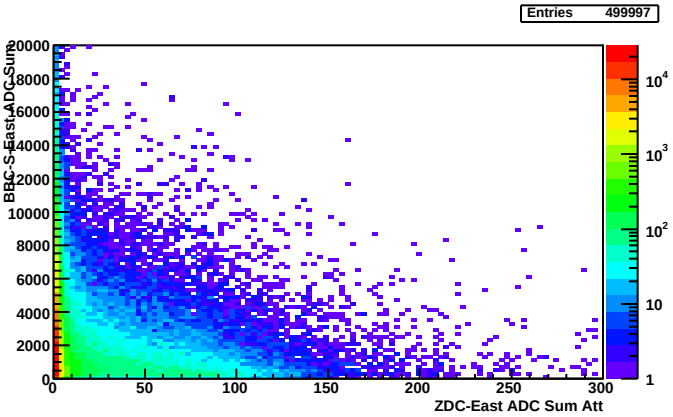

Entries 499997
TOF Mult
ZDC-West ADC Sum Att

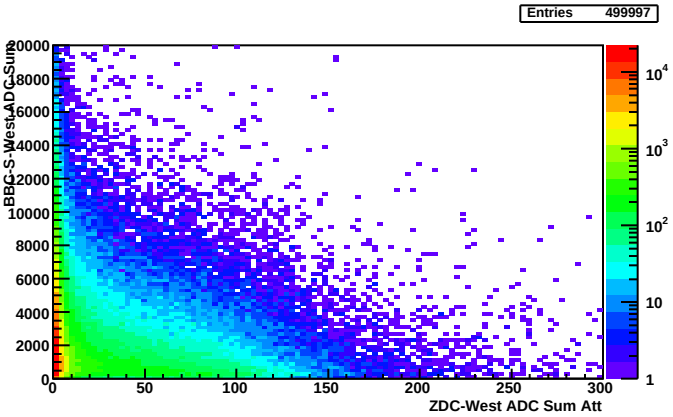

Entries 499997
BBC-L-East ADC Sum
BBC-S-East ADC Sum

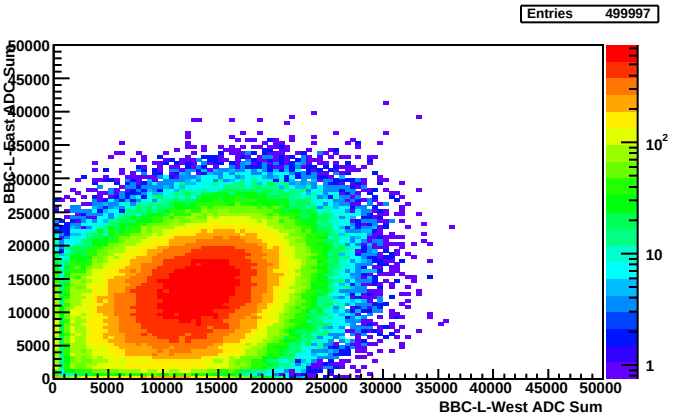

Entries 499997
BBC-L-West ADC Sum
BBC-S-West ADC Sum

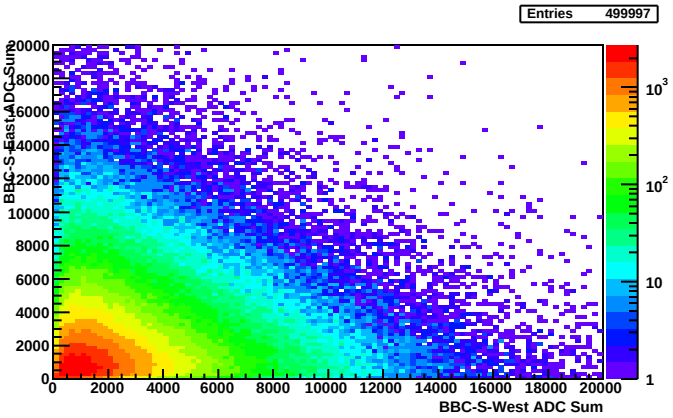

Entries 499997
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

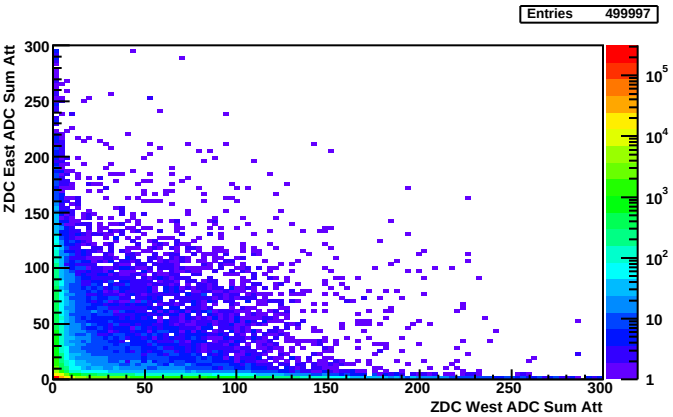

Entries 499997
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

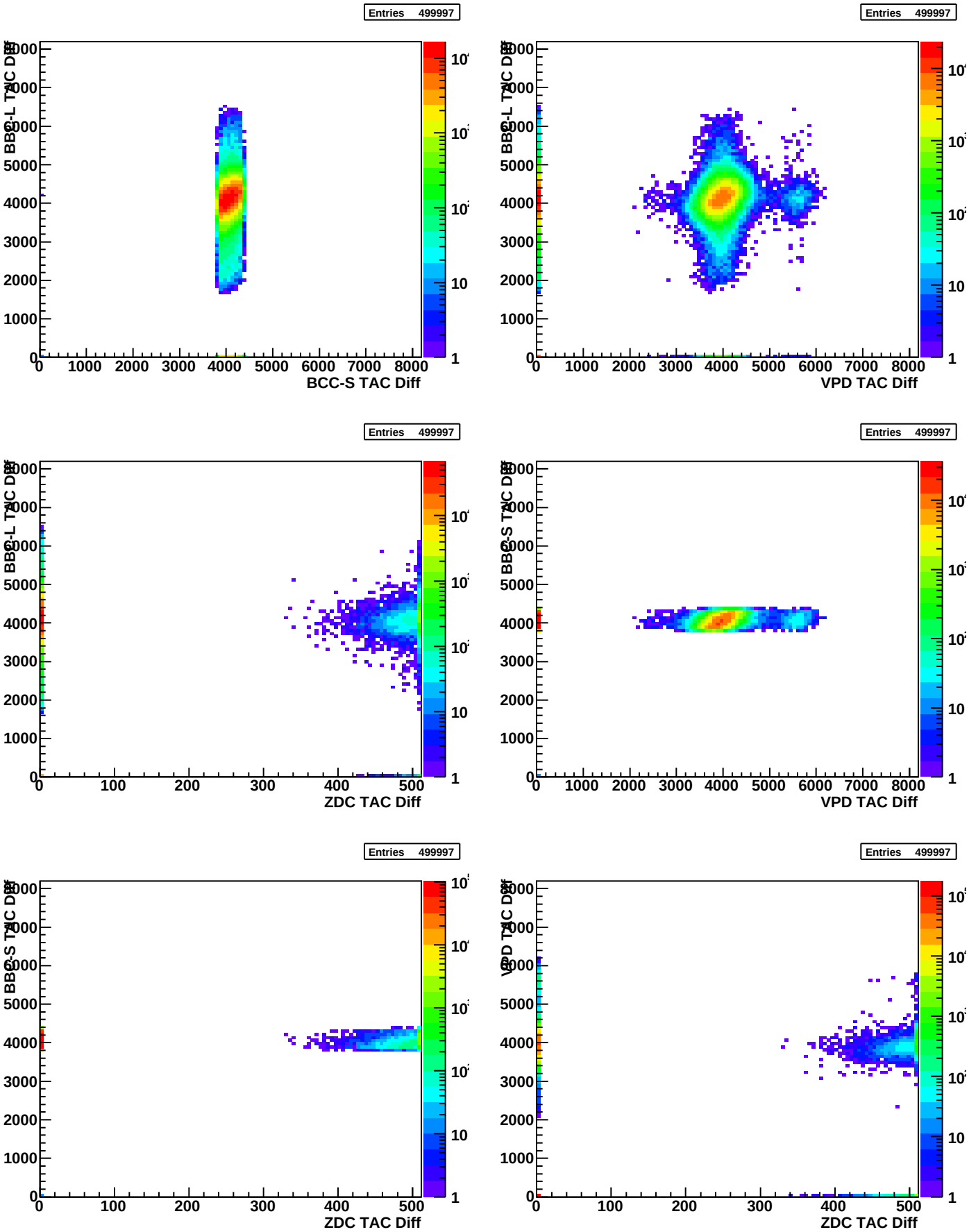

Entries 499997
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

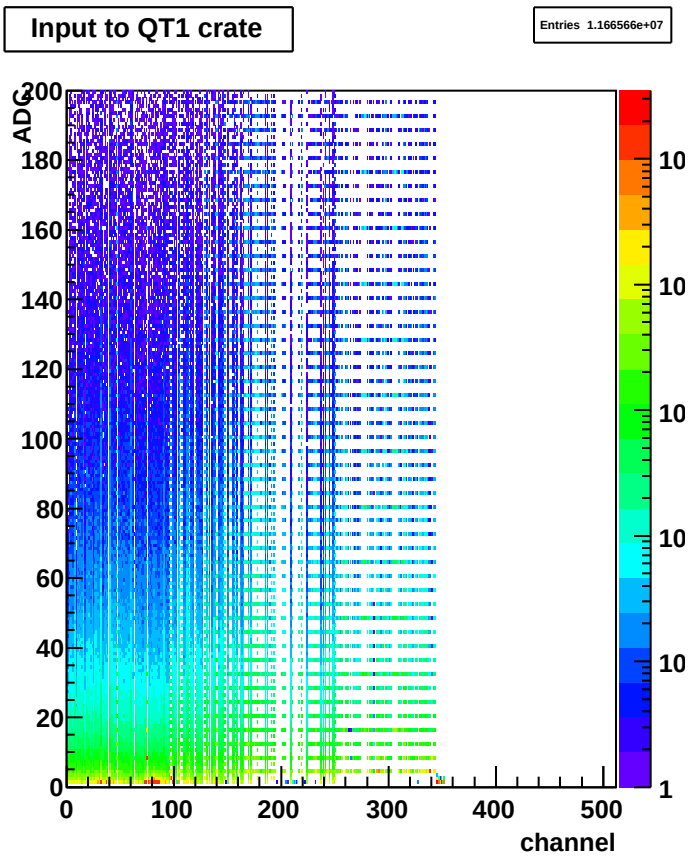

Entries 499997
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

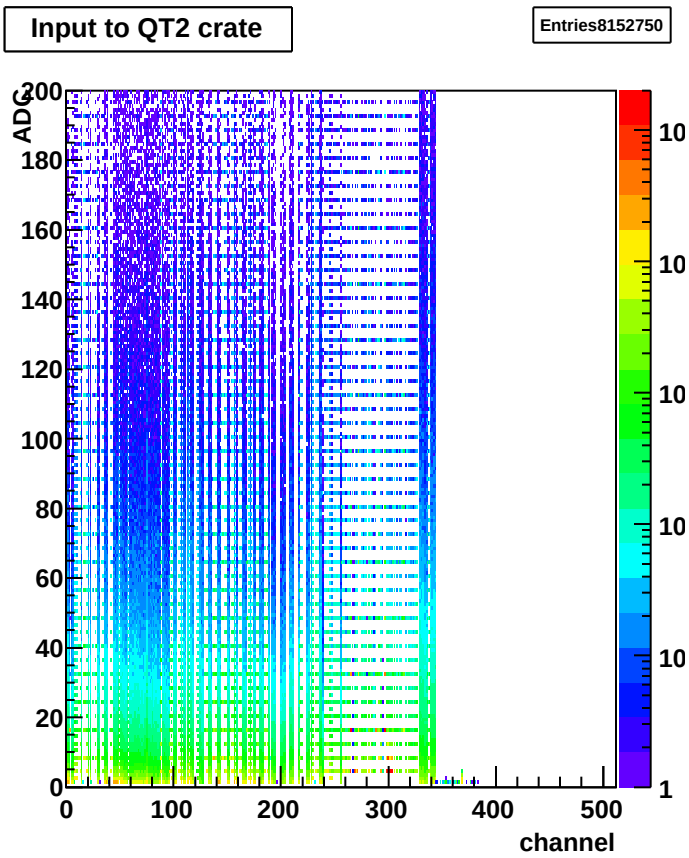

Entries 499997
BBC-L-East ADC Sum
BBC-L-West ADC Sum

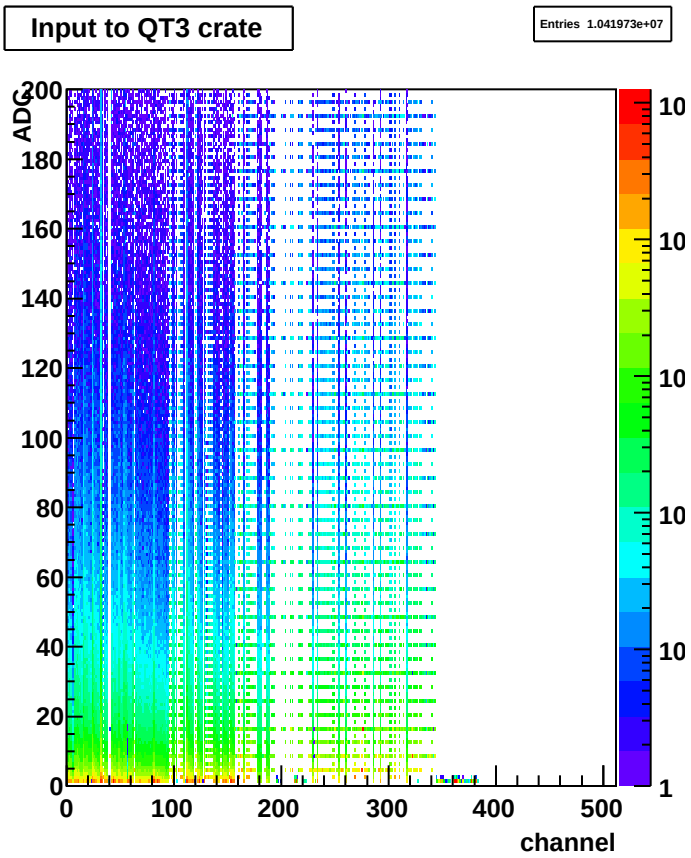

Entries 499997
BBC-S-East ADC Sum
BBC-S-West ADC Sum

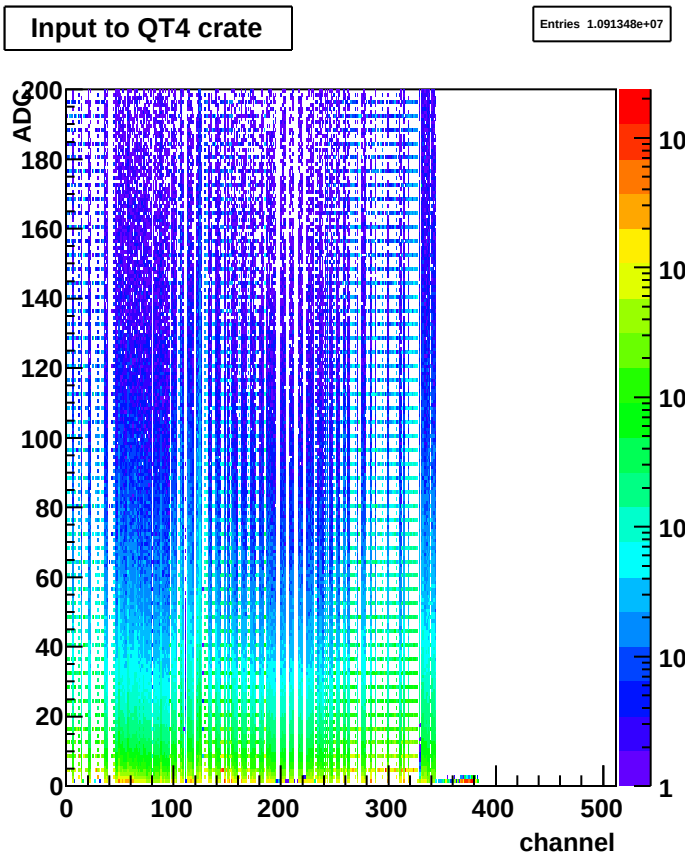

Entries 499997
ZDC East ADC Sum Att
ZDC West ADC Sum Att

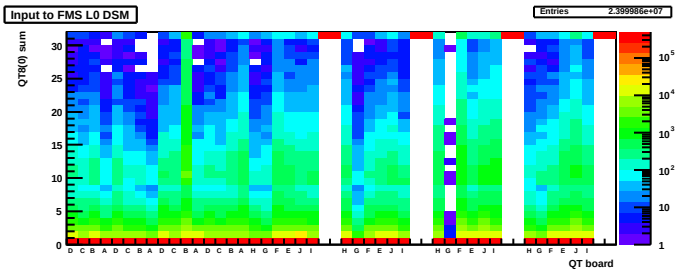

Entries 499997
BBC-L TAC Diff
BCC-S TAC Diff
Entries 499997
BBC-L TAC Diff
VPD TAC Diff
Entries 499997
BBC-L TAC Diff
ZDC TAC Diff
Entries 499997
BBC-S TAC Diff
VPD TAC Diff
Entries 499997
BBC-S TAC Diff
ZDC TAC Diff
Entries 499997
VPD TAC Diff
ZDC TAC Diff

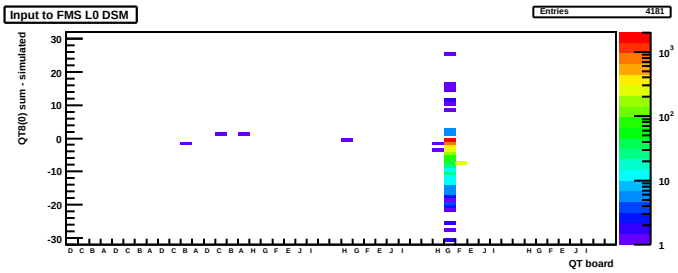

Input to QT1 crate
Entries 1.166566e+07
ADC
channel

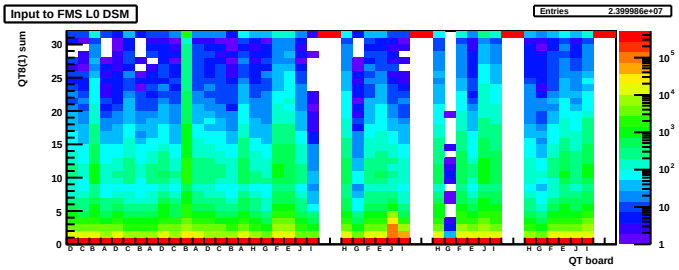

Input to QT2 crate
Entries8152750
ADC
channel

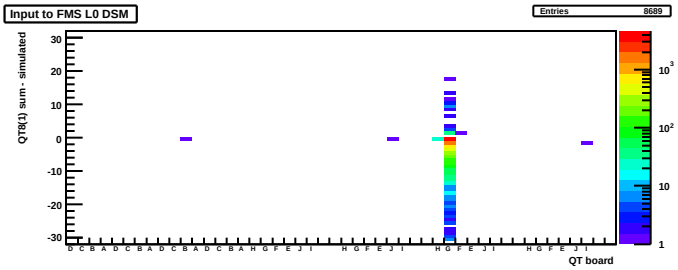

Input to QT3 crate
Entries 1.041973e+07
ADC
channel

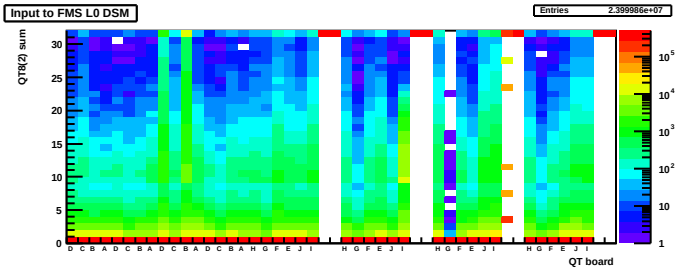

Input to QT4 crate
Entries 1.091348e+07
ADC
channel

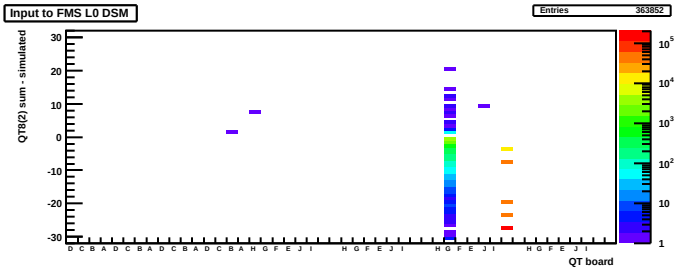

Input to FMS L0 DSM
Entries 2.399986e+07
QT8(0) sum
QT board

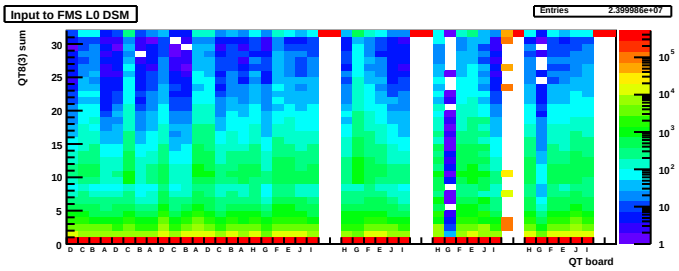

Input to FMS L0 DSM
Entries 4181
QT8(0) sum - simulated
QT board

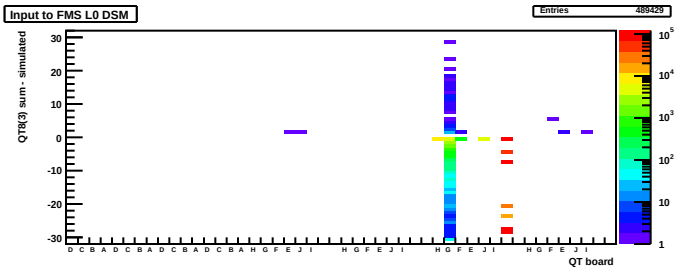

Input to FMS L0 DSM
Entries 2.399986e+07
QT8(1) sum
QT board

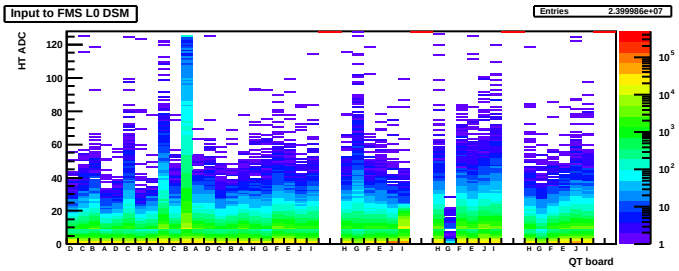

Input to FMS L0 DSM
Entries 8689
QT8(1) sum - simulated
QT board

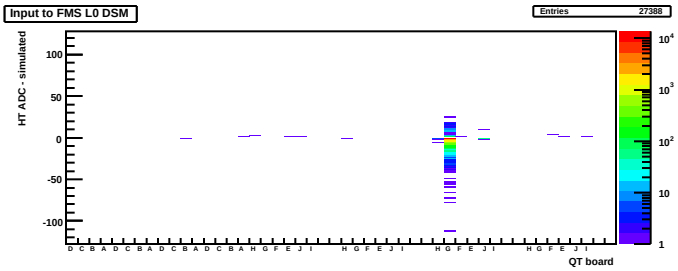

Input to FMS L0 DSM
Entries 2.399986e+07
QT8(2) sum
QT board

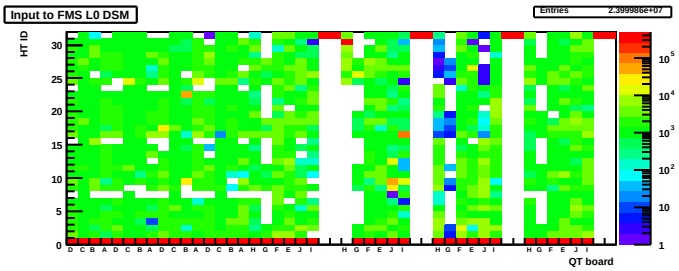

Input to FMS L0 DSM
Entries 363852
QT8(2) sum - simulated
QT board

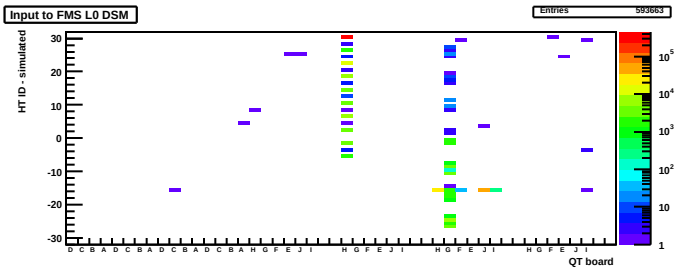

Input to FMS L0 DSM
Entries 2.399986e+07
QT8(3) sum
QT board

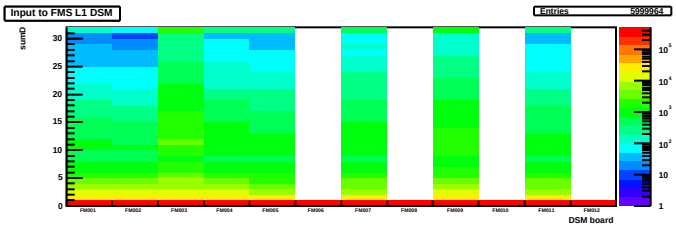

Input to FMS L0 DSM
Entries 489429
QT8(3) sum - simulated
QT board

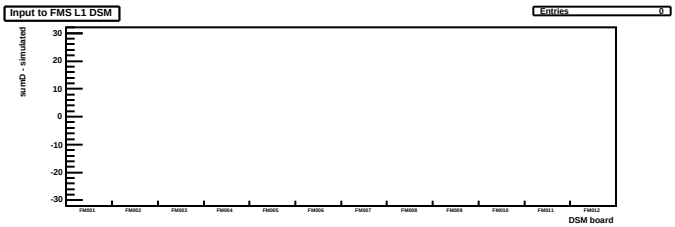

Input to FMS L0 DSM
Entries 2.399986e+07
HT ADC
QT board

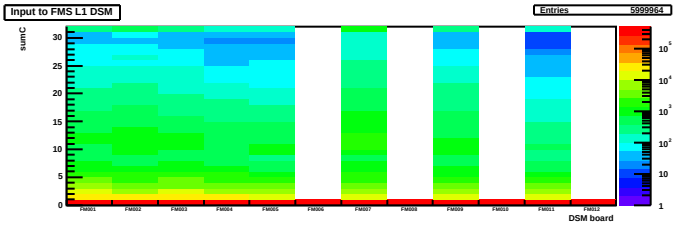

Input to FMS L0 DSM
Entries 27388
HT ADC - simulated
QT board

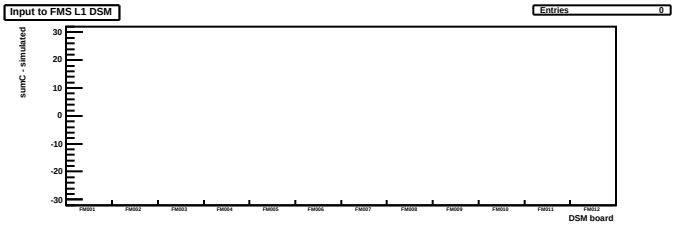

Input to FMS L0 DSM
Entries 2.399986e+07
HT ID
QT board

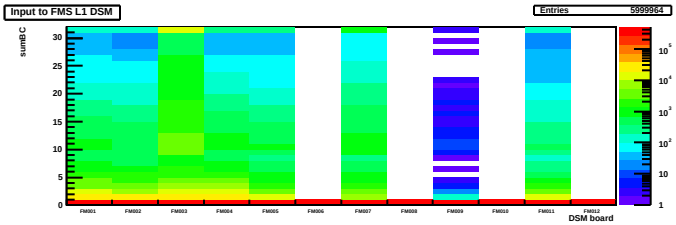

Input to FMS L0 DSM
Entries 593663
HT ID - simulated
QT board

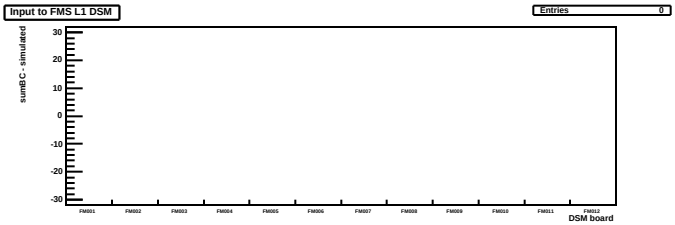

Input to FMS L1 DSM
Entries 5999964
sumD
DSM board

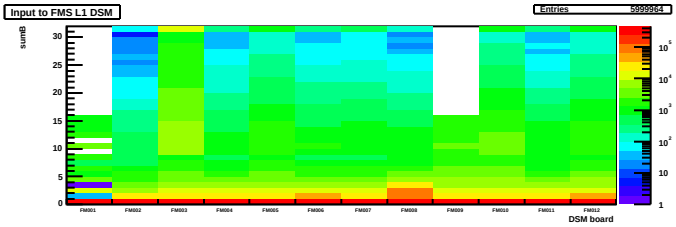

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

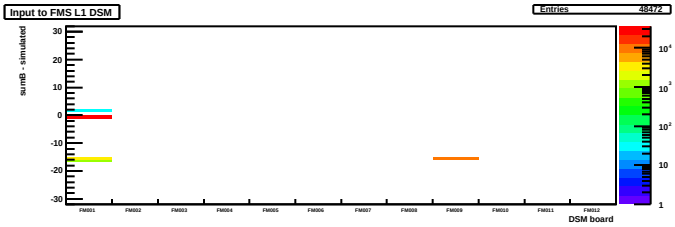

Input to FMS L1 DSM
Entries 5999964
sumC
DSM board

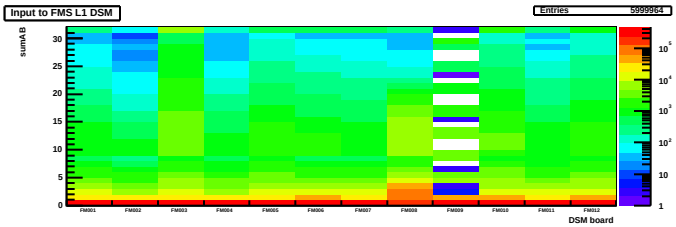

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

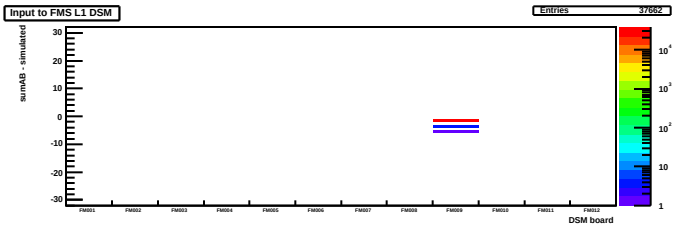

Input to FMS L1 DSM
Entries 5999964
sumBC
DSM board

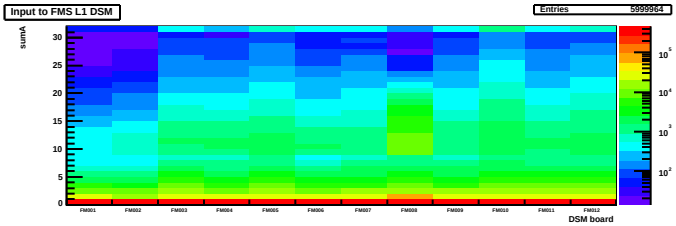

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

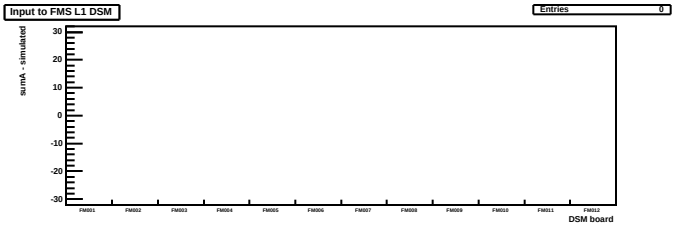

Input to FMS L1 DSM
Entries 5999964
sumB
DSM board

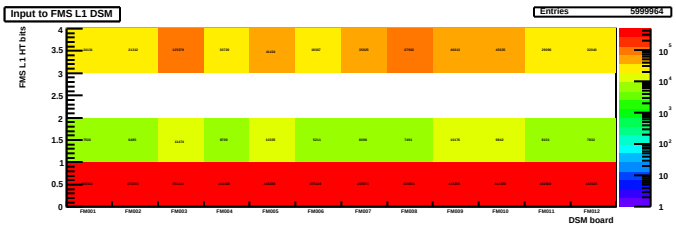

Input to FMS L1 DSM
Entries 48472
sumB - simulated
DSM board

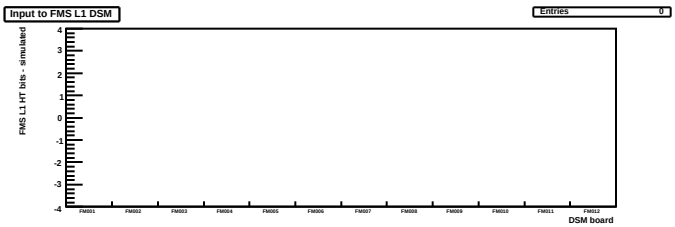

Input to FMS L1 DSM
Entries 5999964
sumAB
DSM board

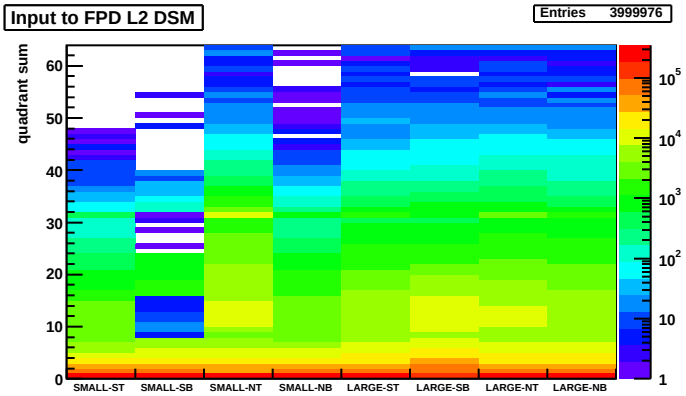

Input to FMS L1 DSM
Entries 37662
sumAB - simulated
DSM board

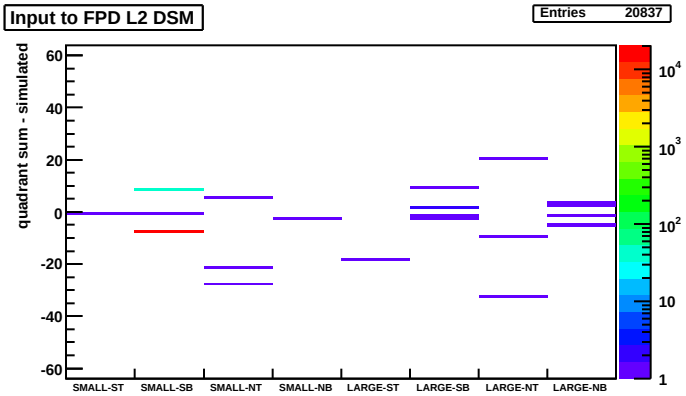

Input to FMS L1 DSM
Entries 5999964
sumA
DSM board

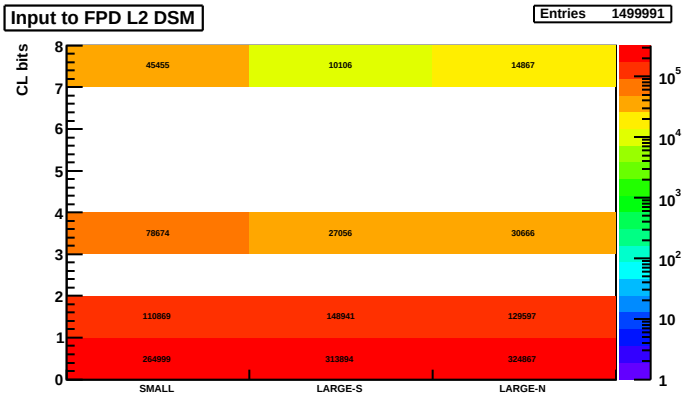

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

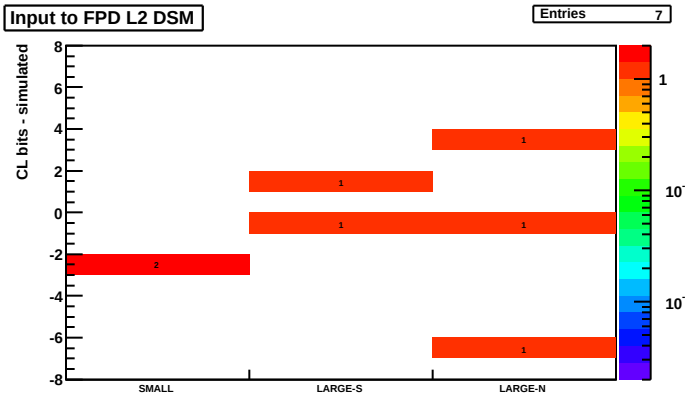

Input to FMS L1 DSM
Entries 5999964
FMS L1 HT bits
DSM board

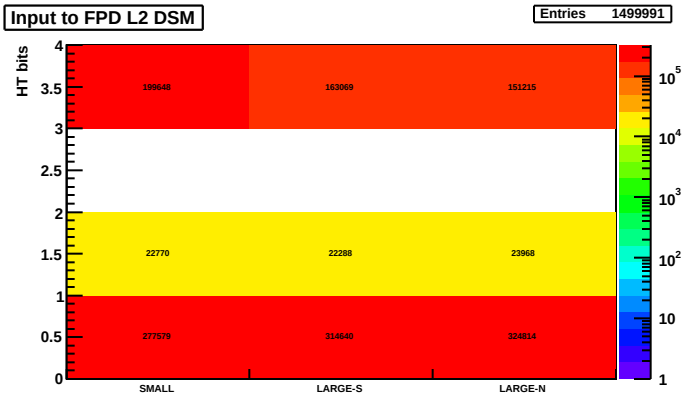

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

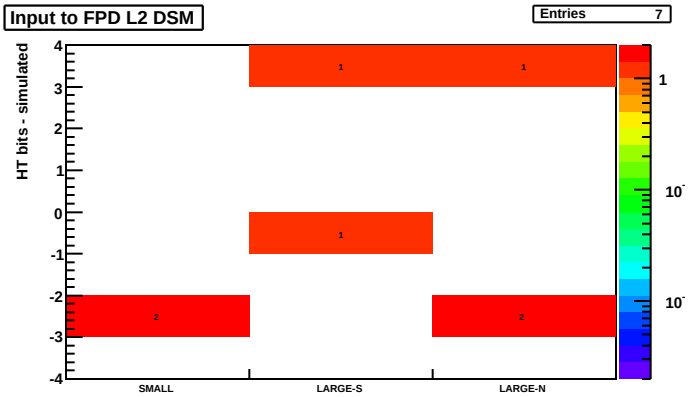

Input to FPD L2 DSM
Entries 3999976
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

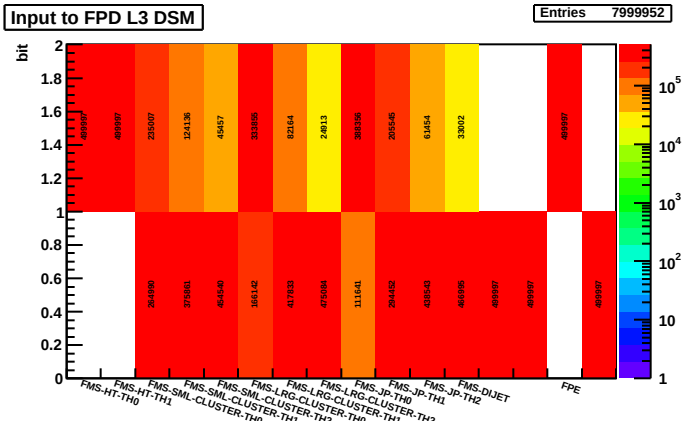

Input to FPD L2 DSM
Entries 20837
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

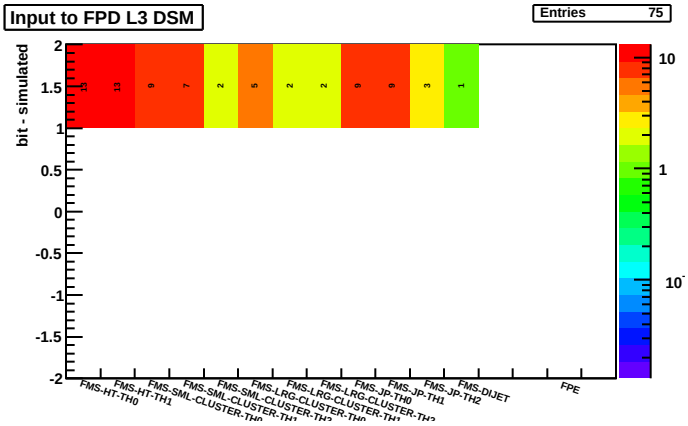

Input to FPD L2 DSM
Entries 1499991
CL bits
45455
10106
14867
78674
27056
30666
110869
148941
129597
264999
313894
324867
SMALL
LARGE-S
LARGE-N

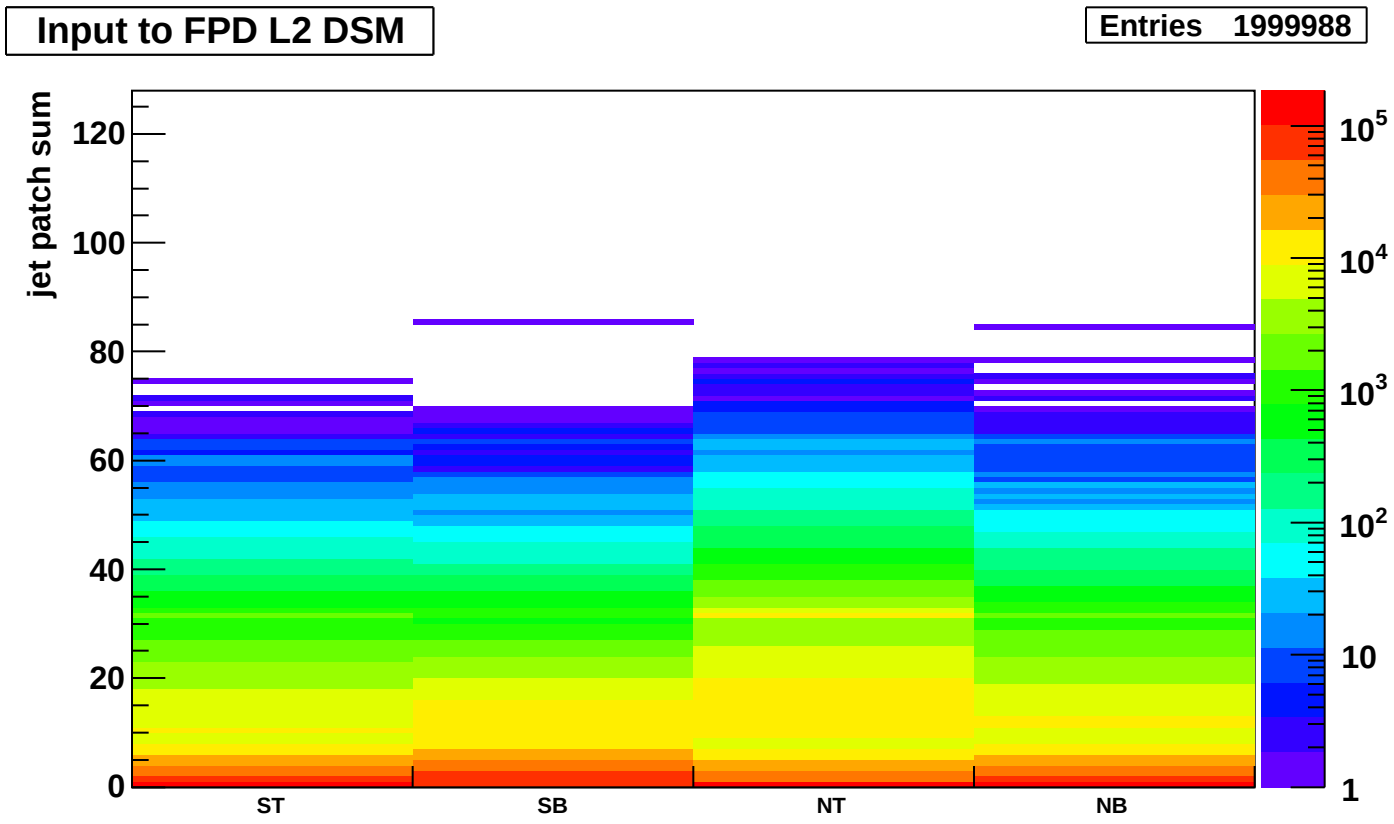

Input to FPD L2 DSM
Entries 7
CL bits - simulated
SMALL
LARGE-S
LARGE-N

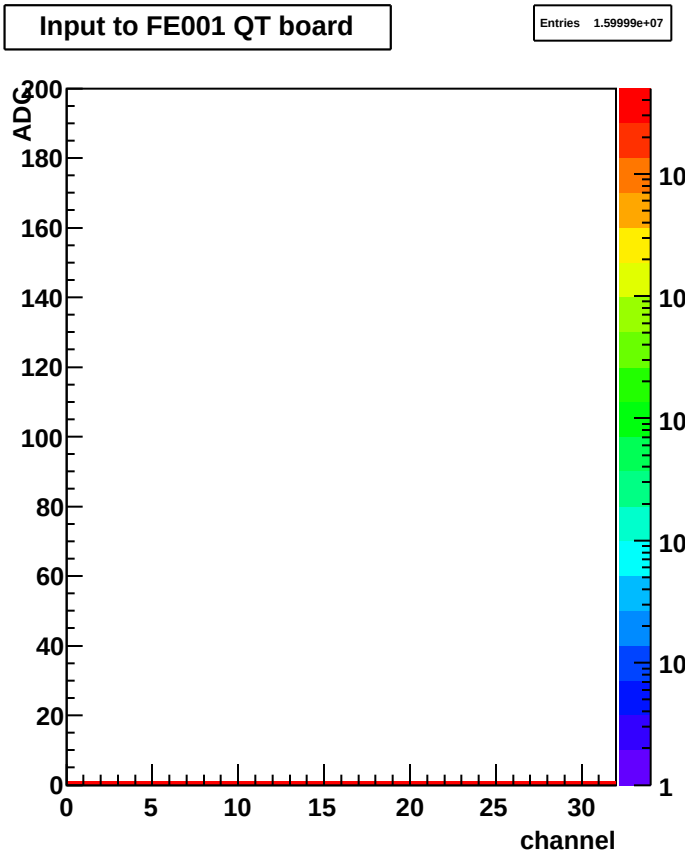

Input to FPD L2 DSM
Entries 1499991
HT bits
199648
163069
151215
22770
22288
23968
277579
314640
324814
SMALL
LARGE-S
LARGE-N

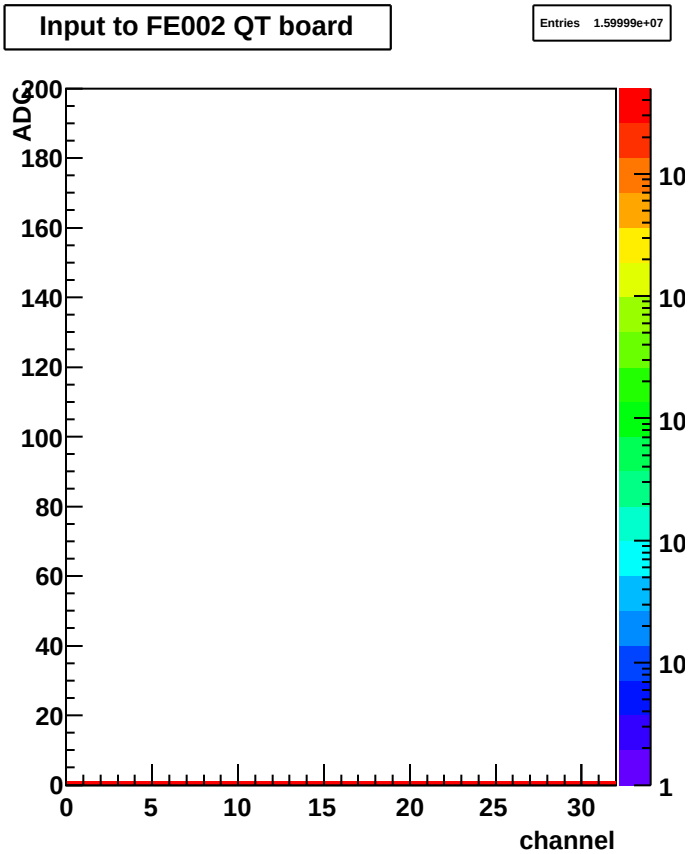

Input to FPD L2 DSM
Entries 7
HT bits - simulated
SMALL
LARGE-S
LARGE-N

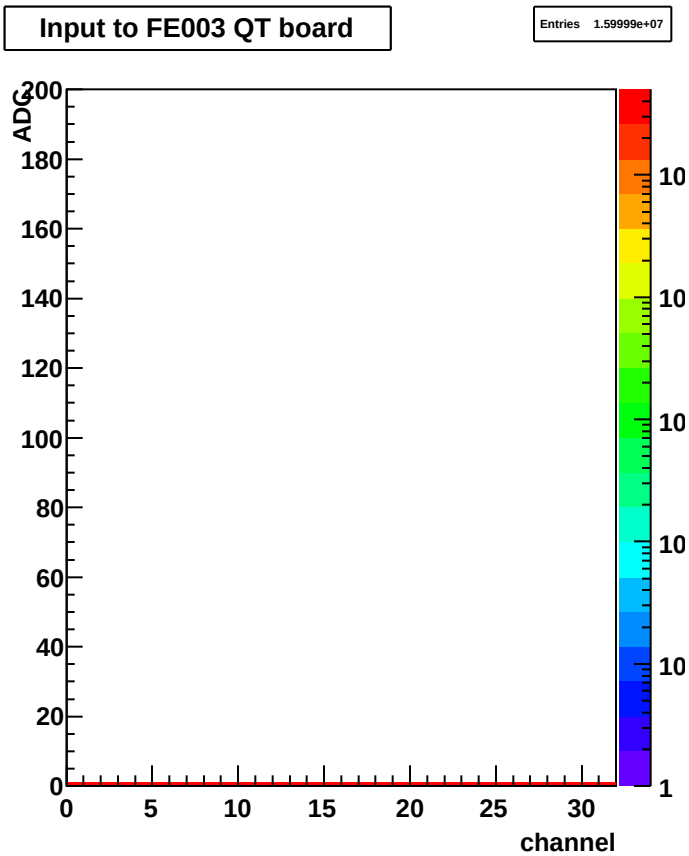

Input to FPD L3 DSM
Entries 7999952
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

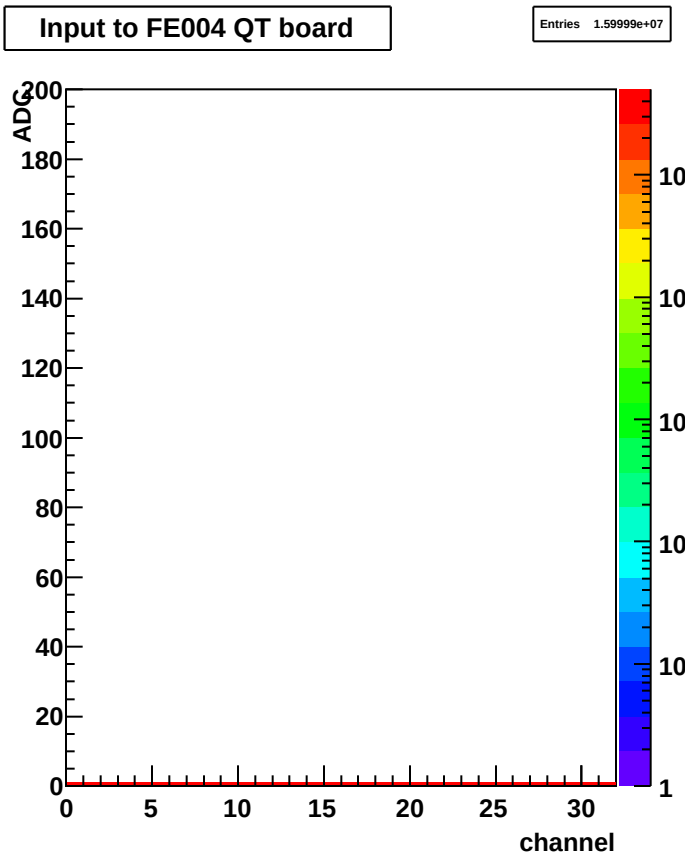

Input to FPD L3 DSM
Entries 75
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

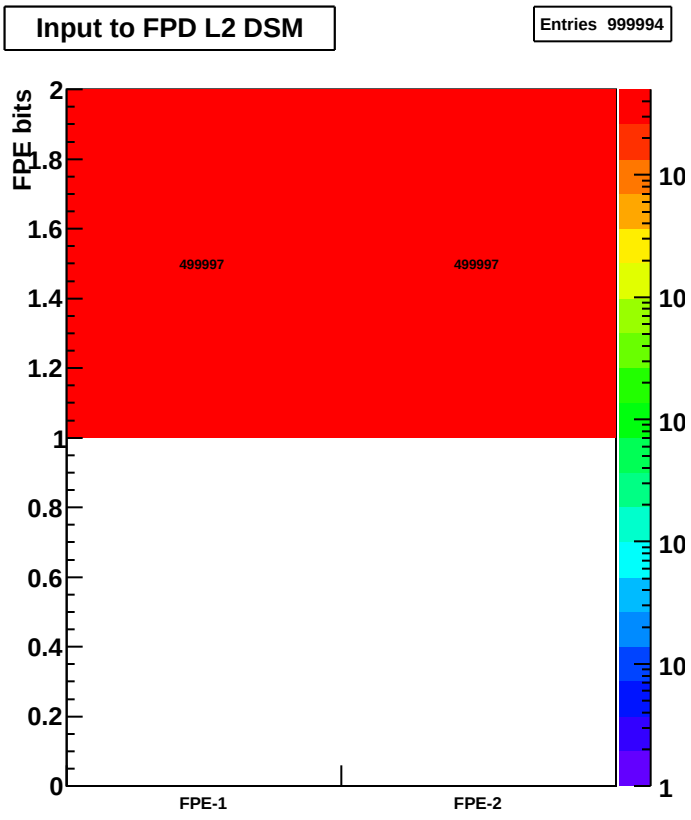

Input to FPD L2 DSM
Entries 1999988
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

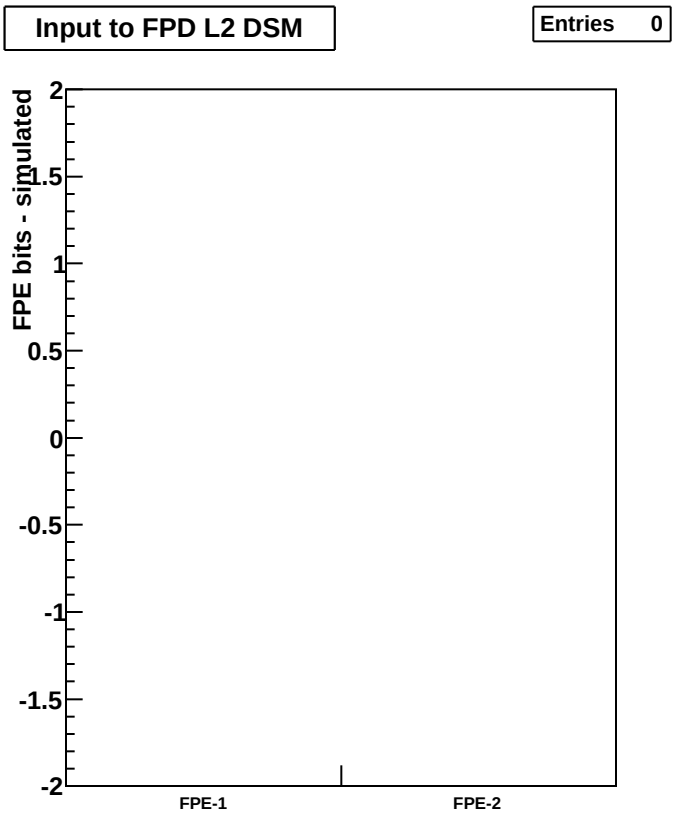

Input to FE001 QT board
Entries 1.59999e+07
ADC
channel


Input to FE002 QT board
Entries 1.59999e+07
ADC
channel


Input to FE003 QT board
Entries 1.59999e+07
ADC
channel


Input to FE004 QT board
Entries 1.59999e+07
ADC
channel


Input to FPD L2 DSM
Entries 999994
FPE bits
499997
499997
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2