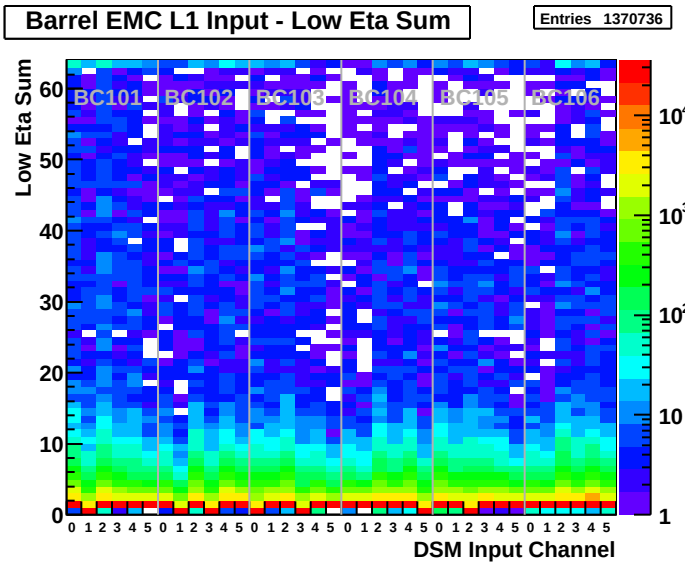
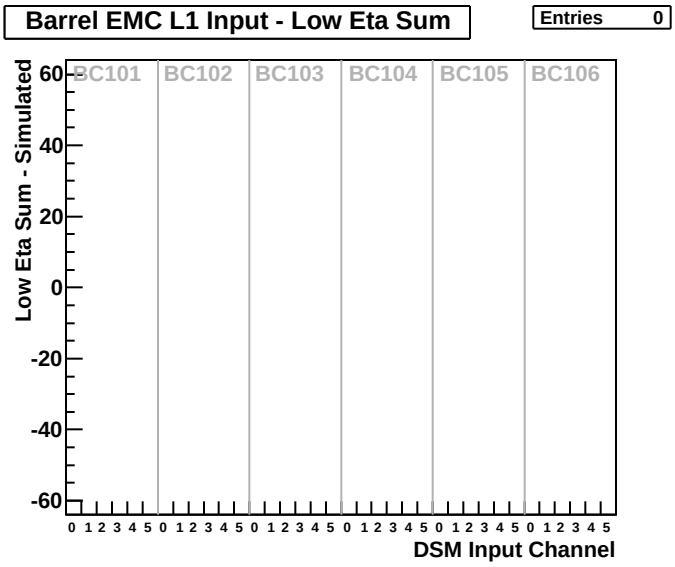
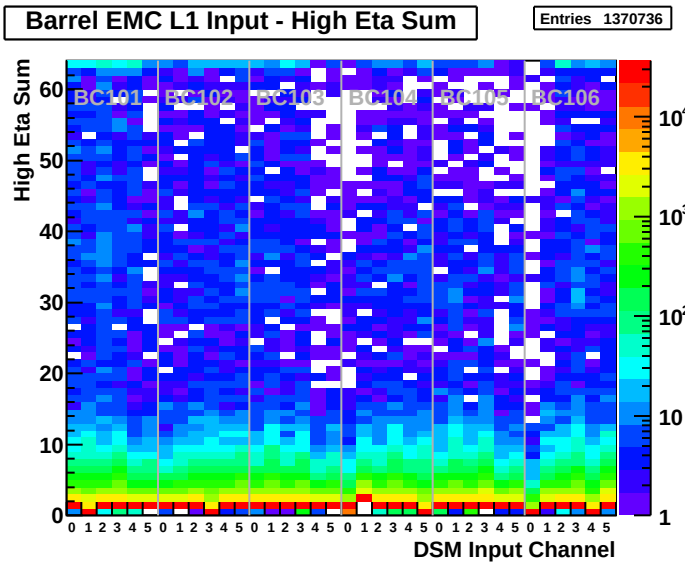
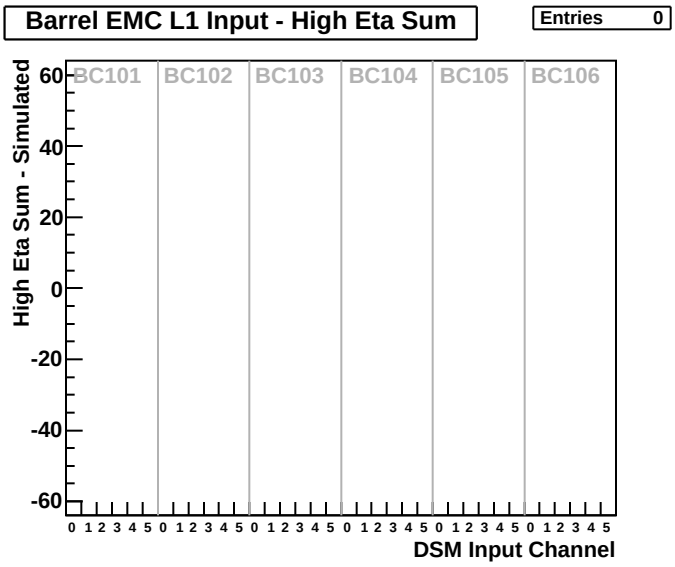
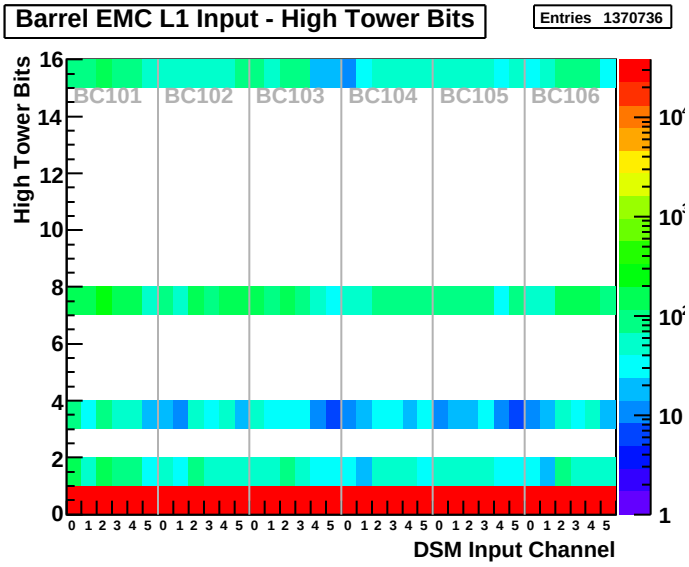
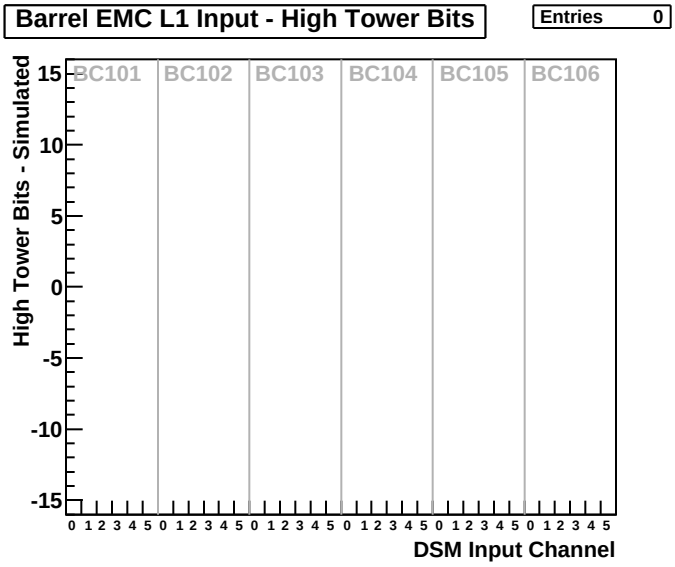

Barrel EMC L1 Input - Low Eta Sum
Entries 1370736
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

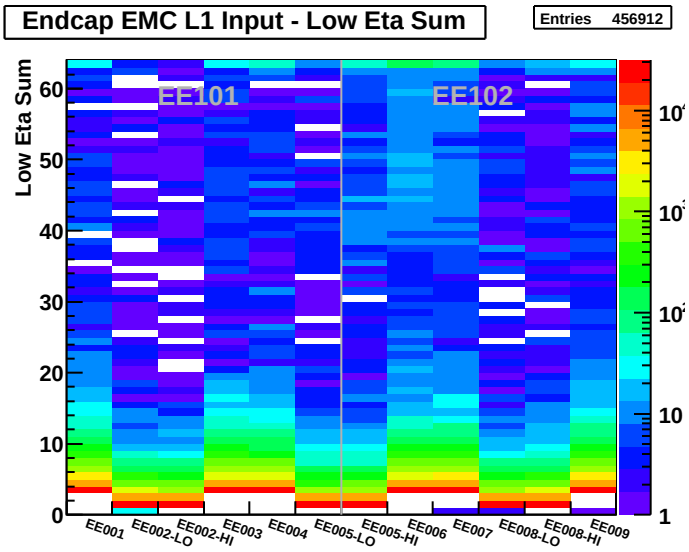

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

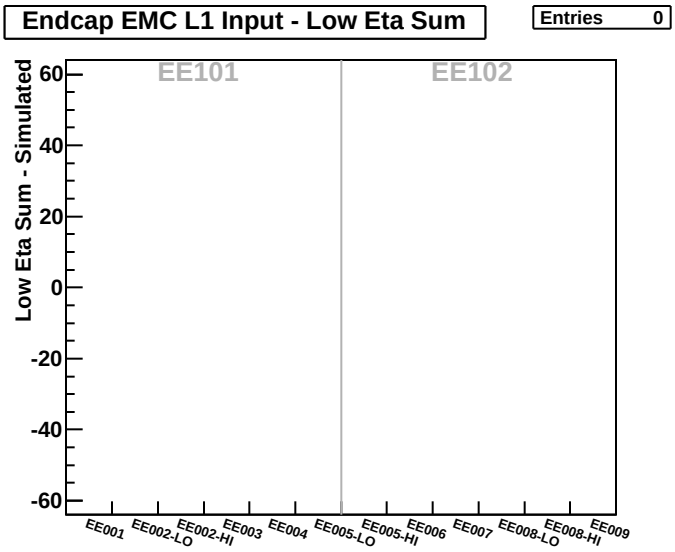

Barrel EMC L1 Input - High Eta Sum
Entries 1370736
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

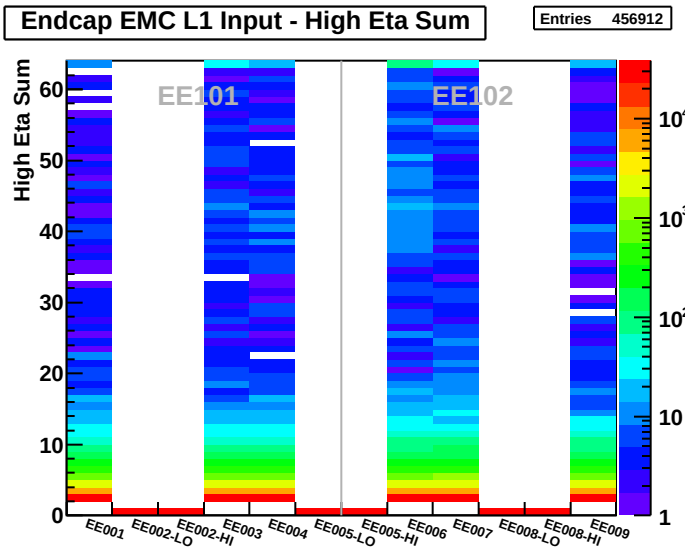

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

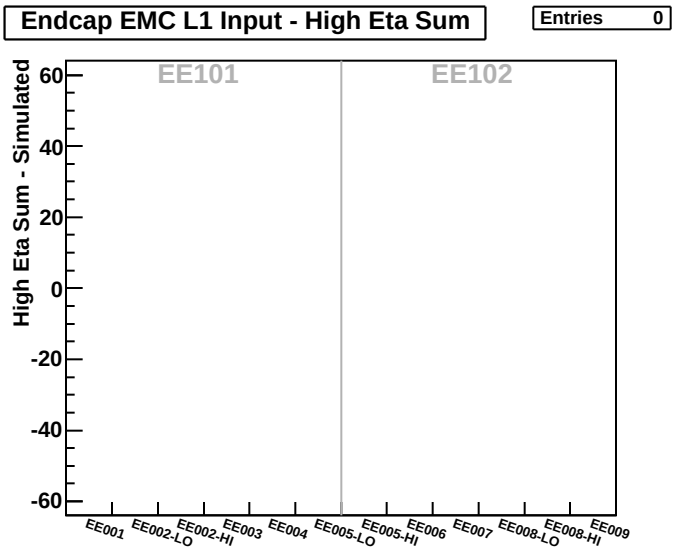

Barrel EMC L1 Input - High Tower Bits
Entries 1370736
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

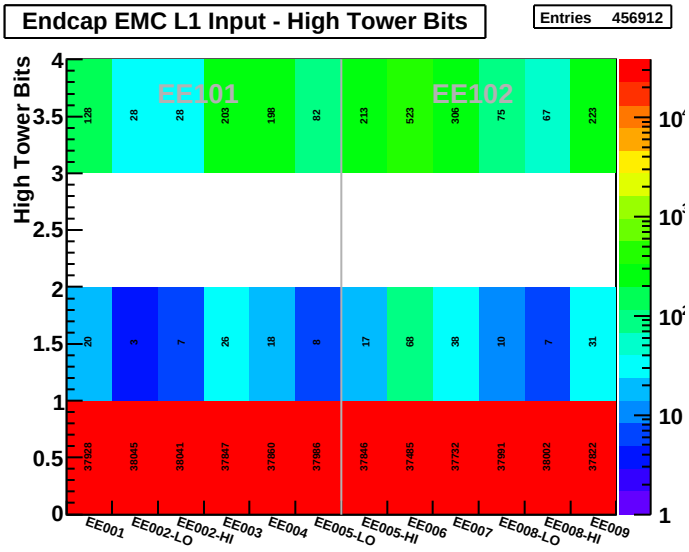

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

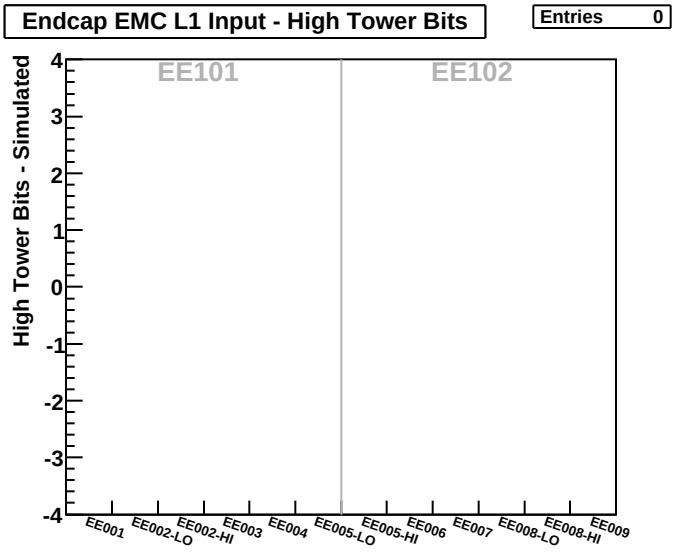

Endcap EMC L1 Input - Low Eta Sum
Entries 456912
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

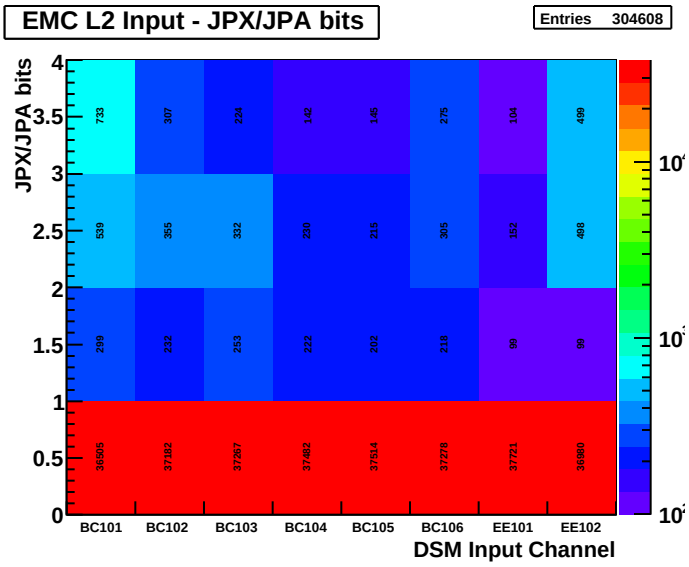

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

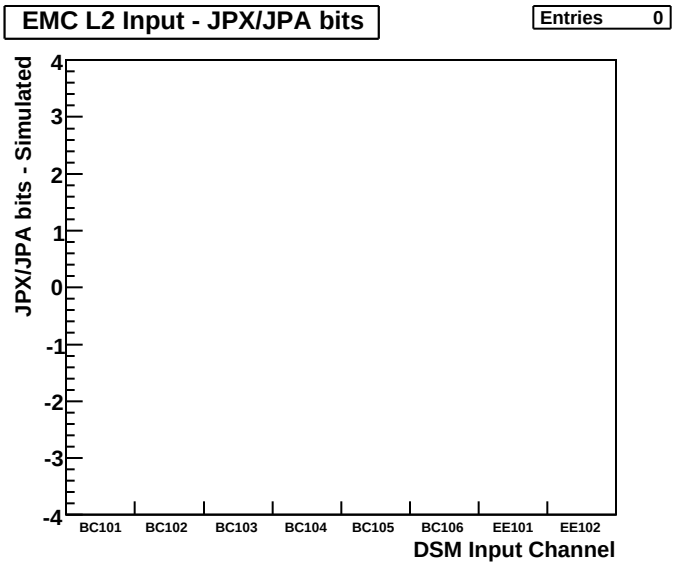

Endcap EMC L1 Input - High Eta Sum
Entries 456912
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

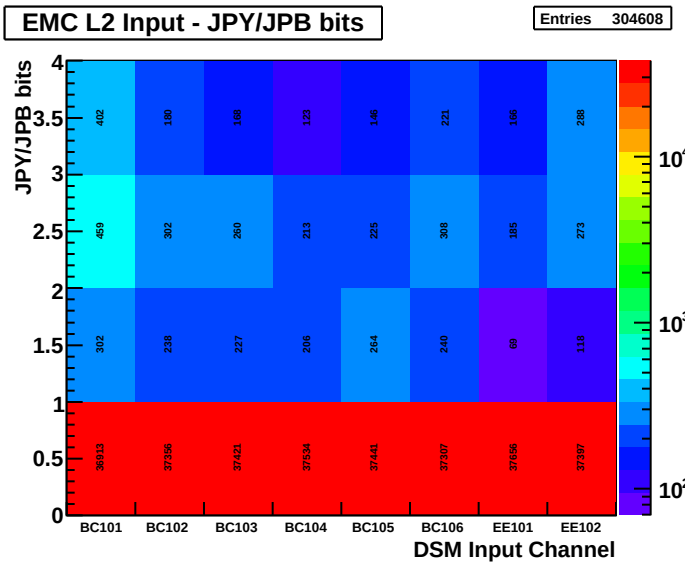

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

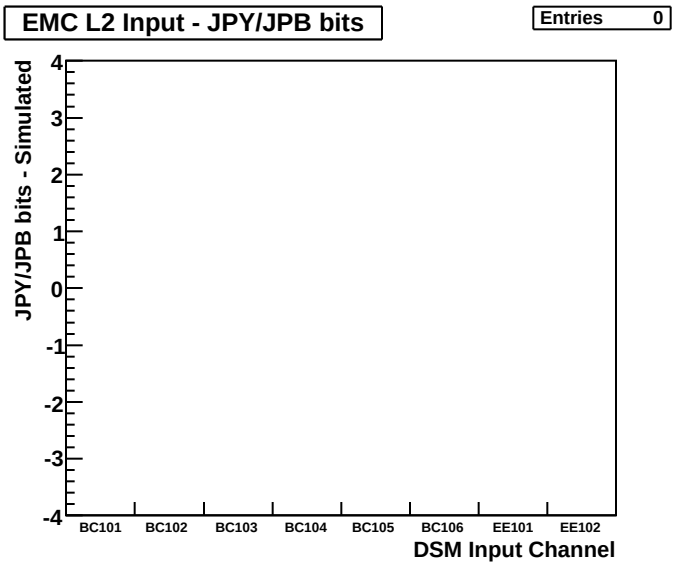

Endcap EMC L1 Input - High Tower Bits
Entries 456912
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

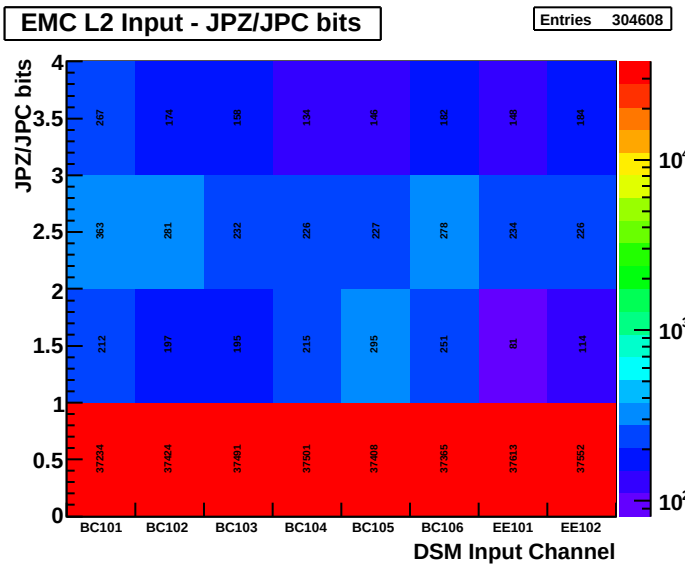

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

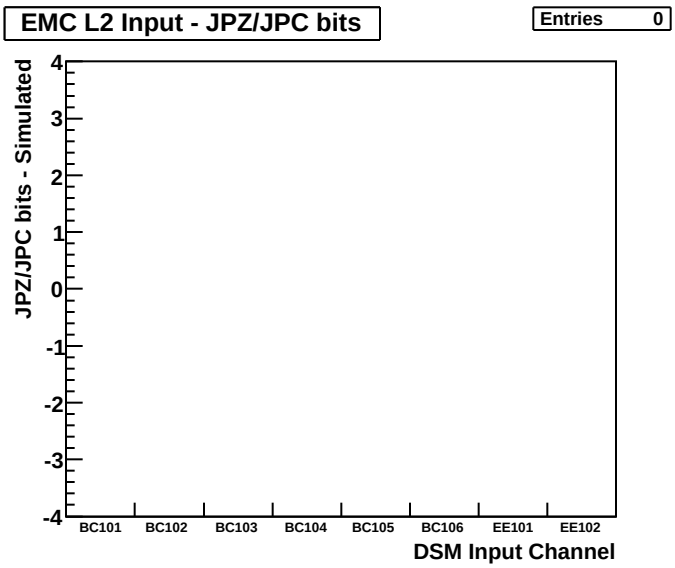

EMC L2 Input - JPX/JPA bits
Entries 304608
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
733 307 224 142 145 275 104 499
539 355 322 230 215 305 152 498
299 232 253 222 202 218 99 99
36505 37182 37267 37482 37514 37278 37721 36980

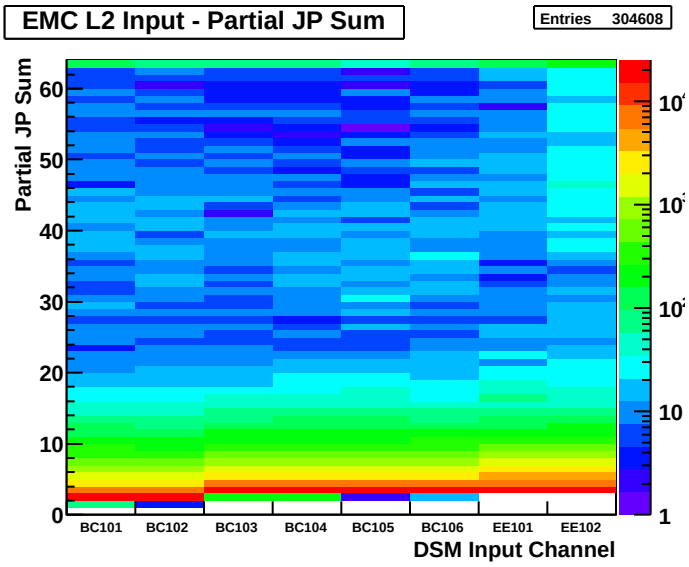

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

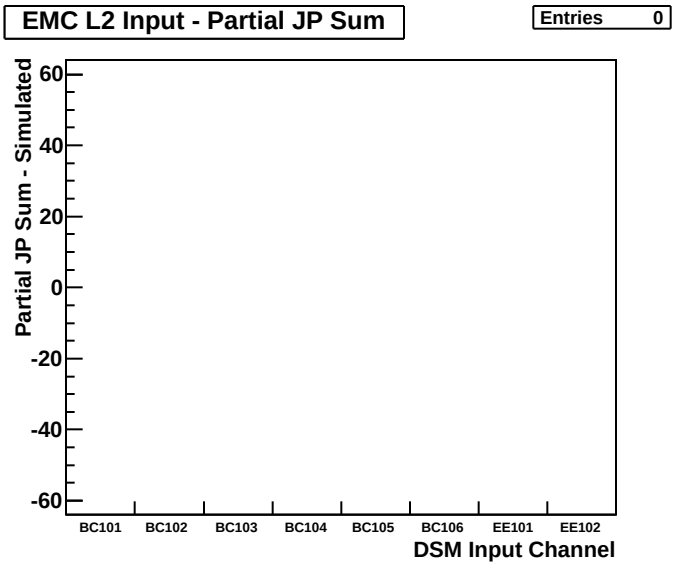

EMC L2 Input - JPY/JPB bits
Entries 304608
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
402 180 168 123 146 221 166 288
459 302 260 213 225 308 185 273
302 238 227 206 264 240 69 118
36913 37356 37421 37534 37441 37307 37656 37397

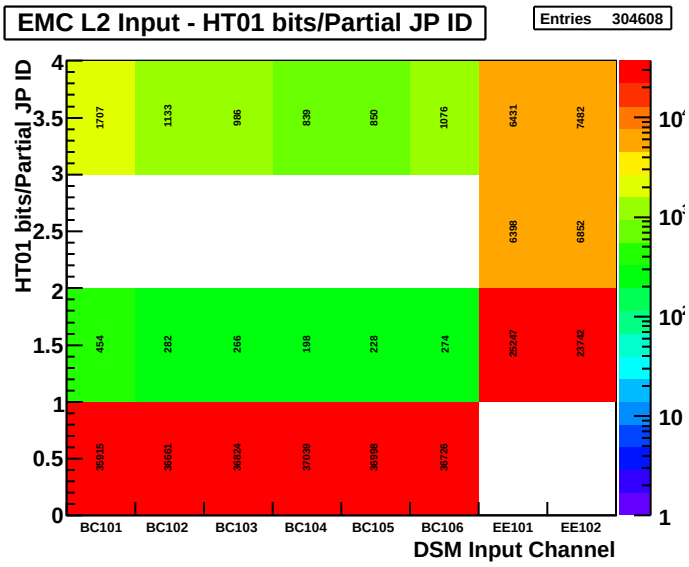

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

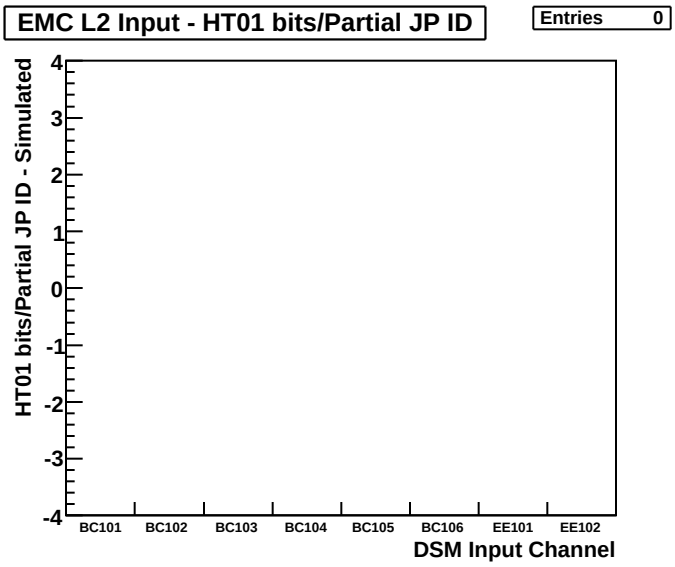

EMC L2 Input - JPZ/JPC bits
Entries 304608
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
267 174 158 134 146 182 148 184
363 281 232 226 227 278 234 226
212 197 195 215 295 251 81 114
37234 37424 37401 37501 37408 37365 37613 37552

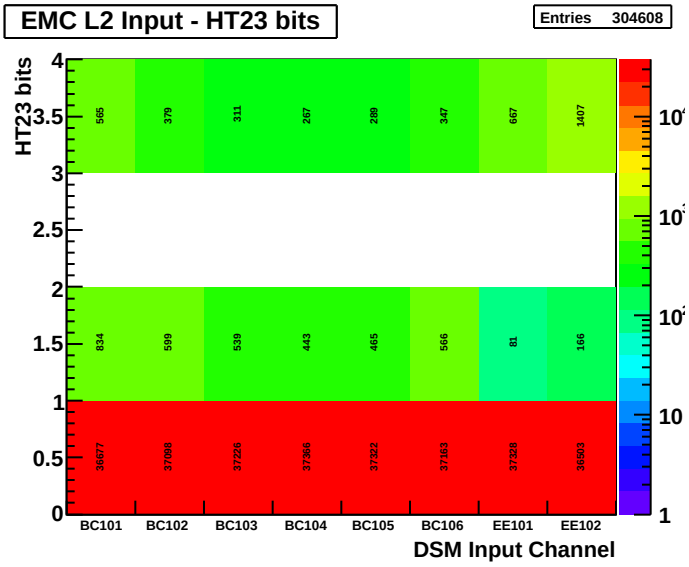

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

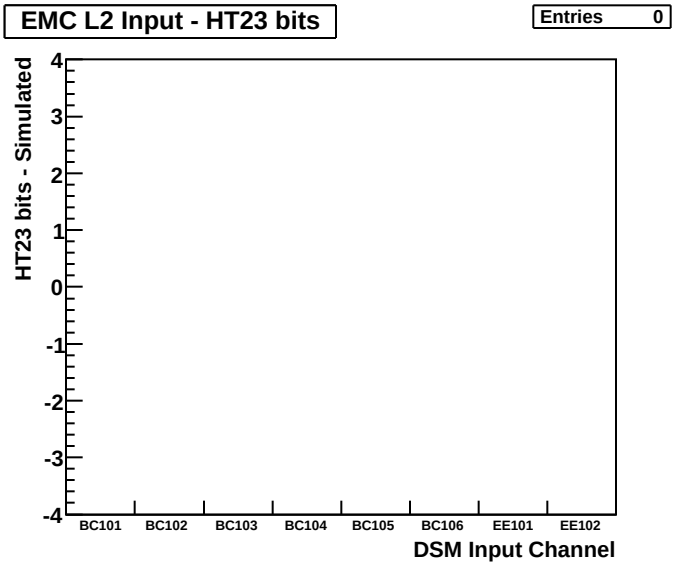

EMC L2 Input - Partial JP Sum
Entries 304608
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

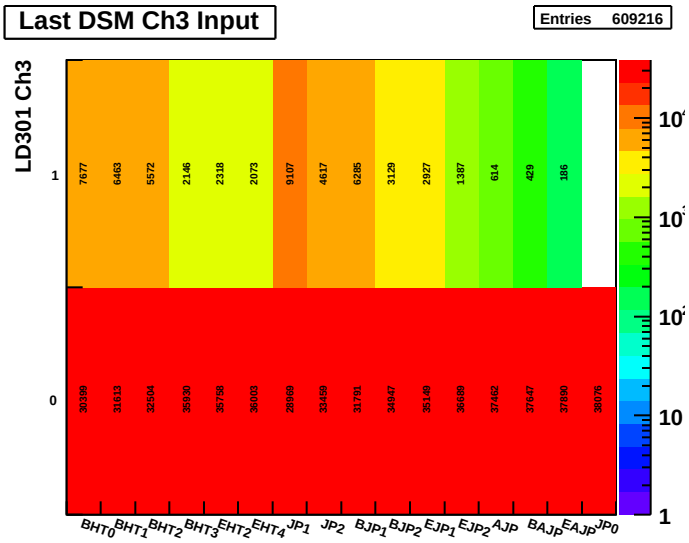

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

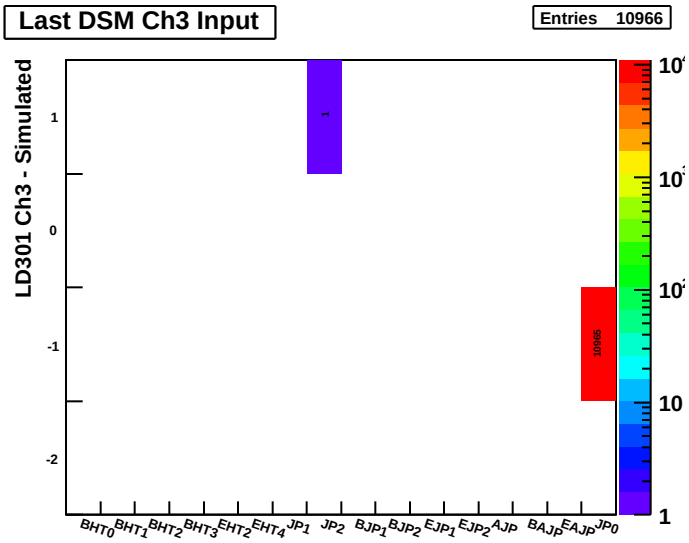

EMC L2 Input - HT01 bits/Partial JP ID
Entries 304608
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

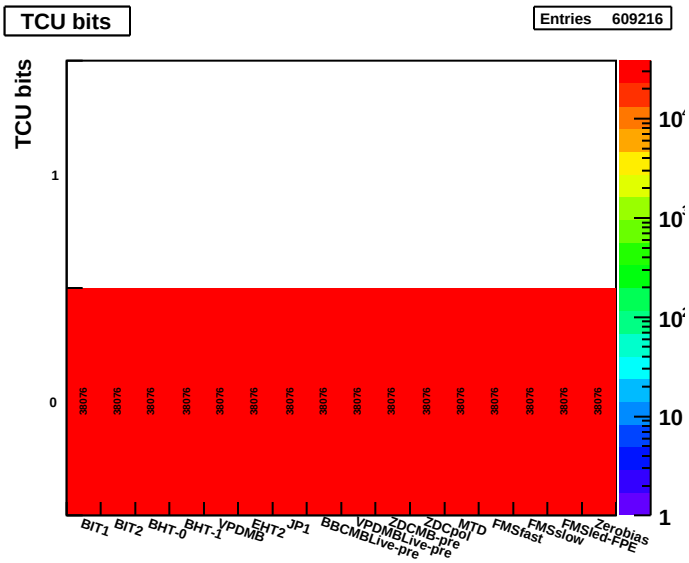

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

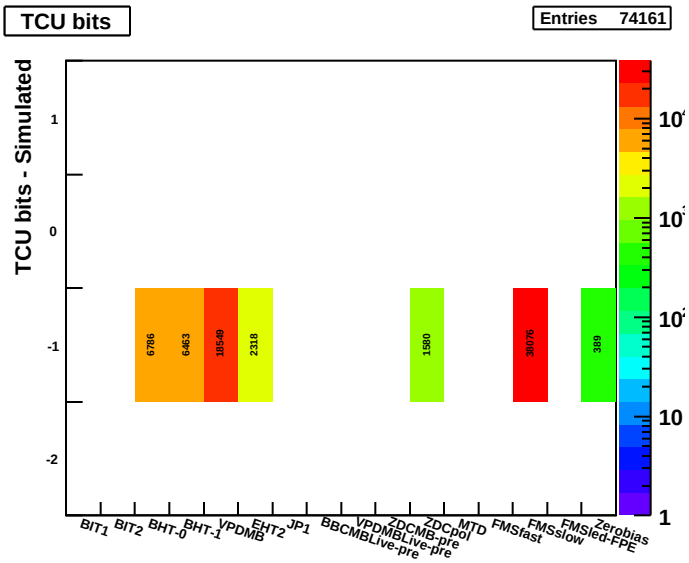

EMC L2 Input - HT23 bits
Entries 304608
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

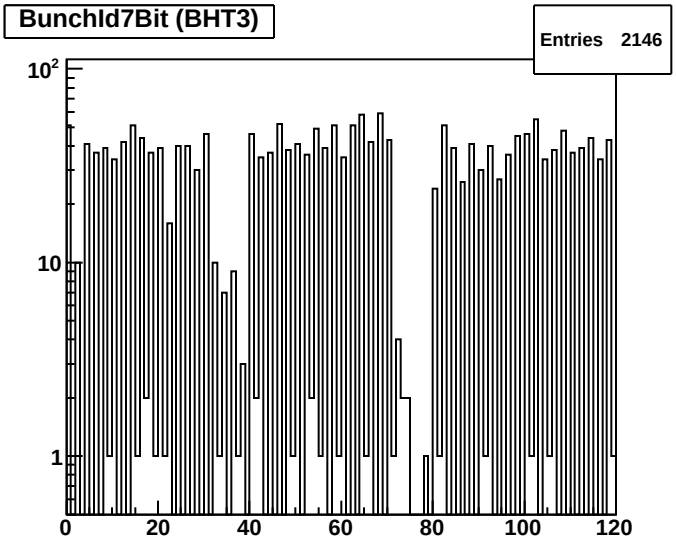

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

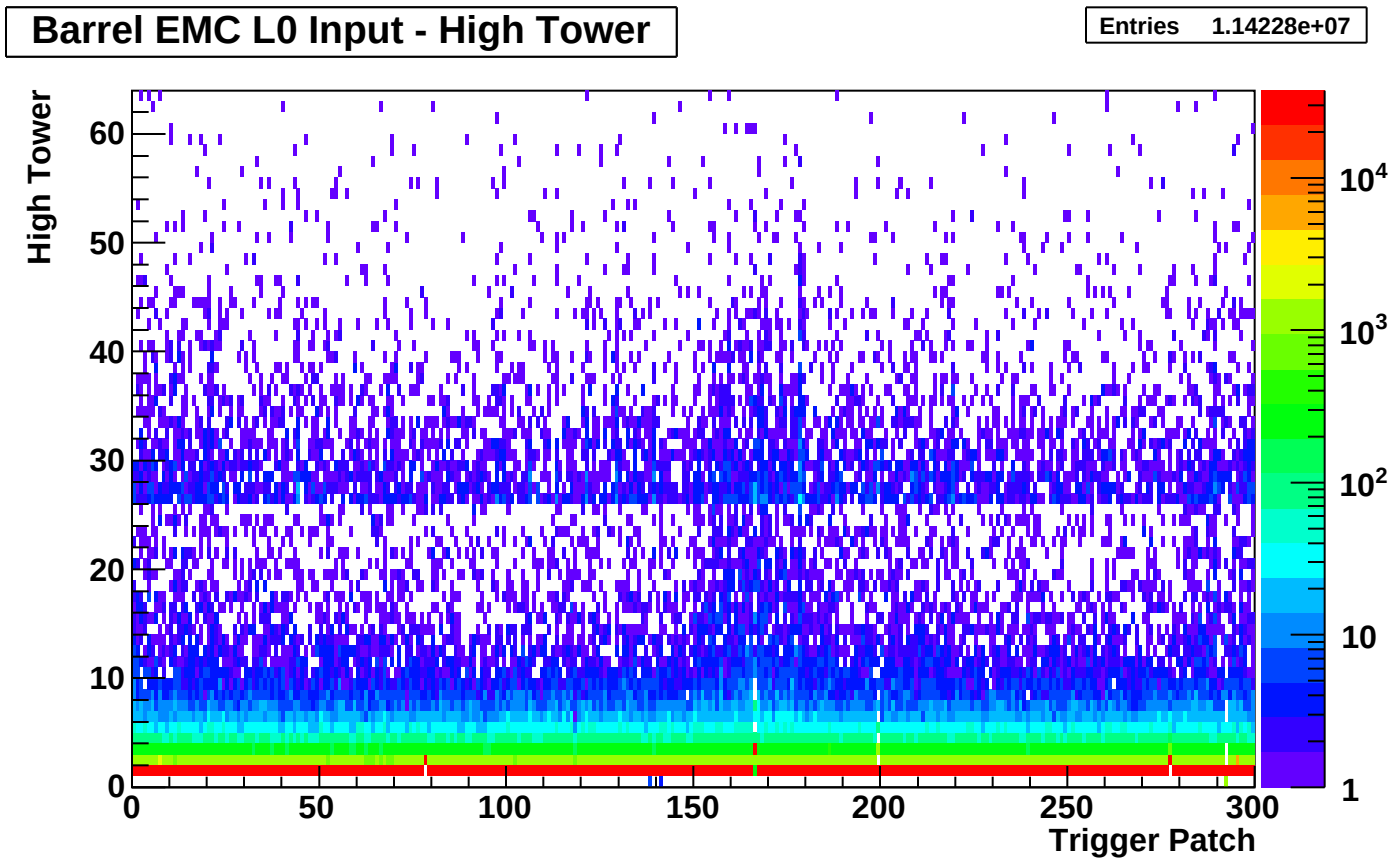

Last DSM Ch3 Input
Entries 609216
LD301 Ch3
7677 6463 5572 2146 2318 2073 9107 4617 6285 3129 2927 1367 614 429 186
30399 31613 32504 35930 35758 36003 29969 33459 31791 34947 35149 36689 37462 37647 37890 38076
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

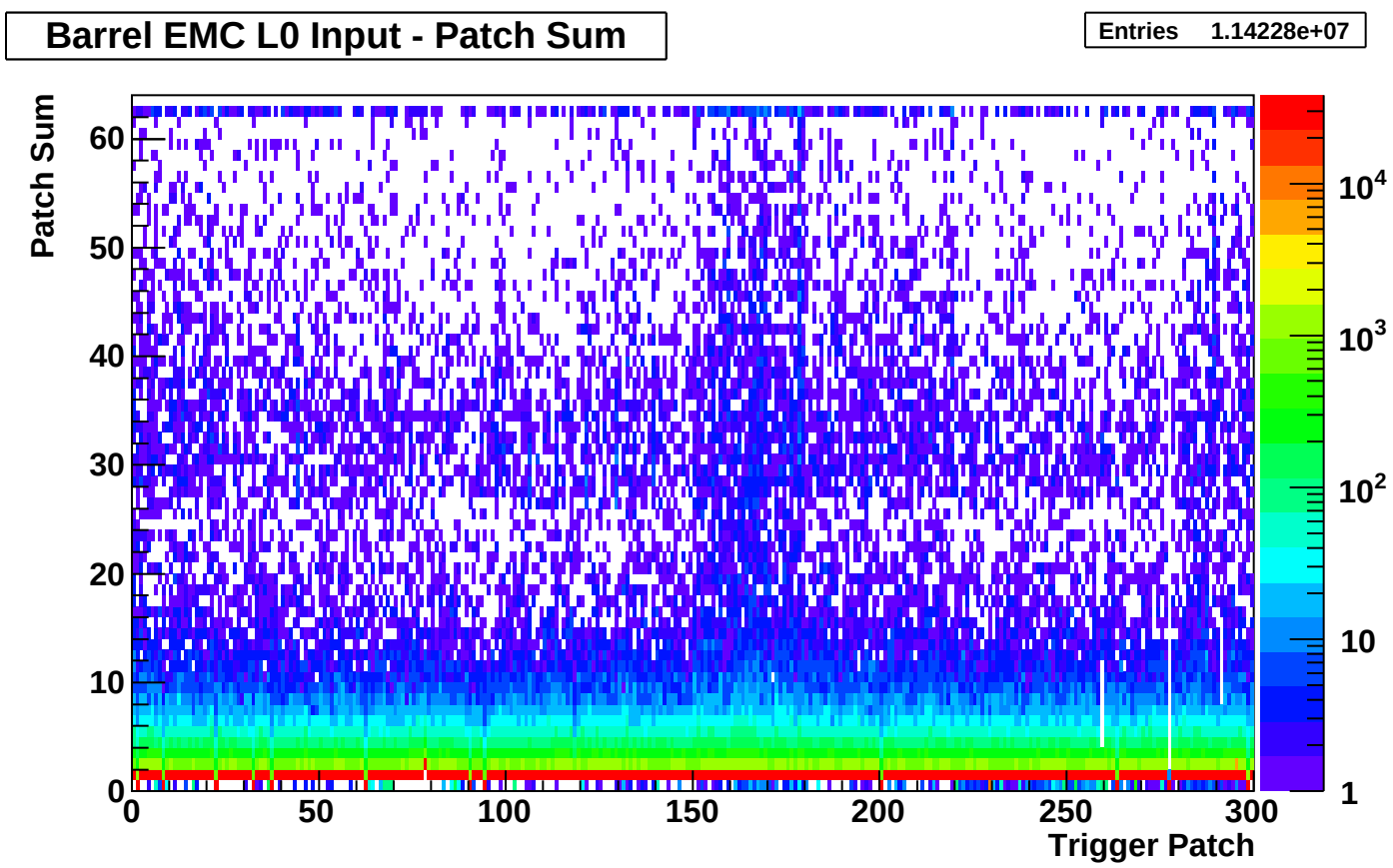

Last DSM Ch3 Input
Entries 10966
LD301 Ch3 - Simulated
1
10965
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

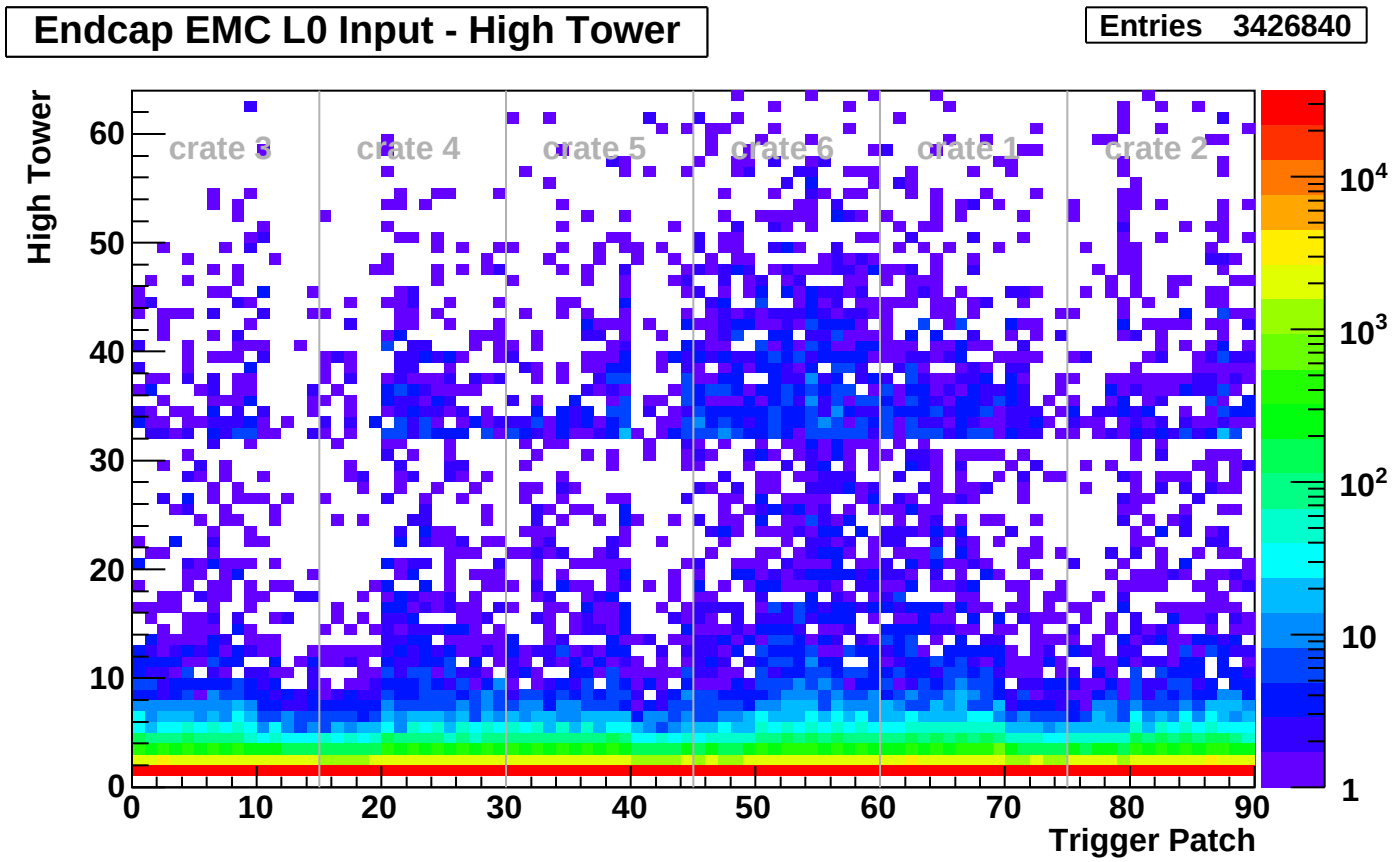

TCU bits
Entries 609216
TCU bits
38076
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

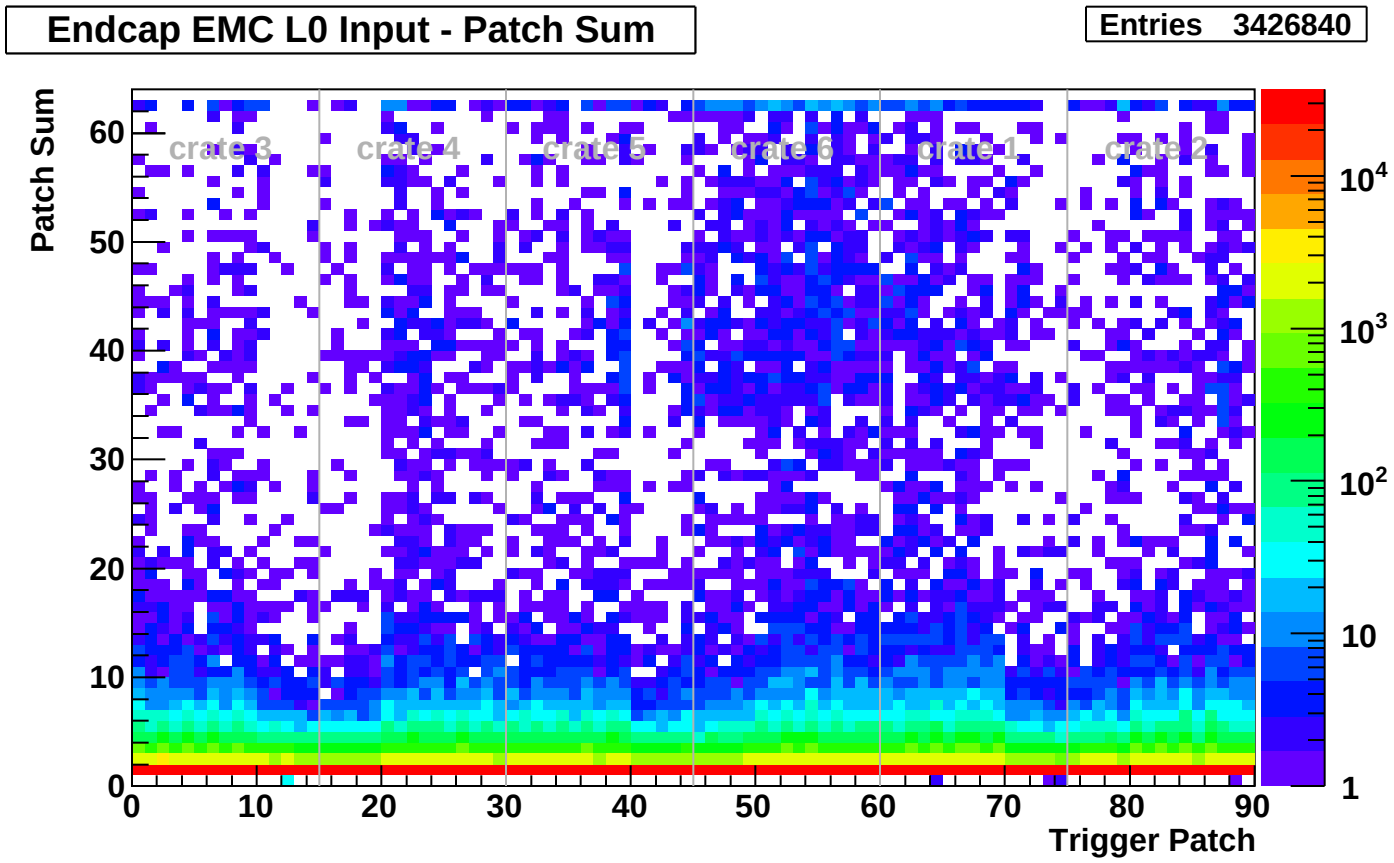

TCU bits
Entries 74161
TCU bits - Simulated
6786 6463 18549 2318 1980 38076 389
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

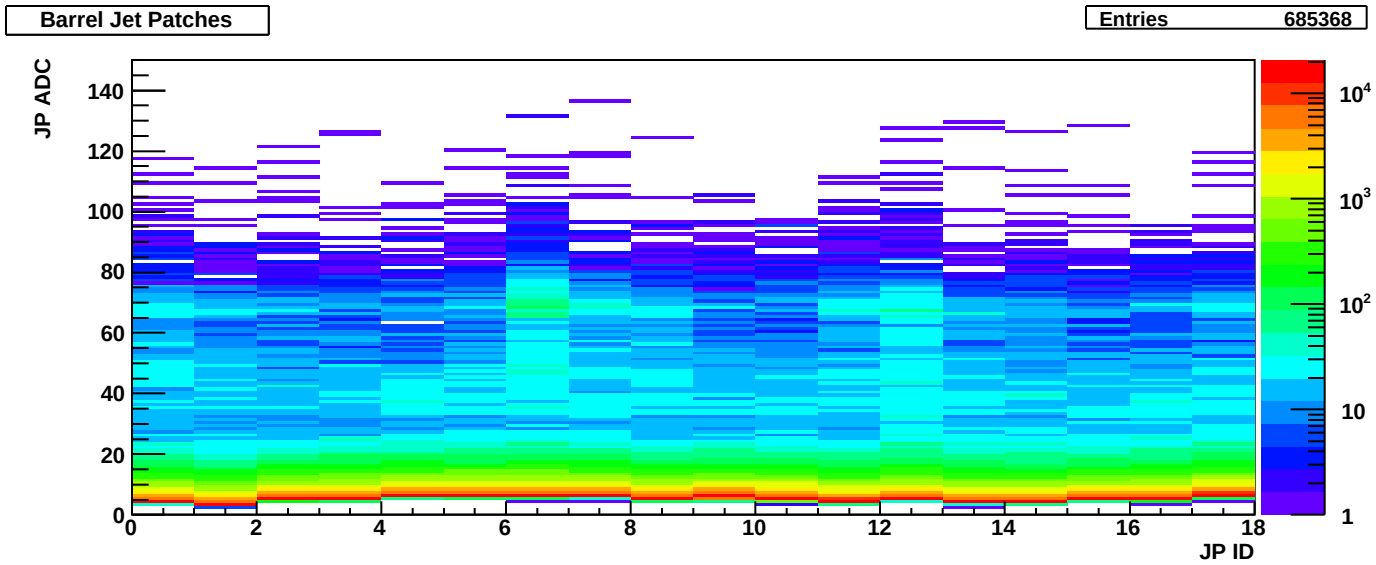

BunchId7Bit (BHT3)
Entries 2146

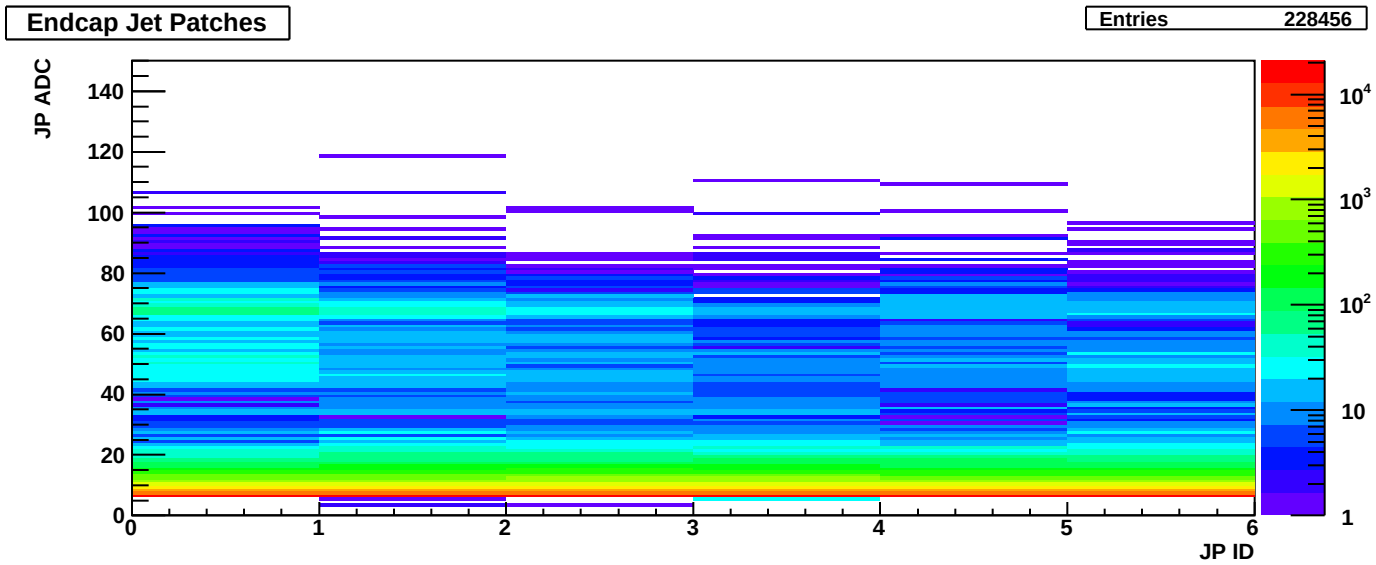

Barrel EMC L0 Input - High Tower
Entries 1.14228e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

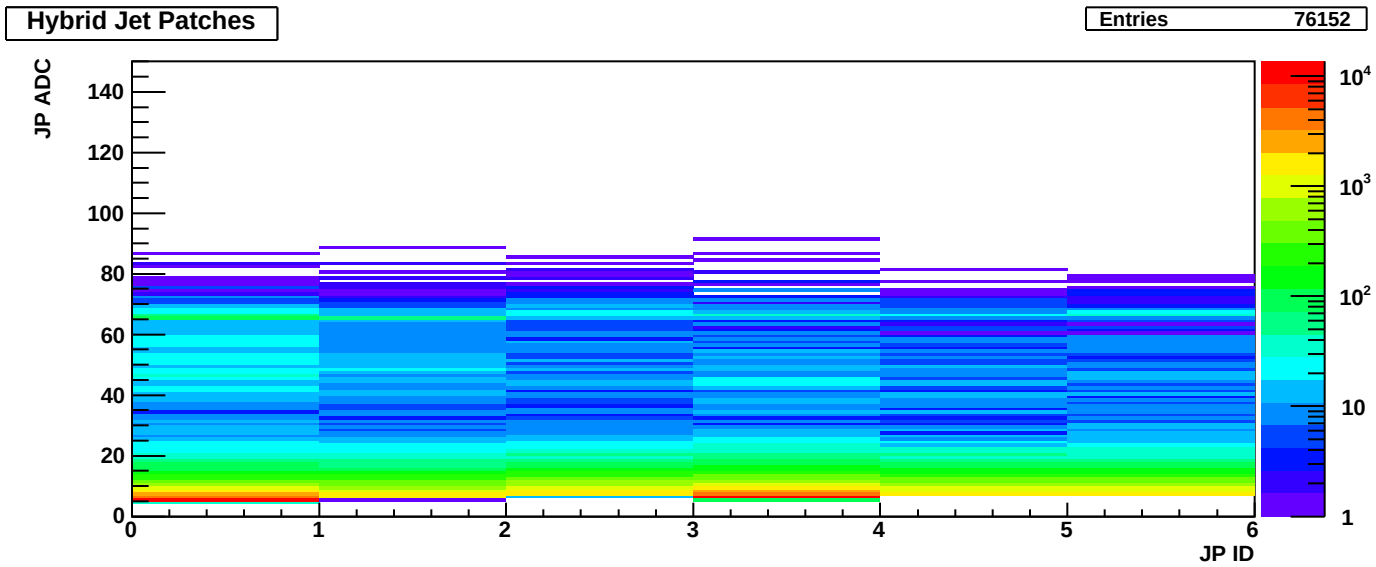

Barrel EMC L0 Input - Patch Sum
Entries 1.14228e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

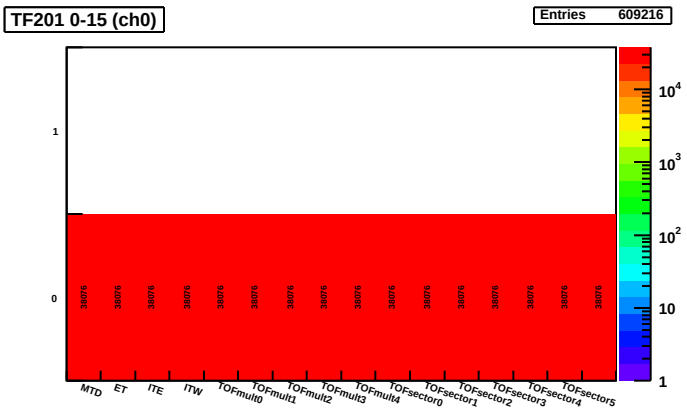

Endcap EMC L0 Input - High Tower
Entries 3426840
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

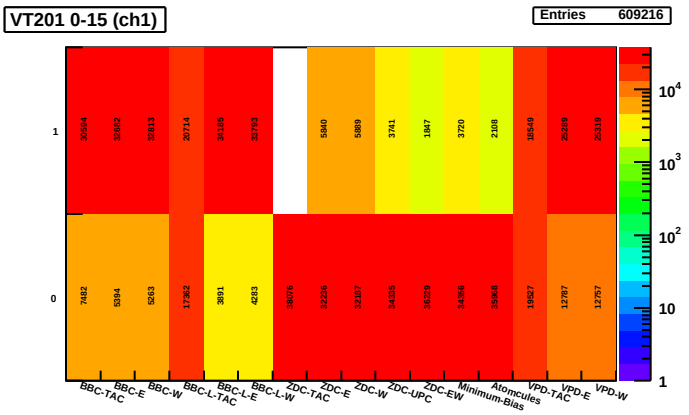

Endcap EMC L0 Input - Patch Sum
Entries 3426840
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

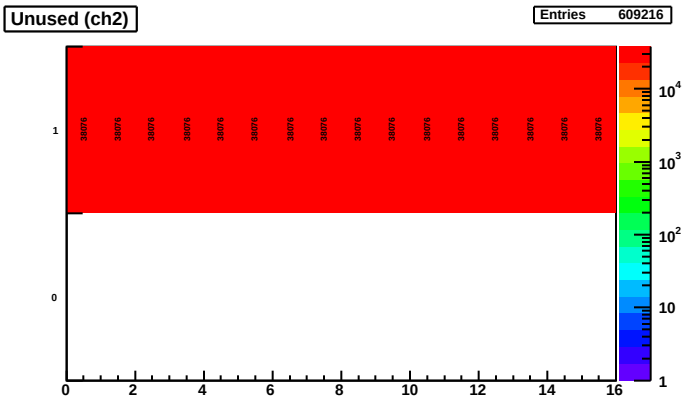

Barrel Jet Patches
Entries 685368
JP ADC
JP ID

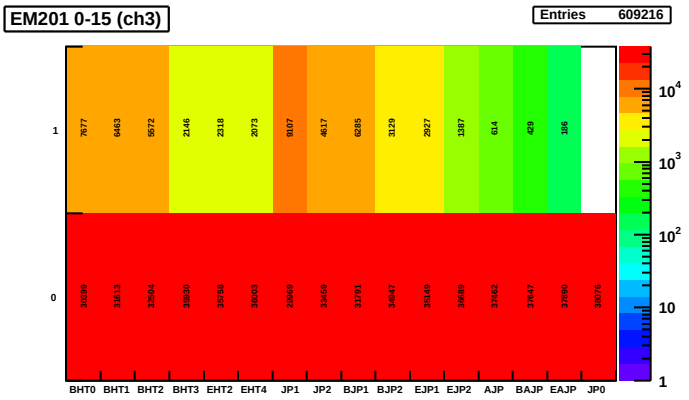

Endcap Jet Patches
Entries 228456
JP ADC
JP ID

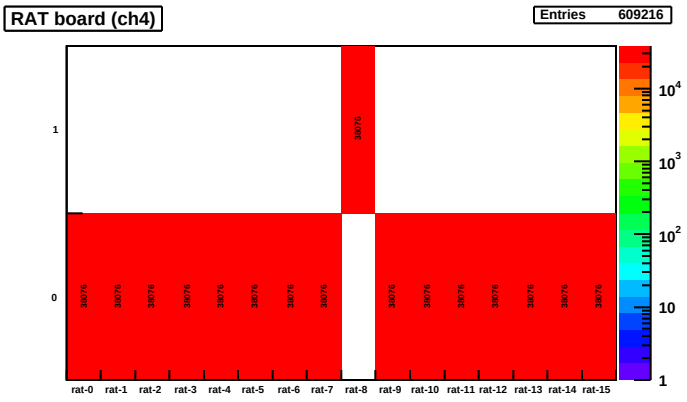

Hybrid Jet Patches
Entries 76152
JP ADC
JP ID

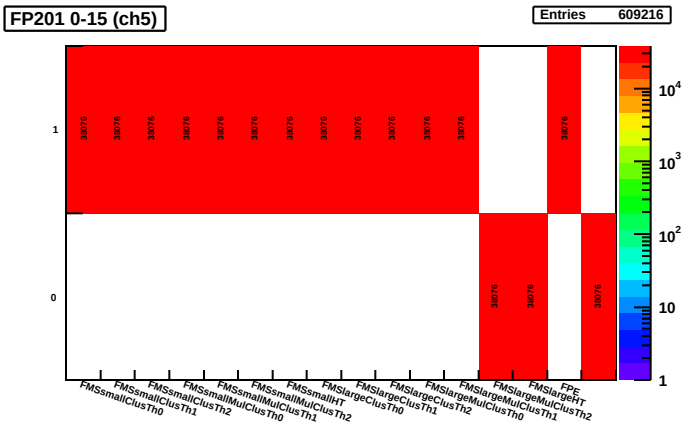

TF201 0-15 (ch0)
Entries 609216
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

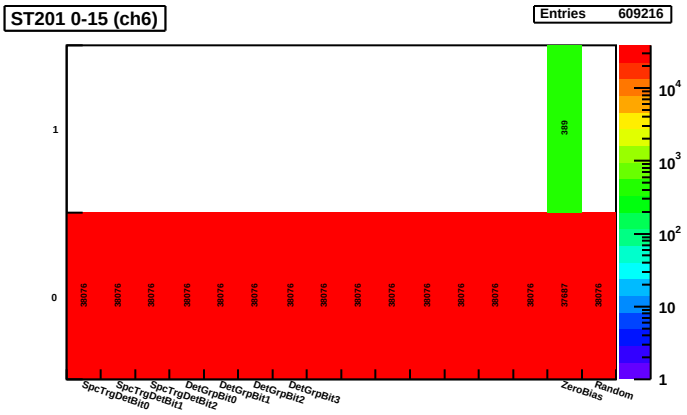

VT201 0-15 (ch1)
Entries 609216
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

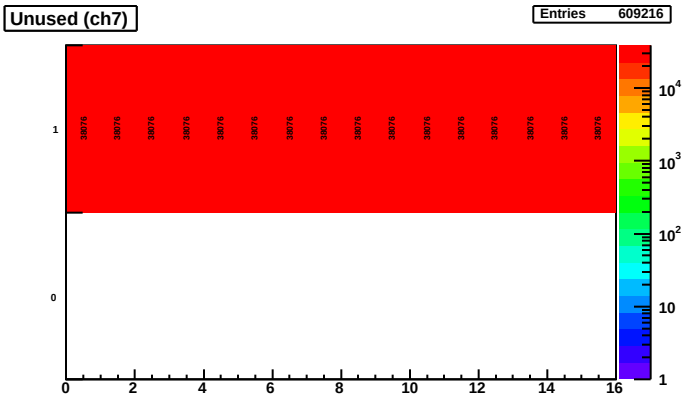

Unused (ch2)
Entries 609216

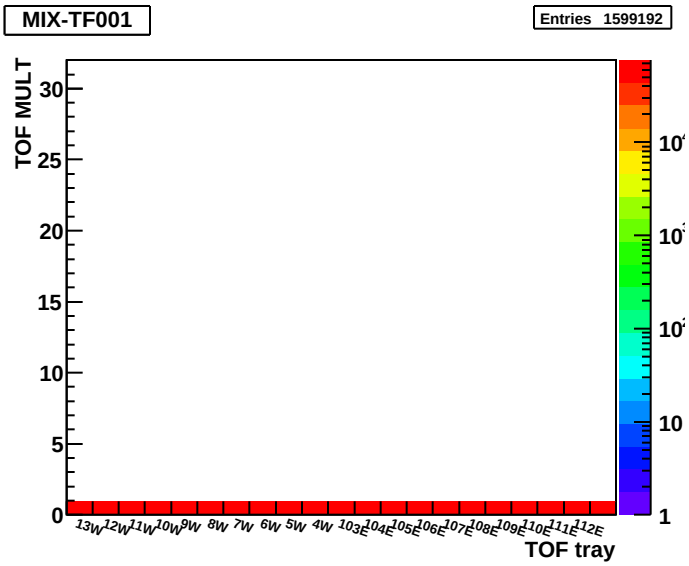

EM201 0-15 (ch3)
Entries 609216
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

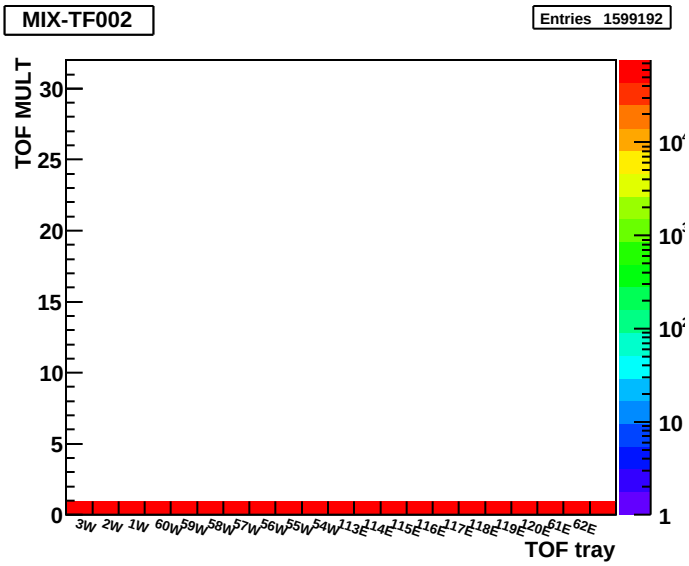

RAT board (ch4)
Entries 609216
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

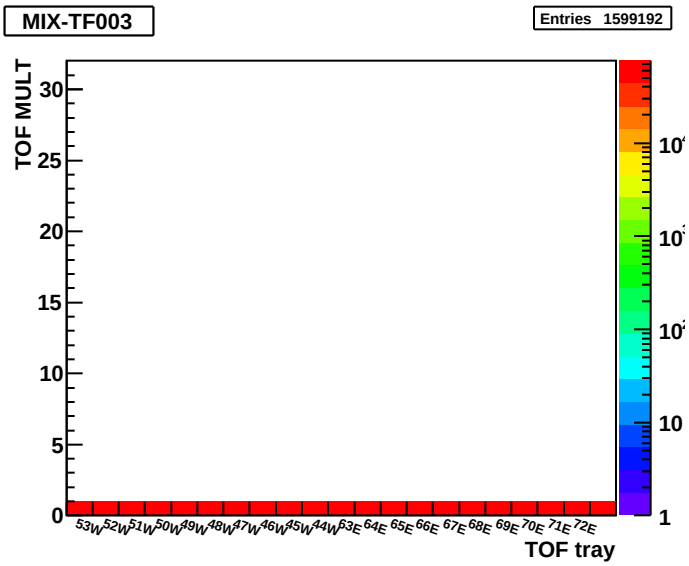

FP201 0-15 (ch5)
Entries 609216
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

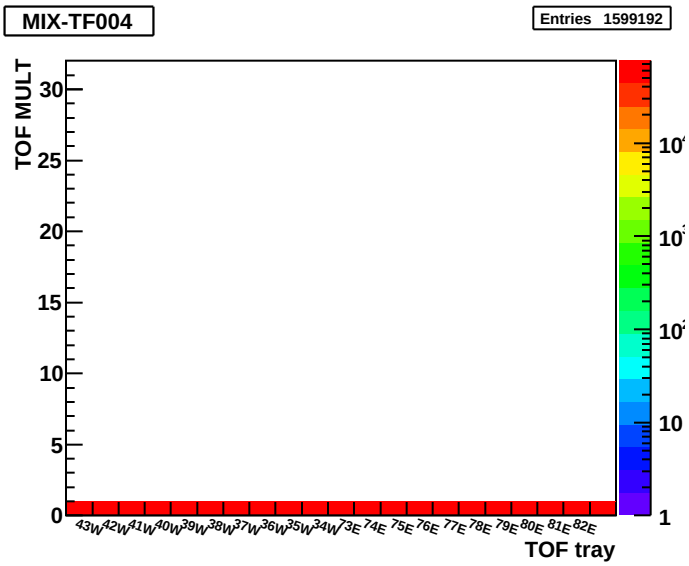

ST201 0-15 (ch6)
Entries 609216
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

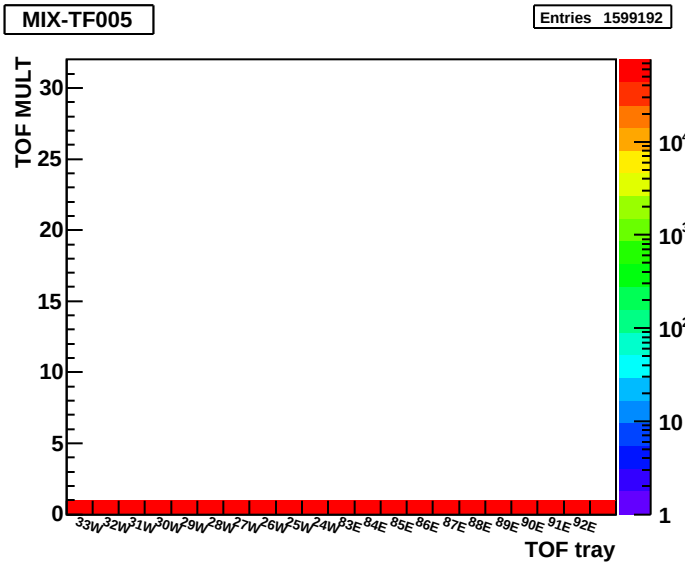

Unused (ch7)
Entries 609216

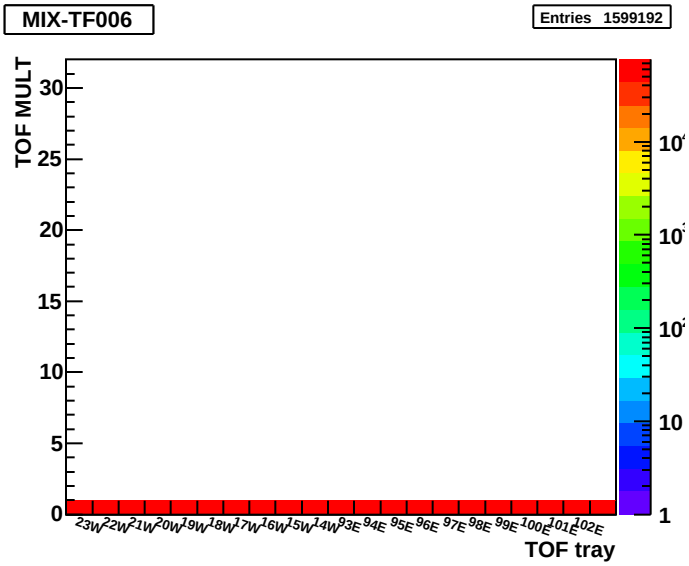

MIX-TF001
Entries 1599192
TOF MULT
TOF tray

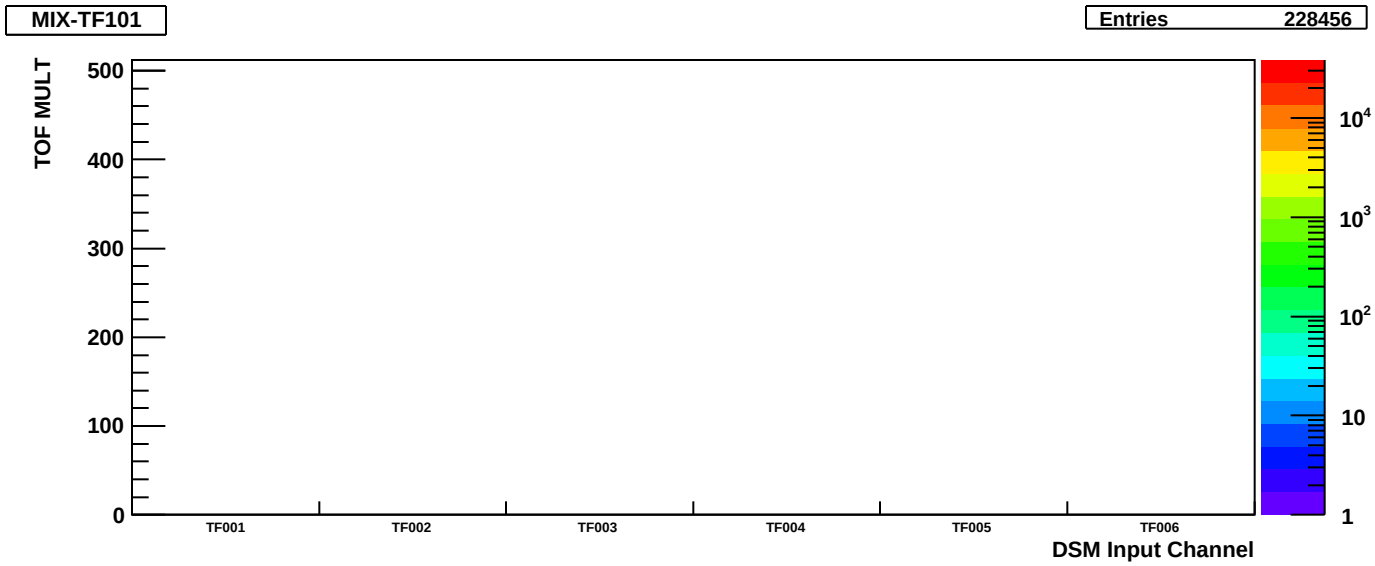

MIX-TF002
Entries 1599192
TOF MULT
TOF tray

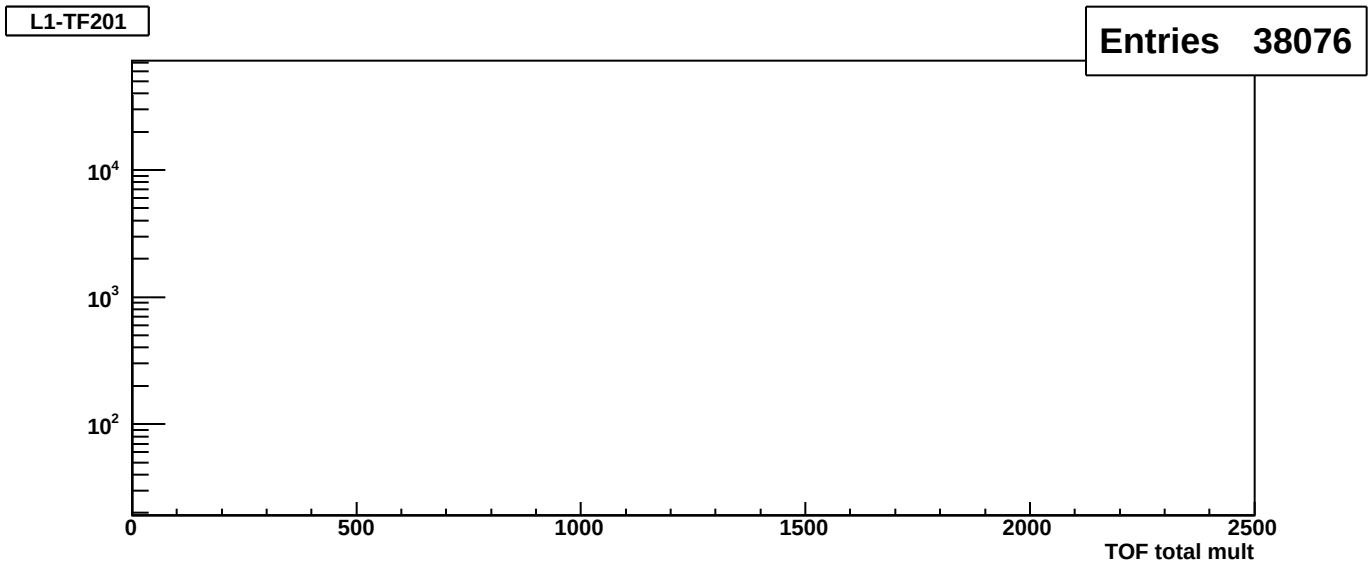

MIX-TF003
Entries 1599192
TOF MULT
TOF tray

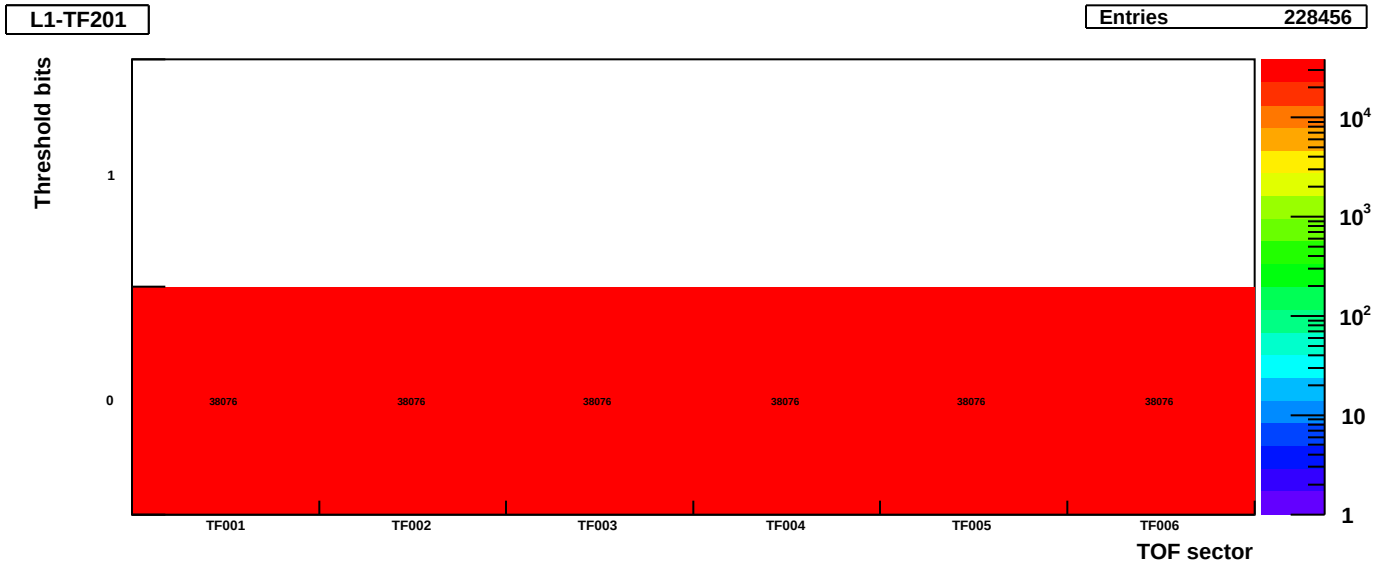

MIX-TF004
Entries 1599192
TOF MULT
TOF tray

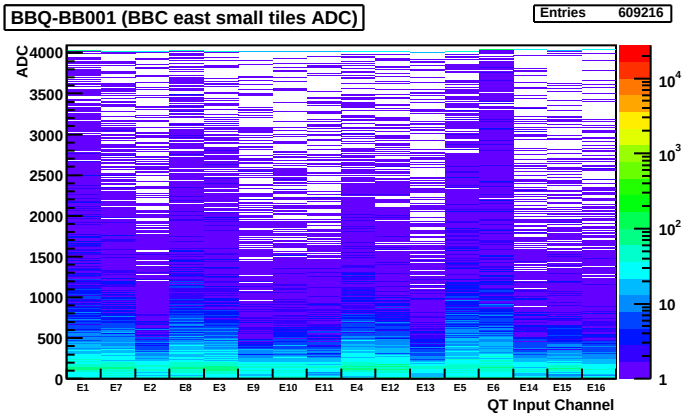

MIX-TF005
Entries 1599192
TOF MULT
TOF tray

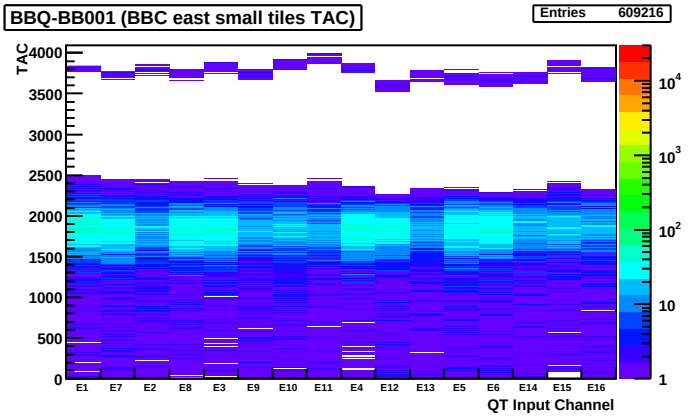

MIX-TF006
Entries 1599192
TOF MULT
TOF tray

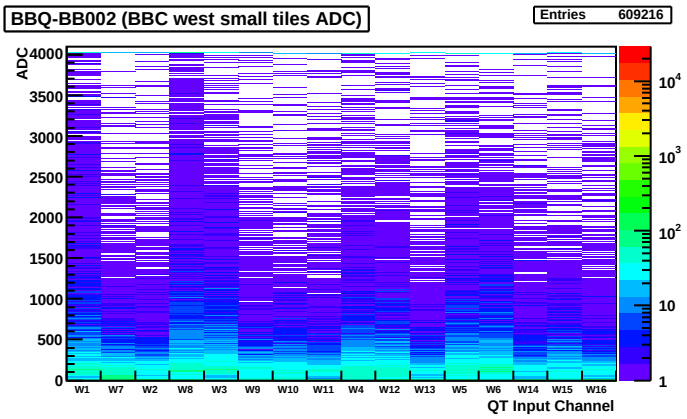

MIX-TF101
Entries 228456
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

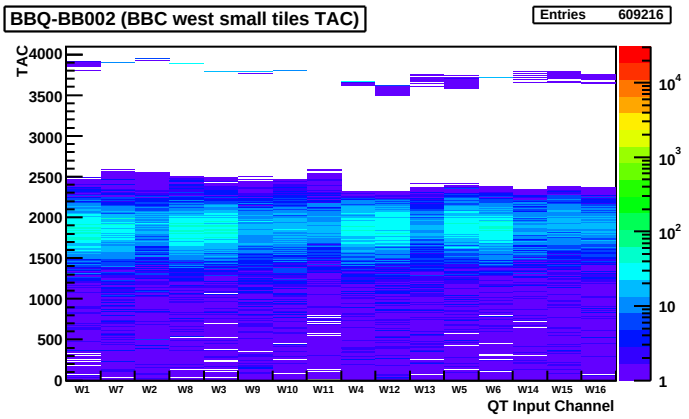

L1-TF201
Entries 38076
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

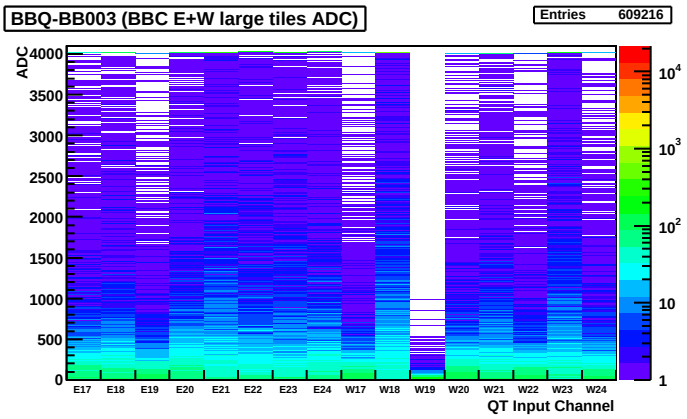

L1-TF201
Entries 228456
Threshold bits
1
0
38076
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

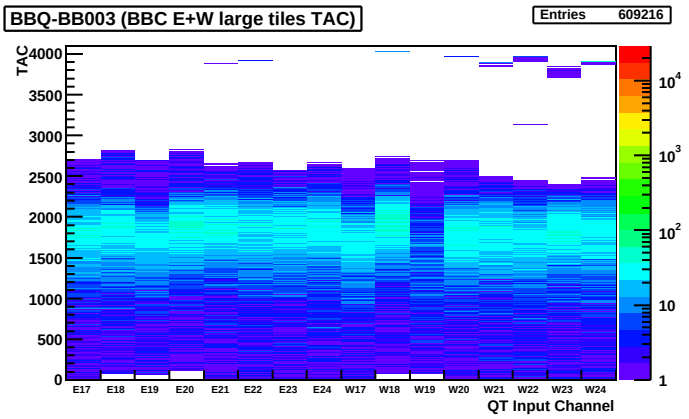

BBQ-BB001 (BBC east small tiles ADC)
Entries 609216
ADC
QT Input Channel

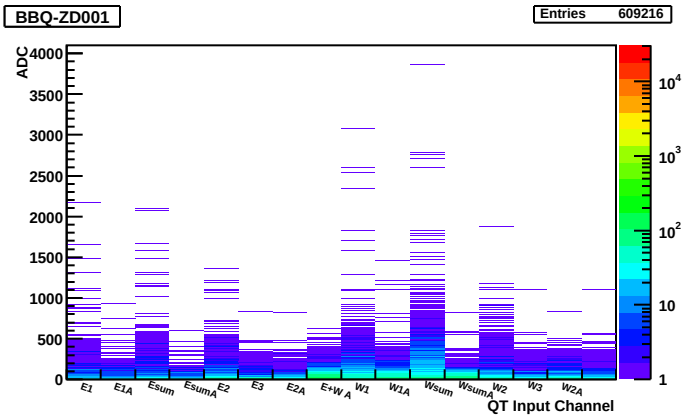

BBQ-BB001 (BBC east small tiles TAC)
Entries 609216
TAC
QT Input Channel

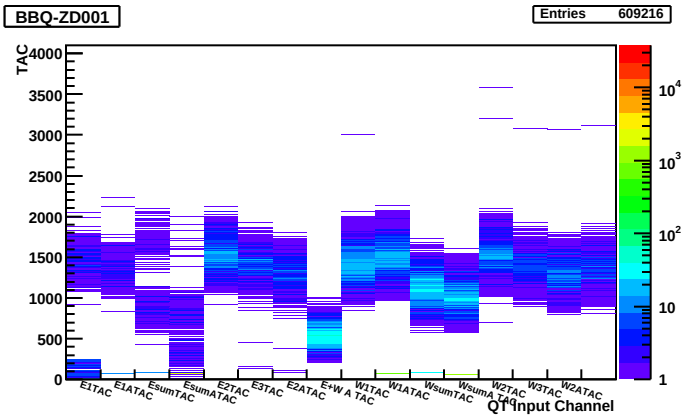

BBQ-BB002 (BBC west small tiles ADC)
Entries 609216
ADC
QT Input Channel

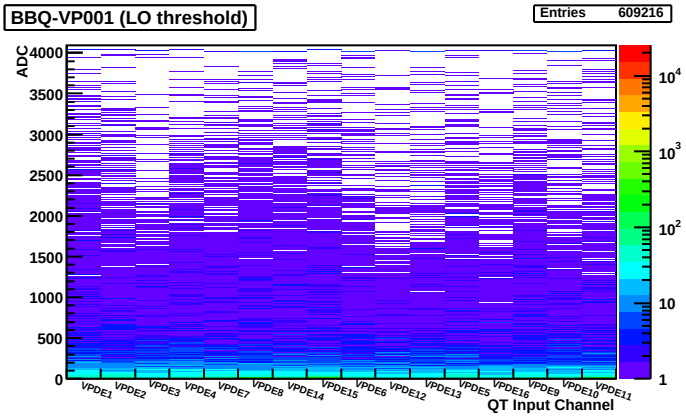

BBQ-BB002 (BBC west small tiles TAC)
Entries 609216
TAC
QT Input Channel

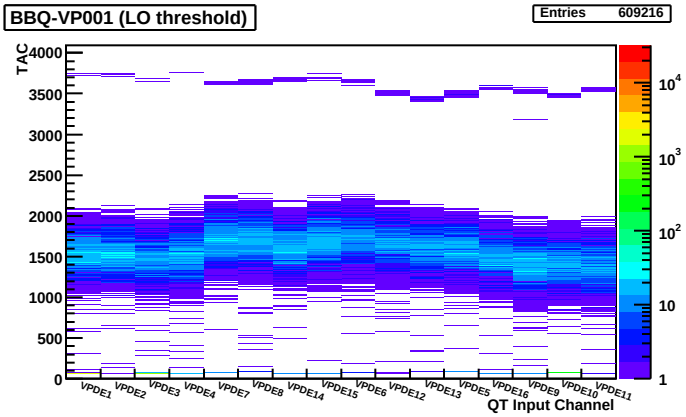

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 609216
ADC
QT Input Channel

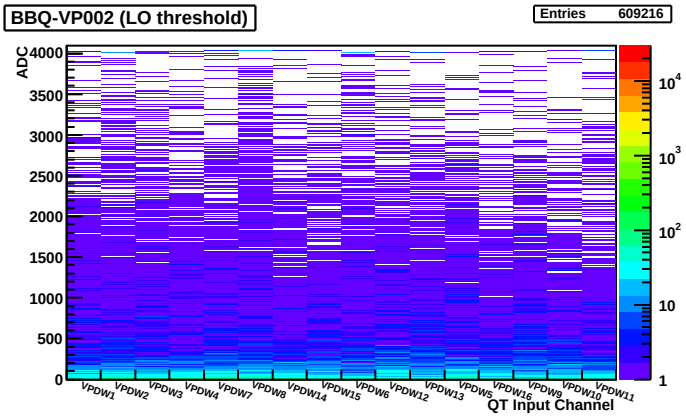

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 609216
TAC
QT Input Channel

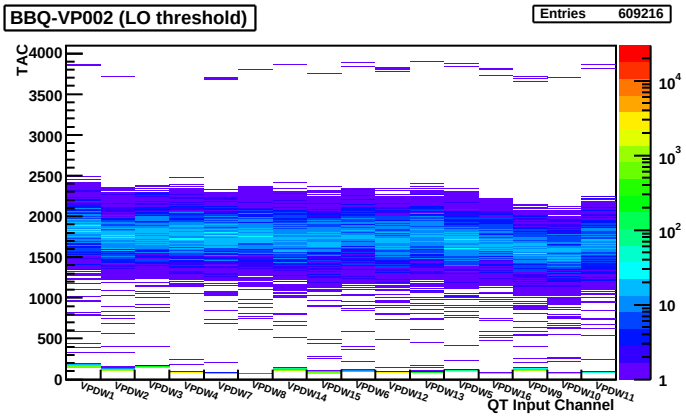

BBQ-ZD001
Entries 609216
ADC
QT Input Channel

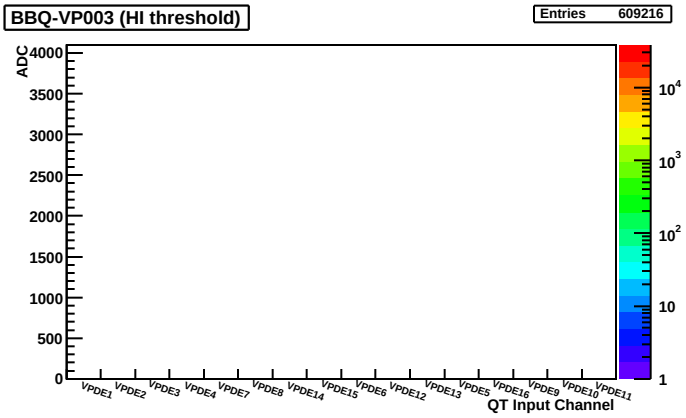

BBQ-ZD001
Entries 609216
TAC
QT Input Channel

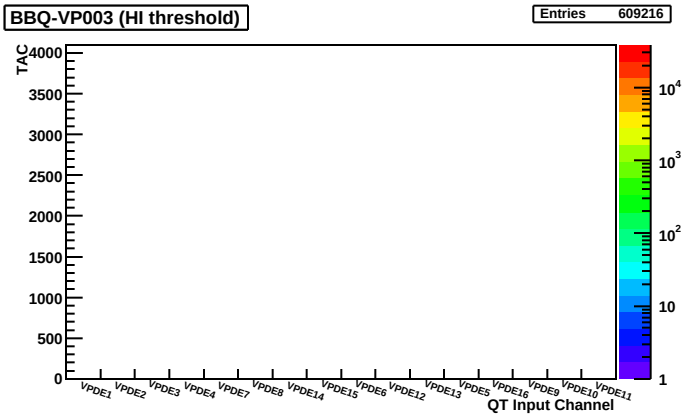

BBQ-VP001 (LO threshold)
Entries 609216
ADC
QT Input Channel

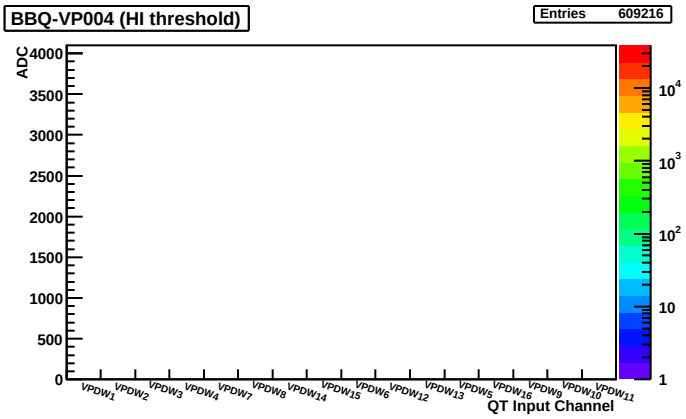

BBQ-VP001 (LO threshold)
Entries 609216
TAC
QT Input Channel

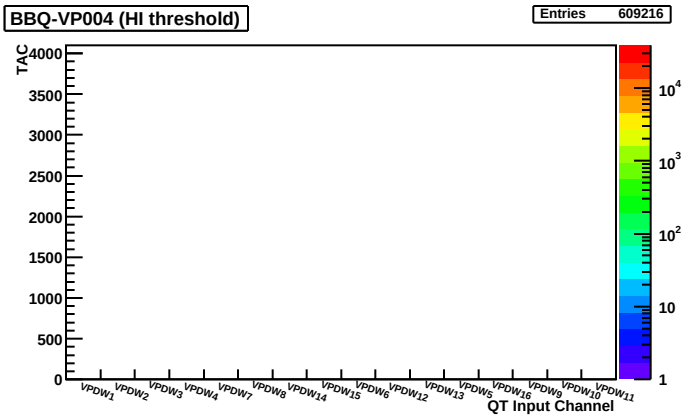

BBQ-VP002 (LO threshold)
Entries 609216
ADC
QT Input Channel

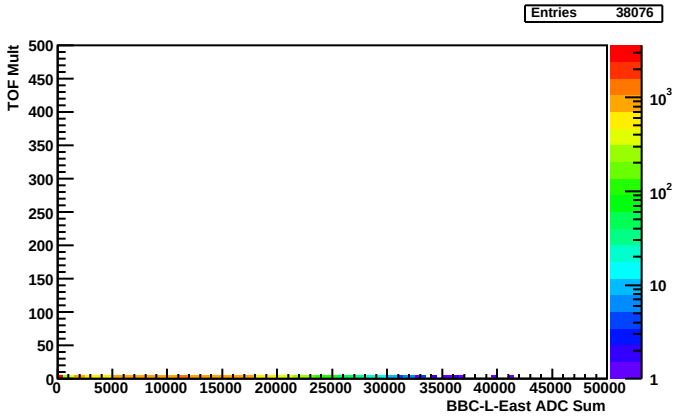

BBQ-VP002 (LO threshold)
Entries 609216
TAC
QT Input Channel

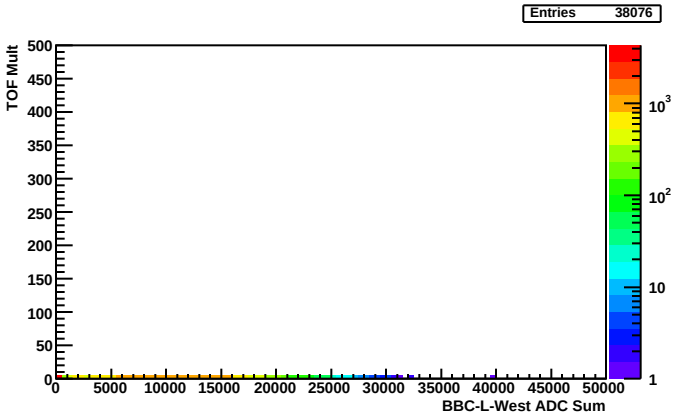

BBQ-VP003 (HI threshold)
Entries 609216
ADC
QT Input Channel

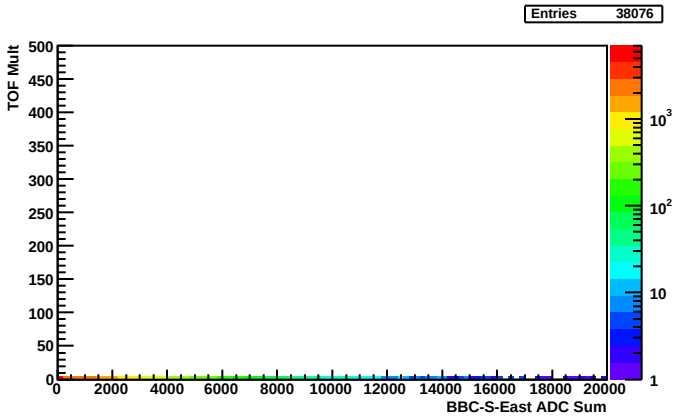

BBQ-VP003 (HI threshold)
Entries 609216
TAC
QT Input Channel

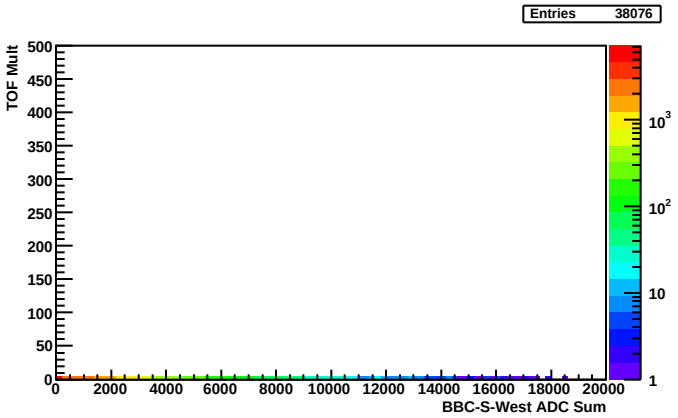

BBQ-VP004 (HI threshold)
Entries 609216
ADC
QT Input Channel

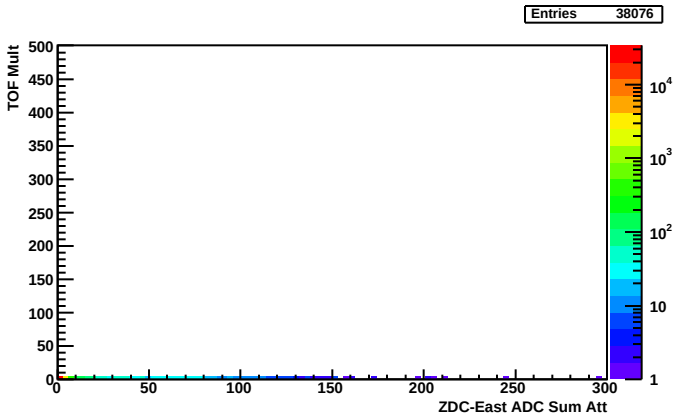

BBQ-VP004 (HI threshold)
Entries 609216
TAC
QT Input Channel

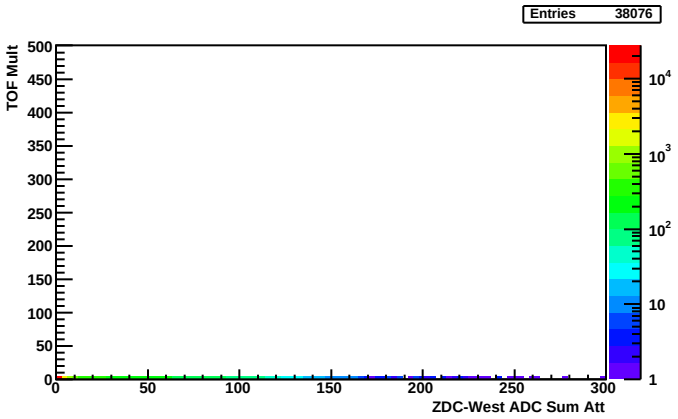

Entries 38076
TOF Mult
BBC-L-East ADC Sum

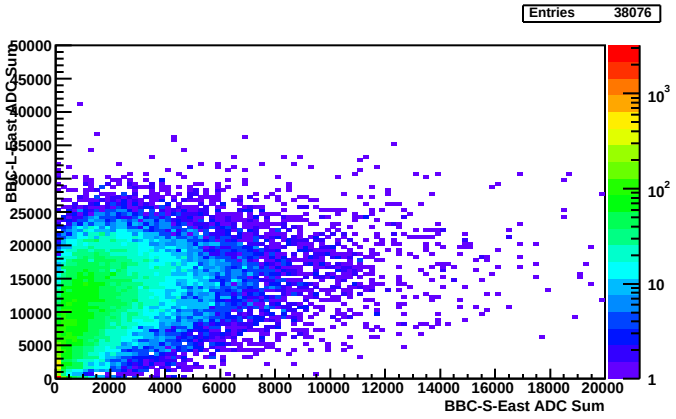

Entries 38076
TOF Mult
BBC-L-West ADC Sum

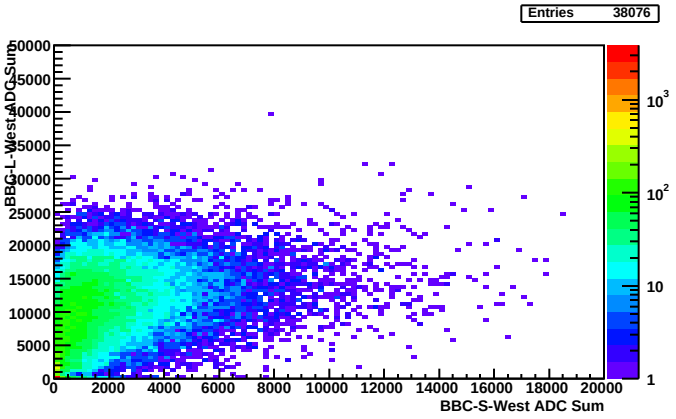

Entries 38076
TOF Mult
BBC-S-East ADC Sum

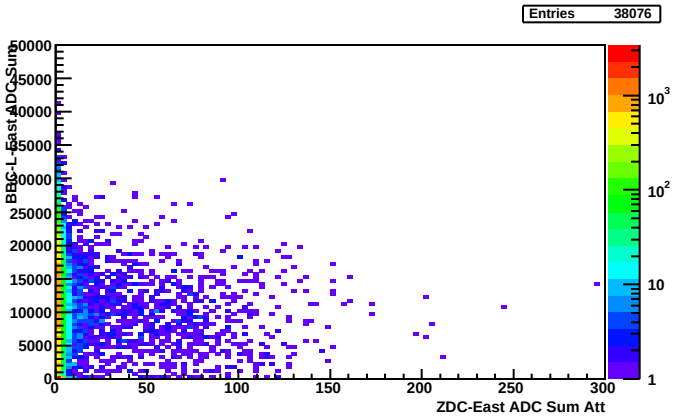

Entries 38076
TOF Mult
BBC-S-West ADC Sum

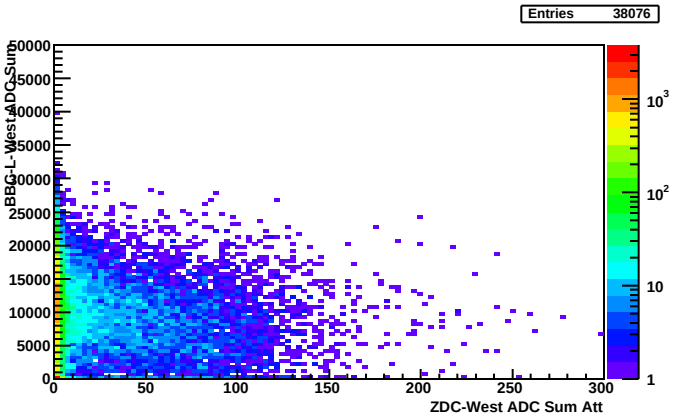

Entries 38076
TOF Mult
ZDC-East ADC Sum Att

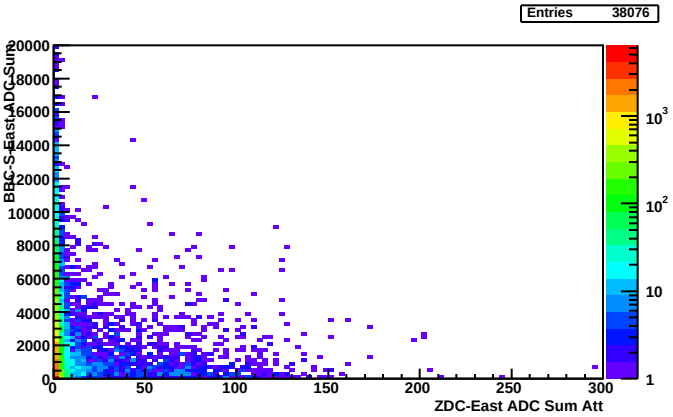

Entries 38076
TOF Mult
ZDC-West ADC Sum Att

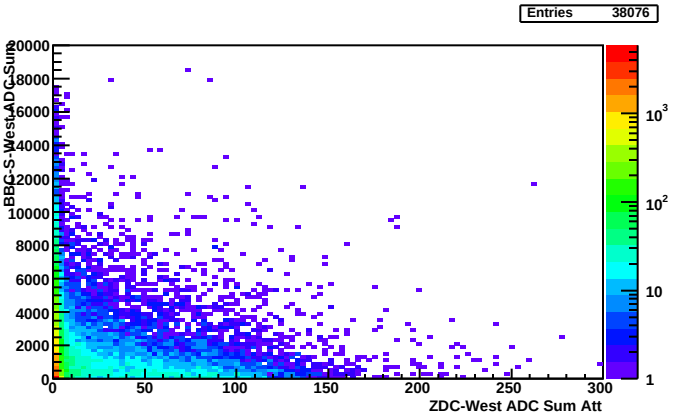

Entries 38076
BBC-L-East ADC Sum
BBC-S-East ADC Sum

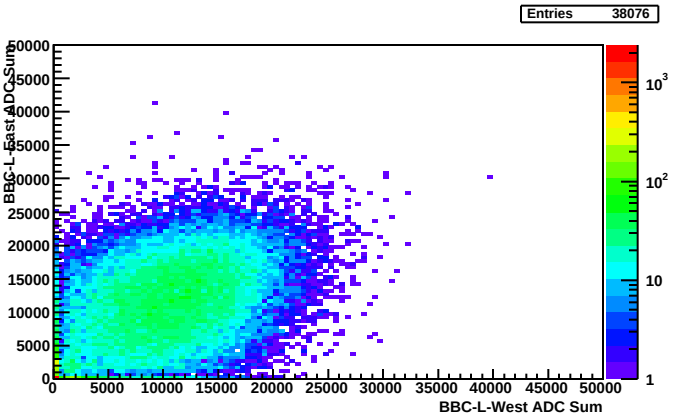

Entries 38076
BBC-L-West ADC Sum
BBC-S-West ADC Sum

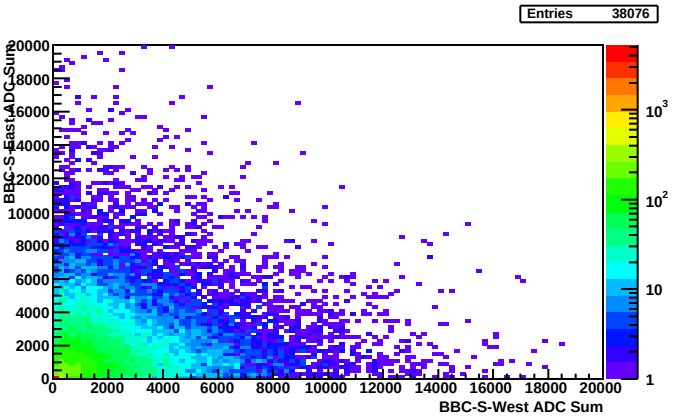

Entries 38076
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

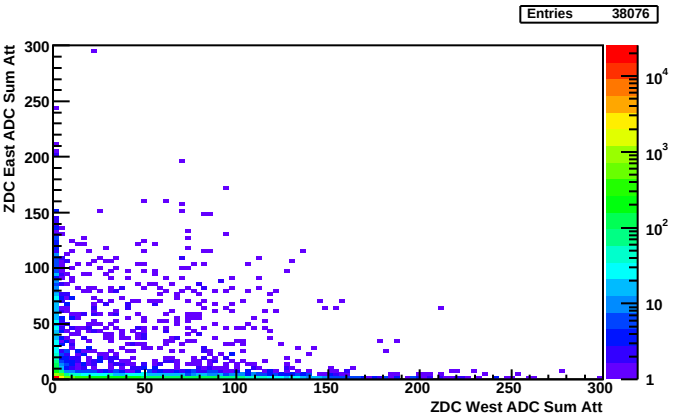

Entries 38076
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

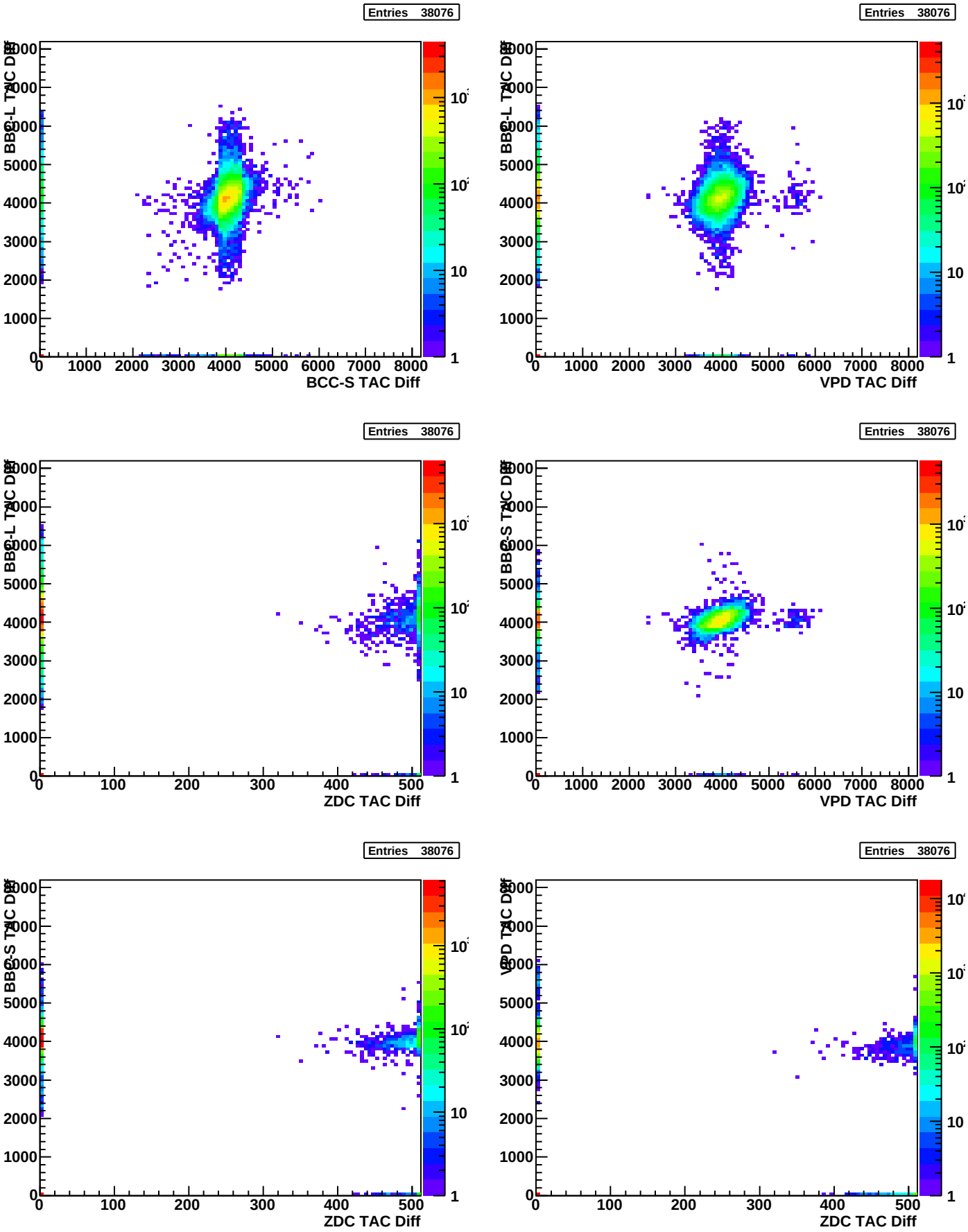

Entries 38076
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

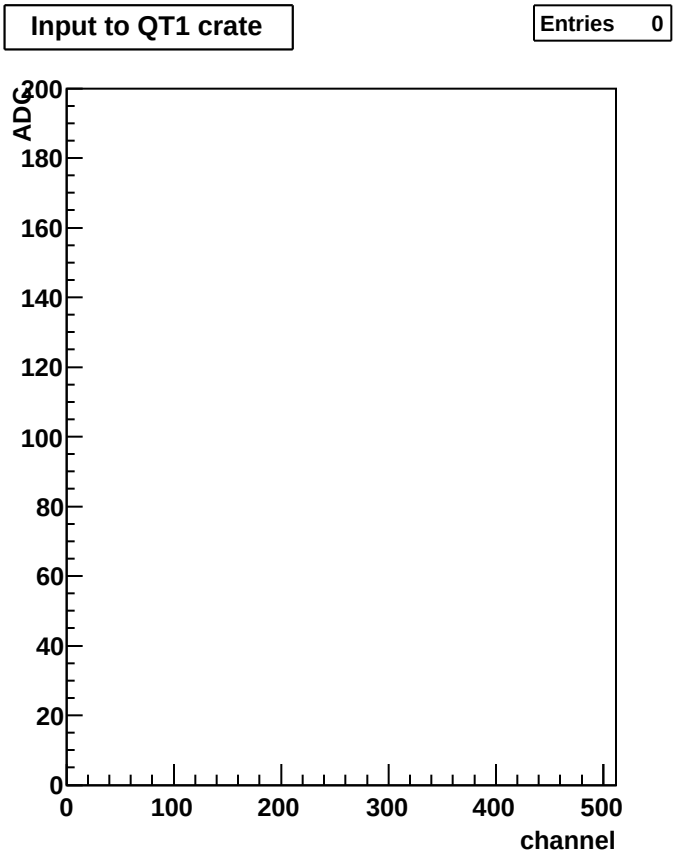

Entries 38076
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

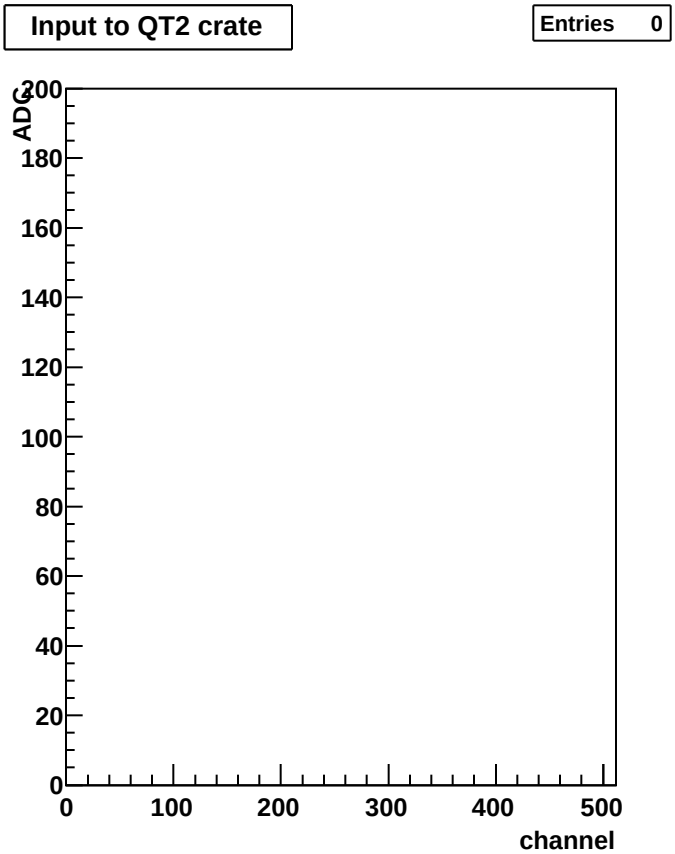

Entries 38076
BBC-L-East ADC Sum
BBC-L-West ADC Sum

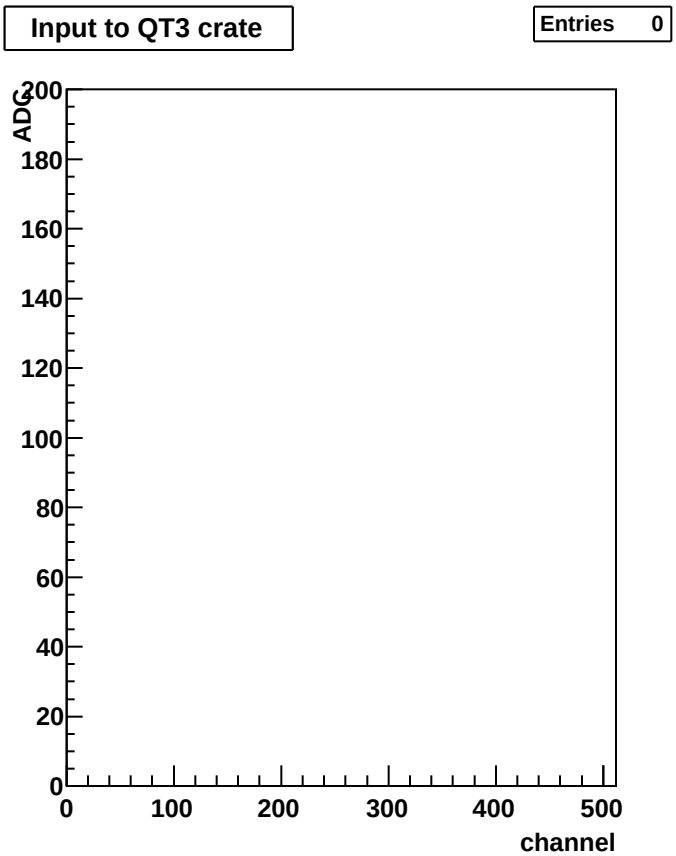

Entries 38076
BBC-S-East ADC Sum
BBC-S-West ADC Sum

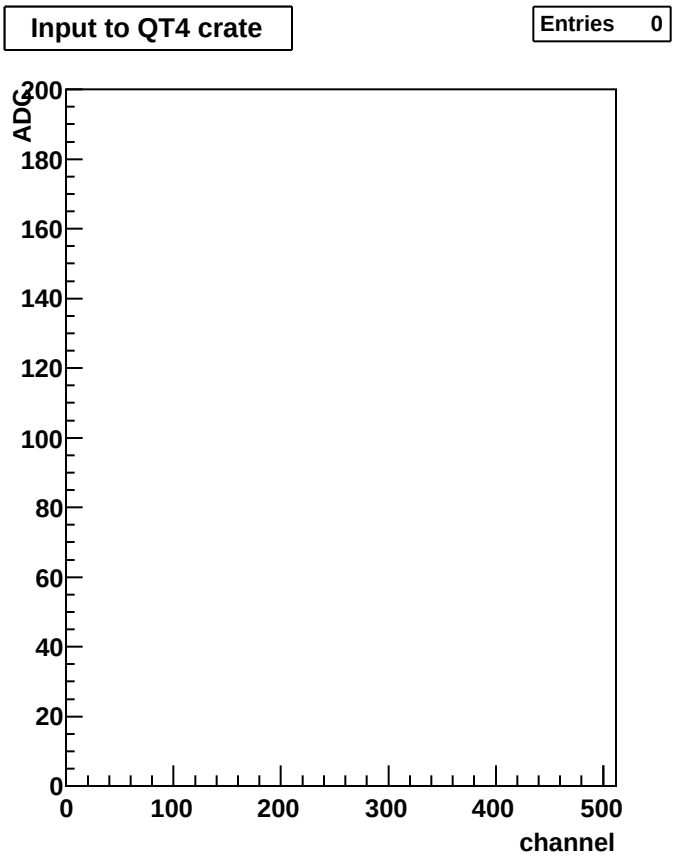

Entries 38076
ZDC East ADC Sum Att
ZDC West ADC Sum Att

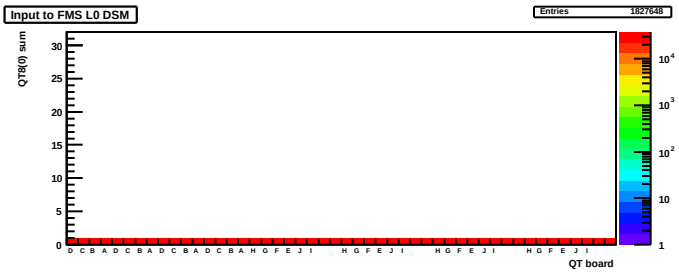

Entries 38076
BBC-L TAC Diff
BCC-S TAC Diff
Entries 38076
BBC-L TAC Diff
VPD TAC Diff
Entries 38076
BBC-L TAC Diff
ZDC TAC Diff
Entries 38076
BBC-S TAC Diff
VPD TAC Diff
Entries 38076
BBC-S TAC Diff
ZDC TAC Diff
Entries 38076
VPD TAC Diff
ZDC TAC Diff

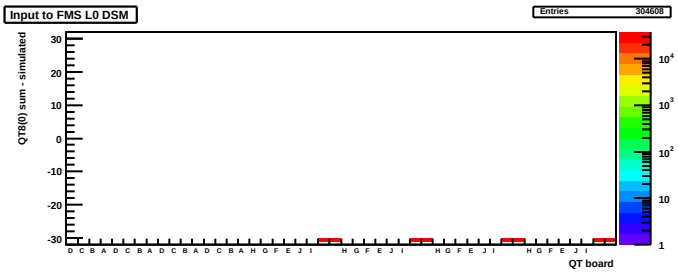

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

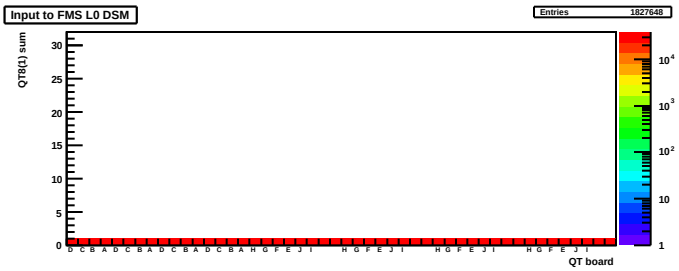

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

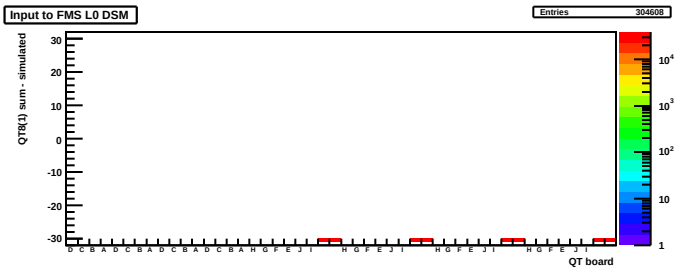

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

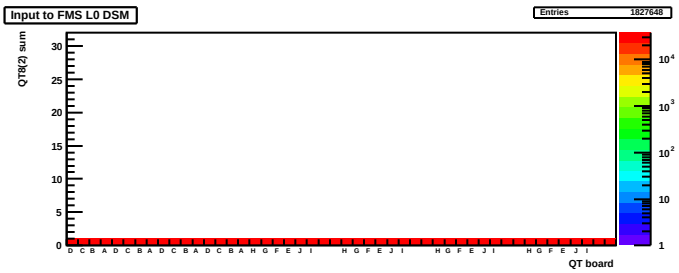

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

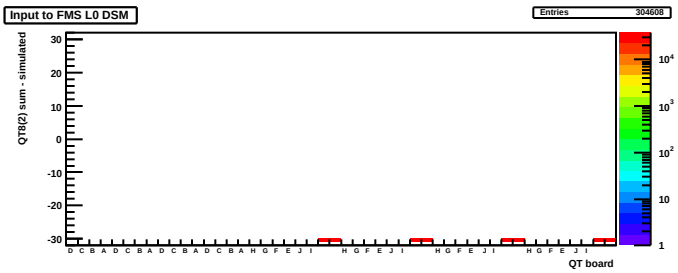

Input to FMS L0 DSM
Entries 1827648
QT8(0) sum
QT board

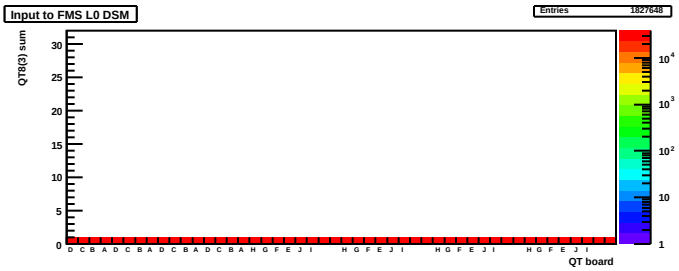

Input to FMS L0 DSM
Entries 304608
QT8(0) sum - simulated
QT board

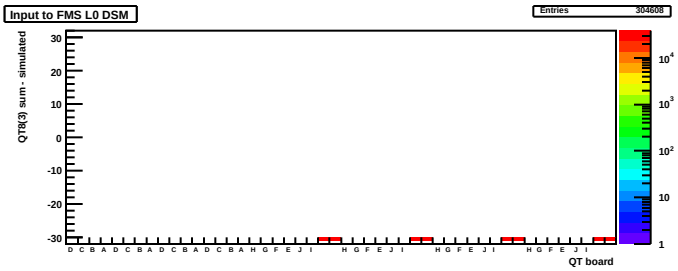

Input to FMS L0 DSM
Entries 1827648
QT8(1) sum
QT board

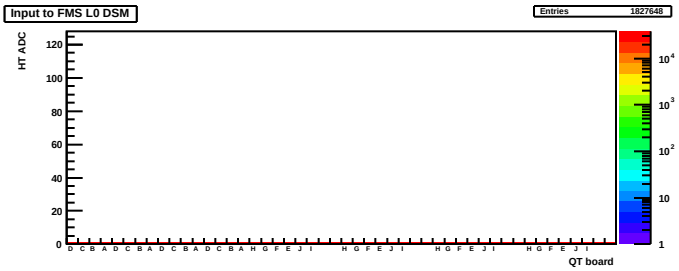

Input to FMS L0 DSM
Entries 304608
QT8(1) sum - simulated
QT board

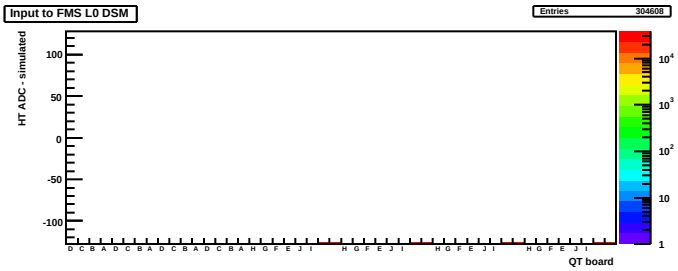

Input to FMS L0 DSM
Entries 1827648
QT8(2) sum
QT board

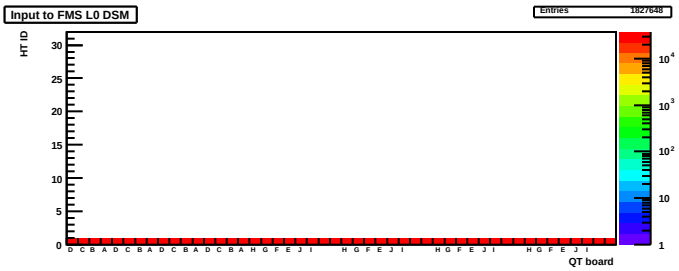

Input to FMS L0 DSM
Entries 304608
QT8(2) sum - simulated
QT board

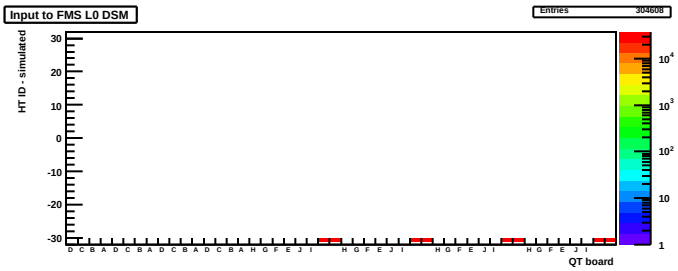

Input to FMS L0 DSM
Entries 1827648
QT8(3) sum
QT board

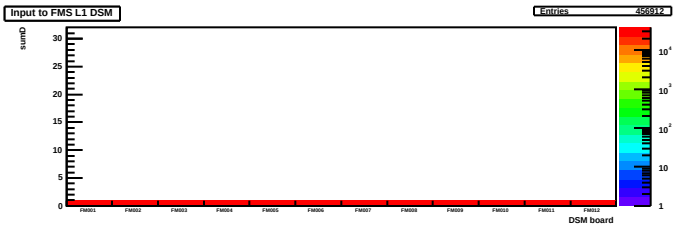

Input to FMS L0 DSM
Entries 304608
QT8(3) sum - simulated
QT board

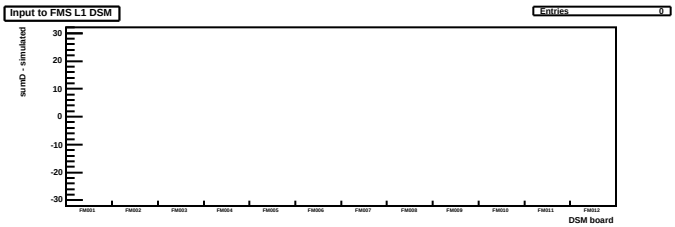

Input to FMS L0 DSM
Entries 1827648
HT ADC
QT board

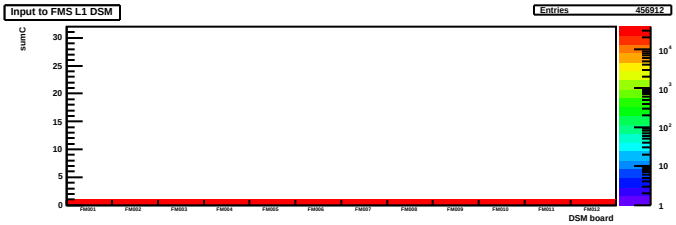

Input to FMS L0 DSM
Entries 304608
HT ADC - simulated
QT board

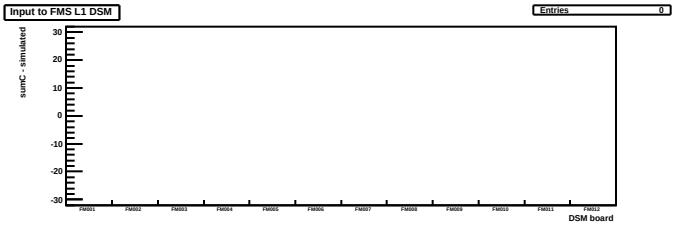

Input to FMS L0 DSM
Entries 1827648
HT ID
QT board

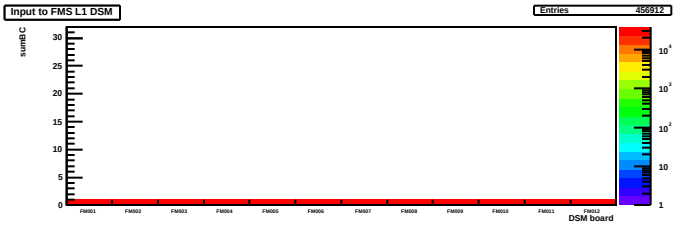

Input to FMS L0 DSM
Entries 304608
HT ID - simulated
QT board

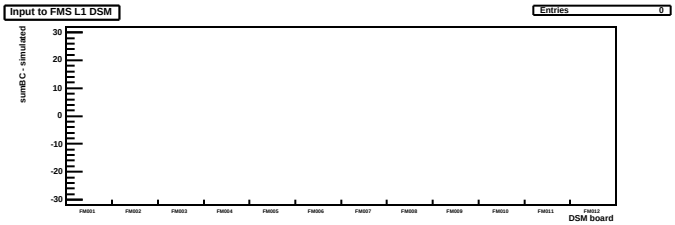

Input to FMS L1 DSM
Entries 456912
sumD
DSM board

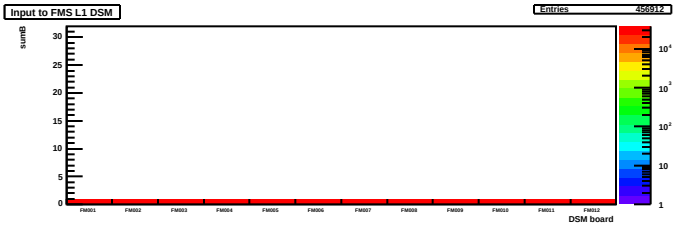

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

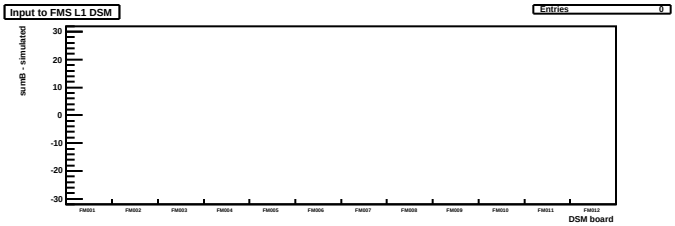

Input to FMS L1 DSM
Entries 456912
sumC
DSM board

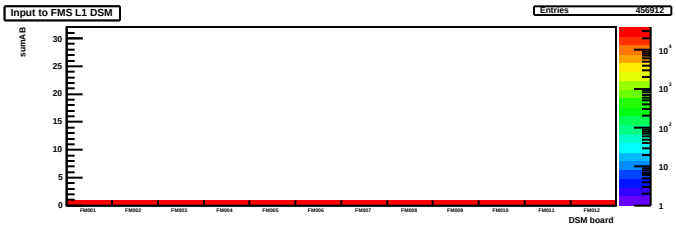

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

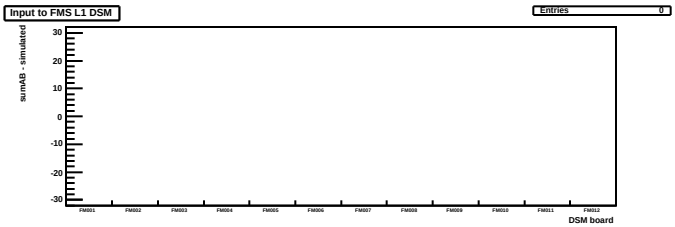

Input to FMS L1 DSM
Entries 456912
sumBC
DSM board

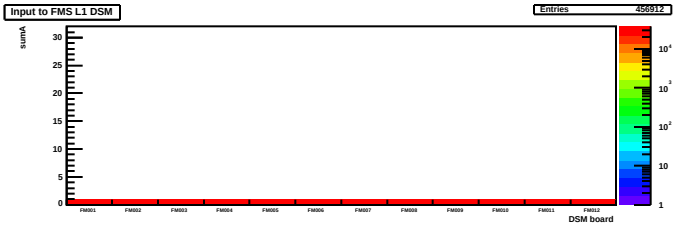

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

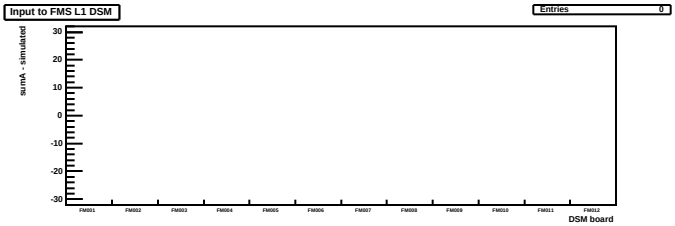

Input to FMS L1 DSM
Entries 456912
sumB
DSM board

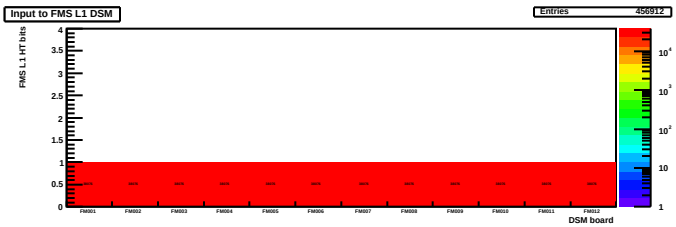

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

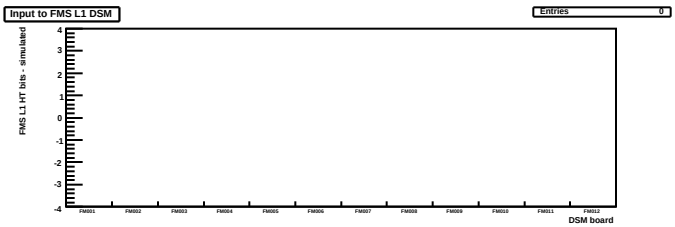

Input to FMS L1 DSM
Entries 456912
sumAB
DSM board

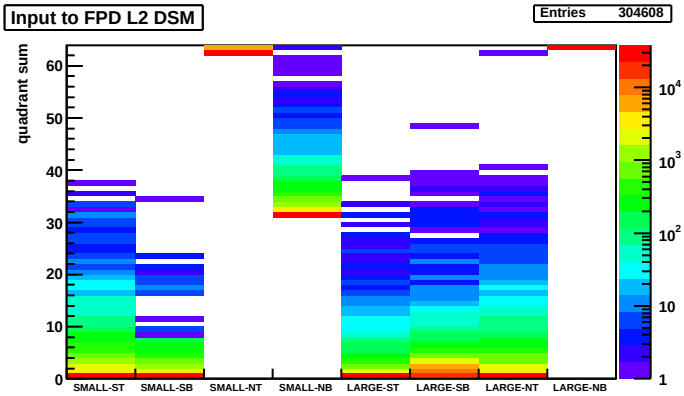

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

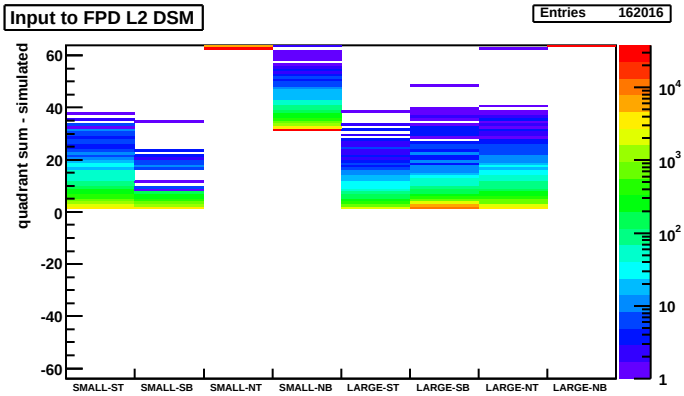

Input to FMS L1 DSM
Entries 456912
sumA
DSM board

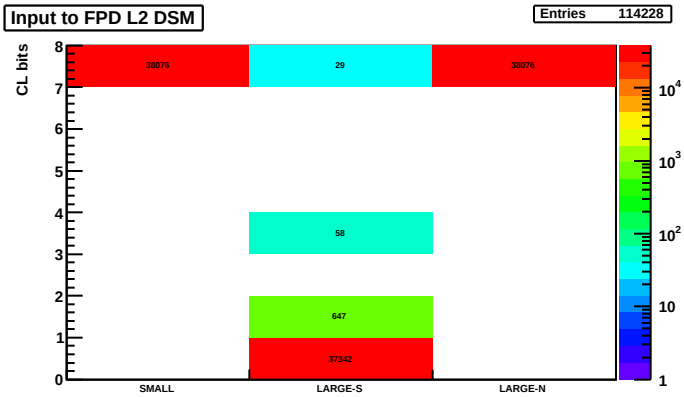

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

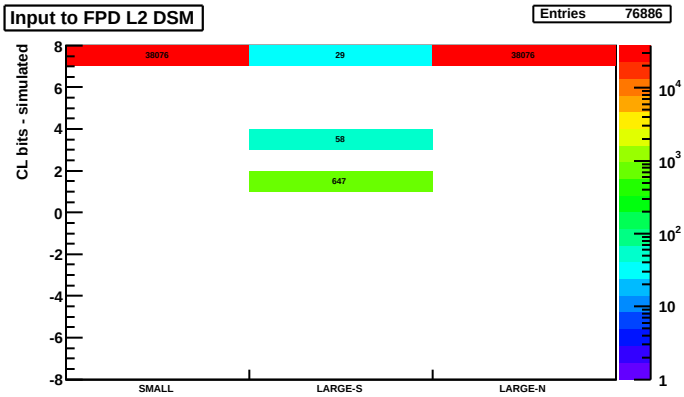

Input to FMS L1 DSM
Entries 456912
FMS L1 HT bits
DSM board

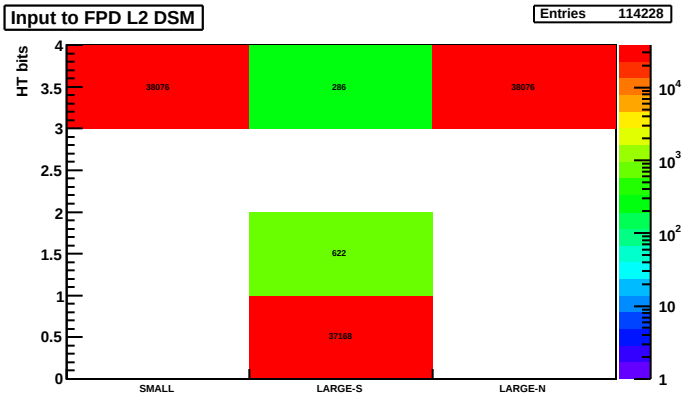

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

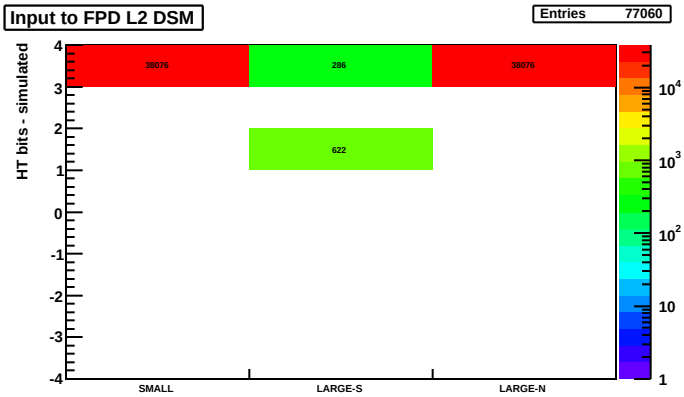

Input to FPD L2 DSM
Entries 304608
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

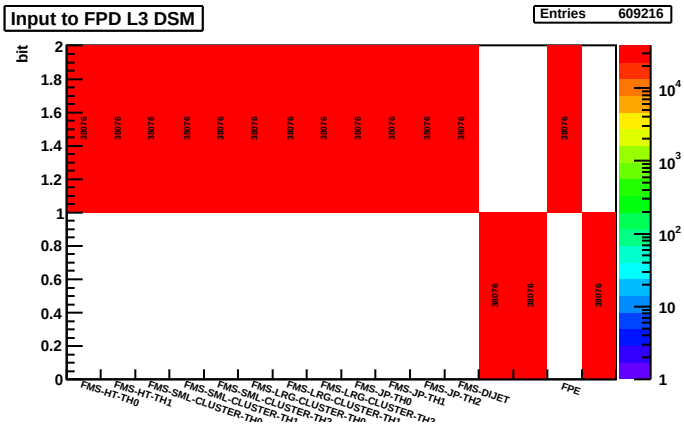

Input to FPD L2 DSM
Entries 162016
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

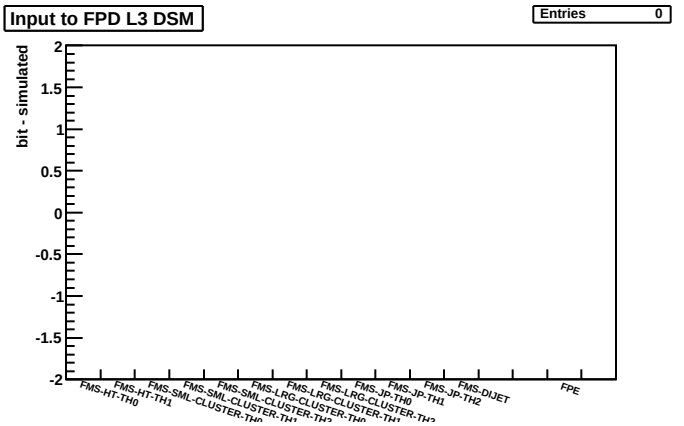

Input to FPD L2 DSM
Entries 114228
CL bits
38076
29
38076
58
647
37342
SMALL
LARGE-S
LARGE-N

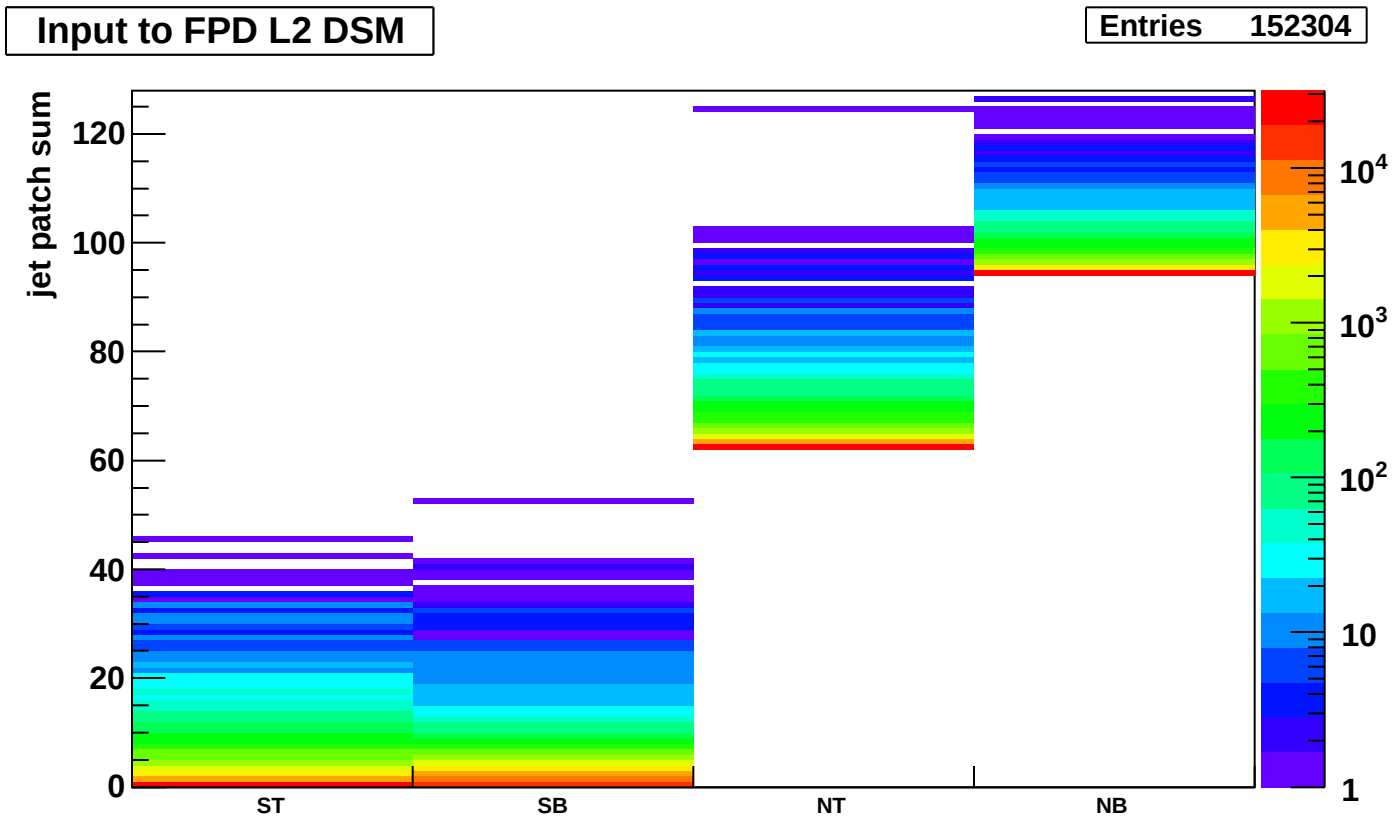

Input to FPD L2 DSM
Entries 76886
CL bits - simulated
38076
29
38076
58
647
SMALL
LARGE-S
LARGE-N

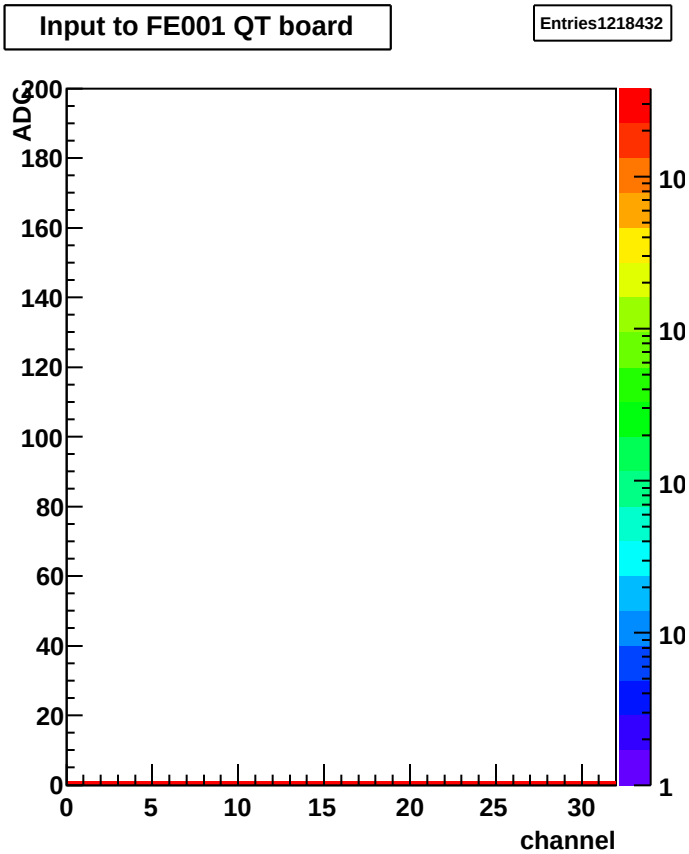

Input to FPD L2 DSM
Entries 114228
HT bits
38076
286
38076
622
37168
SMALL
LARGE-S
LARGE-N

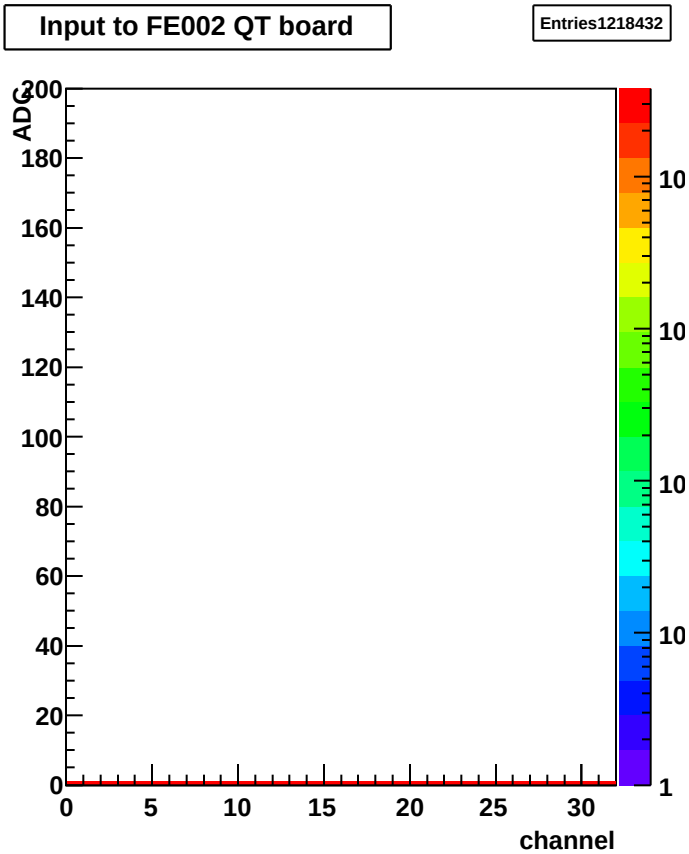

Input to FPD L2 DSM
Entries 77060
HT bits - simulated
38076
286
38076
622
SMALL
LARGE-S
LARGE-N

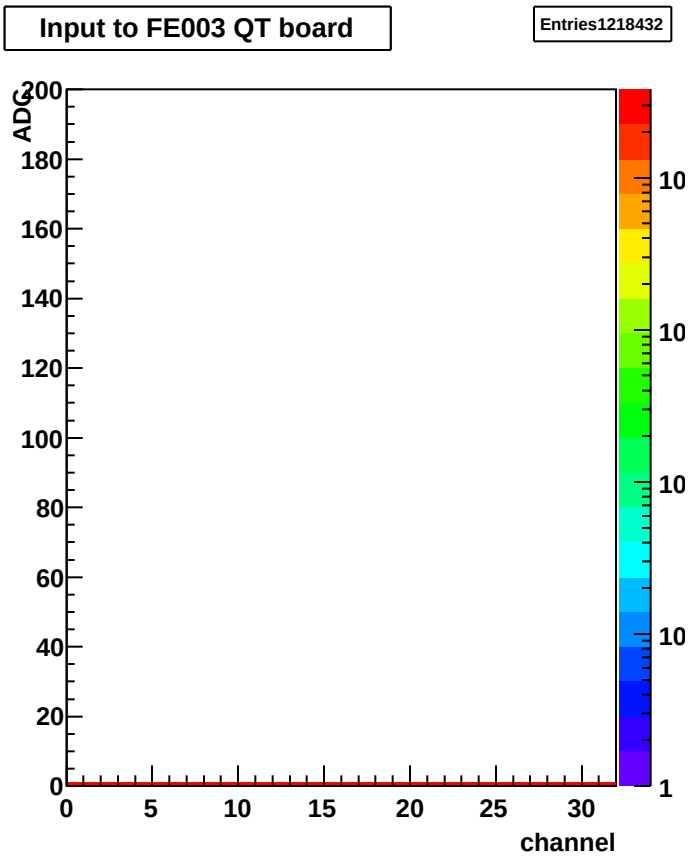

Input to FPD L3 DSM
Entries 609216
bit
38076
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

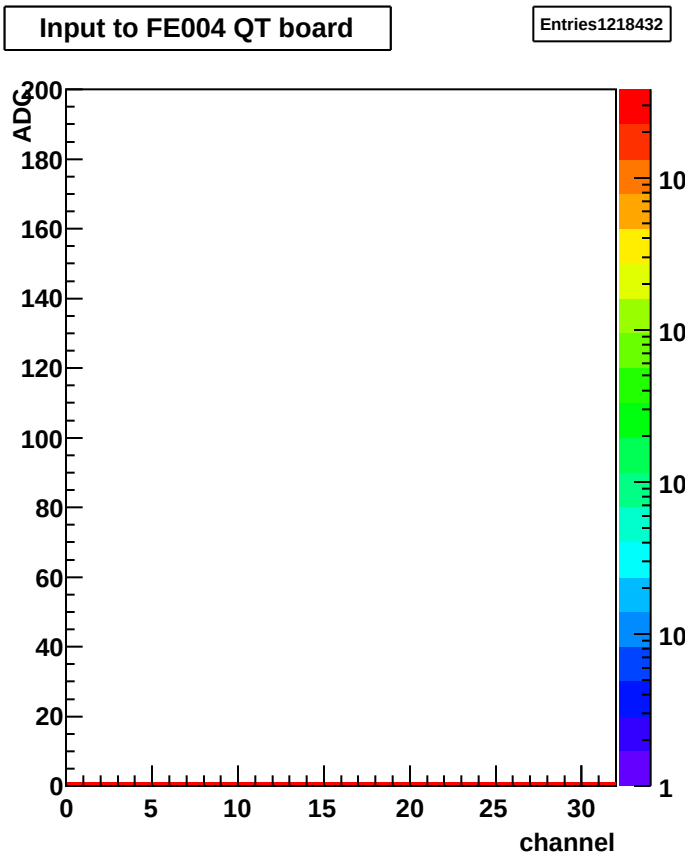

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

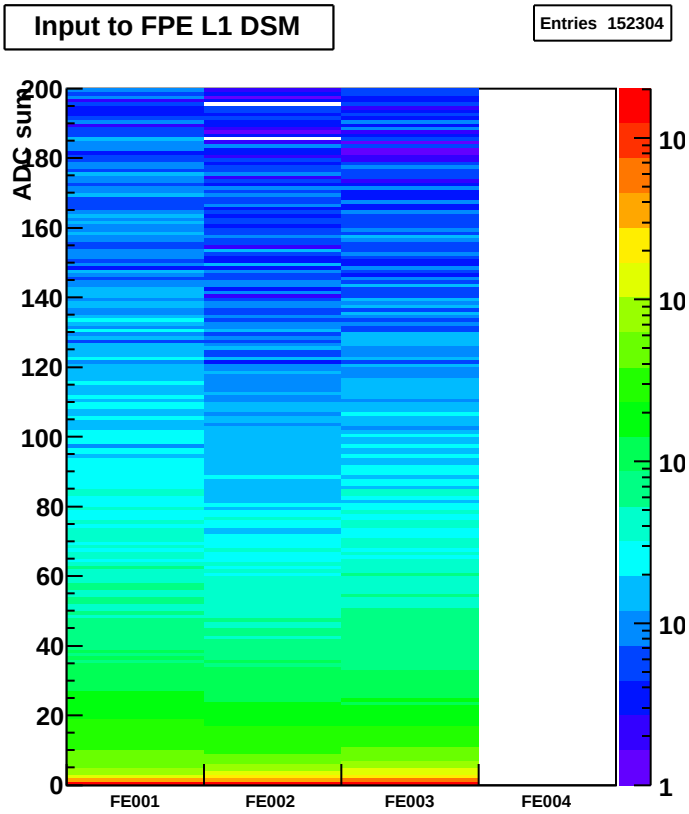

Input to FPD L2 DSM
Entries 152304
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

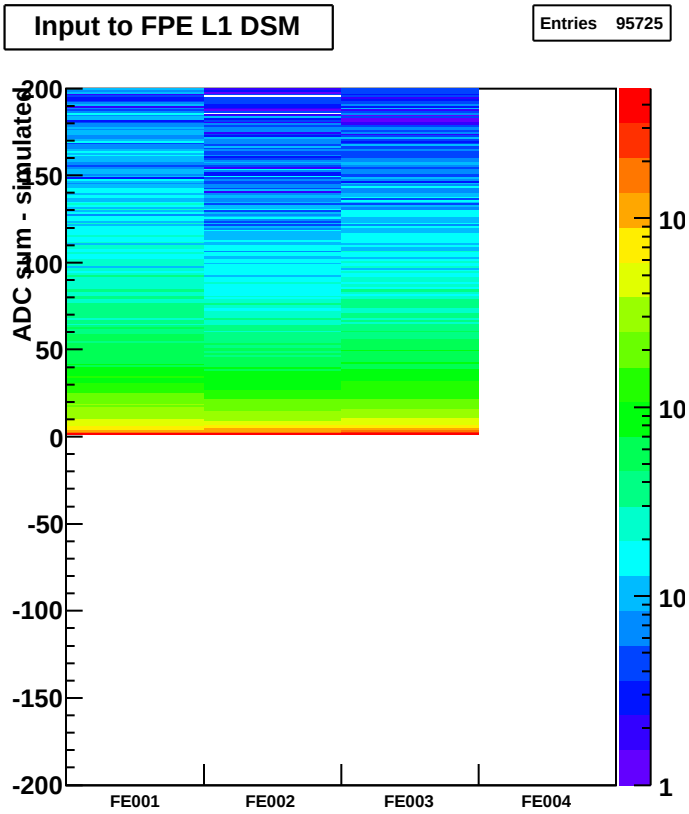

Input to FE001 QT board
Entries1218432
ADC
channel

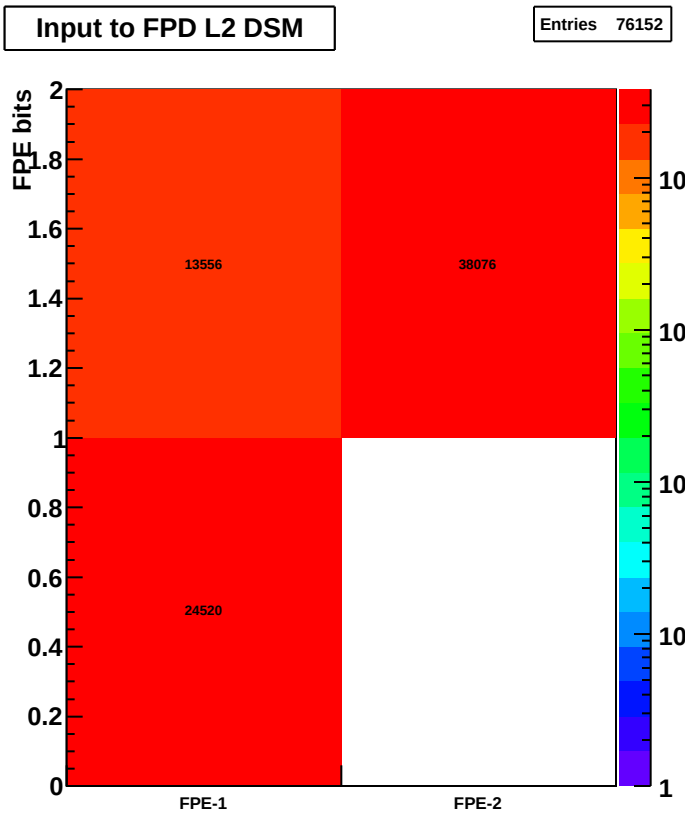

Input to FE002 QT board
Entries1218432
ADC
channel

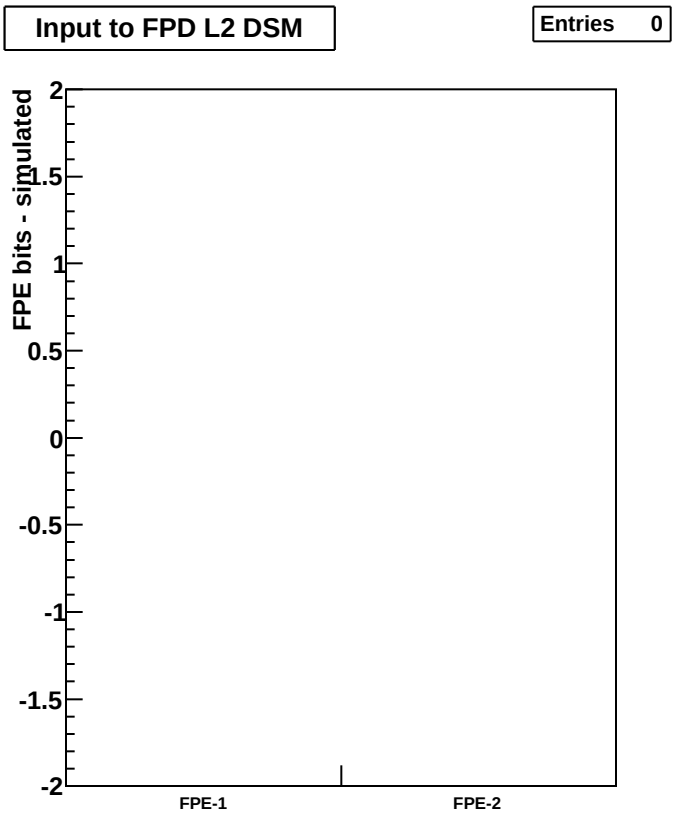

Input to FE003 QT board
Entries1218432
ADC
channel


Input to FE004 QT board
Entries1218432
ADC
channel


Input to FPE L1 DSM
Entries 152304
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 95725
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 76152
FPE bits
13556
38076
24520
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2