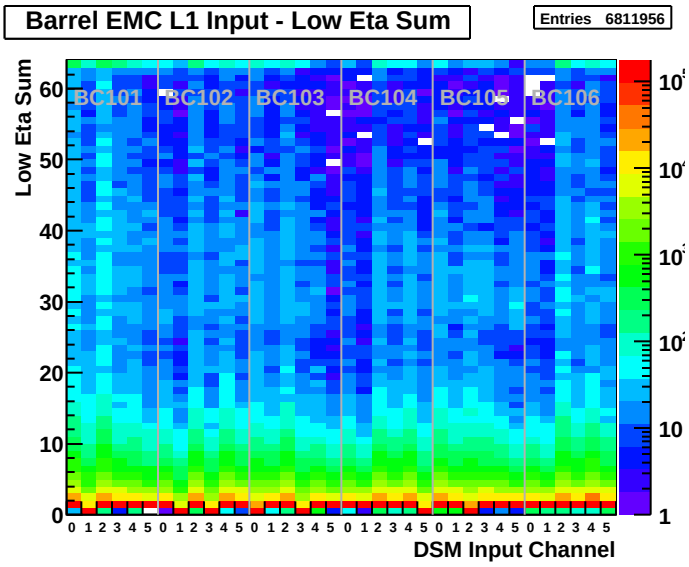
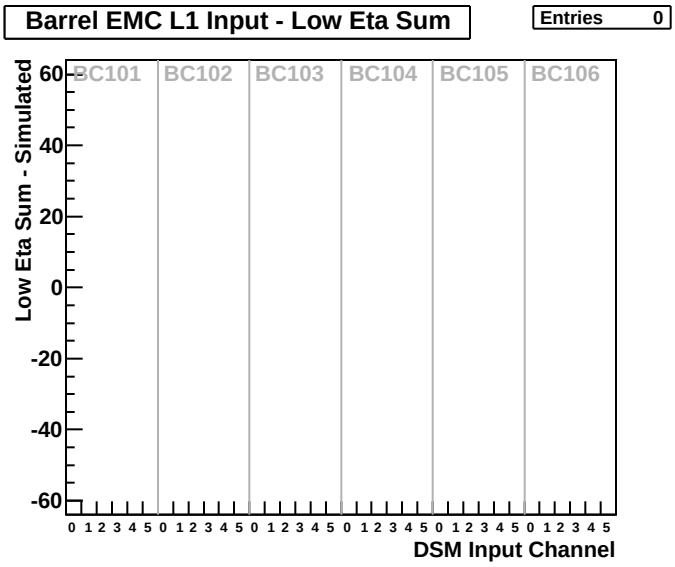
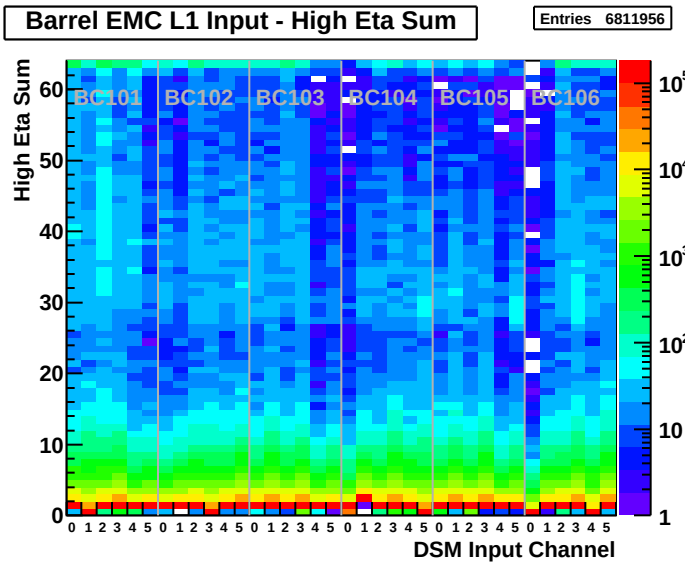
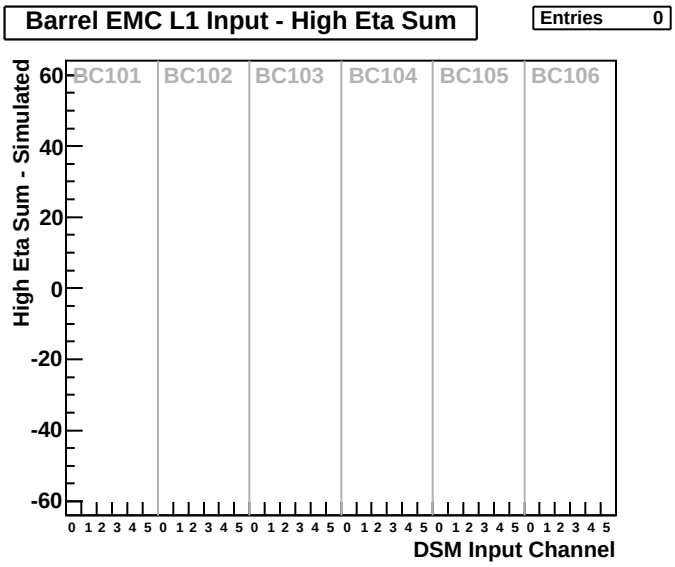
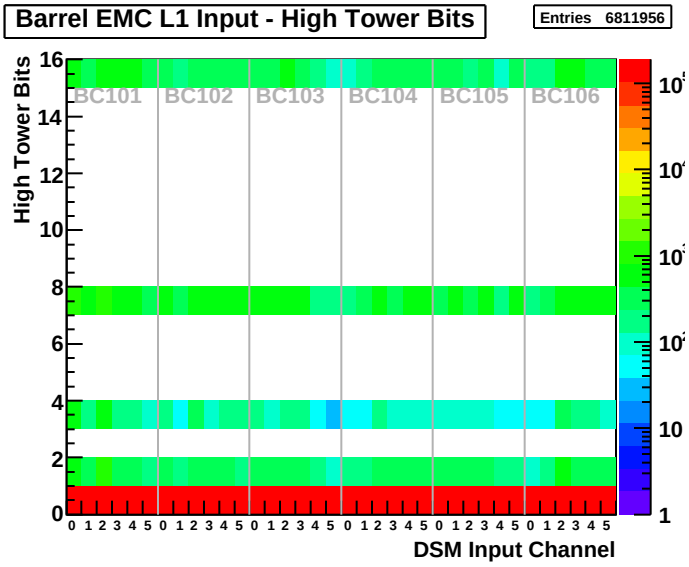
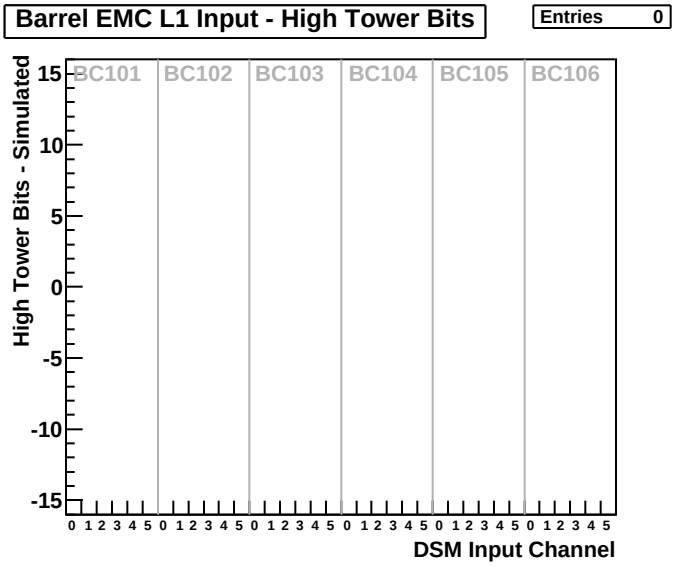

Barrel EMC L1 Input - Low Eta Sum
Entries 6811956
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

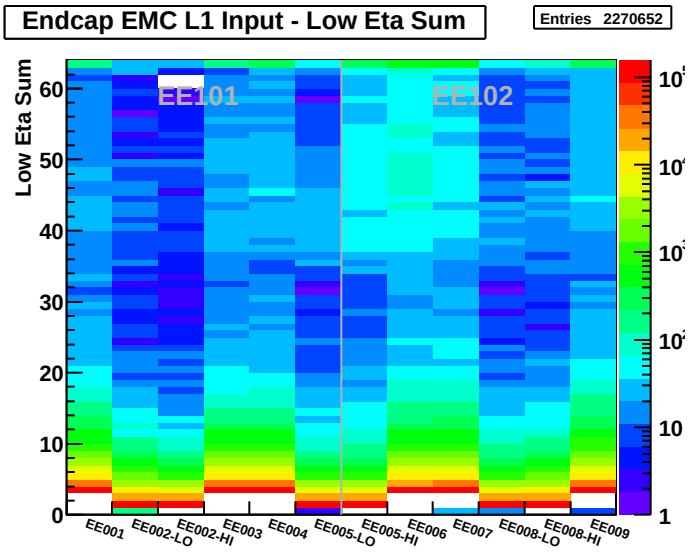

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

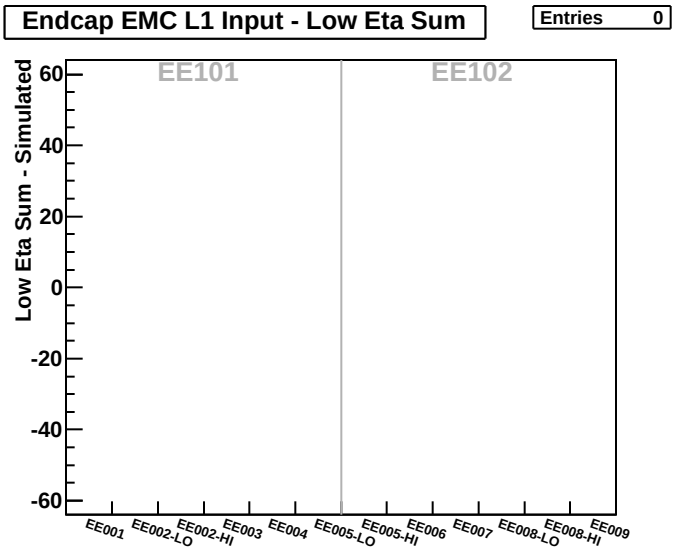

Barrel EMC L1 Input - High Eta Sum
Entries 6811956
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

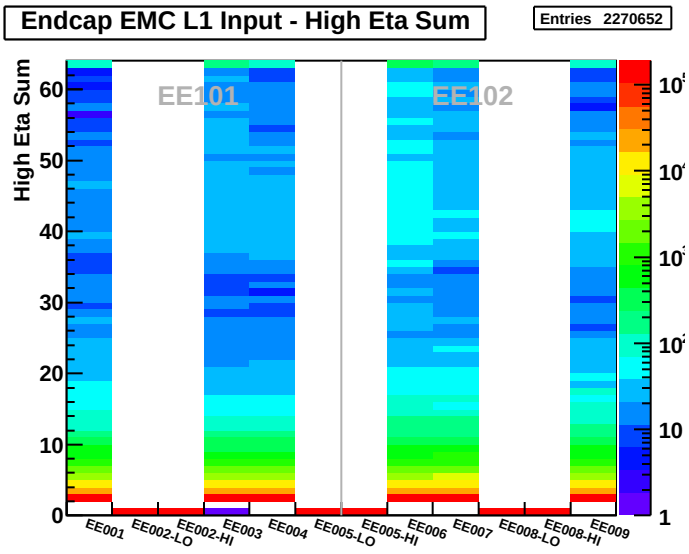

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

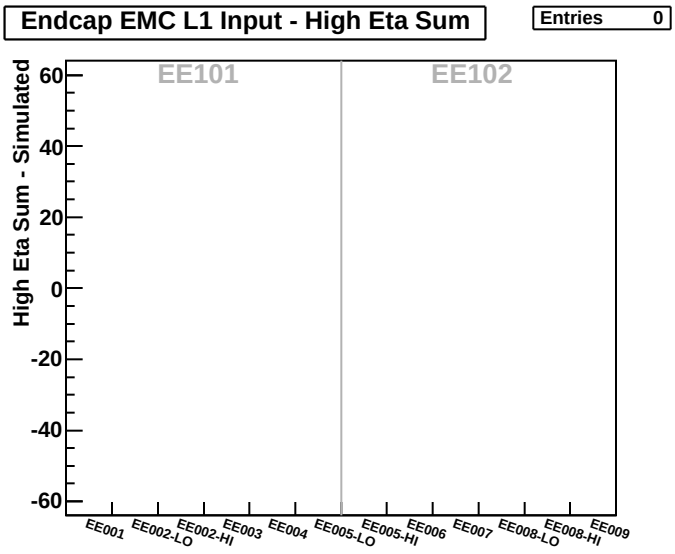

Barrel EMC L1 Input - High Tower Bits
Entries 6811956
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

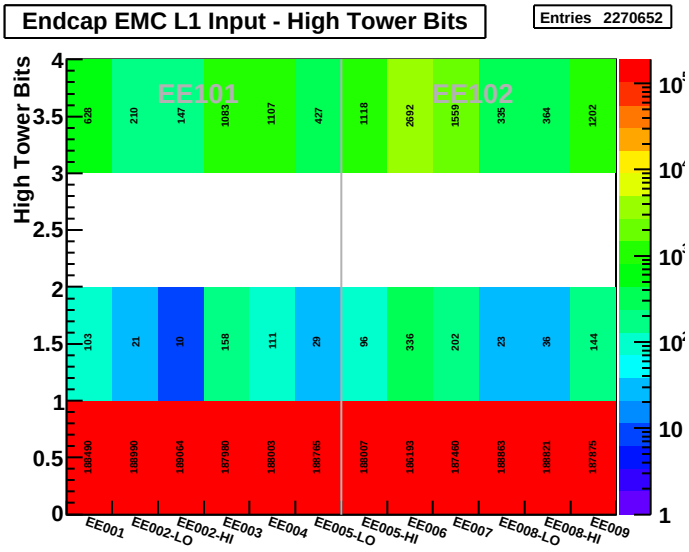

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

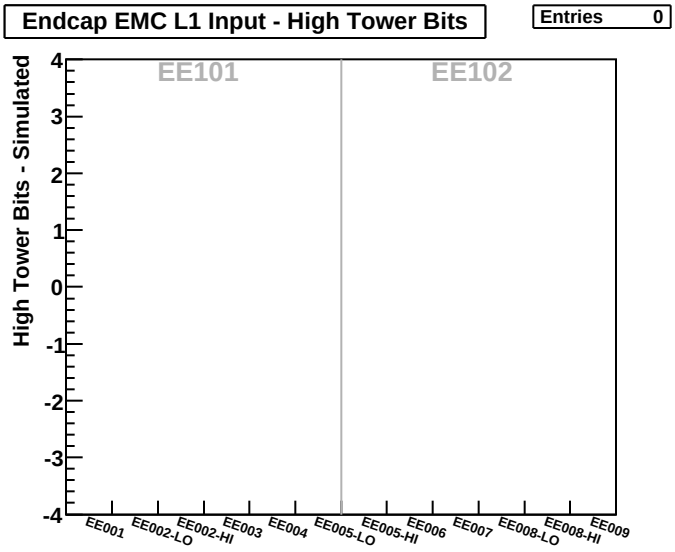

Endcap EMC L1 Input - Low Eta Sum
Entries 2270652
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

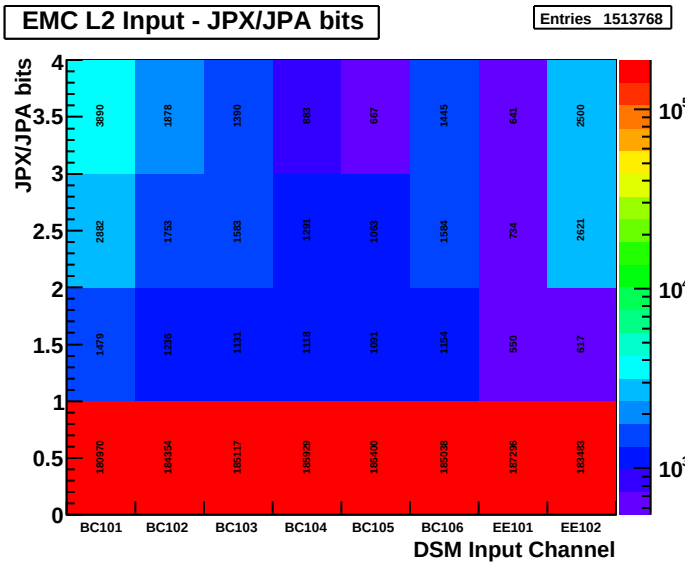

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

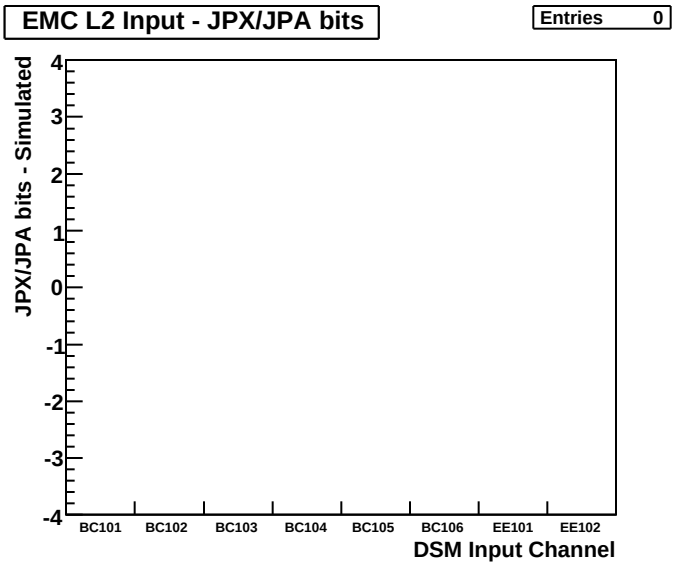

Endcap EMC L1 Input - High Eta Sum
Entries 2270652
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

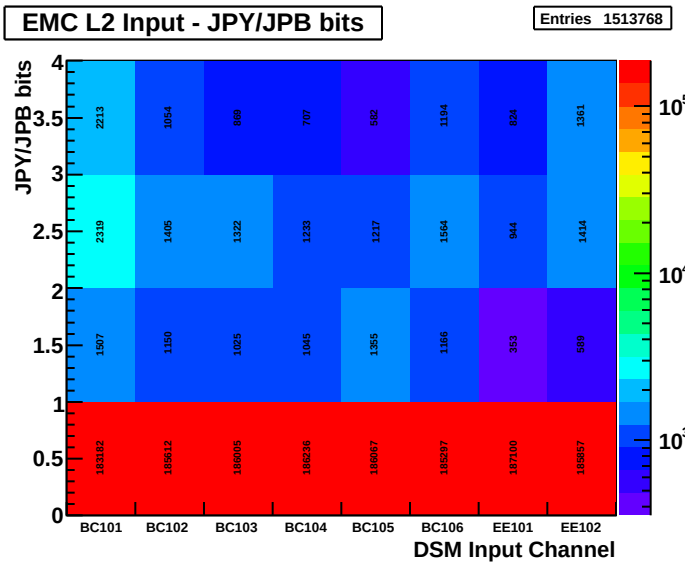

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

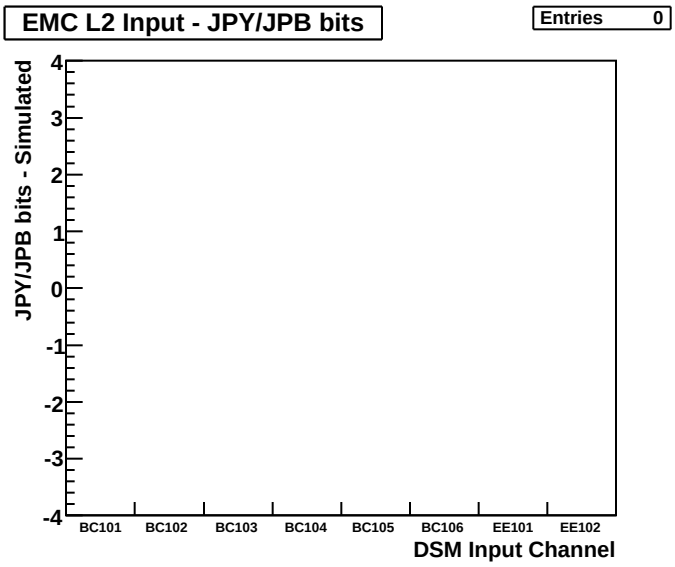

Endcap EMC L1 Input - High Tower Bits
Entries 2270652
High Tower Bits
EE101
EE102
628 210 147 1083 1107 427 1118 2592 1559 335 364 1202
103 21 10 158 111 29 96 336 202 23 36 144
188490 188990 189064 187980 188003 188765 188007 186193 187460 188863 188821 187875
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

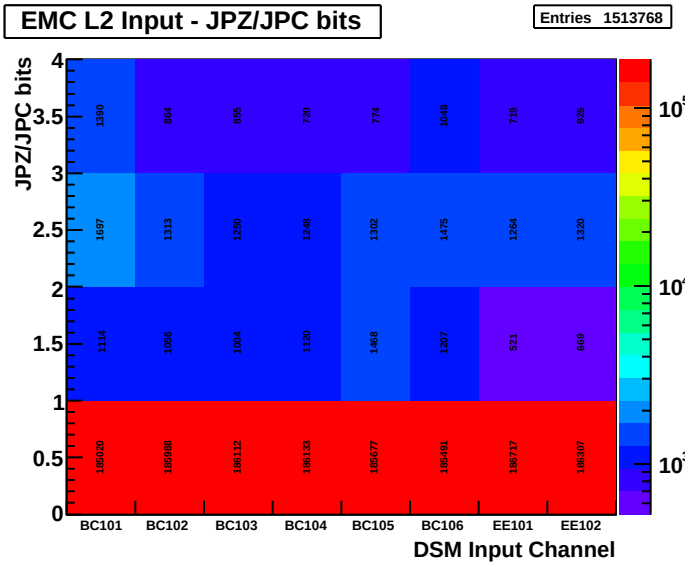

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

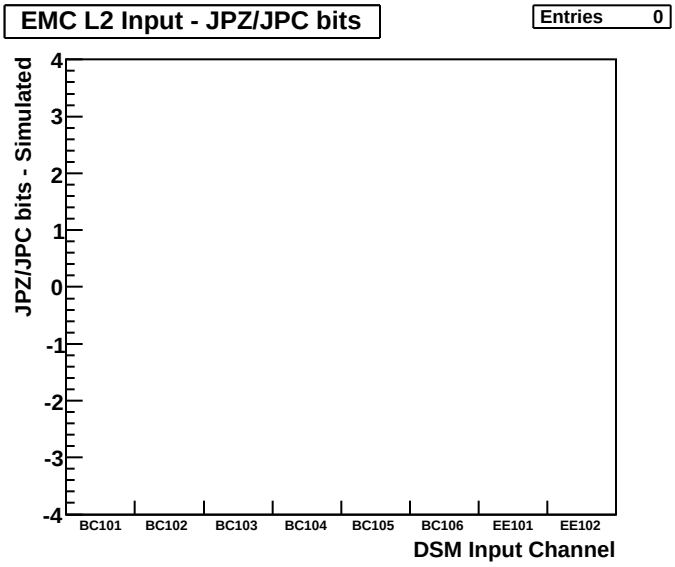

EMC L2 Input - JPX/JPA bits
Entries 1513768
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3890 1878 1390 883 667 1445 641 2500
2882 1753 1583 1291 1063 1584 734 2621
1479 1226 1131 1118 1091 1154 550 617
180970 184354 185117 185929 186400 185038 187296 183483

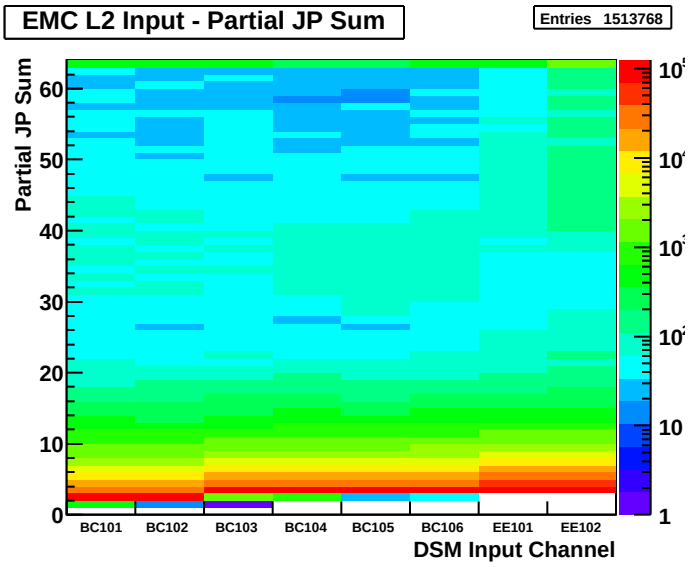

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

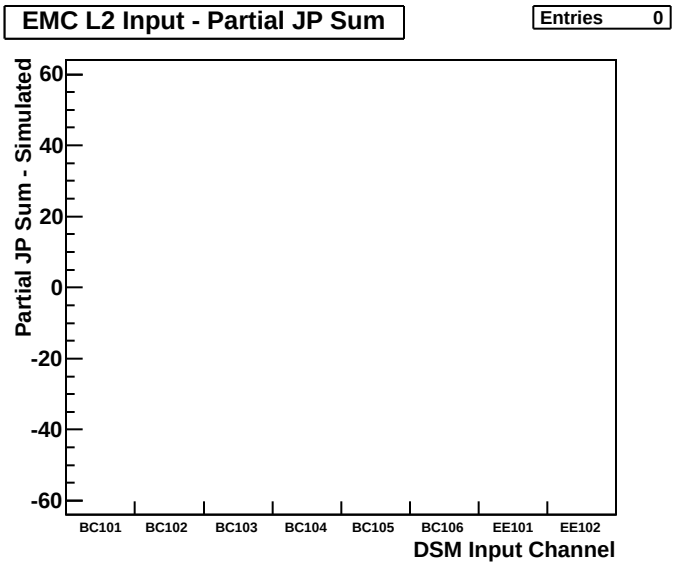

EMC L2 Input - JPY/JPB bits
Entries 1513768
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2213 1054 869 707 582 1194 824 1361
2319 1405 1322 1233 1217 1564 944 1414
1507 1150 1025 1045 1355 1166 353 589
183182 186612 186005 186236 186067 185297 187100 183857

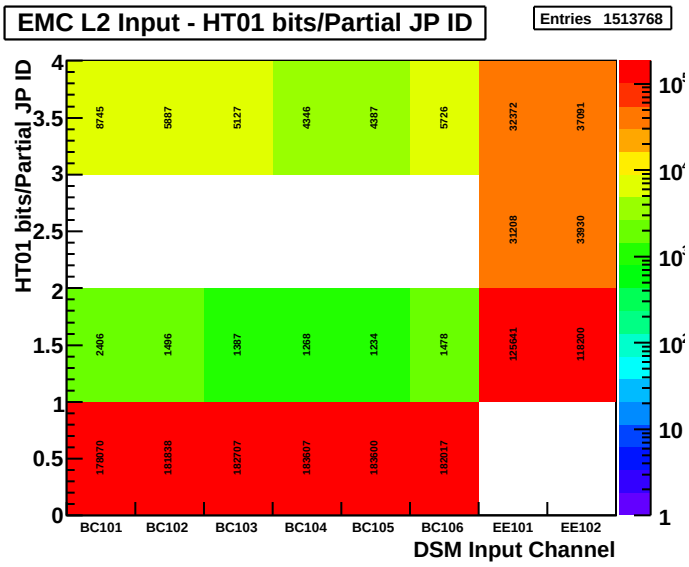

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

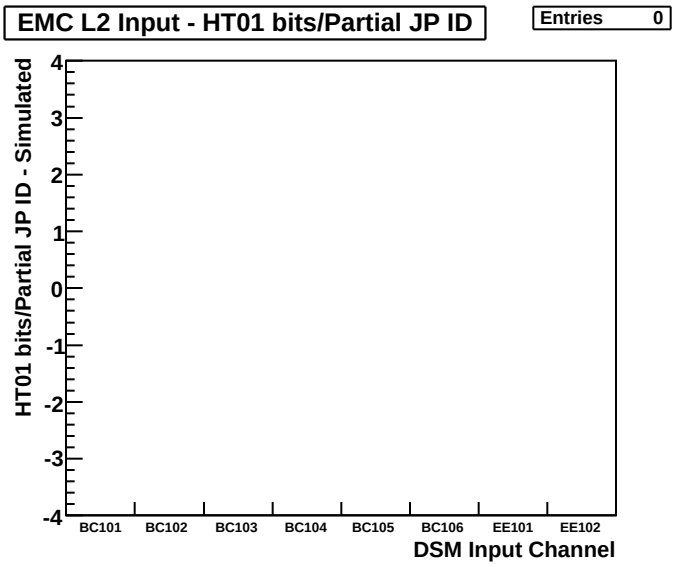

EMC L2 Input - JPZ/JPC bits
Entries 1513768
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1390 864 855 720 774 1048 719 925
1697 1313 1250 1248 1302 1475 1264 1320
1114 1056 1004 1120 1468 1207 521 669
185020 185388 186112 186133 185677 185491 186717 186307

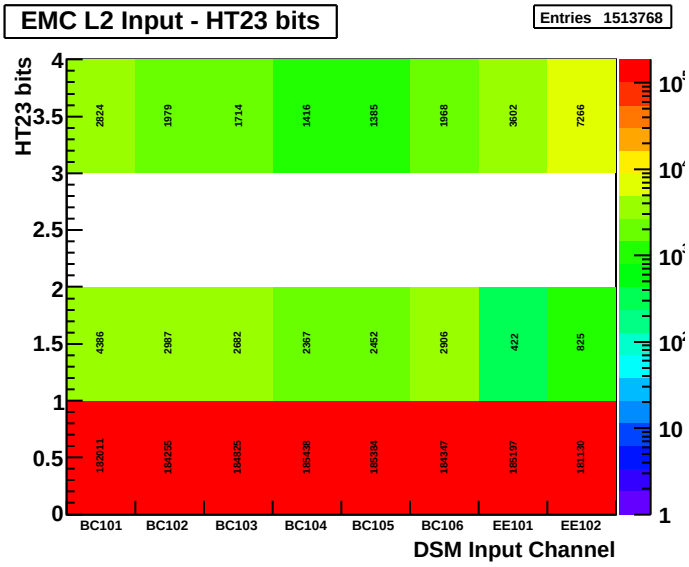

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

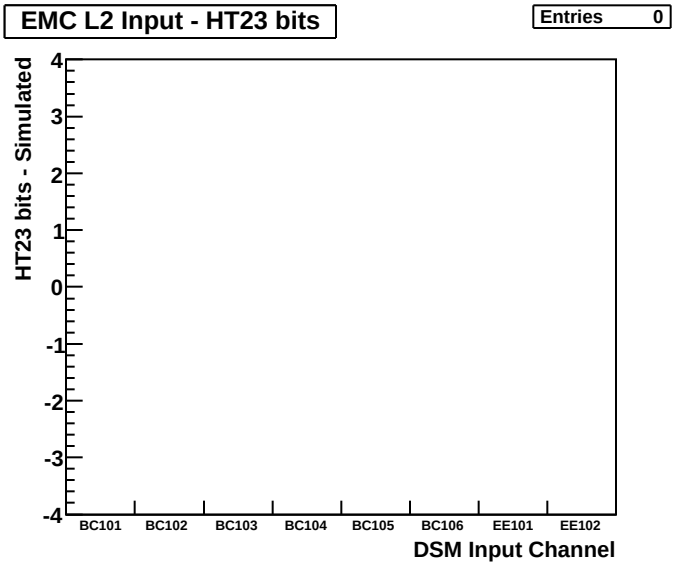

EMC L2 Input - Partial JP Sum
Entries 1513768
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

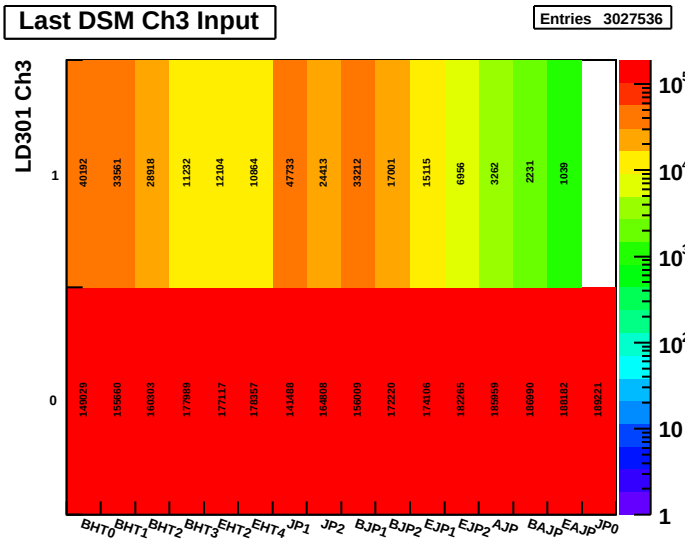

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

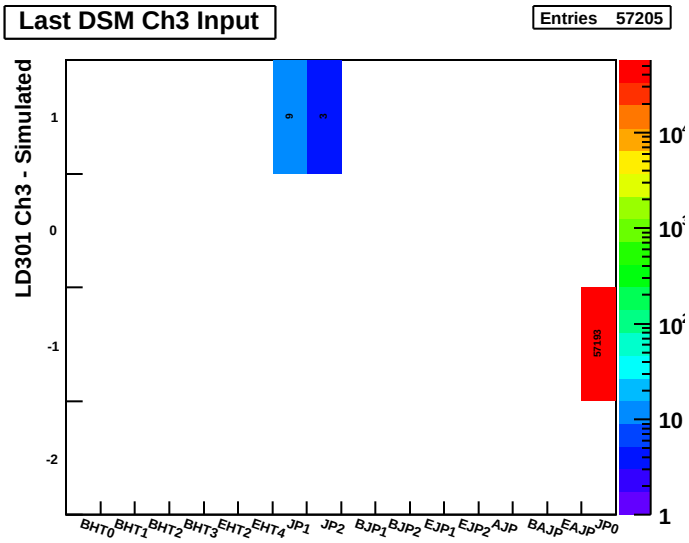

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1513768
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

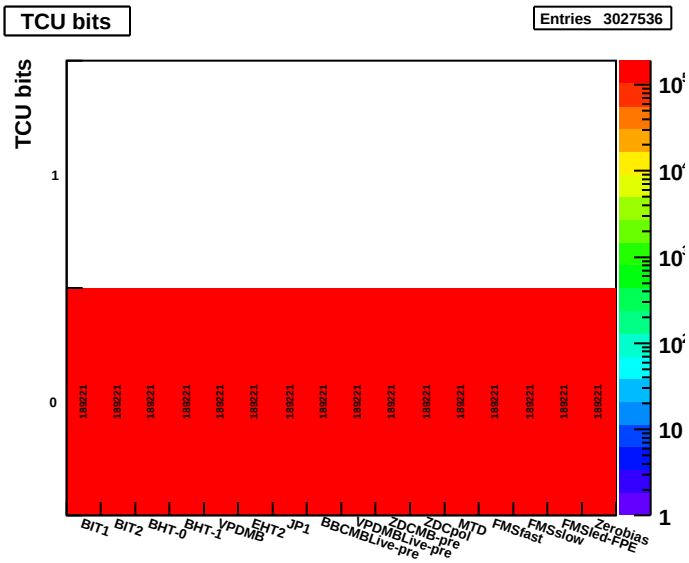

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

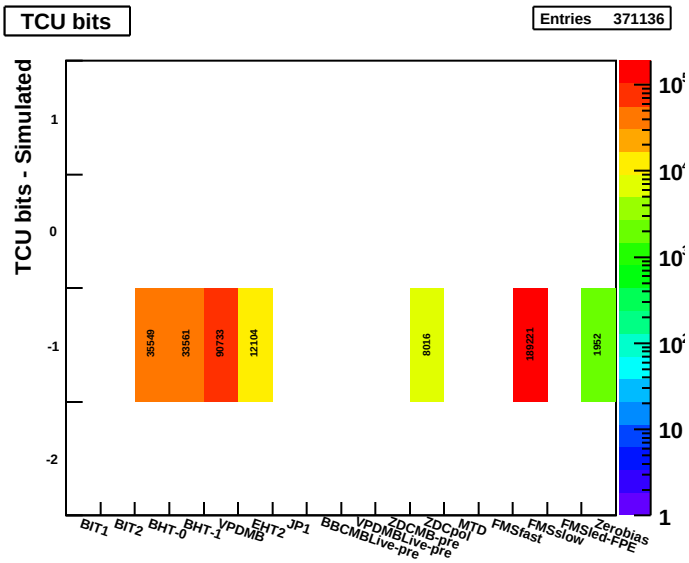

EMC L2 Input - HT23 bits
Entries 1513768
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

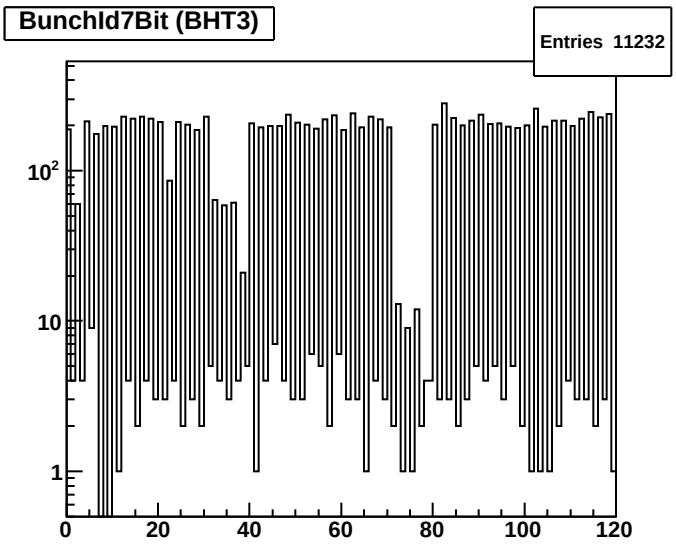

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

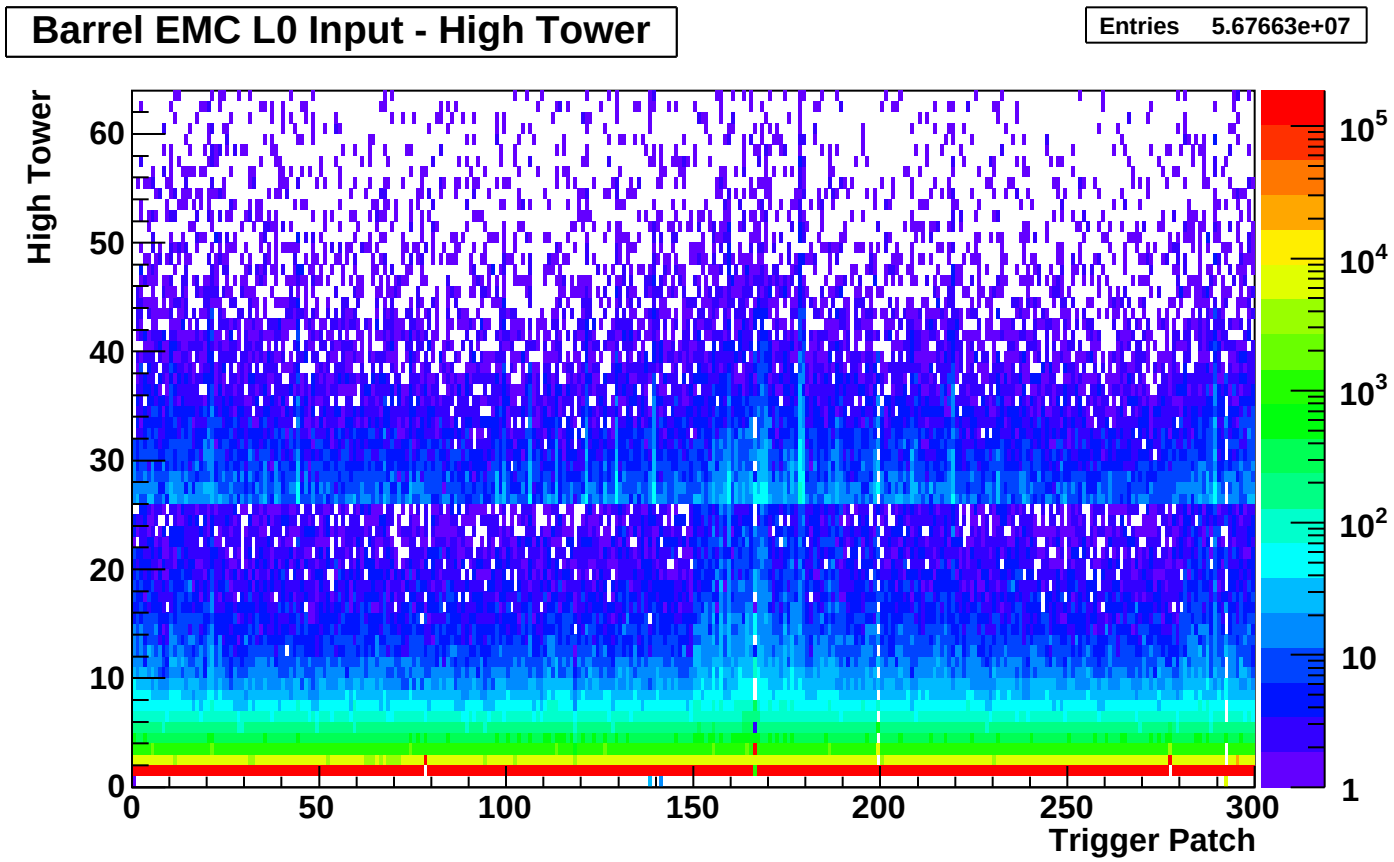

Last DSM Ch3 Input
Entries 3027536
LD301 Ch3
40192 33561 28918 11232 12104 10864 47733 24413 3212 17001 15115 6956 3262 2231 1039
149029 155660 160303 177989 177117 178357 141488 164008 156009 172220 174106 182265 185959 186990 188182 189221
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

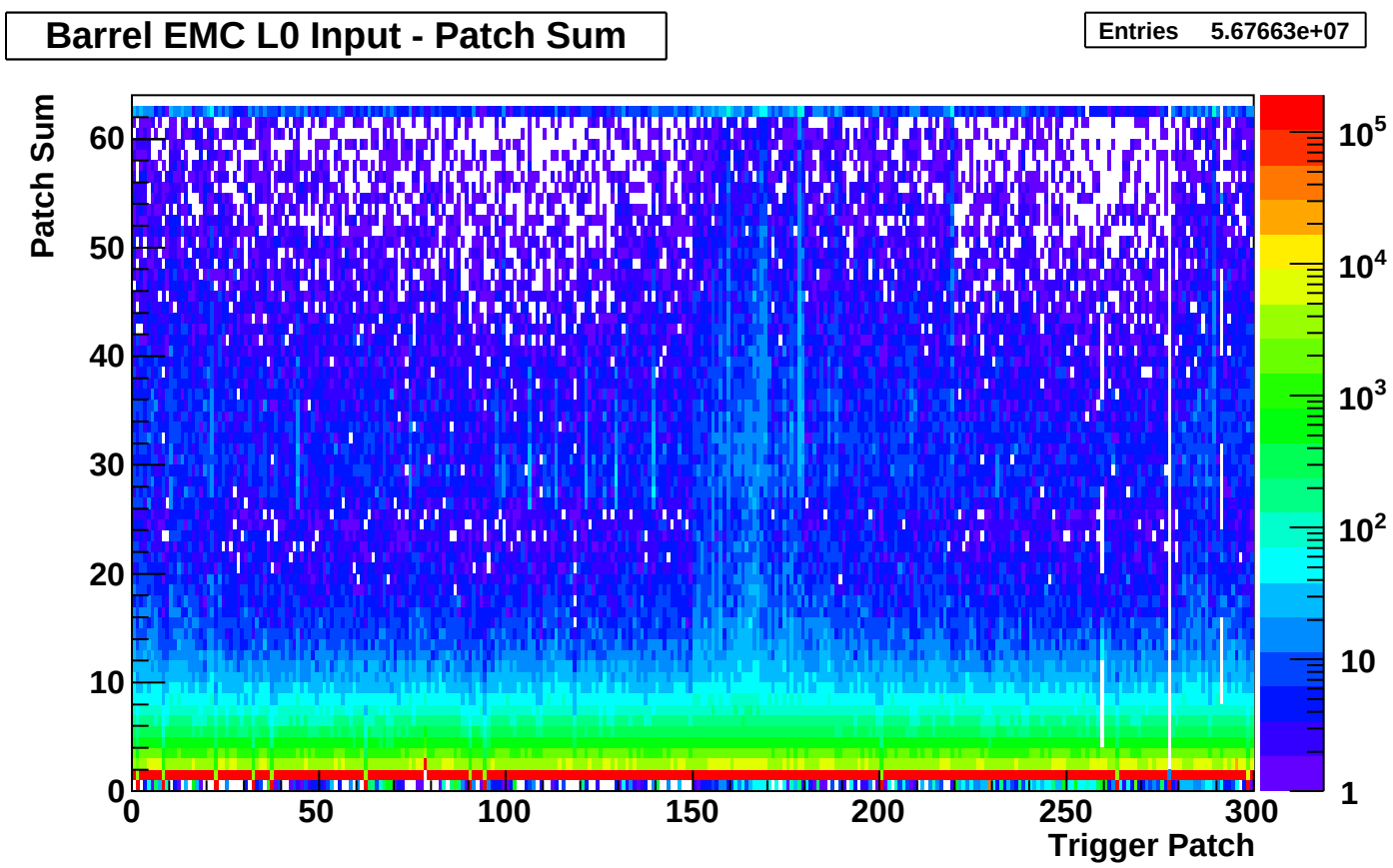

Last DSM Ch3 Input
Entries 57205
LD301 Ch3 - Simulated
9 3
57193
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

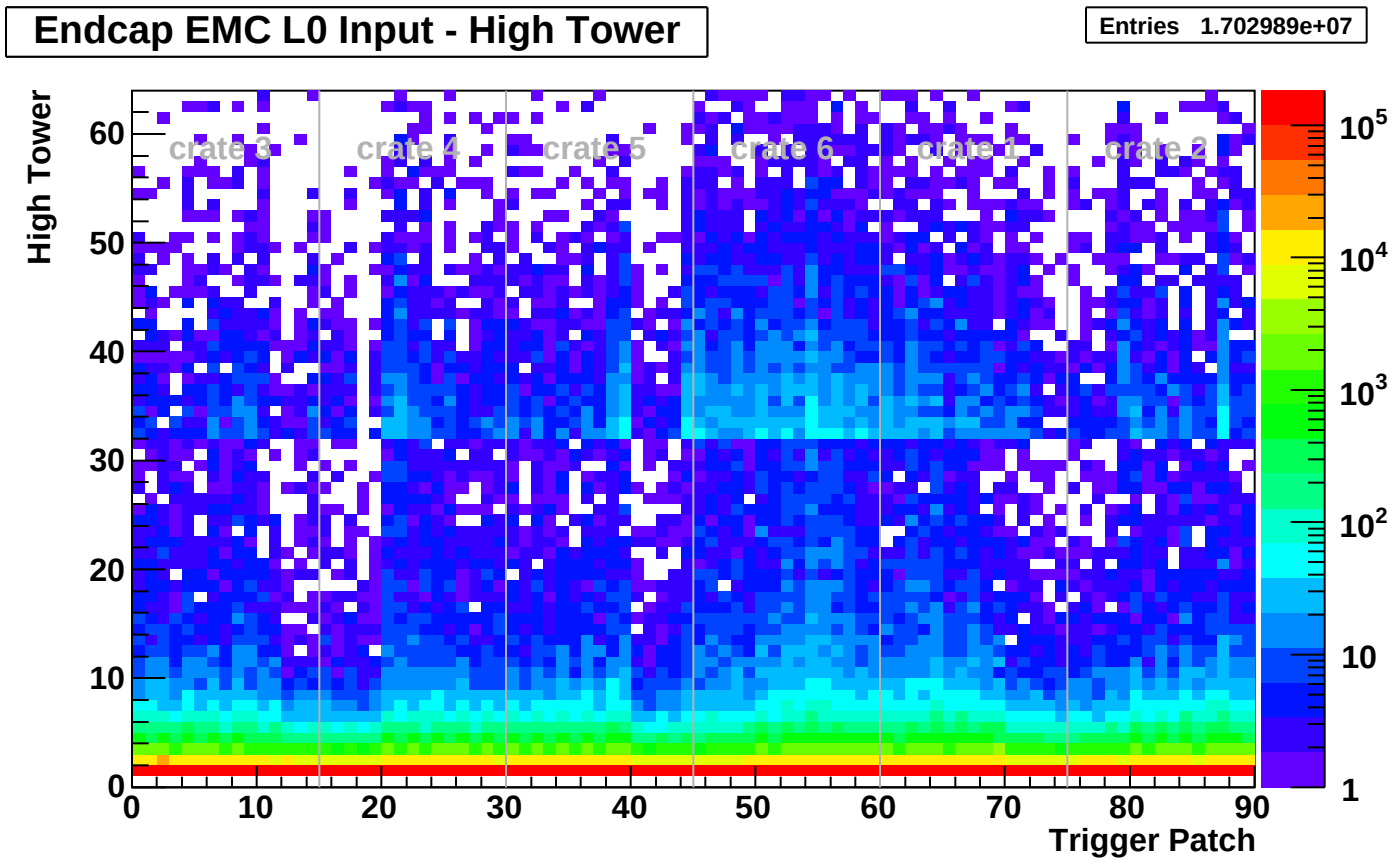

TCU bits
Entries 3027536
TCU bits
189221
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

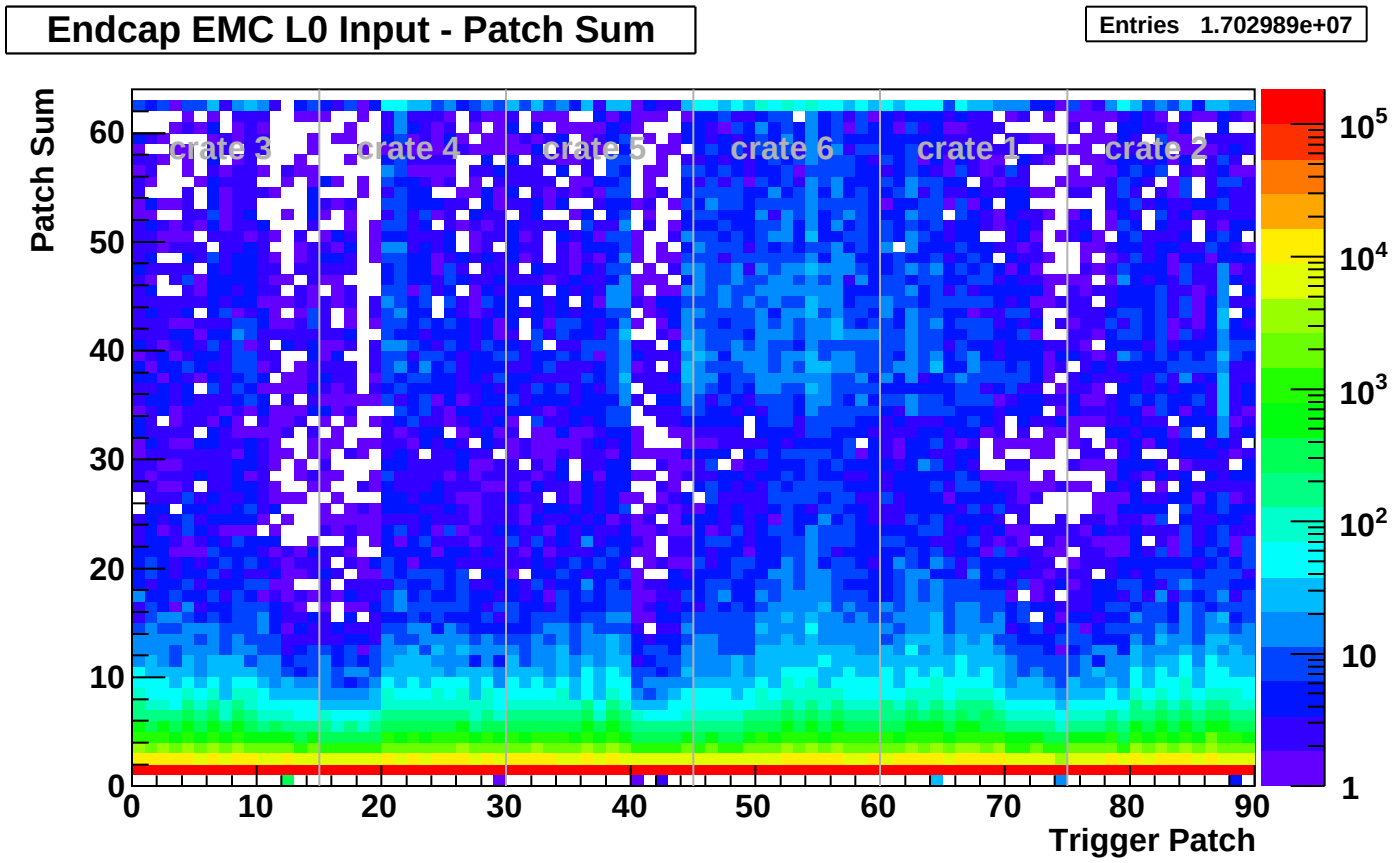

TCU bits
Entries 371136
TCU bits - Simulated
35540 33561 90733 12104 8016 189221 1952
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

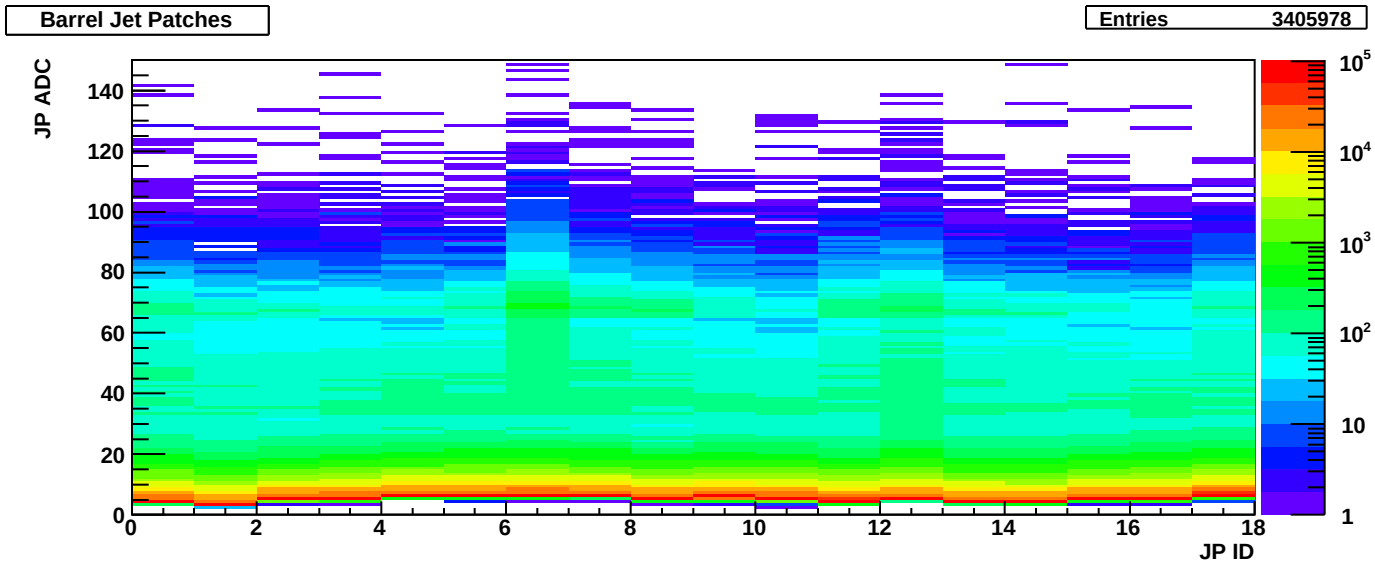

BunchId7Bit (BHT3)
Entries 11232

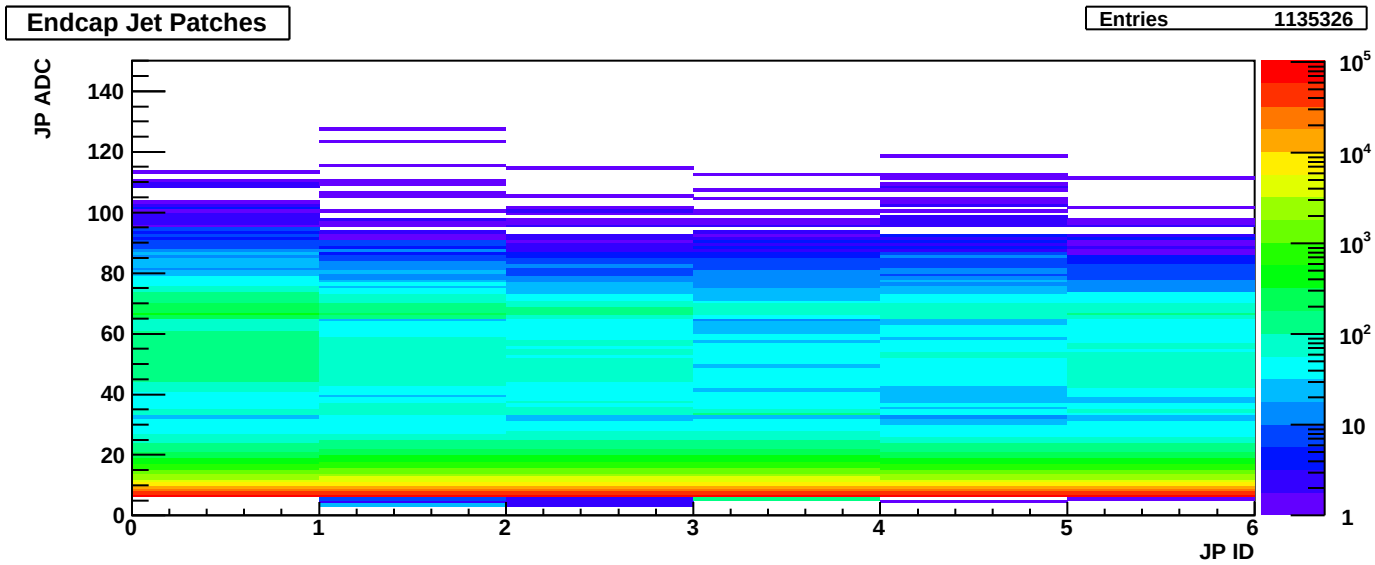

Barrel EMC L0 Input - High Tower
Entries 5.67663e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

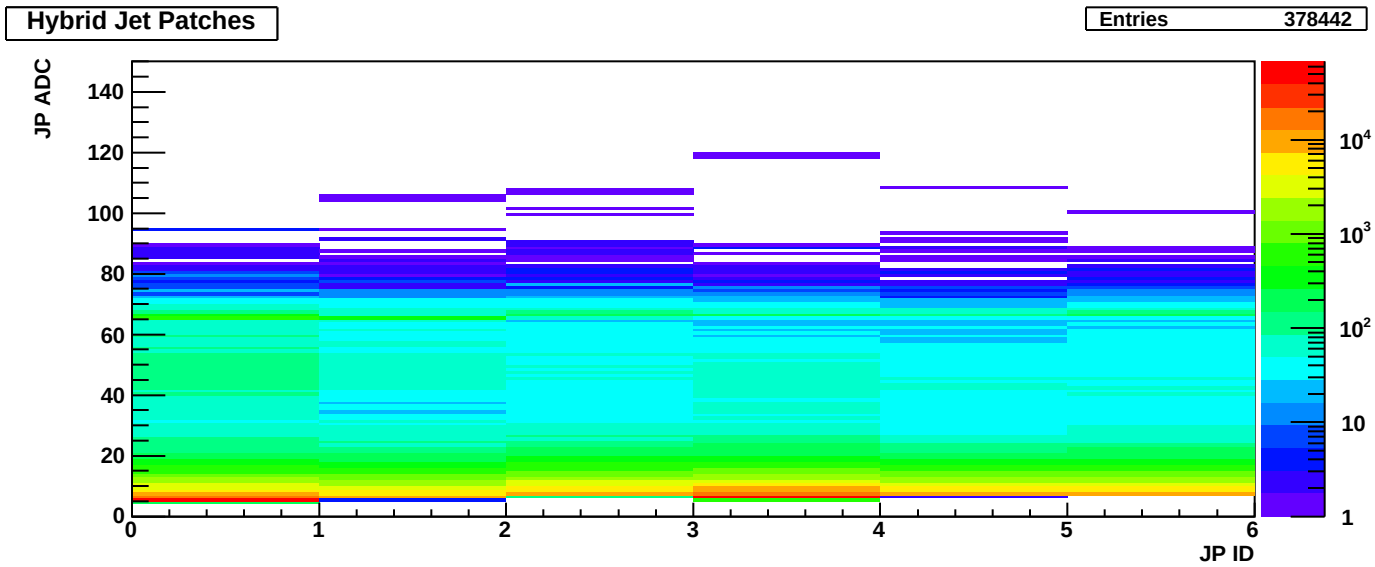

Barrel EMC L0 Input - Patch Sum
Entries 5.67663e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

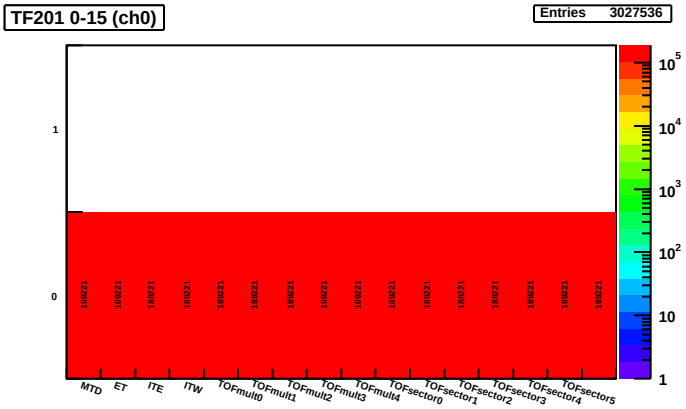

Endcap EMC L0 Input - High Tower
Entries 1.702989e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

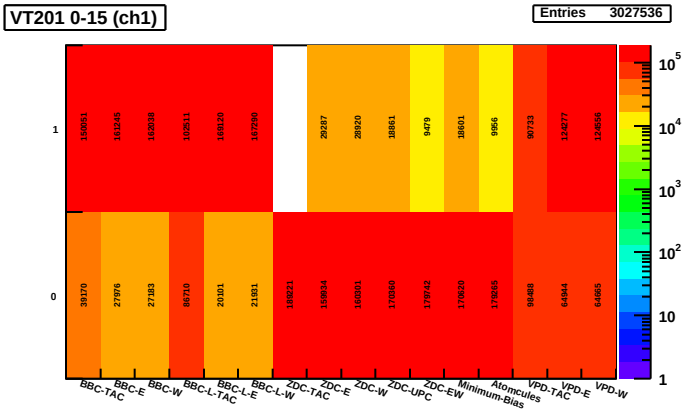

Endcap EMC L0 Input - Patch Sum
Entries 1.702989e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

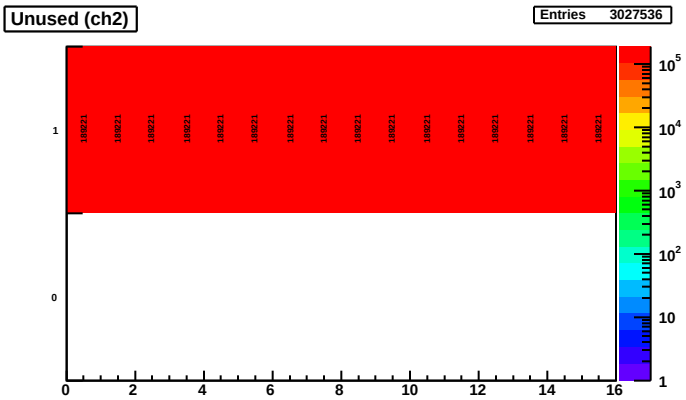

Barrel Jet Patches
Entries 3405978
JP ADC
JP ID

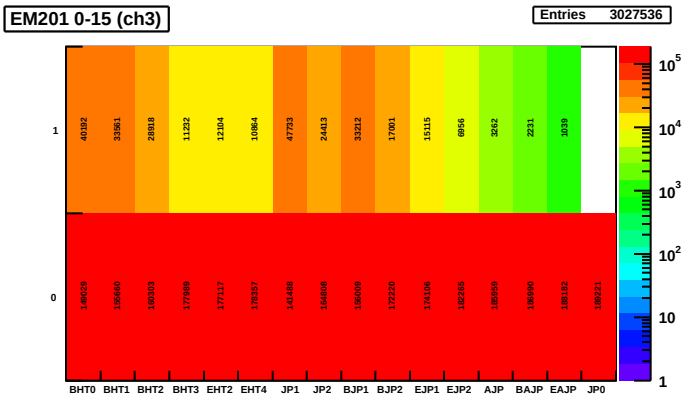

Endcap Jet Patches
Entries 1135326
JP ADC
JP ID

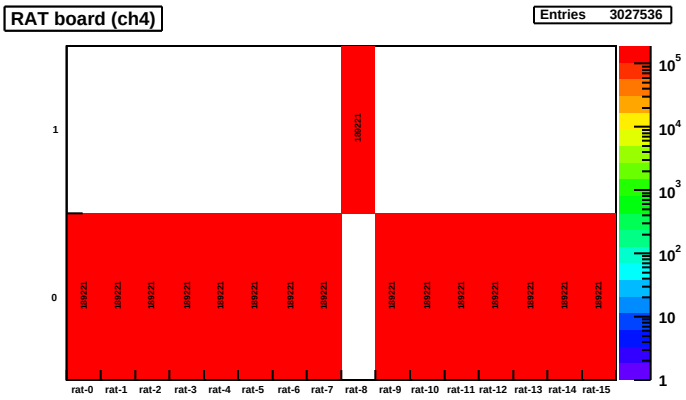

Hybrid Jet Patches
Entries 378442
JP ADC
JP ID

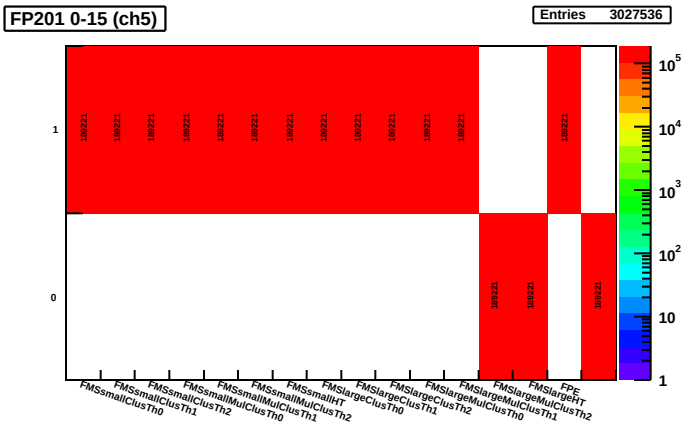

TF201 0-15 (ch0)
Entries 3027536
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

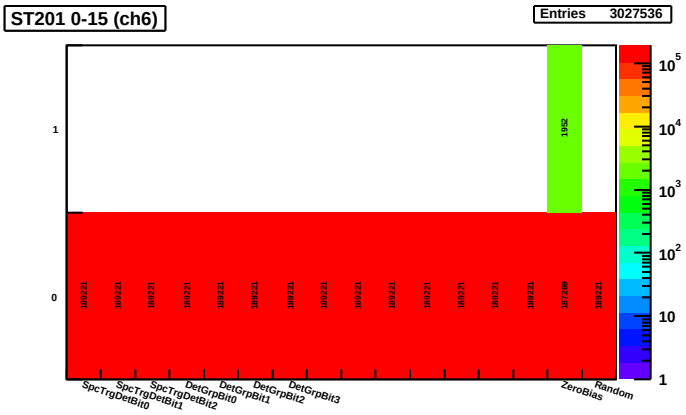

VT201 0-15 (ch1)
Entries 3027536
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

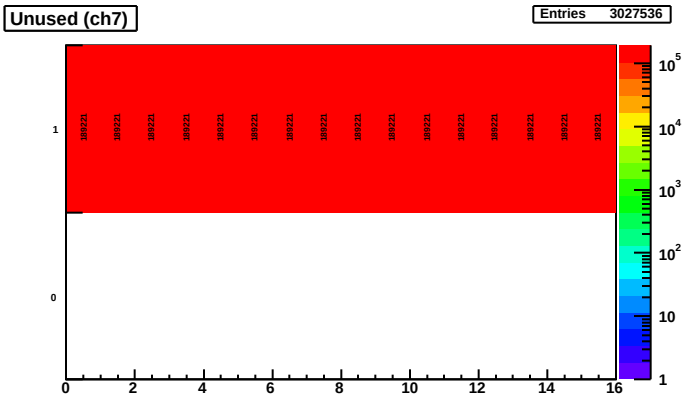

Unused (ch2)
Entries 3027536

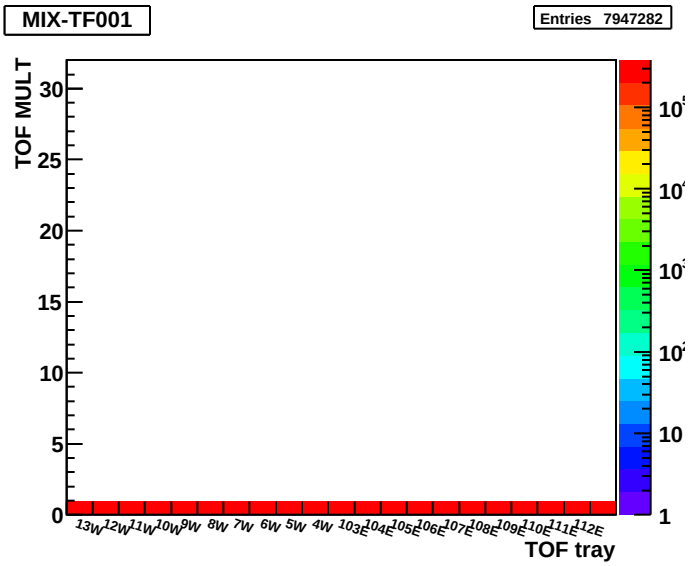

EM201 0-15 (ch3)
Entries 3027536
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

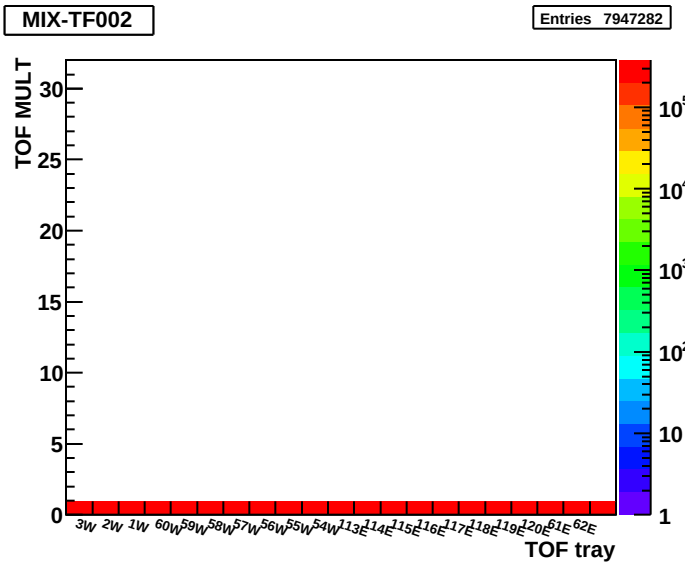

RAT board (ch4)
Entries 3027536
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

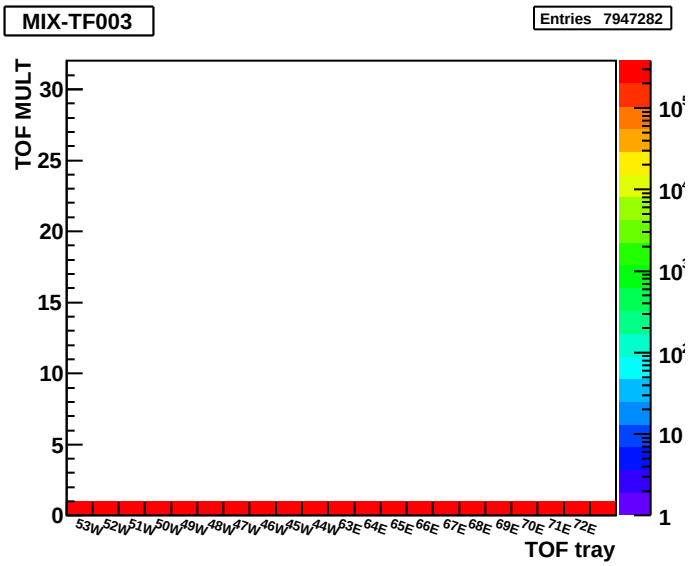

FP201 0-15 (ch5)
Entries 3027536
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

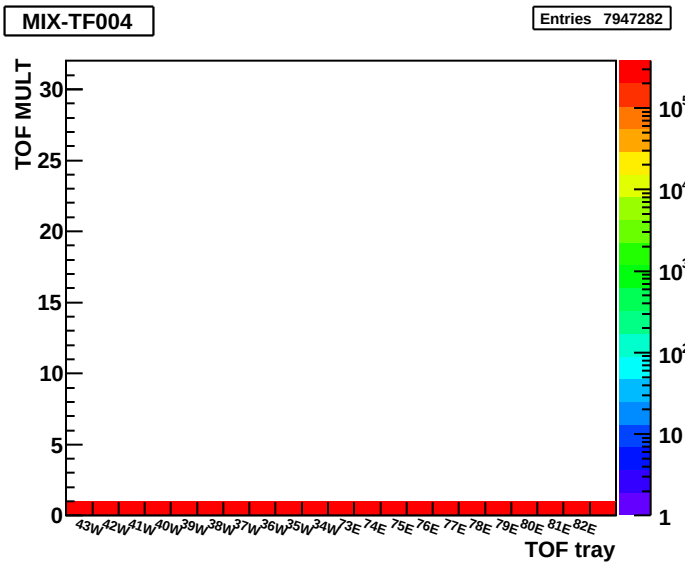

ST201 0-15 (ch6)
Entries 3027536
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

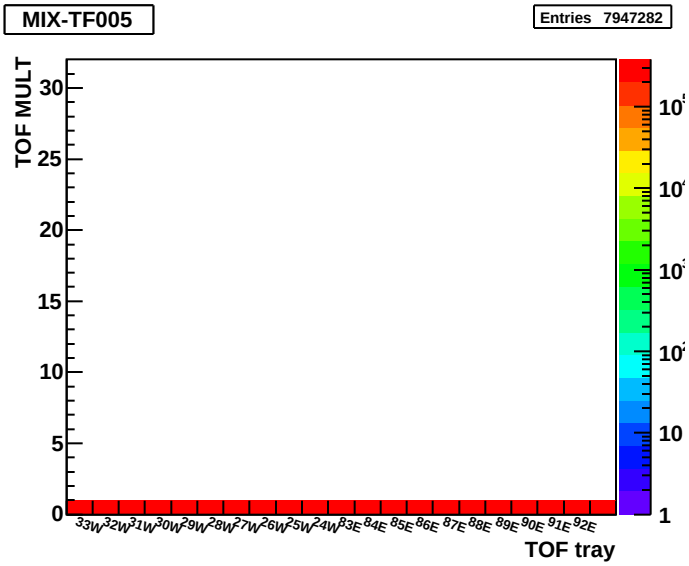

Unused (ch7)
Entries 3027536

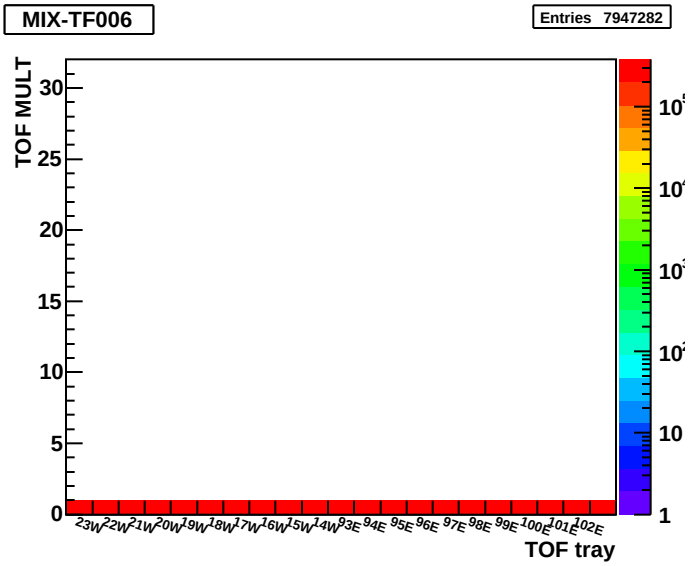

MIX-TF001
Entries 7947282
TOF MULT
TOF tray

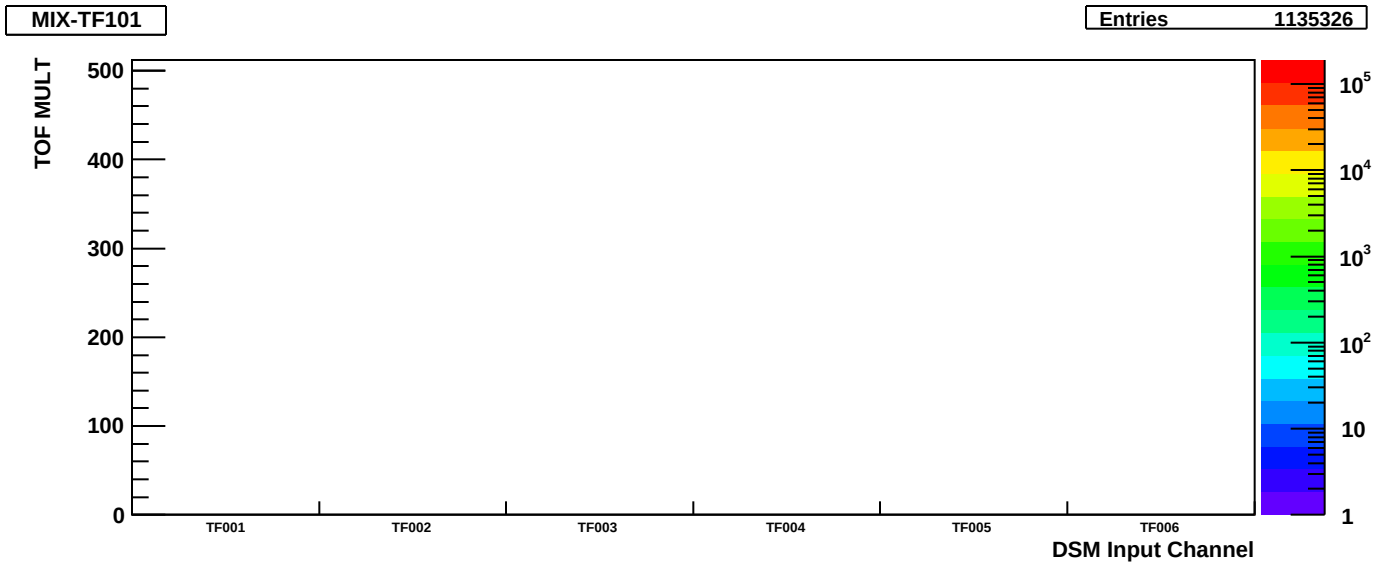

MIX-TF002
Entries 7947282
TOF MULT
TOF tray

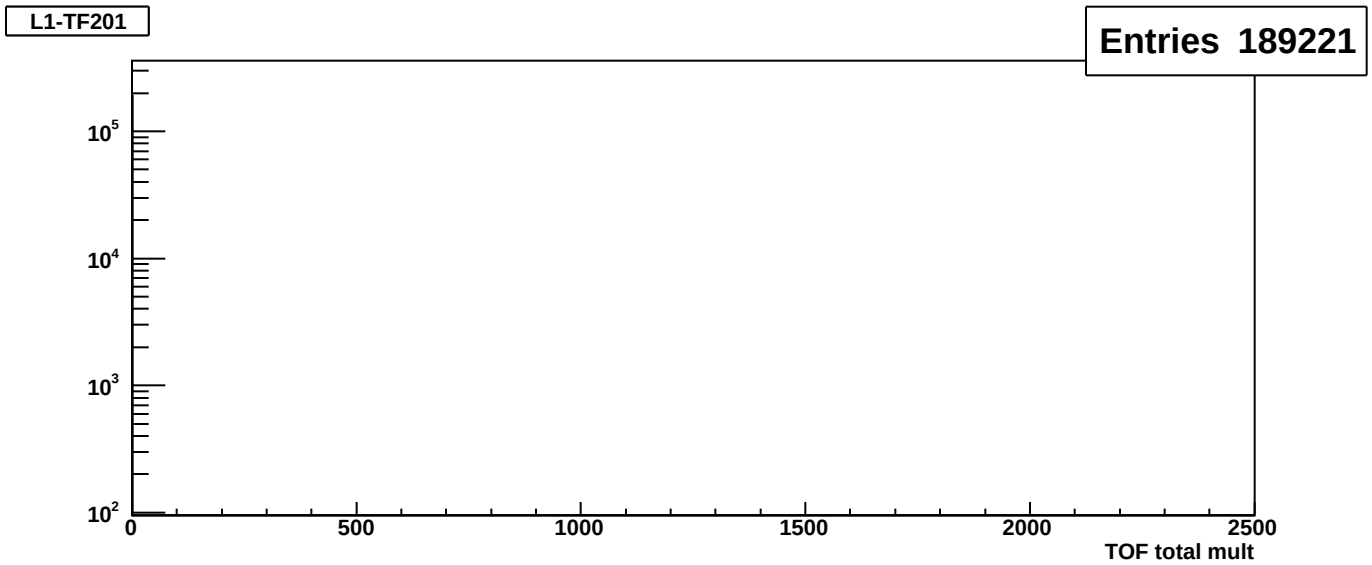

MIX-TF003
Entries 7947282
TOF MULT
TOF tray

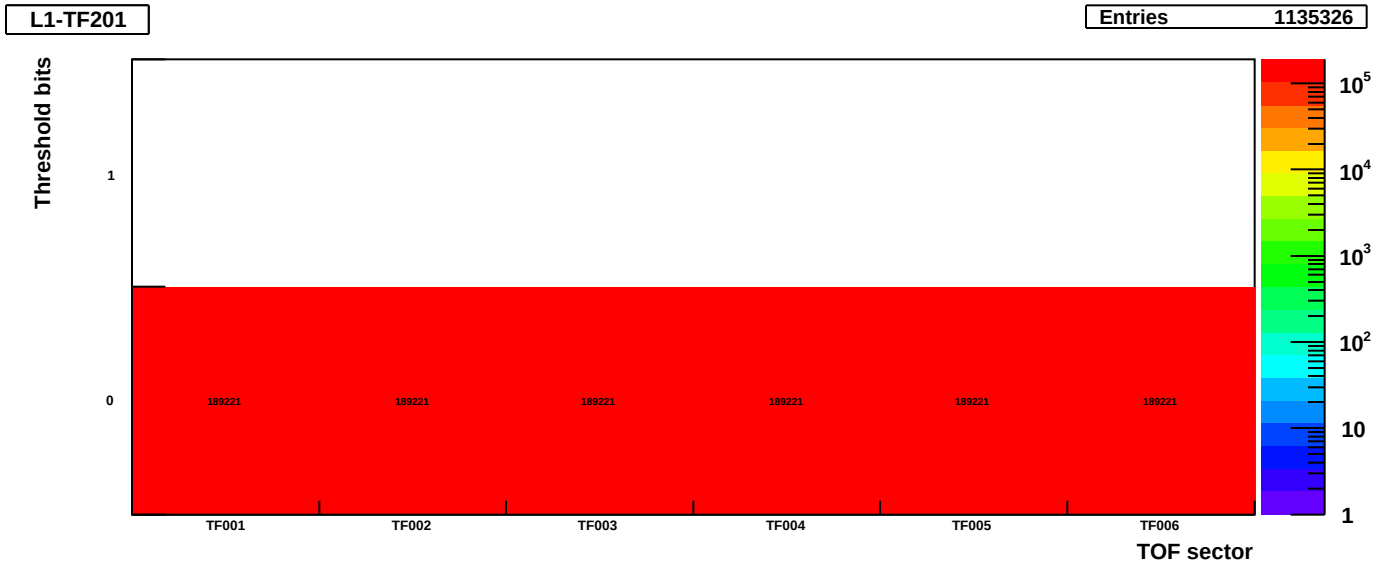

MIX-TF004
Entries 7947282
TOF MULT
TOF tray

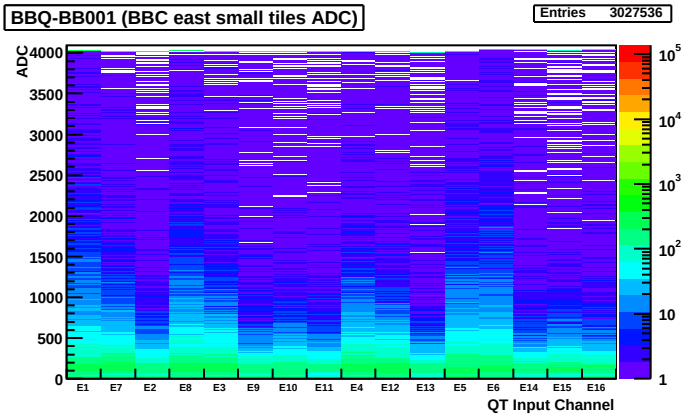

MIX-TF005
Entries 7947282
TOF MULT
TOF tray

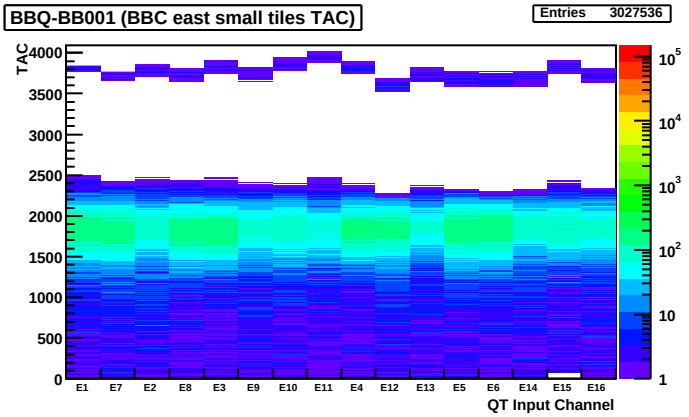

MIX-TF006
Entries 7947282
TOF MULT
TOF tray

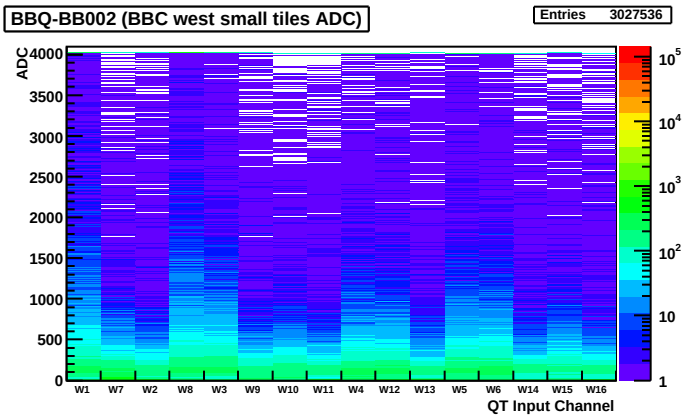

MIX-TF101
Entries 1135326
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

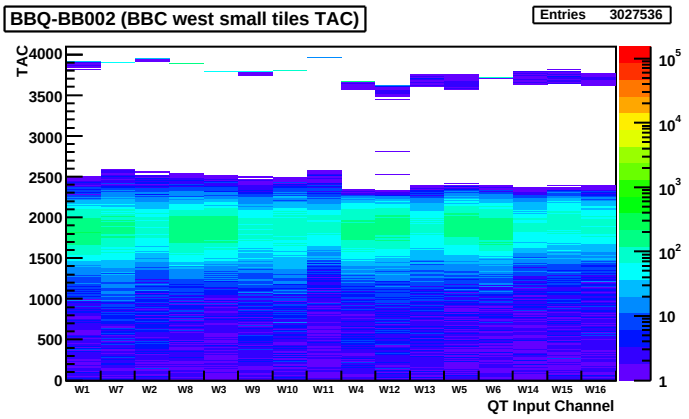

L1-TF201
Entries 189221
10^5
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

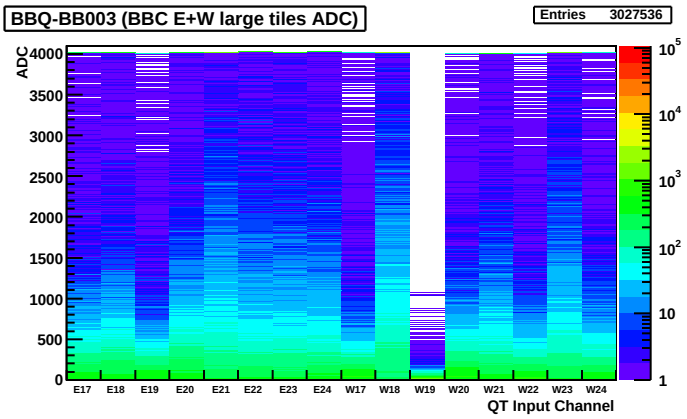

L1-TF201
Entries 1135326
Threshold bits
1
0
189221
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

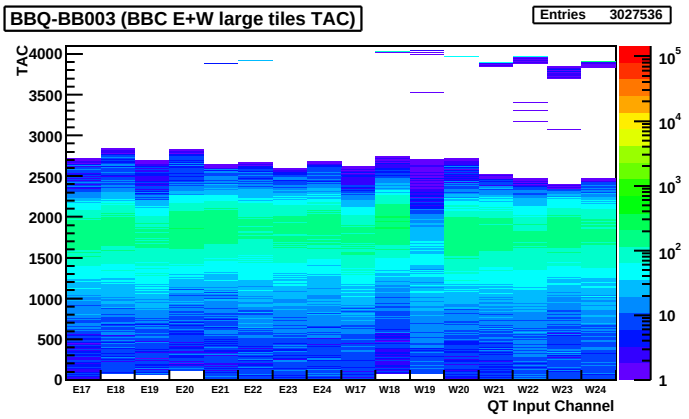

BBQ-BB001 (BBC east small tiles ADC)
Entries 3027536
ADC
QT Input Channel

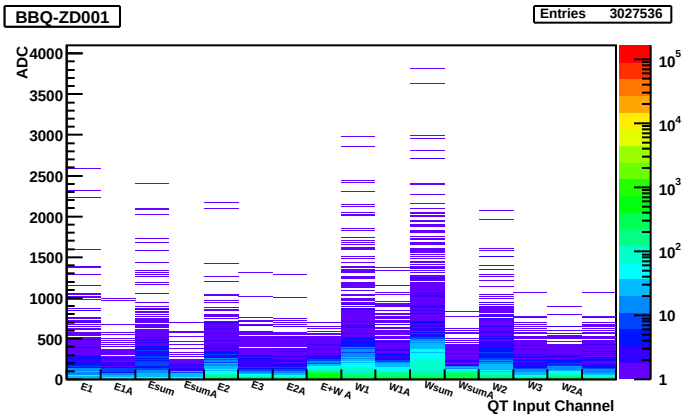

BBQ-BB001 (BBC east small tiles TAC)
Entries 3027536
TAC
QT Input Channel

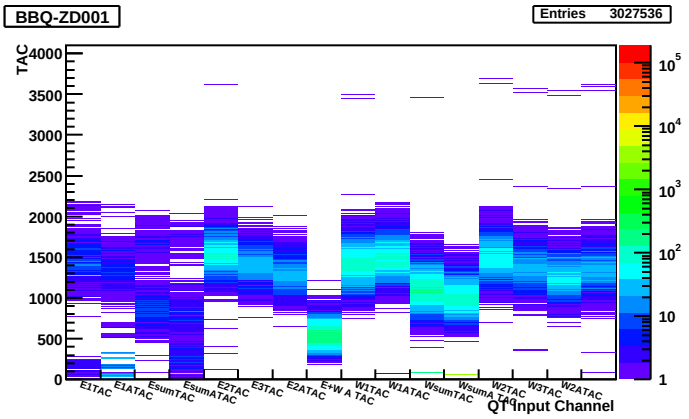

BBQ-BB002 (BBC west small tiles ADC)
Entries 3027536
ADC
QT Input Channel

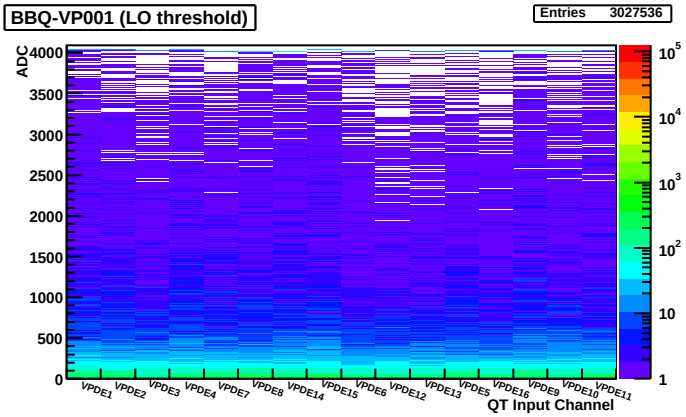

BBQ-BB002 (BBC west small tiles TAC)
Entries 3027536
TAC
QT Input Channel

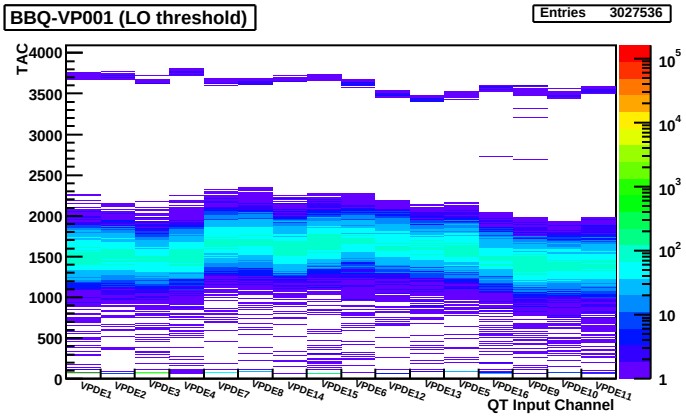

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3027536
ADC
QT Input Channel

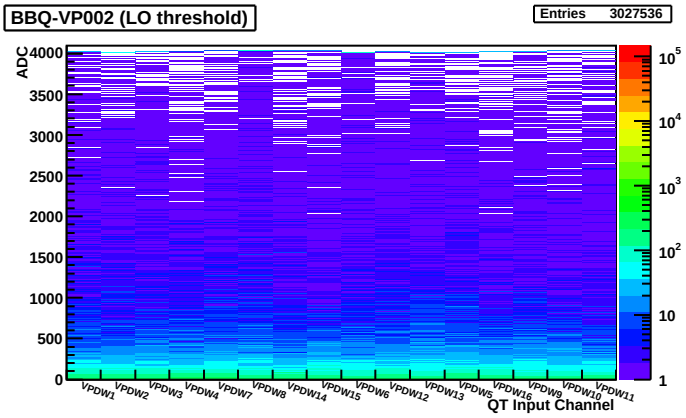

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3027536
TAC
QT Input Channel

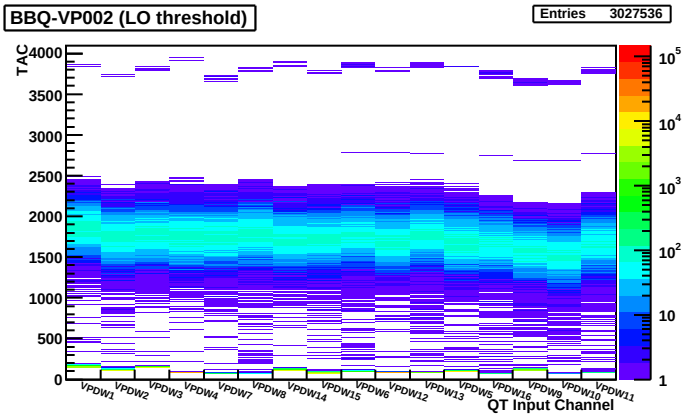

BBQ-ZD001
Entries 3027536
ADC
QT Input Channel

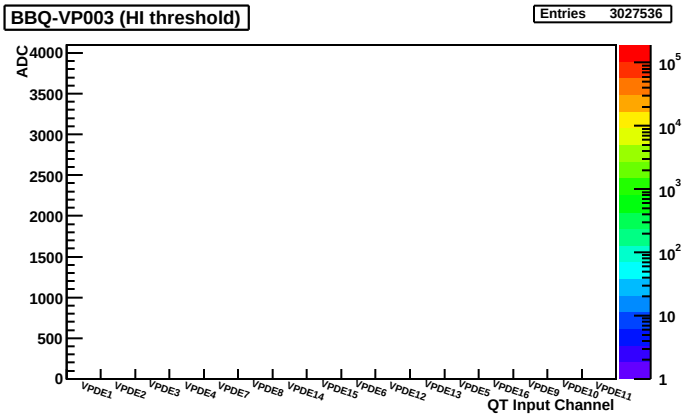

BBQ-ZD001
Entries 3027536
TAC
QT Input Channel

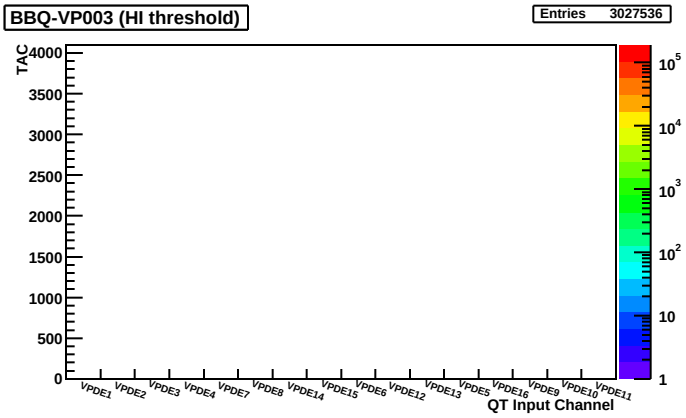

BBQ-VP001 (LO threshold)
Entries 3027536
ADC
QT Input Channel

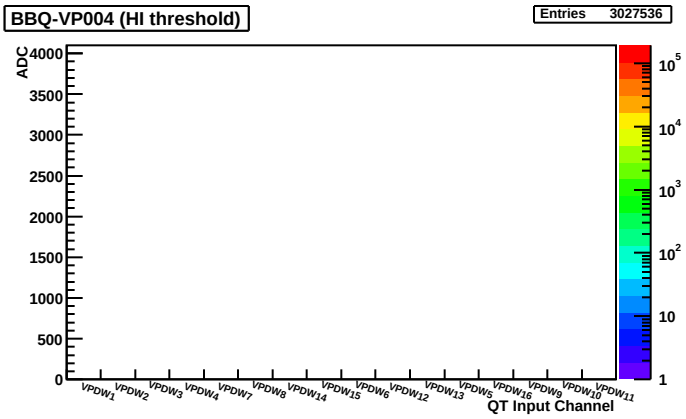

BBQ-VP001 (LO threshold)
Entries 3027536
TAC
QT Input Channel

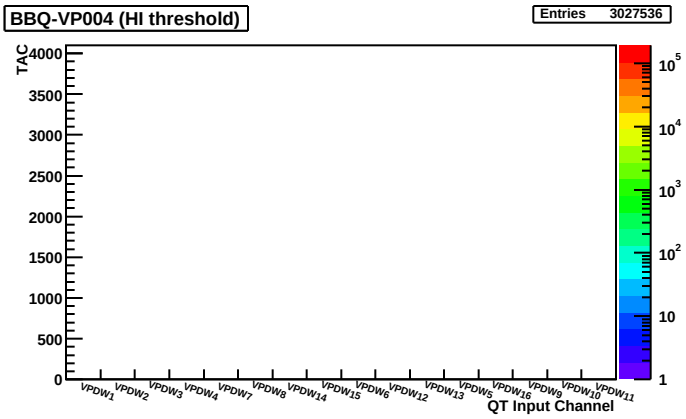

BBQ-VP002 (LO threshold)
Entries 3027536
ADC
QT Input Channel

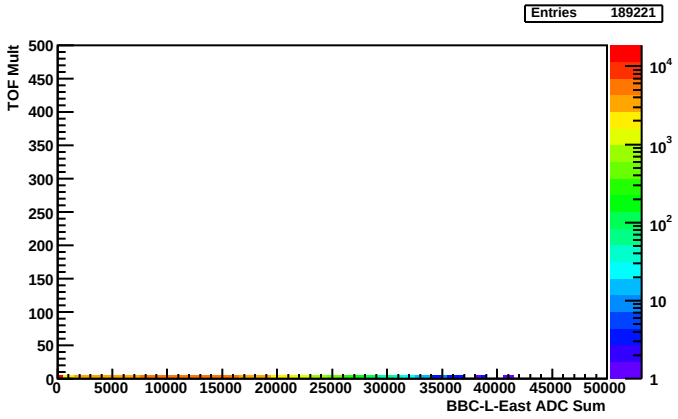

BBQ-VP002 (LO threshold)
Entries 3027536
TAC
QT Input Channel

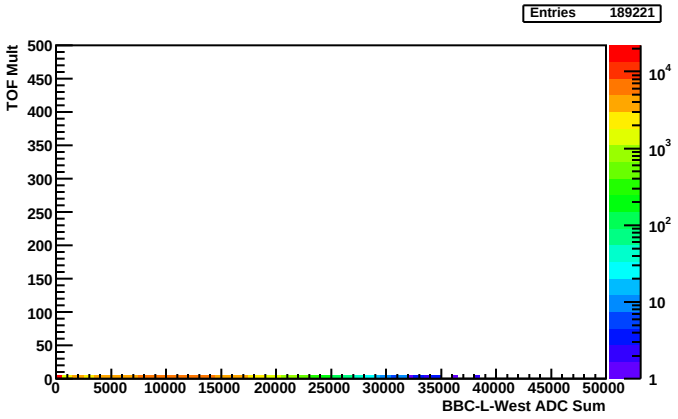

BBQ-VP003 (HI threshold)
Entries 3027536
ADC
QT Input Channel

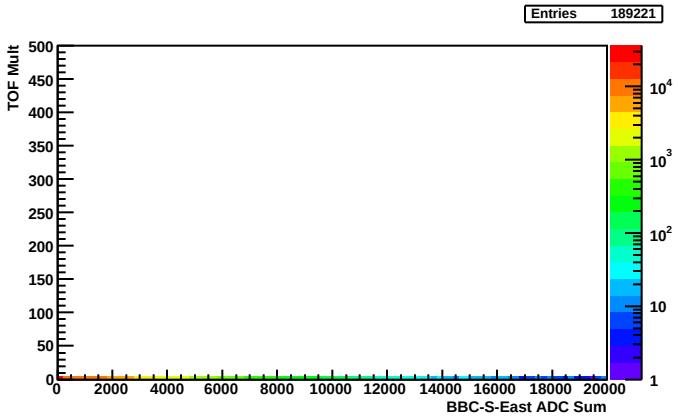

BBQ-VP003 (HI threshold)
Entries 3027536
TAC
QT Input Channel

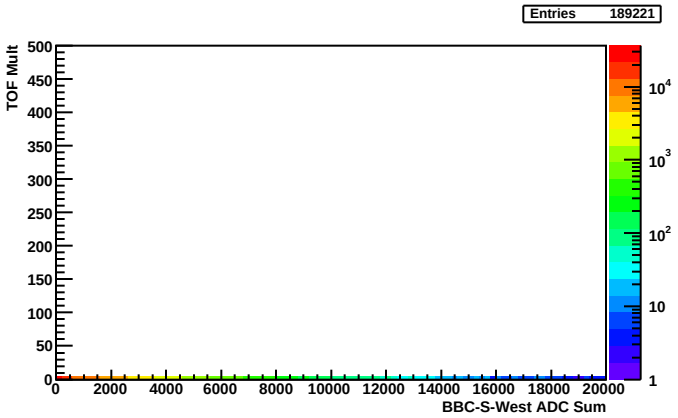

BBQ-VP004 (HI threshold)
Entries 3027536
ADC
QT Input Channel

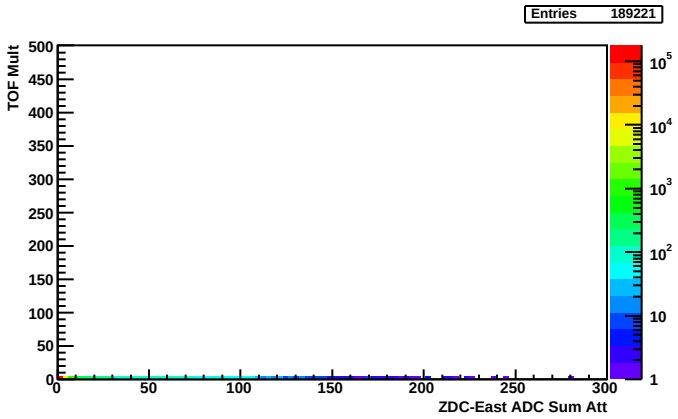

BBQ-VP004 (HI threshold)
Entries 3027536
TAC
QT Input Channel

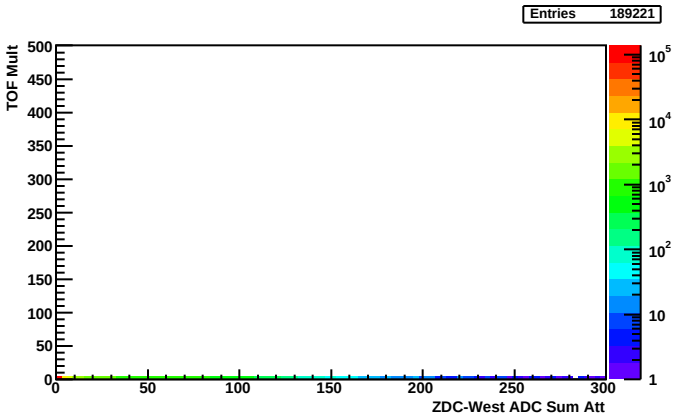

Entries 189221
TOF Mult
BBC-L-East ADC Sum

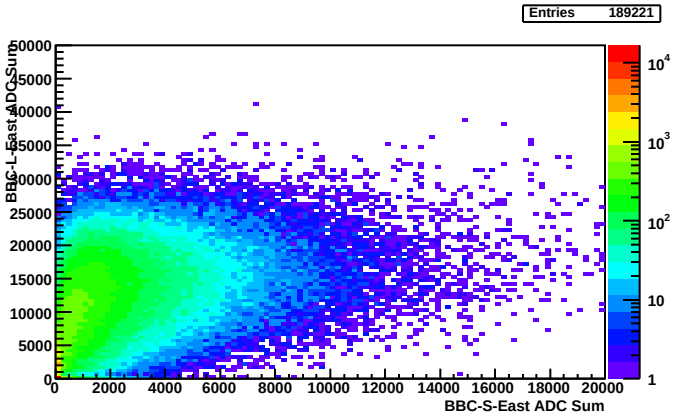

Entries 189221
TOF Mult
BBC-L-West ADC Sum

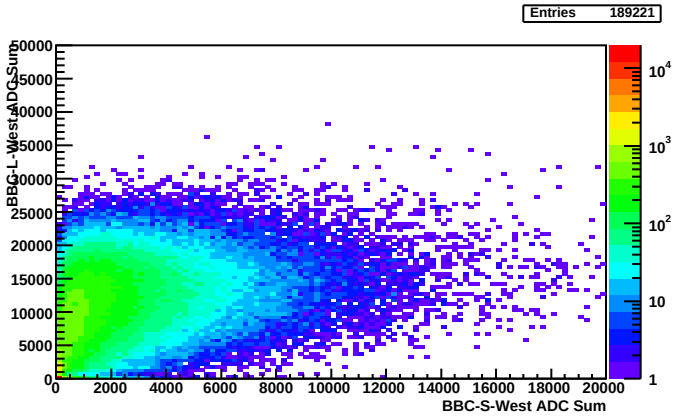

Entries 189221
TOF Mult
BBC-S-East ADC Sum

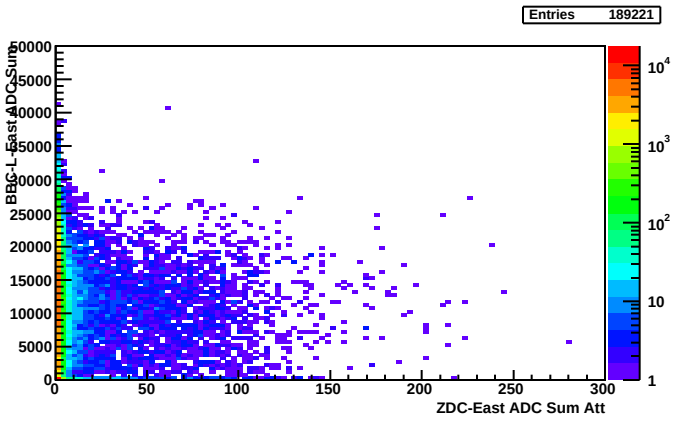

Entries 189221
TOF Mult
BBC-S-West ADC Sum

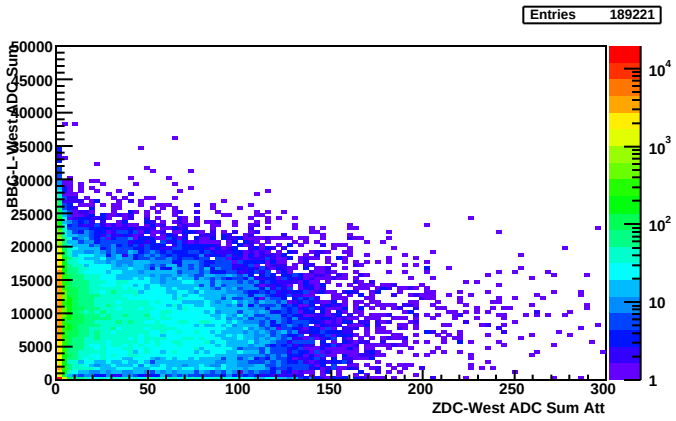

Entries 189221
TOF Mult
ZDC-East ADC Sum Att

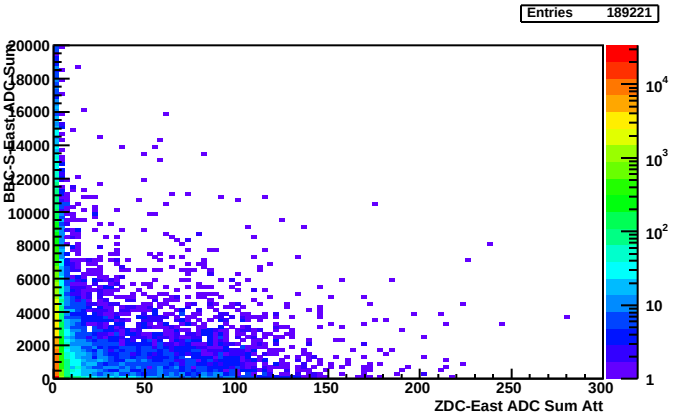

Entries 189221
TOF Mult
ZDC-West ADC Sum Att

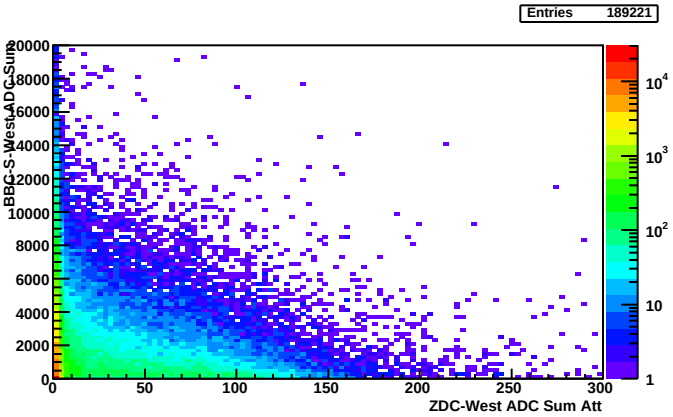

Entries 189221
BBC-L-East ADC Sum
BBC-S-East ADC Sum

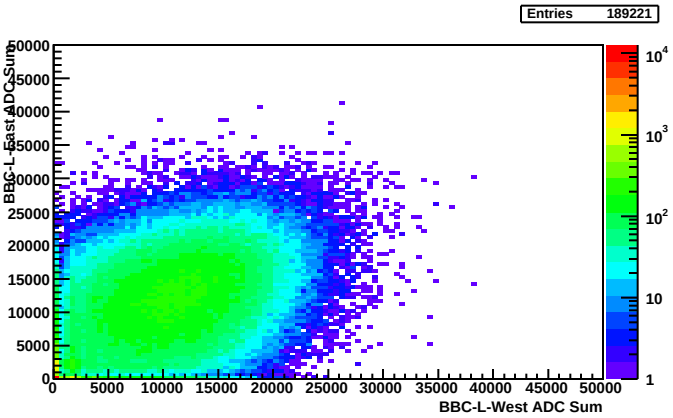

Entries 189221
BBC-L-West ADC Sum
BBC-S-West ADC Sum

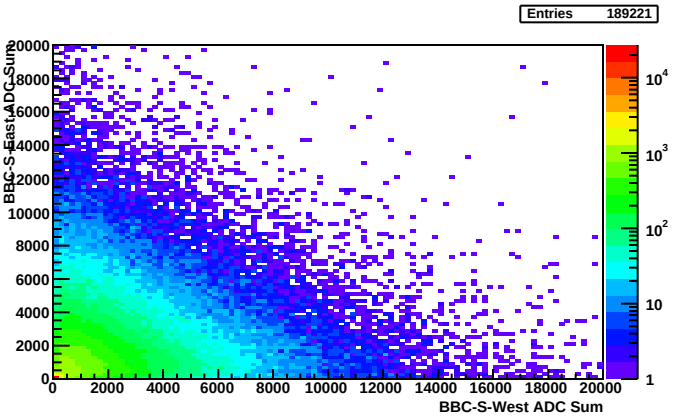

Entries 189221
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

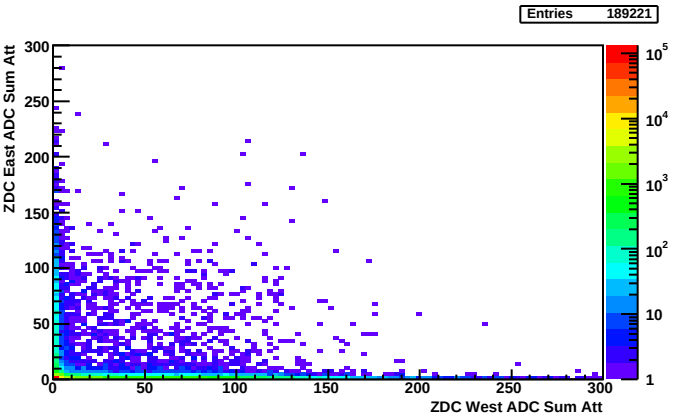

Entries 189221
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

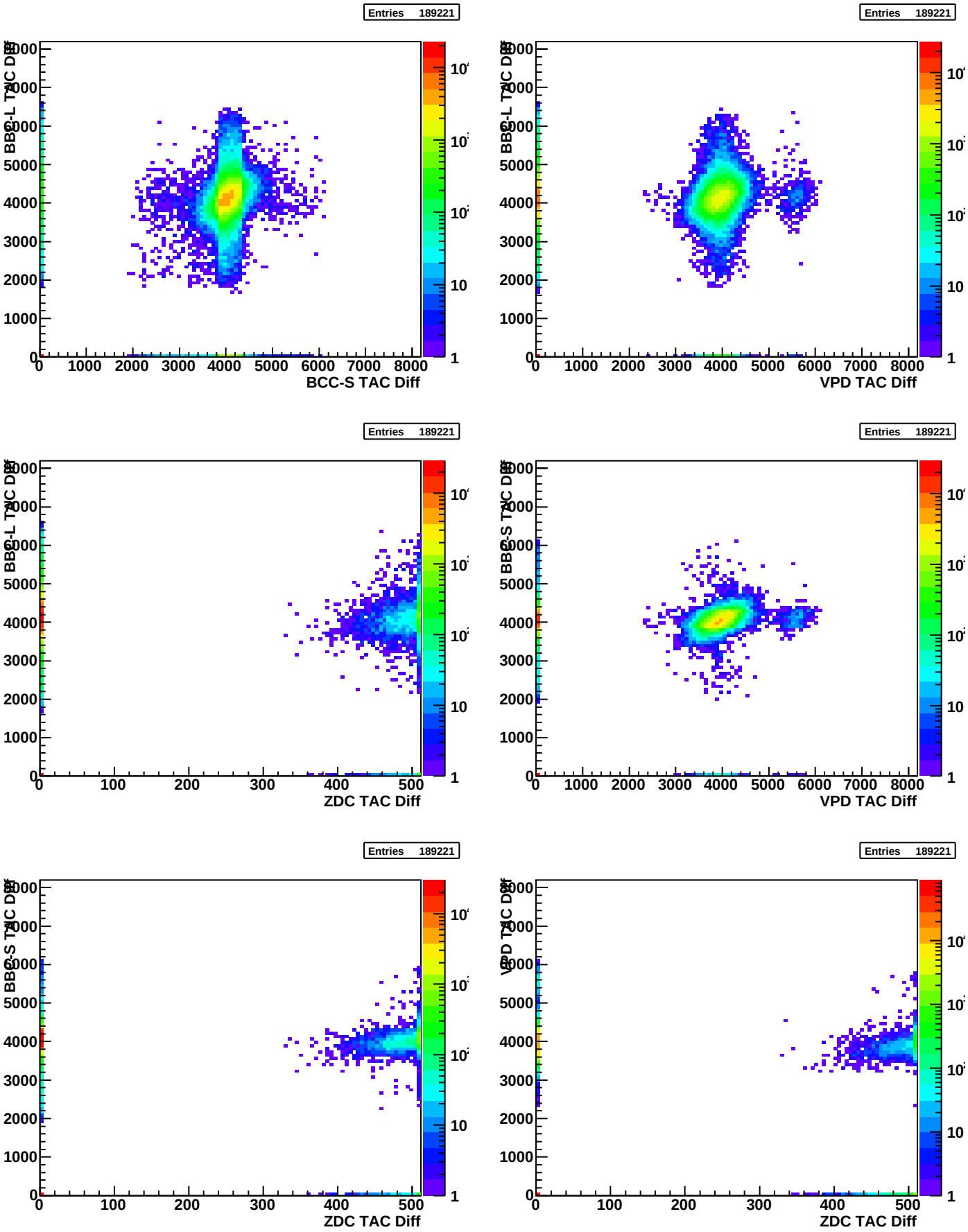

Entries 189221
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

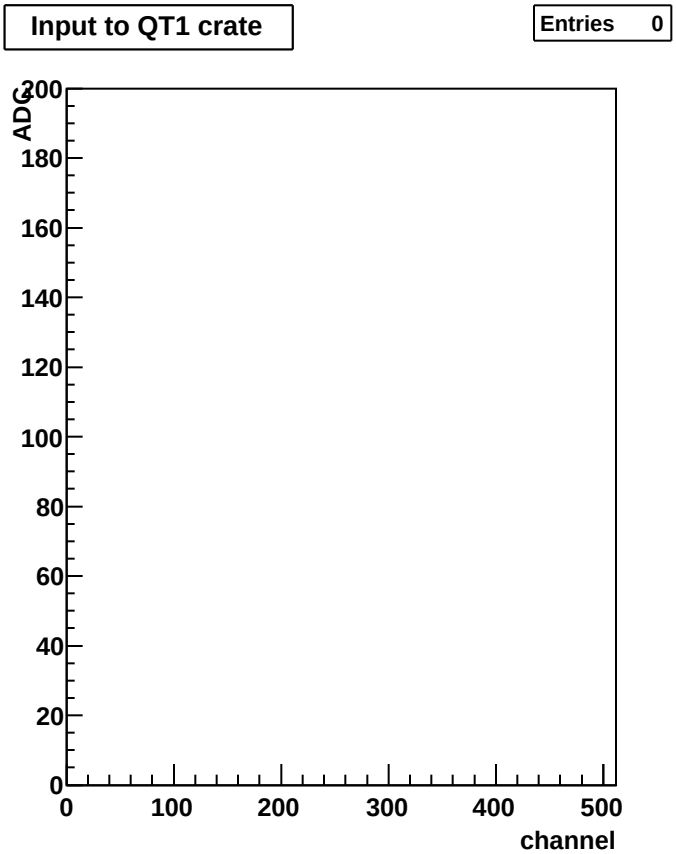

Entries 189221
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

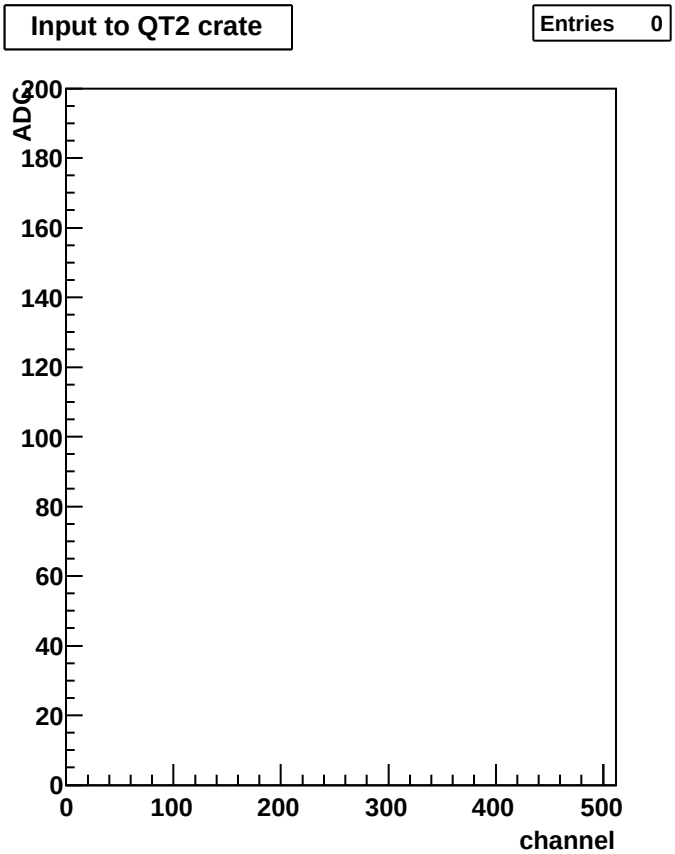

Entries 189221
BBC-L-East ADC Sum
BBC-L-West ADC Sum

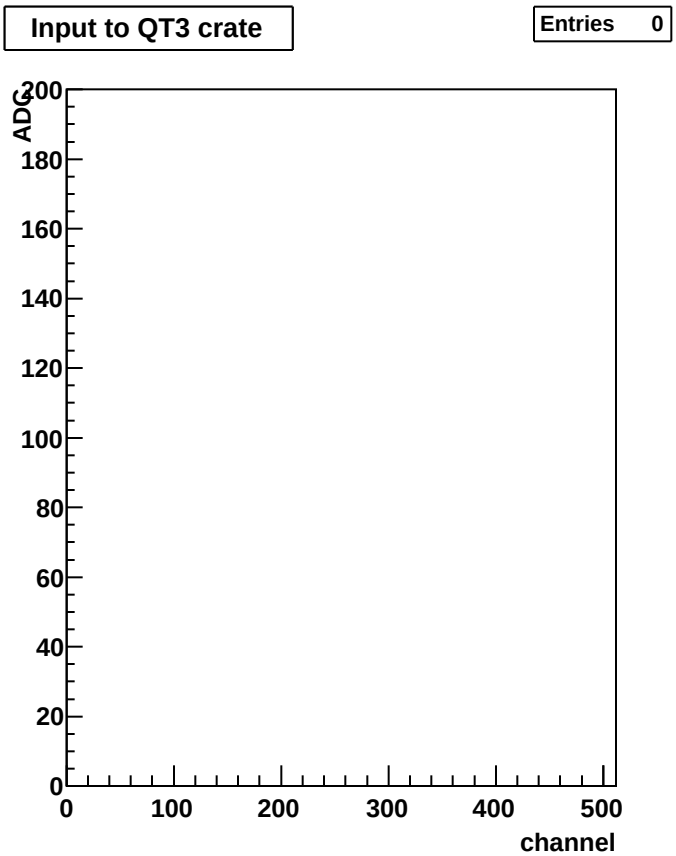

Entries 189221
BBC-S-East ADC Sum
BBC-S-West ADC Sum

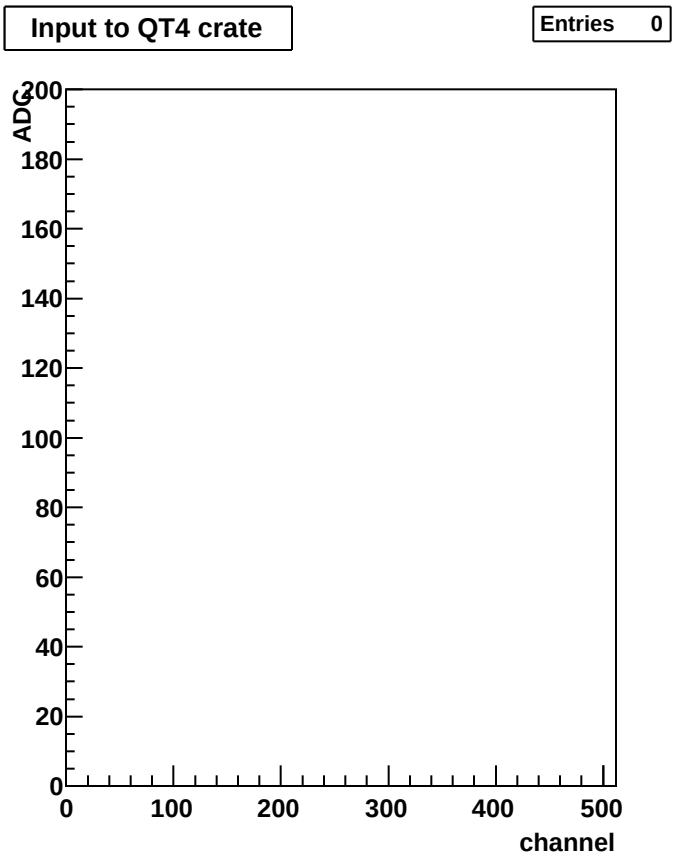

Entries 189221
ZDC East ADC Sum Att
ZDC West ADC Sum Att

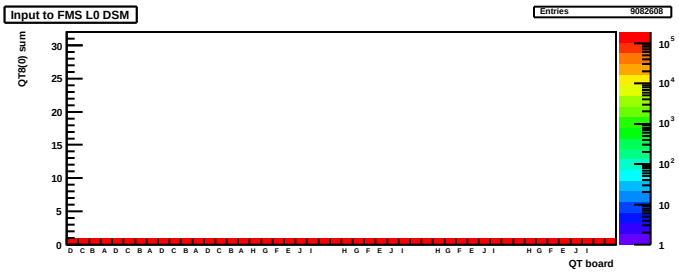

Entries 189221
BBC-L TAC Diff
BCC-S TAC Diff
Entries 189221
BBC-L TAC Diff
VPD TAC Diff
Entries 189221
BBC-L TAC Diff
ZDC TAC Diff
Entries 189221
BBC-S TAC Diff
VPD TAC Diff
Entries 189221
BBC-S TAC Diff
ZDC TAC Diff
Entries 189221
VPD TAC Diff
ZDC TAC Diff

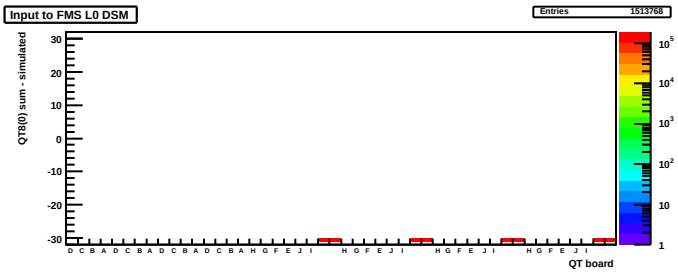

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

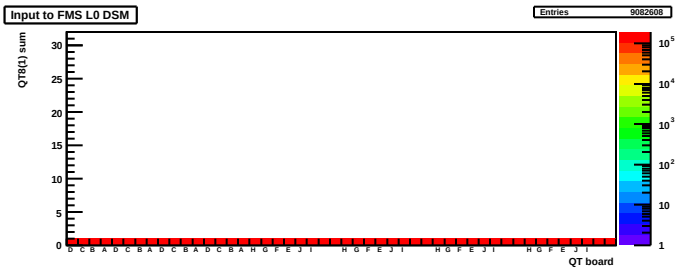

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

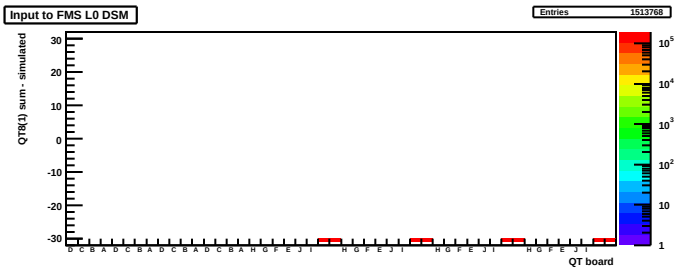

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

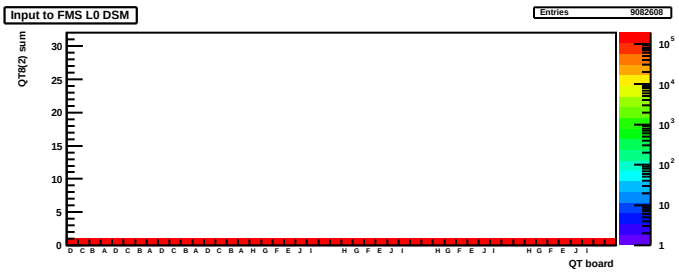

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

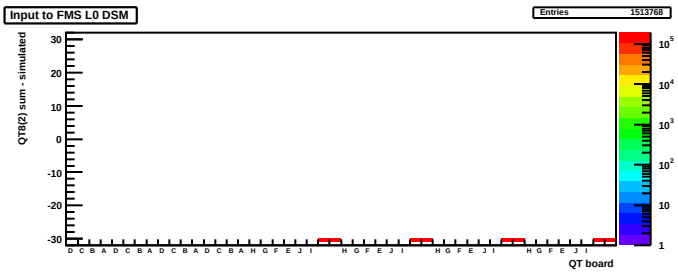

Input to FMS L0 DSM
Entries 9082608
QT8(0) sum
QT board

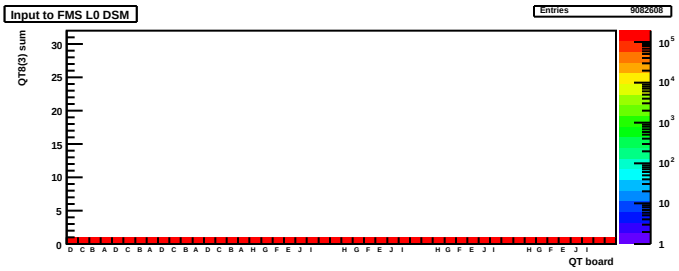

Input to FMS L0 DSM
Entries 1513768
QT8(0) sum - simulated
QT board

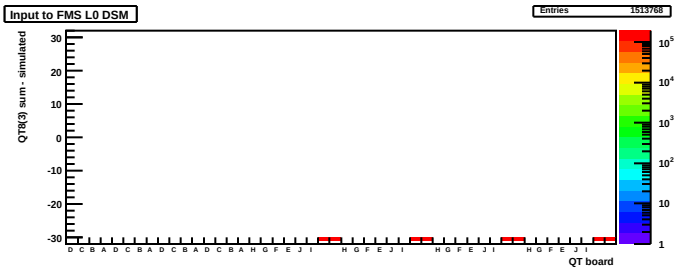

Input to FMS L0 DSM
Entries 9082608
QT8(1) sum
QT board

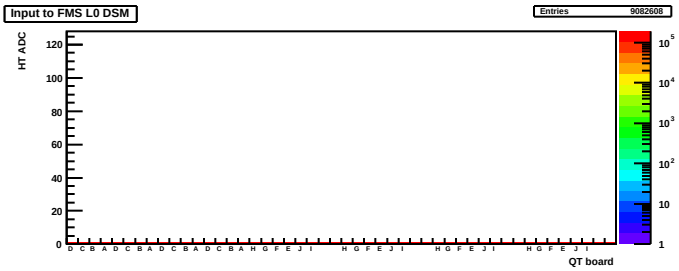

Input to FMS L0 DSM
Entries 1513768
QT8(1) sum - simulated
QT board

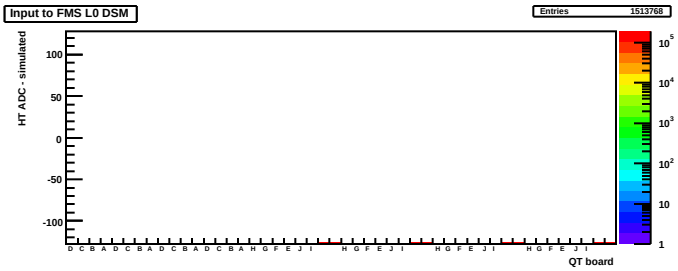

Input to FMS L0 DSM
Entries 9082608
QT8(2) sum
QT board

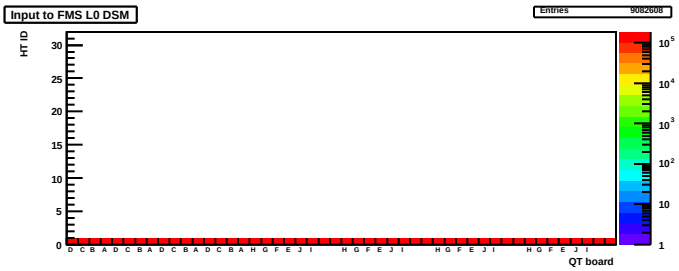

Input to FMS L0 DSM
Entries 1513768
QT8(2) sum - simulated
QT board

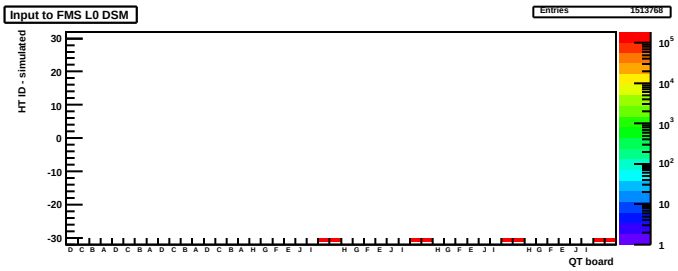

Input to FMS L0 DSM
Entries 9082608
QT8(3) sum
QT board

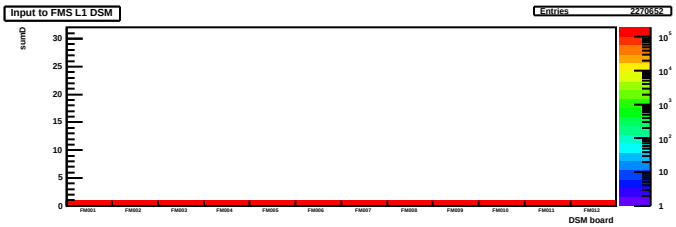

Input to FMS L0 DSM
Entries 1513768
QT8(3) sum - simulated
QT board

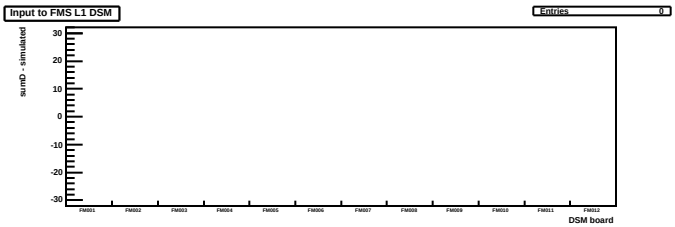

Input to FMS L0 DSM
Entries 9082608
HT ADC
QT board

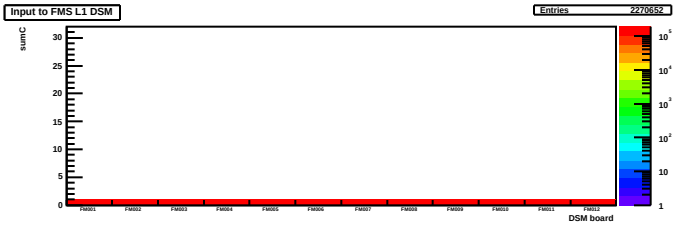

Input to FMS L0 DSM
Entries 1513768
HT ADC - simulated
QT board

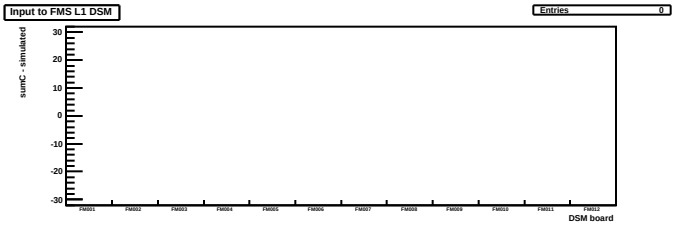

Input to FMS L0 DSM
Entries 9082608
HT ID
QT board

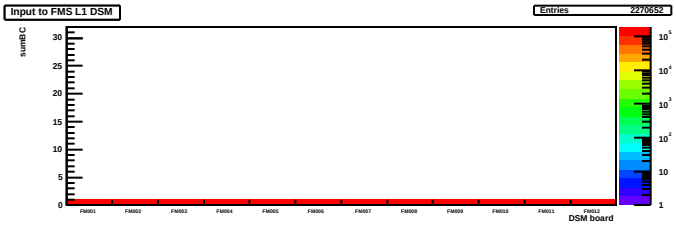

Input to FMS L0 DSM
Entries 1513768
HT ID - simulated
QT board

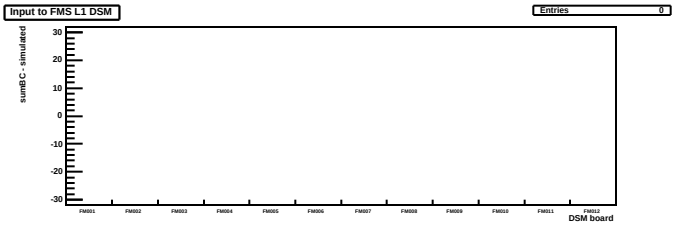

Input to FMS L1 DSM
Entries 2270652
sumD
DSM board

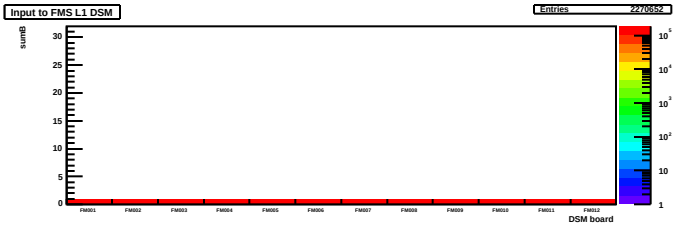

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

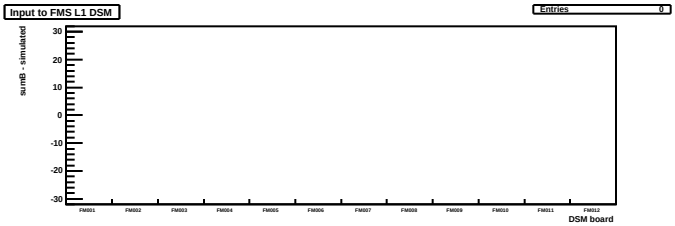

Input to FMS L1 DSM
Entries 2270652
sumC
DSM board

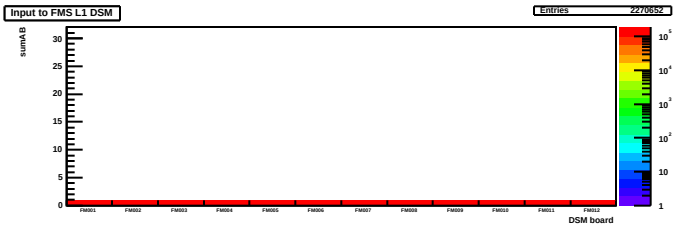

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

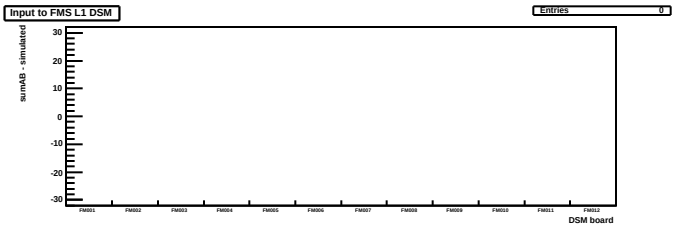

Input to FMS L1 DSM
Entries 2270652
sumBC
DSM board

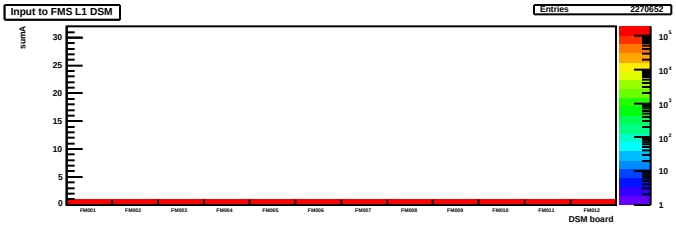

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

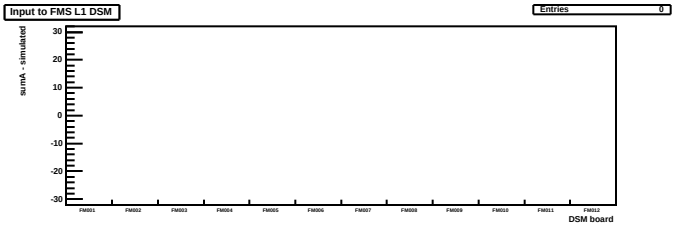

Input to FMS L1 DSM
Entries 2270652
sumB
DSM board

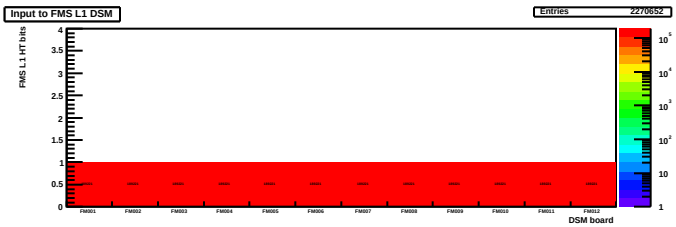

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

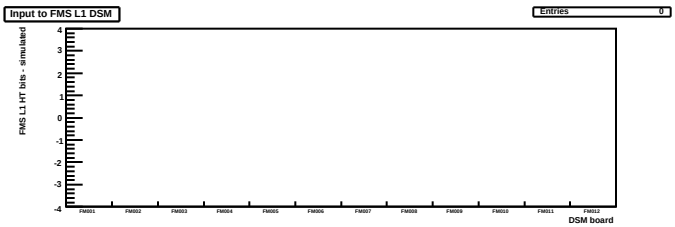

Input to FMS L1 DSM
Entries 2270652
sumAB
DSM board

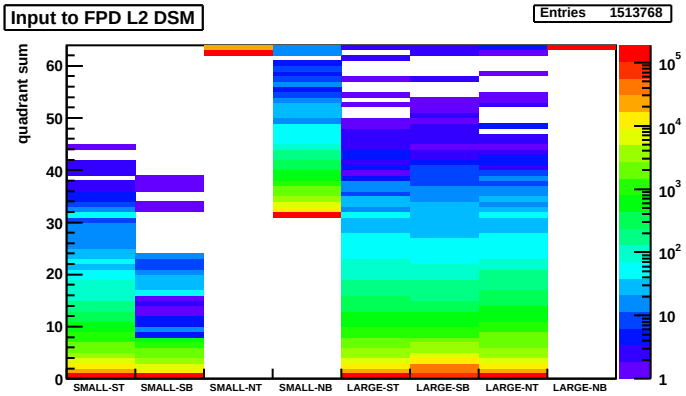

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

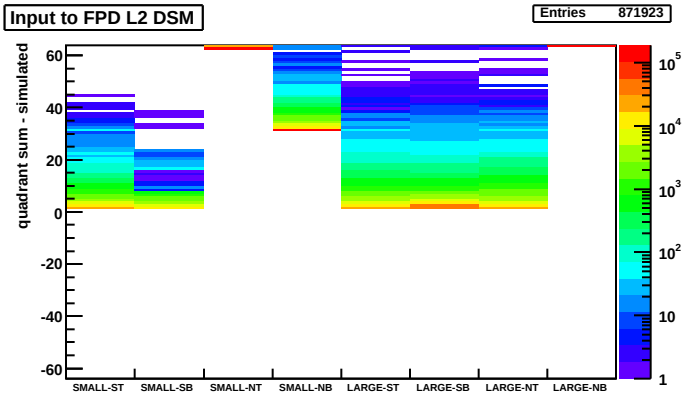

Input to FMS L1 DSM
Entries 2270652
sumA
DSM board

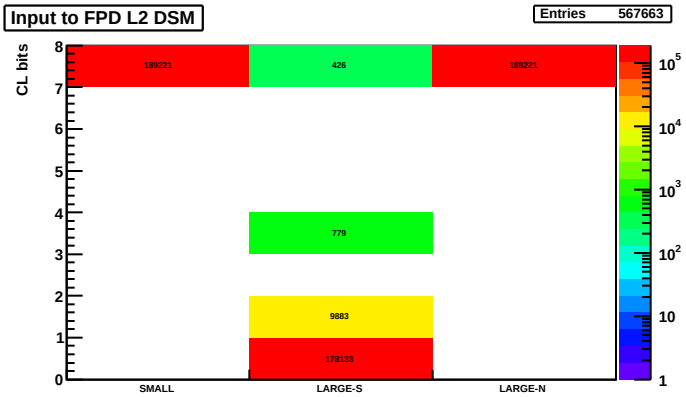

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

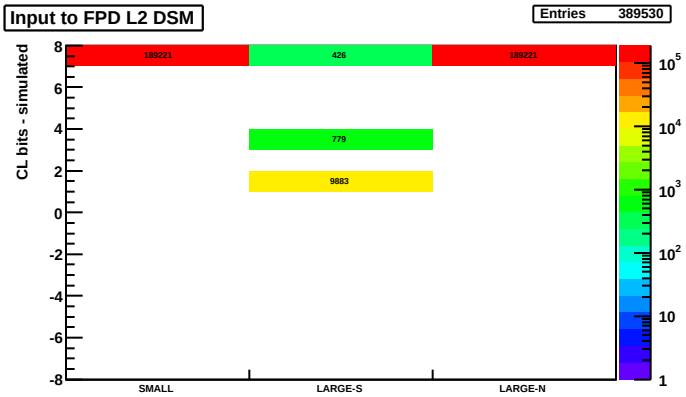

Input to FMS L1 DSM
Entries 2270652
FMS L1 HT bits
DSM board

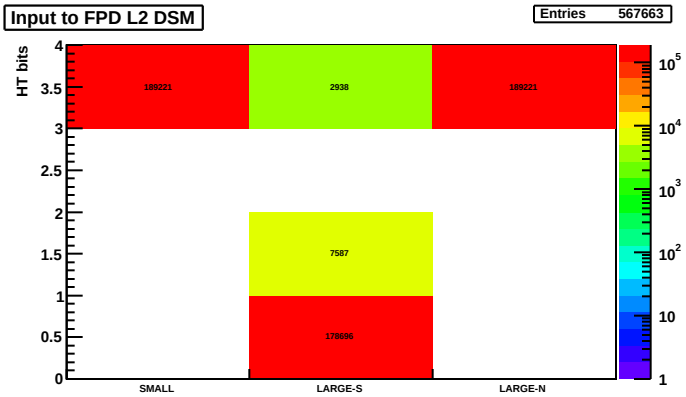

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

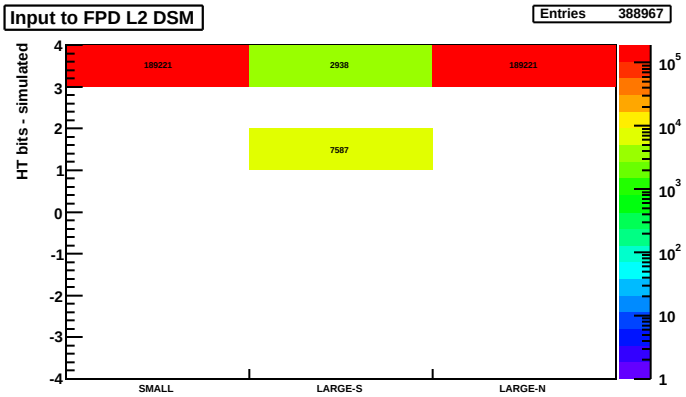

Input to FPD L2 DSM
Entries 1513768
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

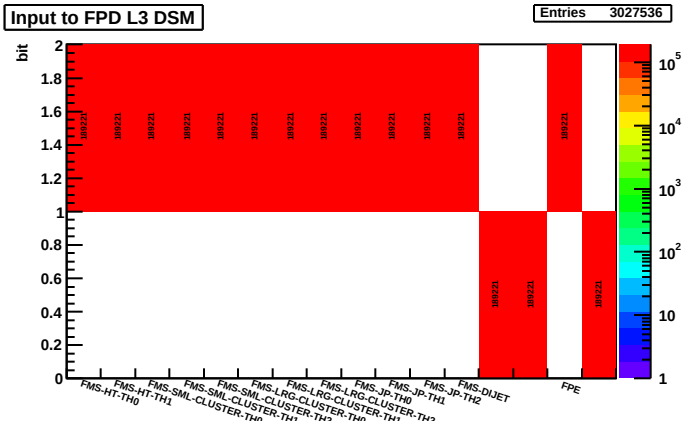

Input to FPD L2 DSM
Entries 871923
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

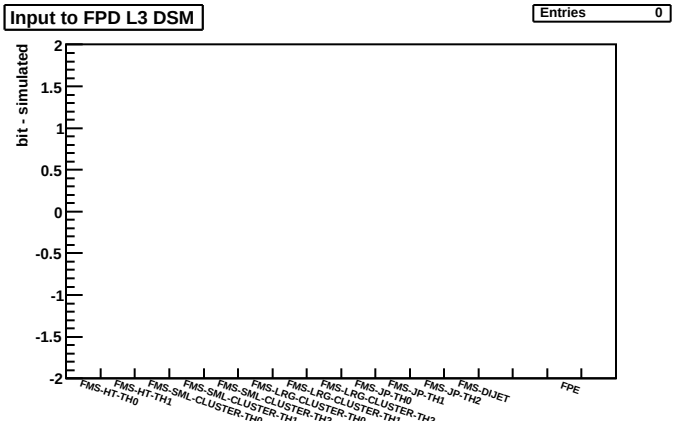

Input to FPD L2 DSM
Entries 567663
CL bits
189221
426
189221
779
9883
178133
SMALL
LARGE-S
LARGE-N

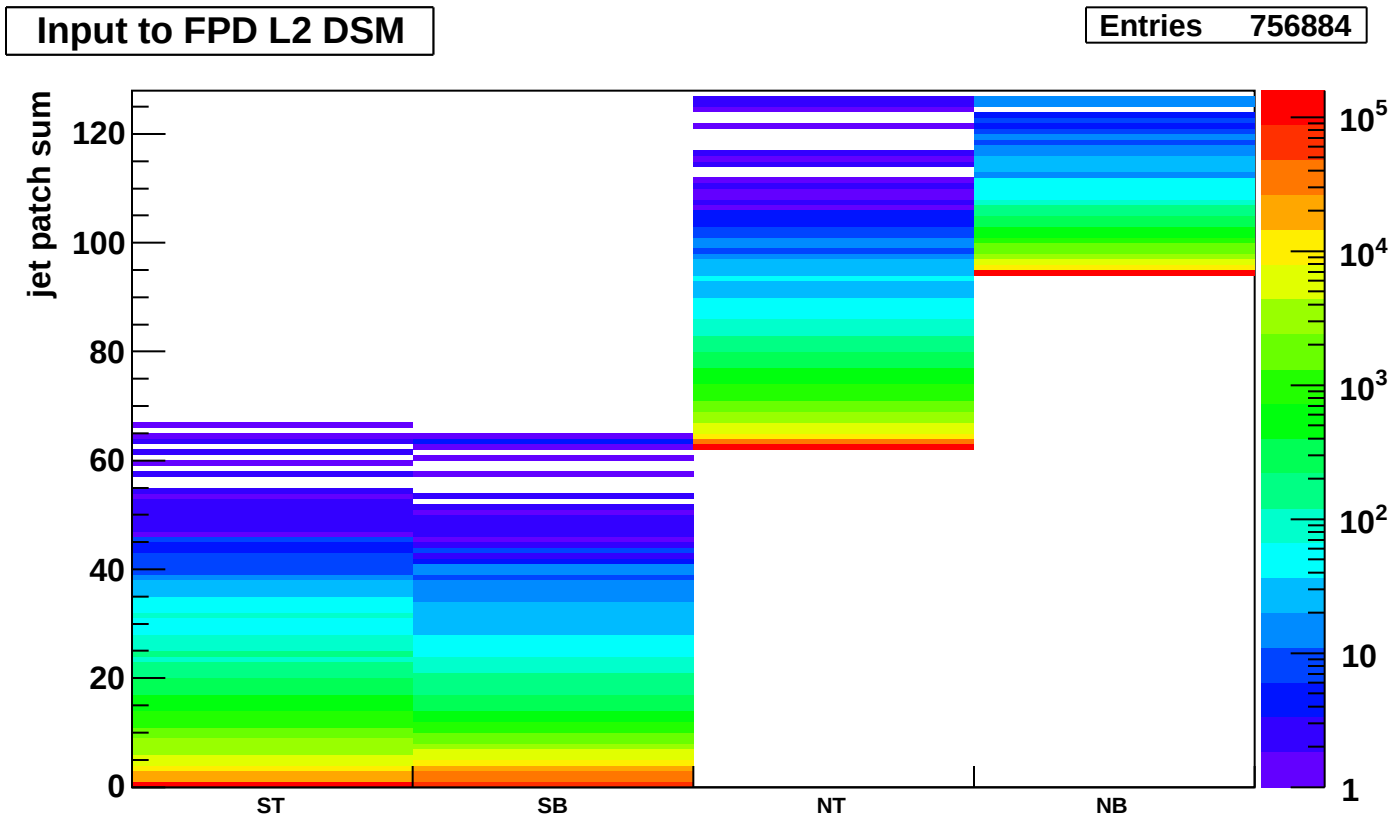

Input to FPD L2 DSM
Entries 389530
CL bits - simulated
189221
426
189221
779
9883
SMALL
LARGE-S
LARGE-N

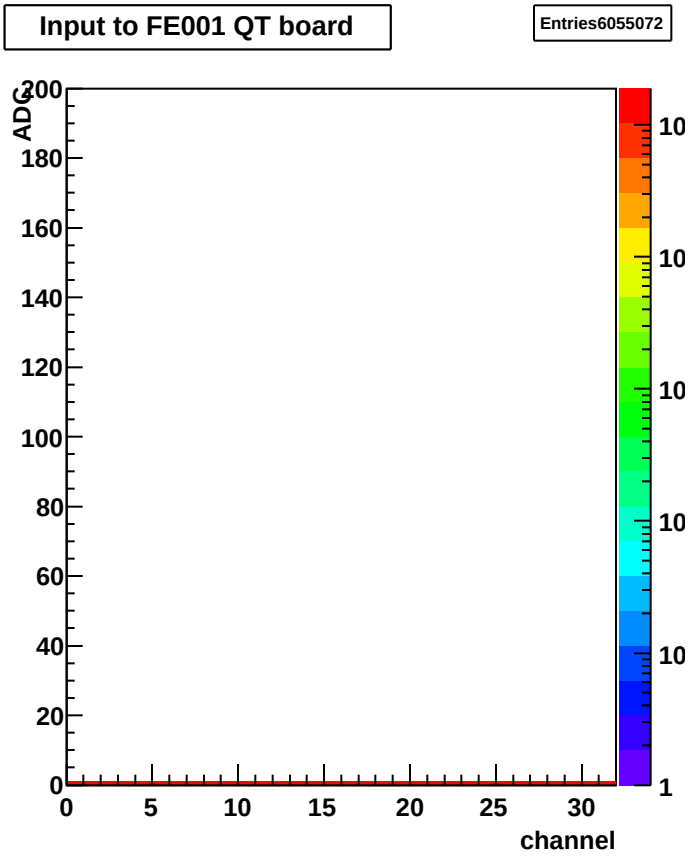

Input to FPD L2 DSM
Entries 567663
HT bits
189221
2938
189221
7587
178696
SMALL
LARGE-S
LARGE-N

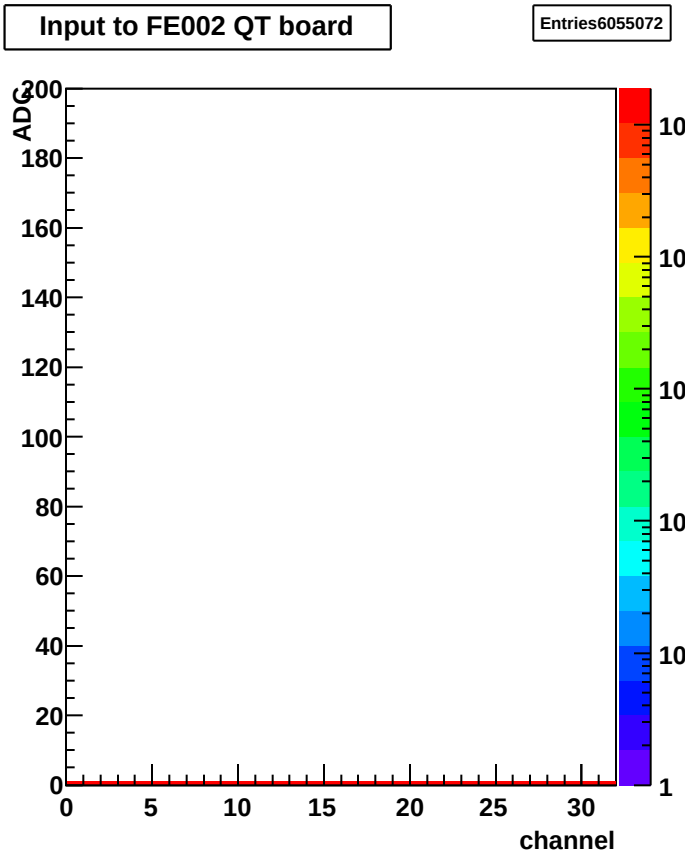

Input to FPD L2 DSM
Entries 388967
HT bits - simulated
189221
2938
189221
7587
SMALL
LARGE-S
LARGE-N

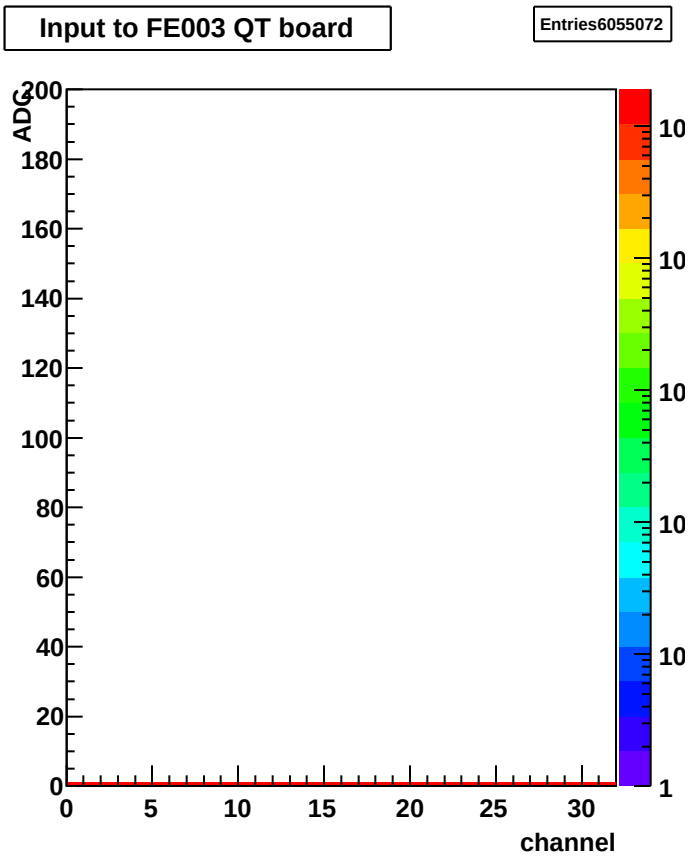

Input to FPD L3 DSM
Entries 3027536
bit
189221
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

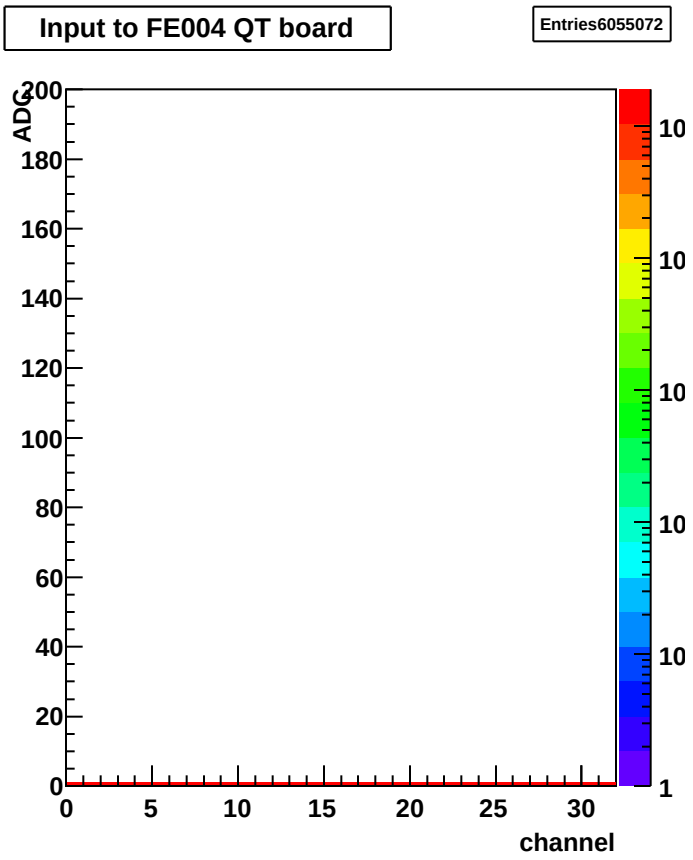

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

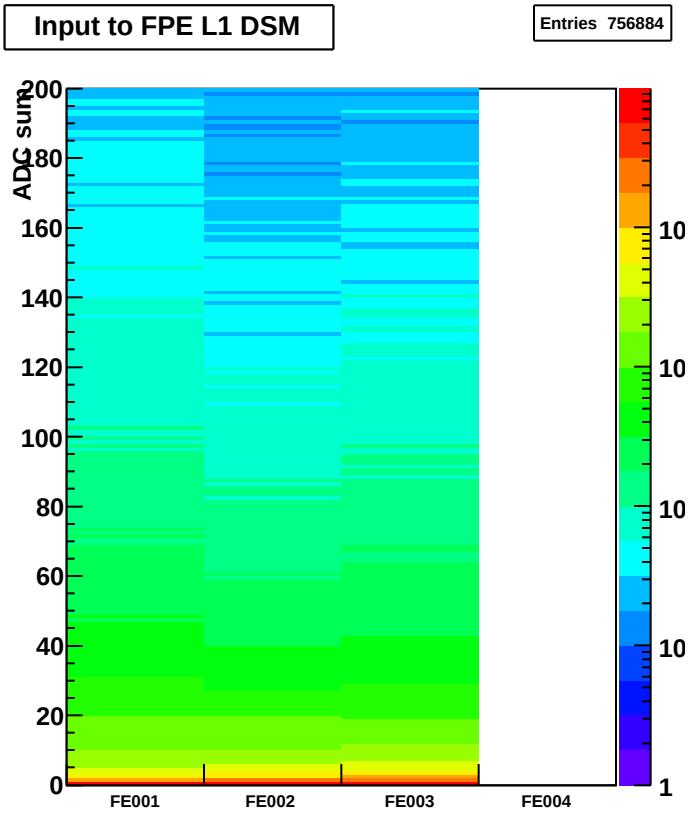

Input to FPD L2 DSM
Entries 756884
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

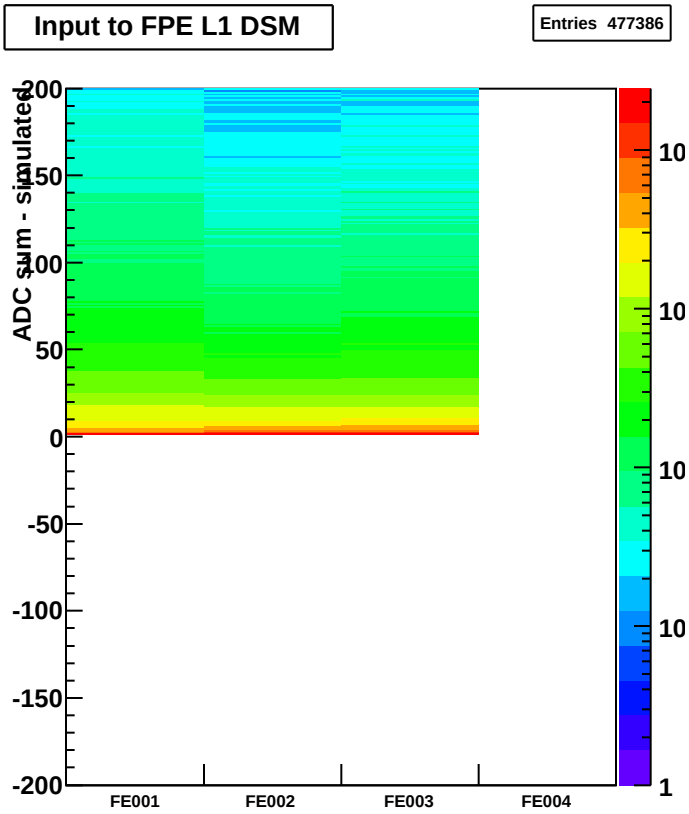

Input to FE001 QT board
Entries6055072
ADC
channel

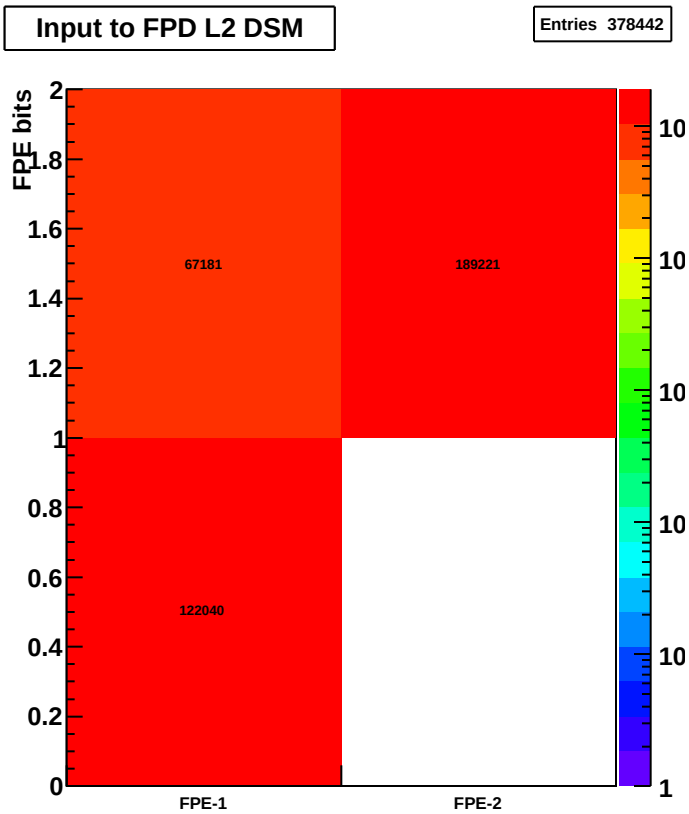

Input to FE002 QT board
Entries6055072
ADC
channel

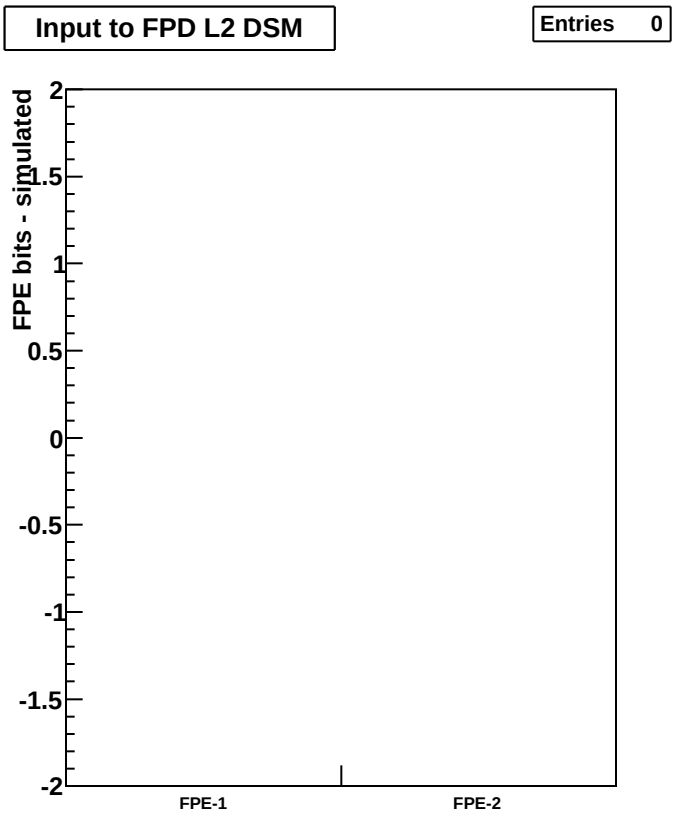

Input to FE003 QT board
Entries6055072
ADC
channel


Input to FE004 QT board
Entries6055072
ADC
channel


Input to FPE L1 DSM
Entries 756884
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 477386
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 378442
FPE bits
67181
189221
122040
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2