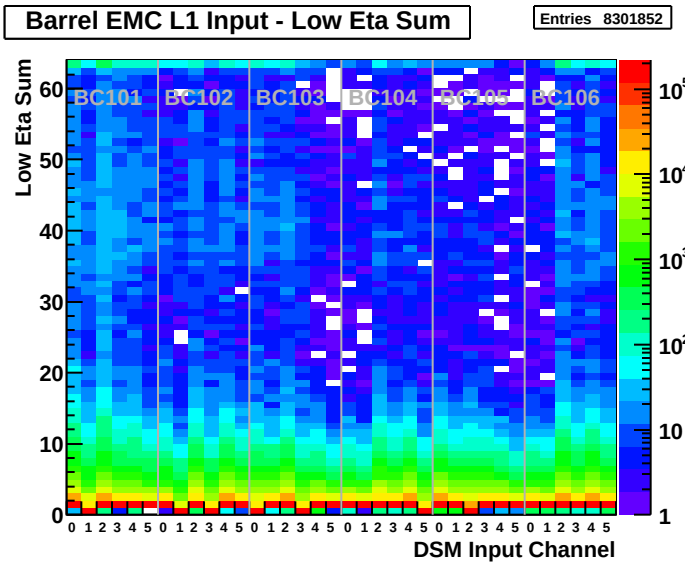
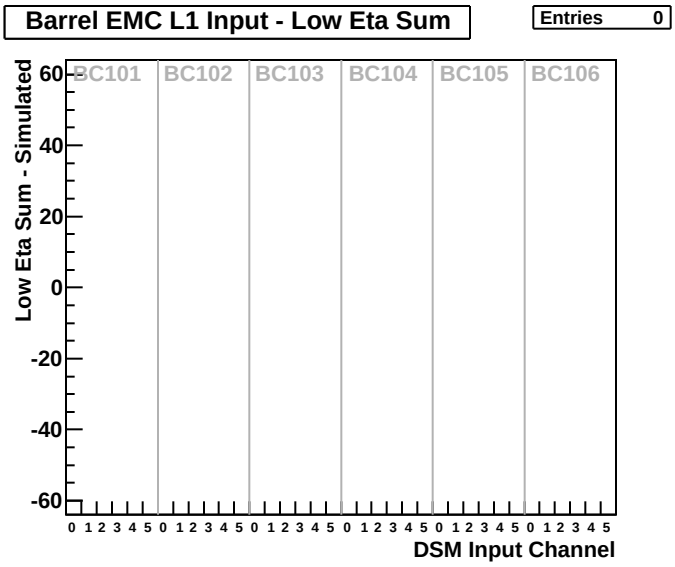
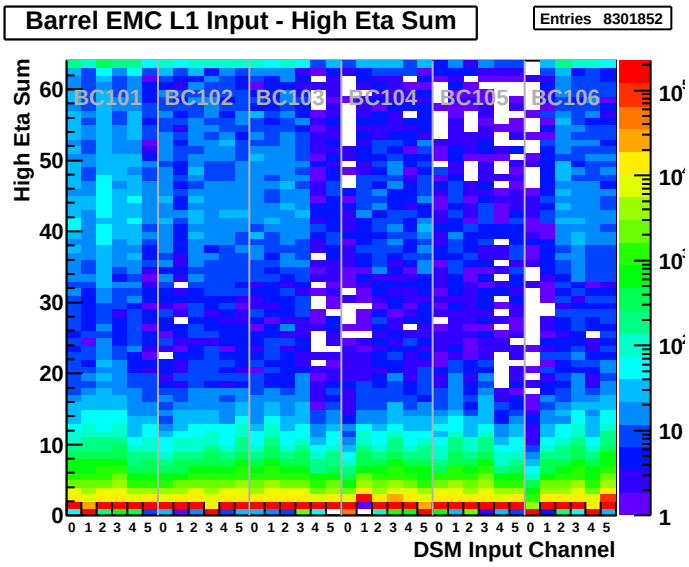
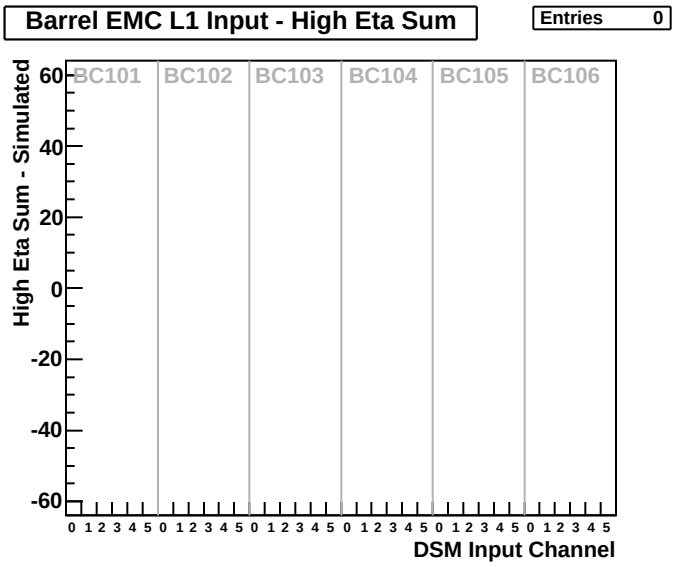
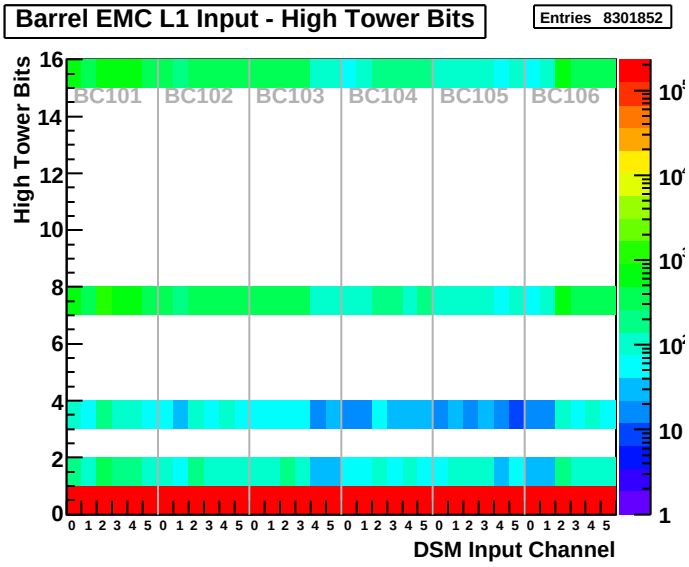
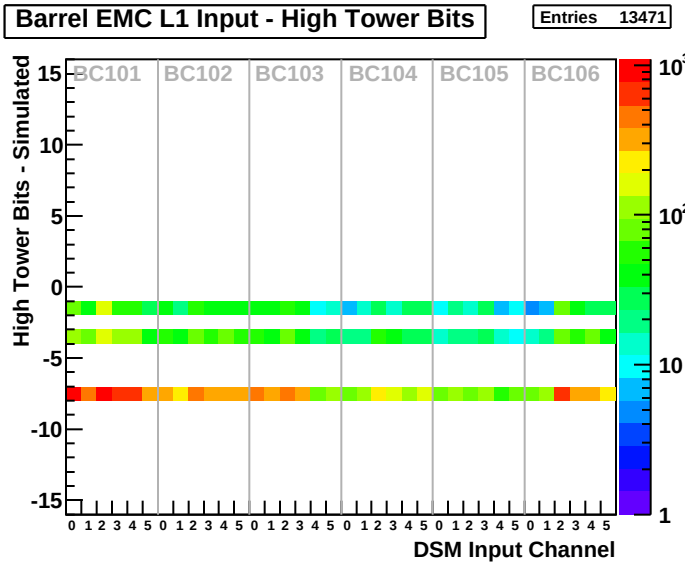

Barrel EMC L1 Input - Low Eta Sum
Entries 8301852
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

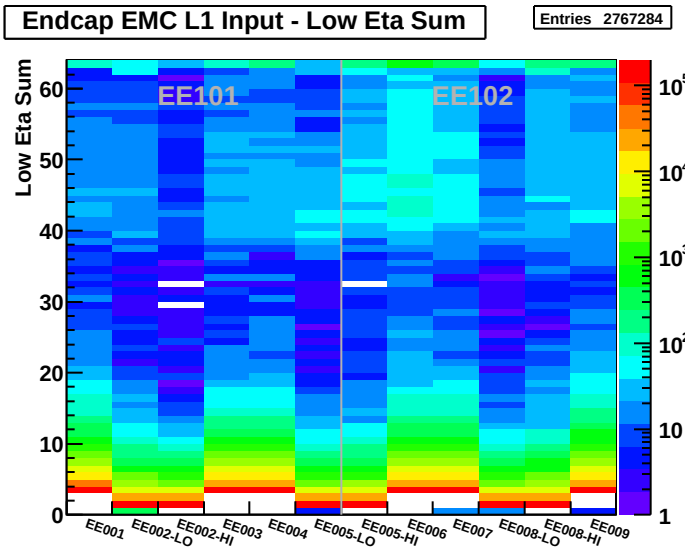

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

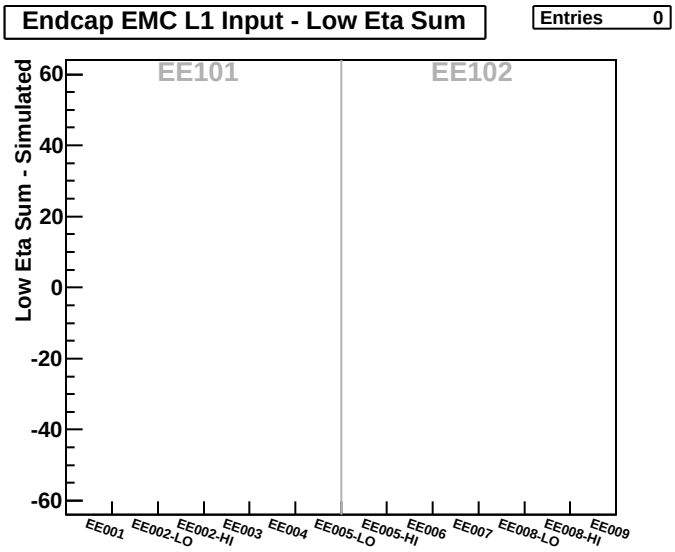

Barrel EMC L1 Input - High Eta Sum
Entries 8301852
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

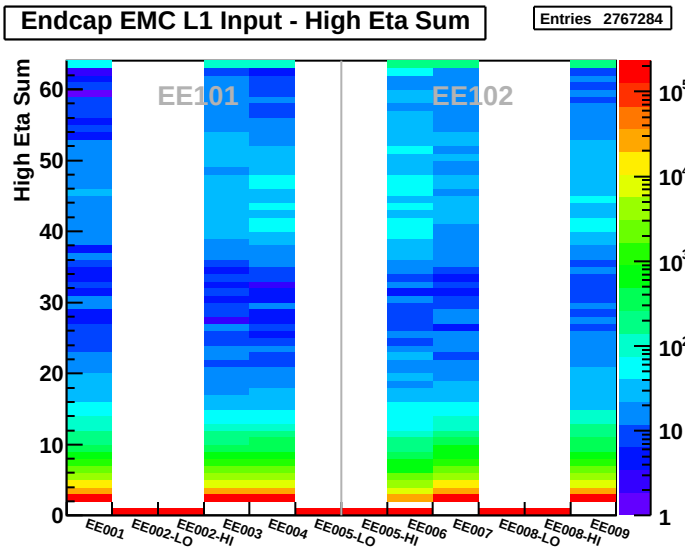

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

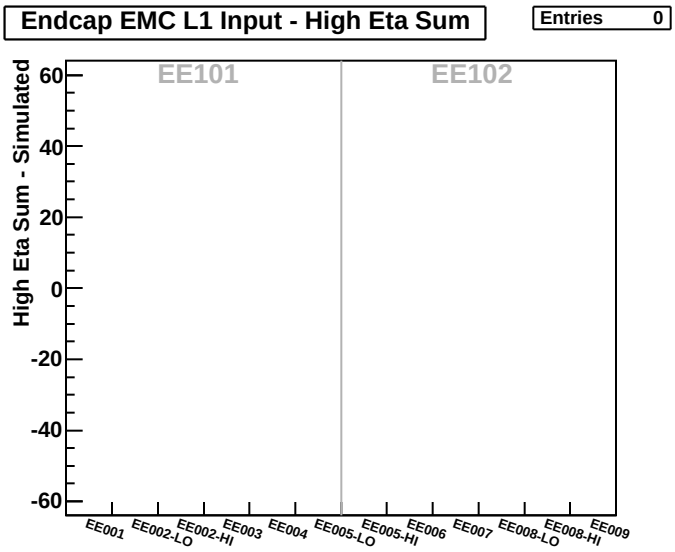

Barrel EMC L1 Input - High Tower Bits
Entries 8301852
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

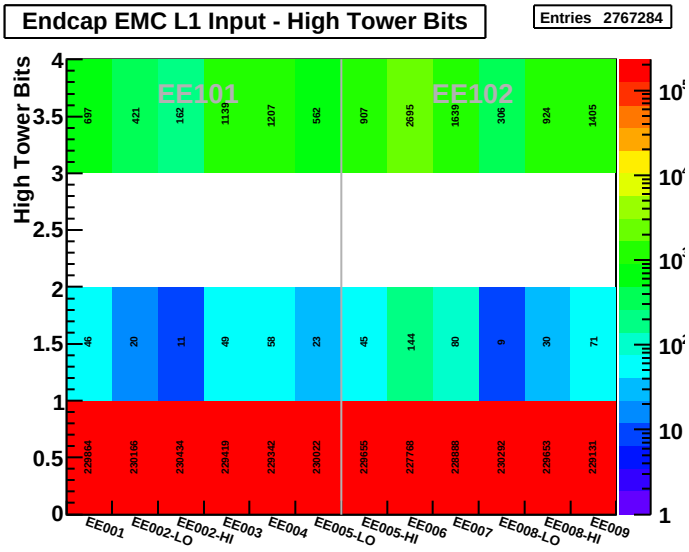

Barrel EMC L1 Input - High Tower Bits
Entries 13471
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

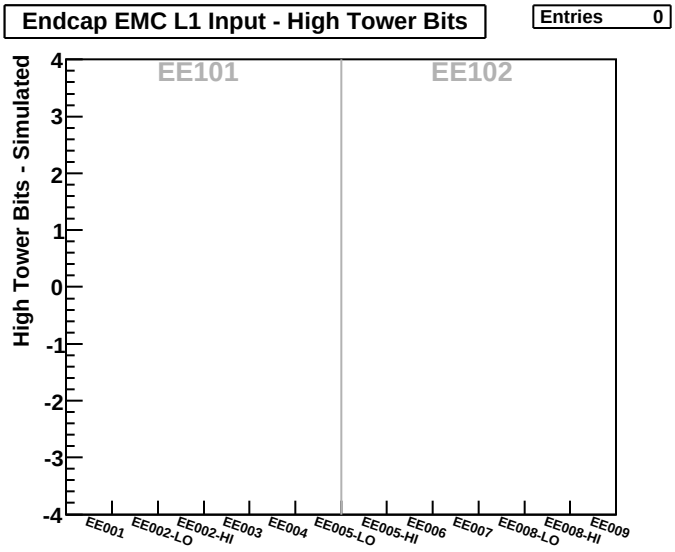

Endcap EMC L1 Input - Low Eta Sum
Entries 2767284
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

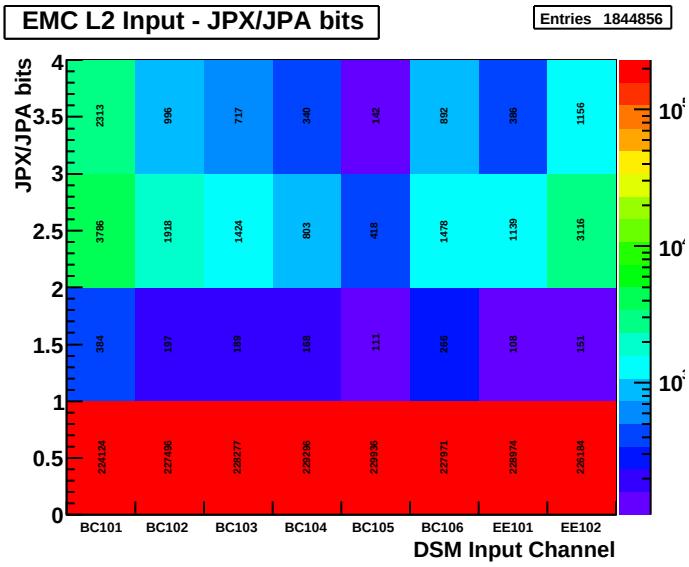

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

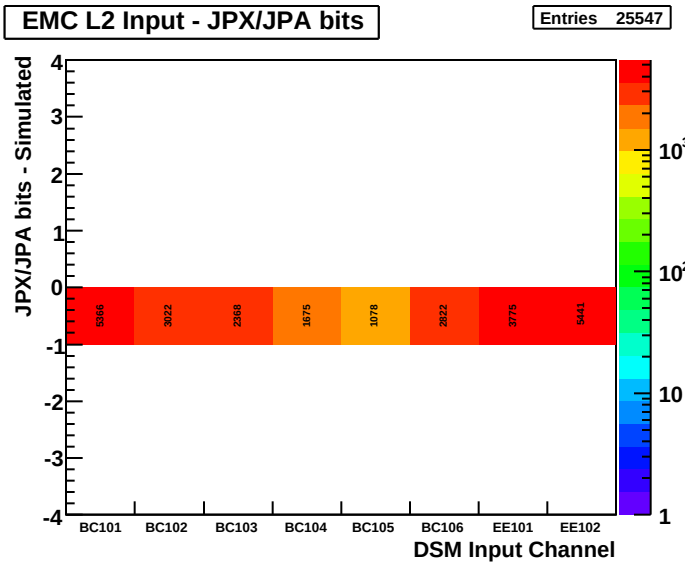

Endcap EMC L1 Input - High Eta Sum
Entries 2767284
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

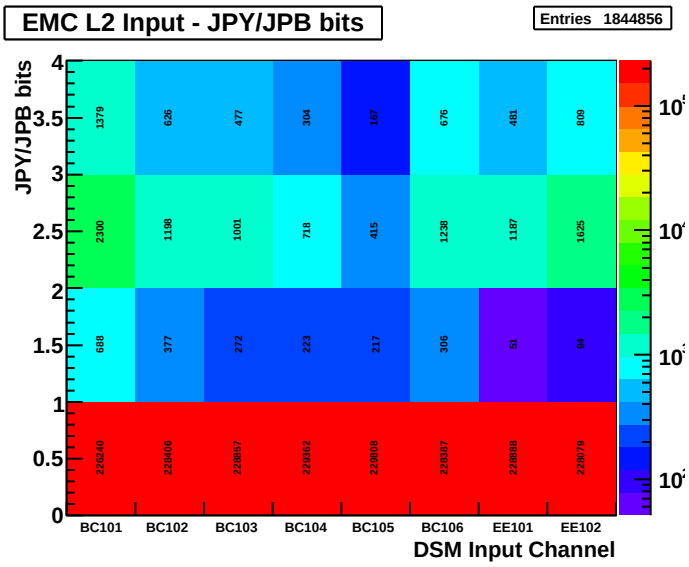

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

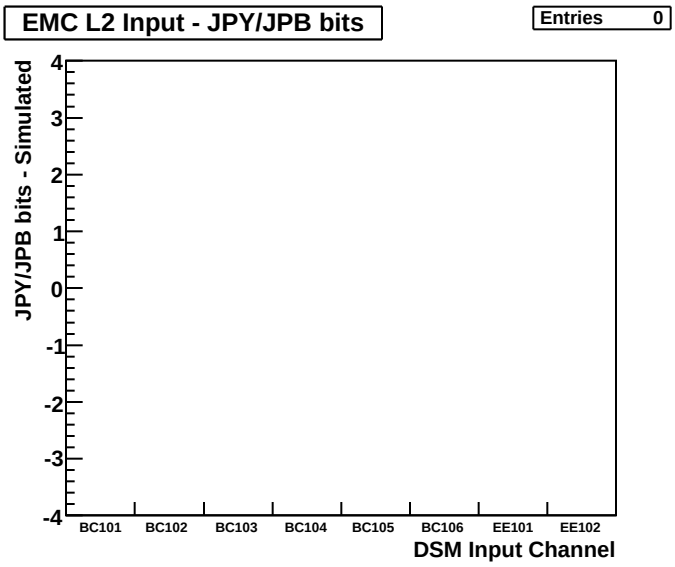

Endcap EMC L1 Input - High Tower Bits
Entries 2767284
High Tower Bits
EE101
EE102
697 421 162 1139 1207 562 907 2595 1639 306 924 1405
46 20 11 49 58 23 45 144 80 9 30 71
229864 230166 230434 229419 229342 230022 229655 227768 228888 230292 229653 229131
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

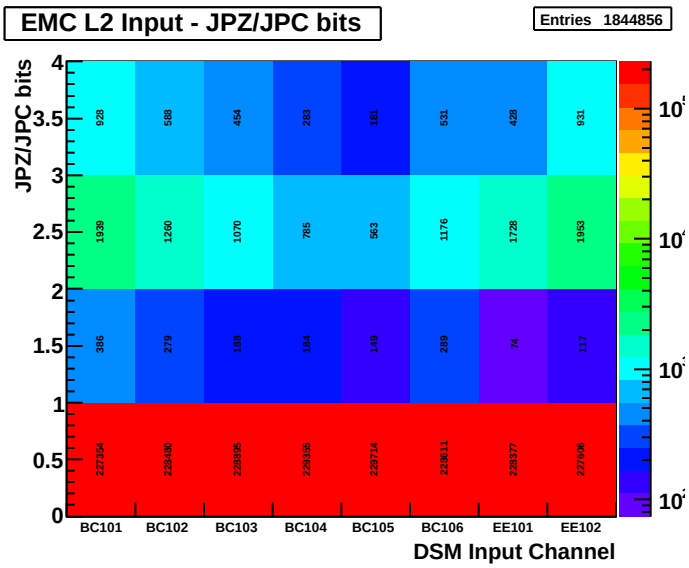

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

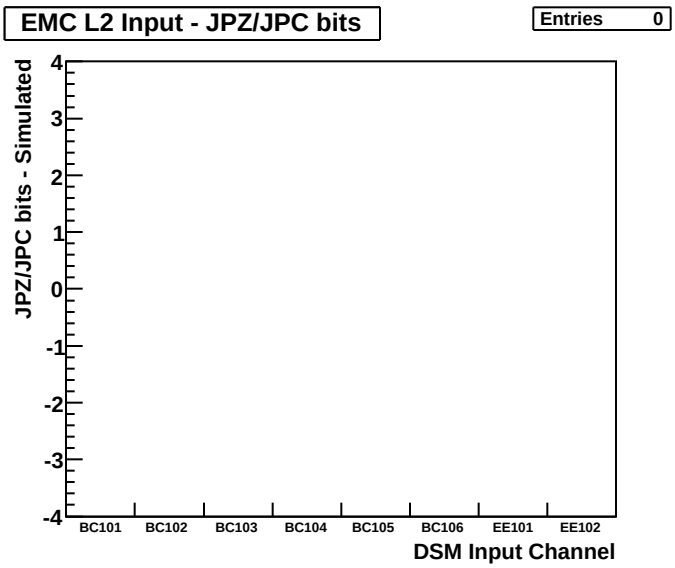

EMC L2 Input - JPX/JPA bits
Entries 1844856
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2313 996 717 340 142 892 386 1156
3786 1918 1424 803 418 1478 1139 3116
384 197 189 168 111 266 108 151
224124 227496 228277 228286 229936 227971 228974 228184

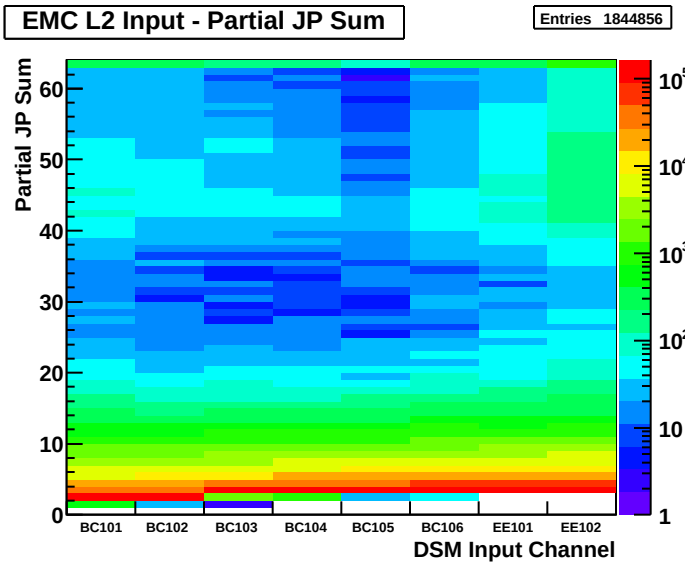

EMC L2 Input - JPX/JPA bits
Entries 25547
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5366 3022 2368 1675 1078 2822 3775 5441

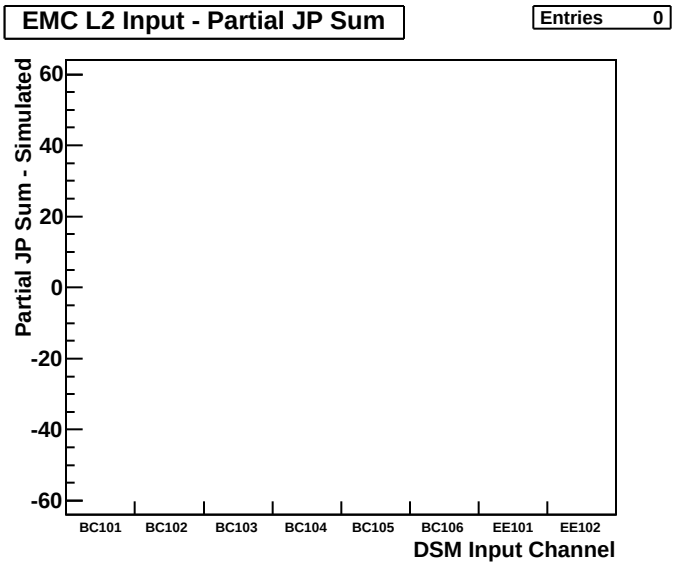

EMC L2 Input - JPY/JPB bits
Entries 1844856
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1379 626 477 304 167 676 481 809
2300 1198 1001 718 415 1238 1187 1625
688 377 272 223 217 306 51 94
226240 228406 228857 228962 229808 228387 228888 228079

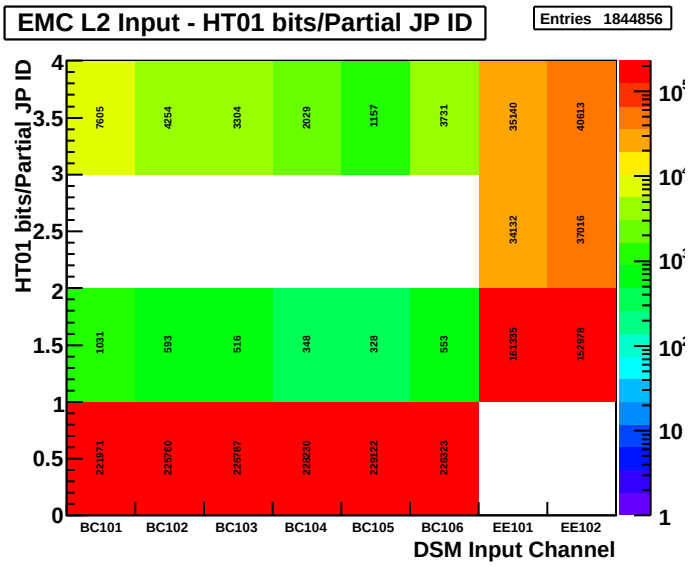

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

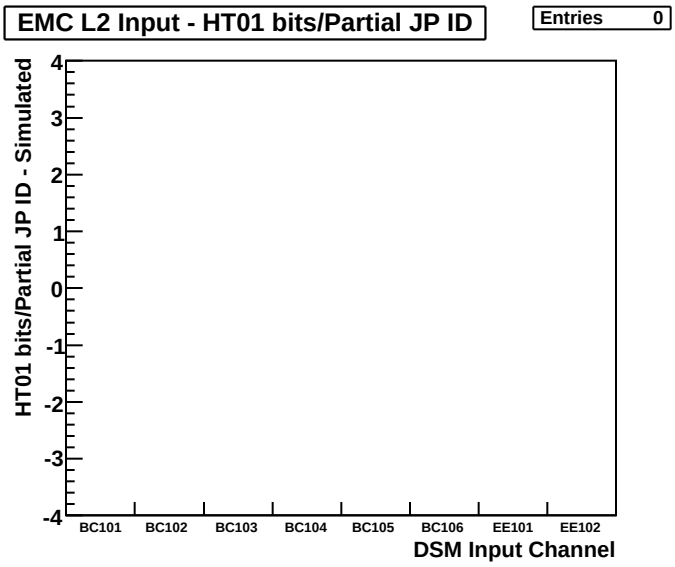

EMC L2 Input - JPZ/JPC bits
Entries 1844856
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
928 588 454 283 181 531 428 931
1939 1260 1070 785 563 1176 1728 1953
386 279 188 184 149 289 74 117
227354 228480 228895 229355 229714 228611 228377 227606

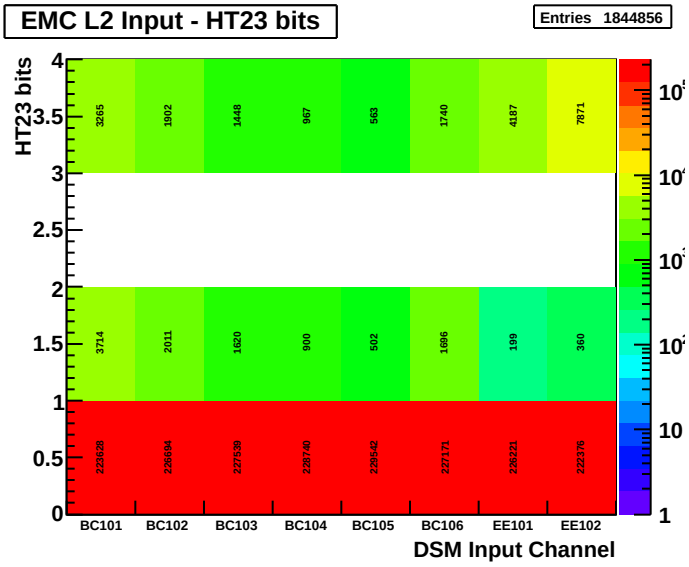

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

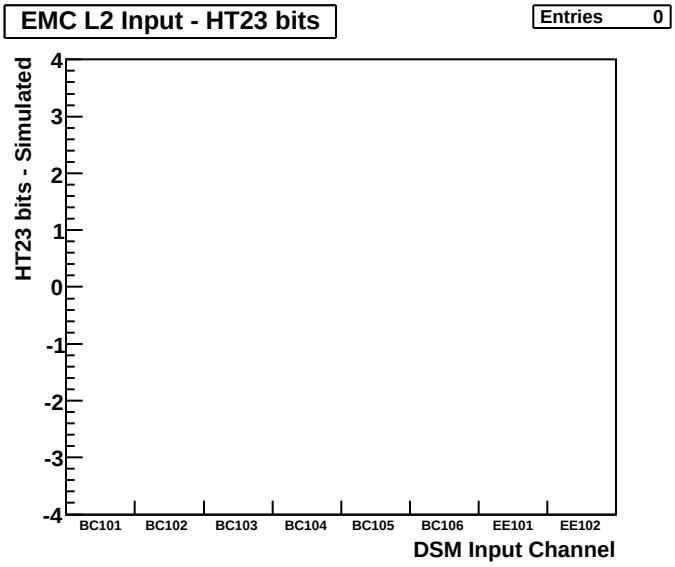

EMC L2 Input - Partial JP Sum
Entries 1844856
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

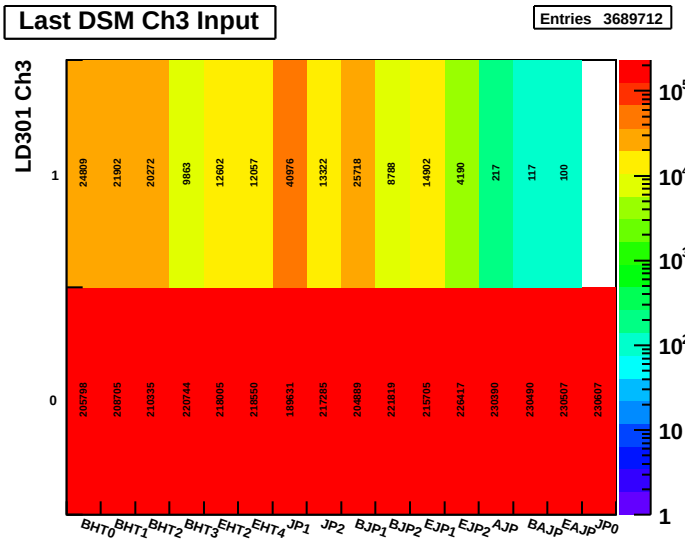

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

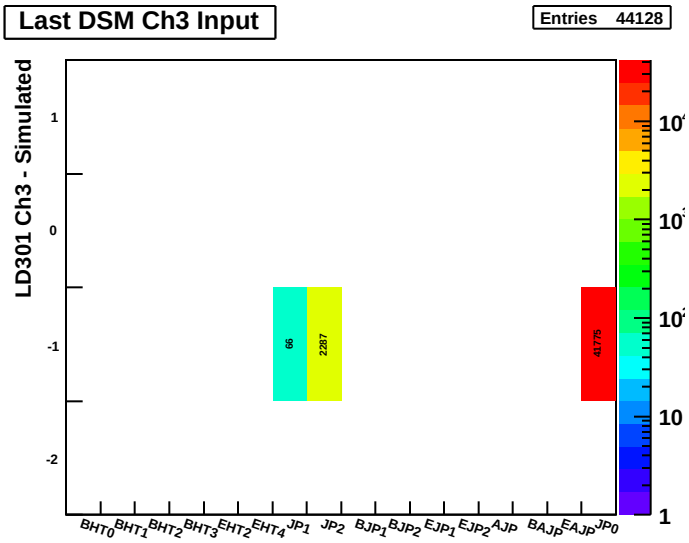

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1844856
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

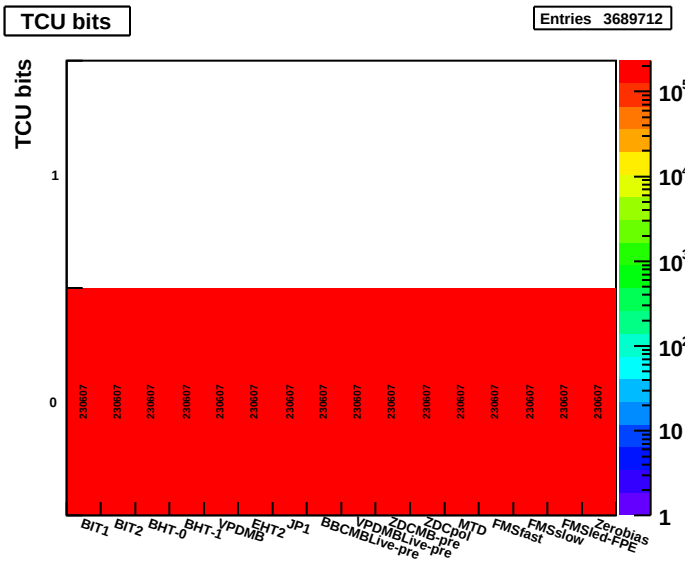

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

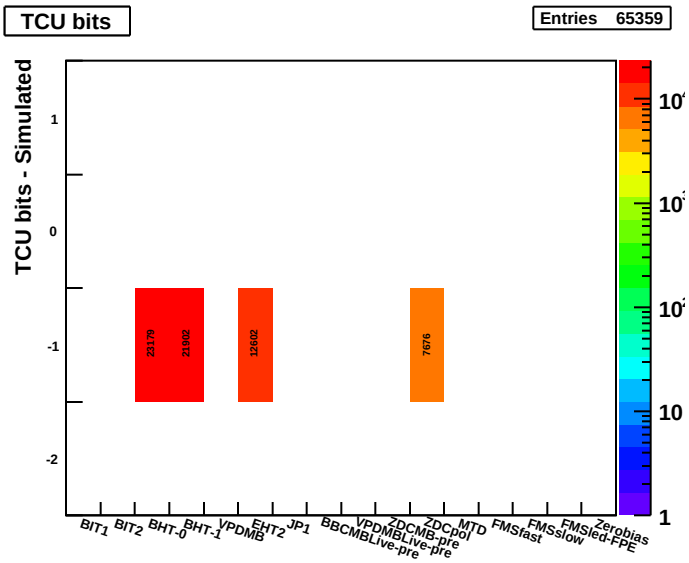

EMC L2 Input - HT23 bits
Entries 1844856
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

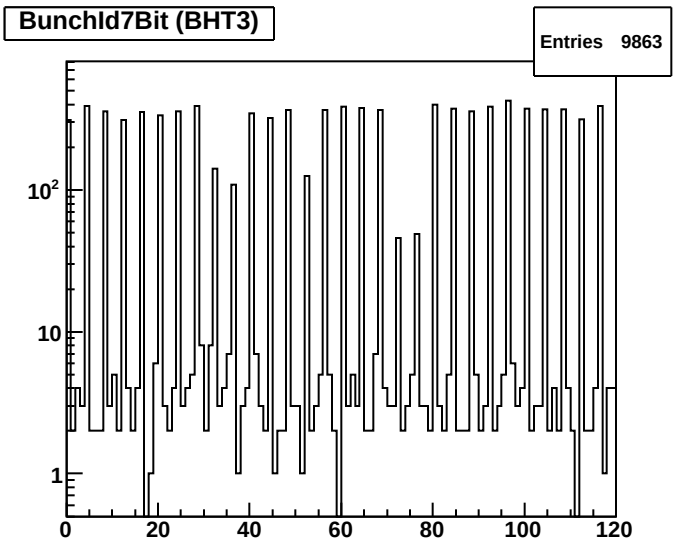

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

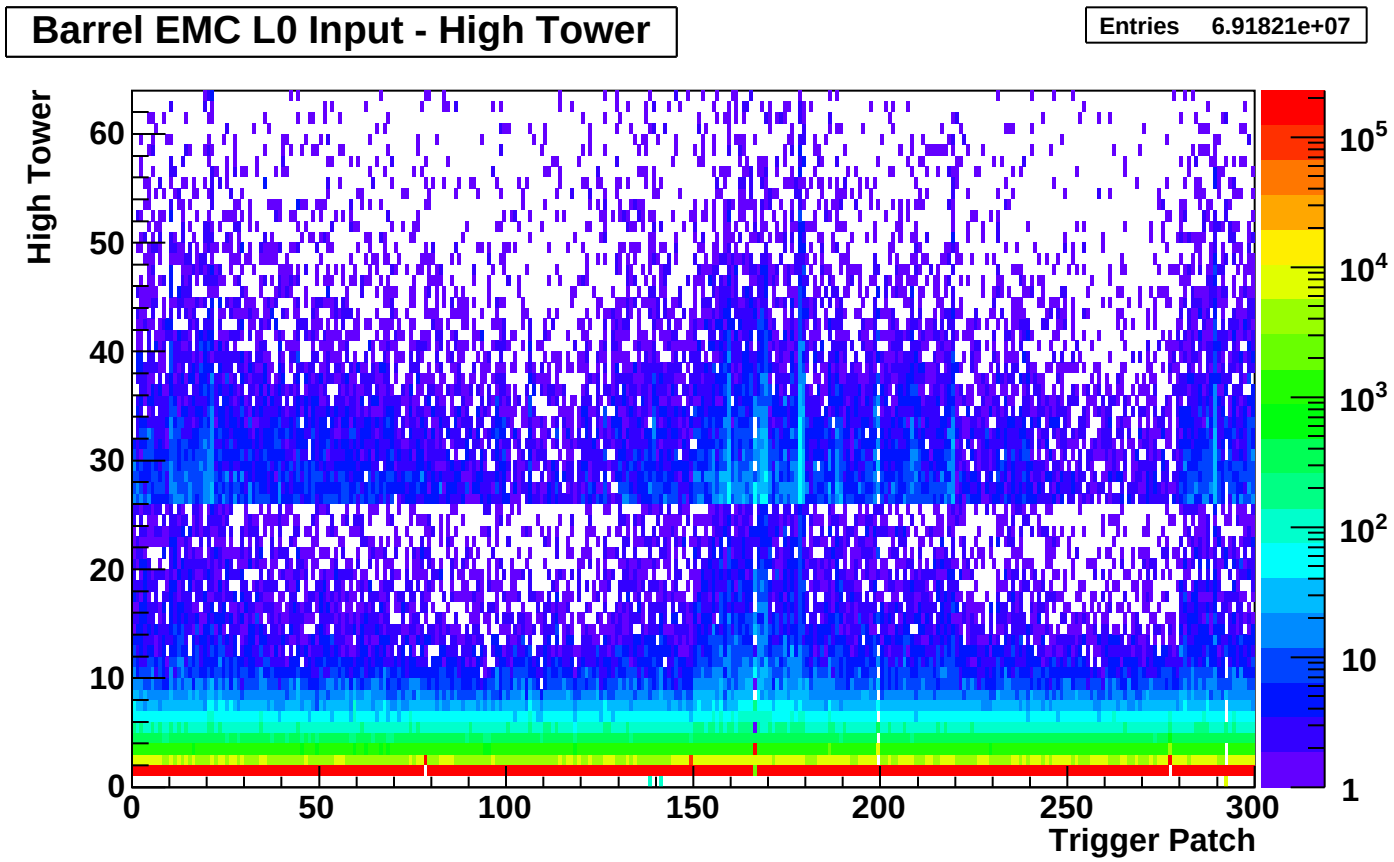

Last DSM Ch3 Input
Entries 3689712
LD301 Ch3
24809 21902 20272 9863 12602 12057 40976 13322 25718 8788 14902 4190 217 117 100
205788 208705 210335 220744 218005 216550 189631 217285 204889 221819 215705 226417 230390 230490 230507 230607
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

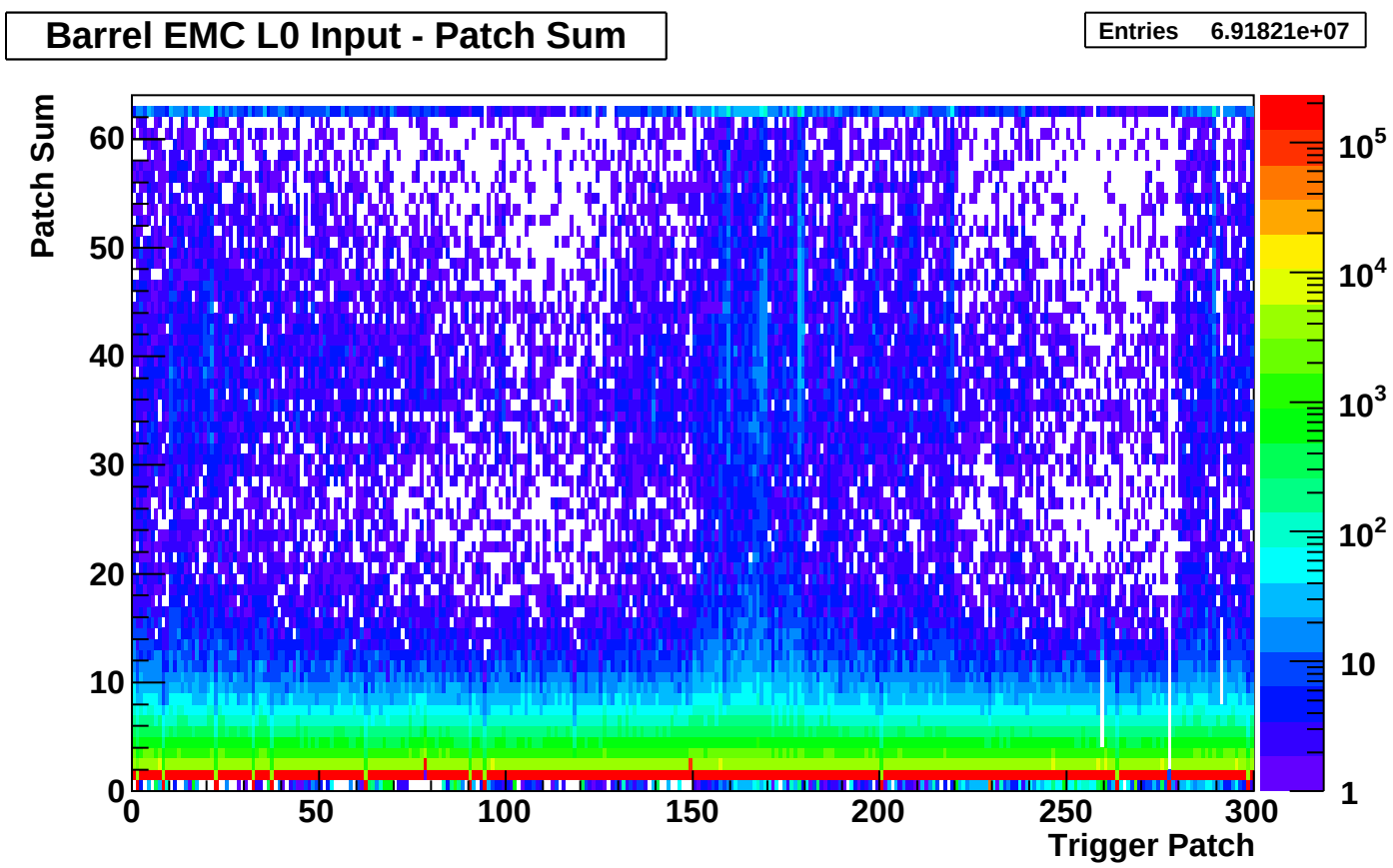

Last DSM Ch3 Input
Entries 44128
LD301 Ch3 - Simulated
66 2287 41775
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

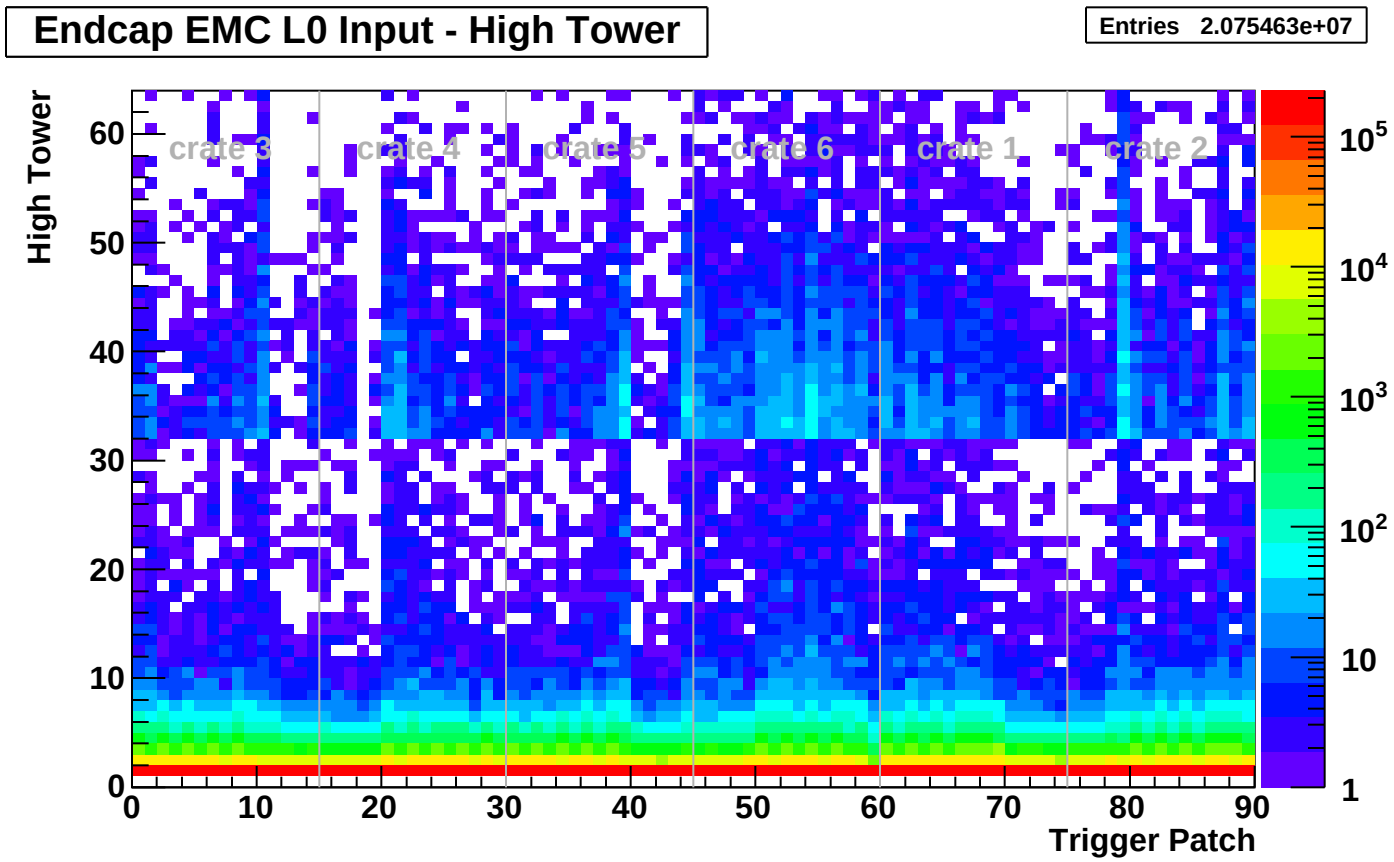

TCU bits
Entries 3689712
TCU bits
230607 230607 230607 230607 230607 230607 230607 230607 230607 230607 230607 230607 230607 230607 230607
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

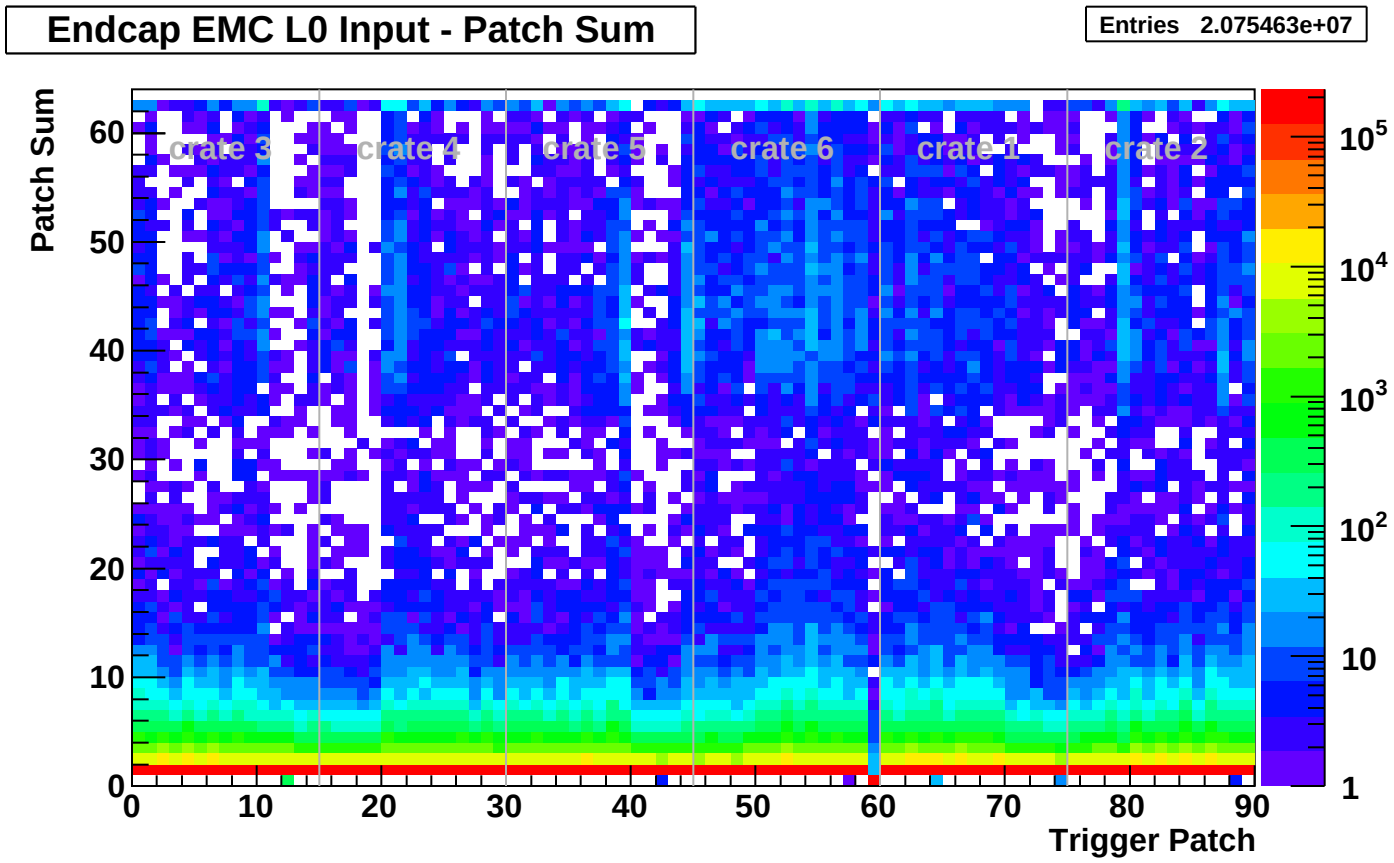

TCU bits
Entries 65359
TCU bits - Simulated
23179 21902 12602 7676
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

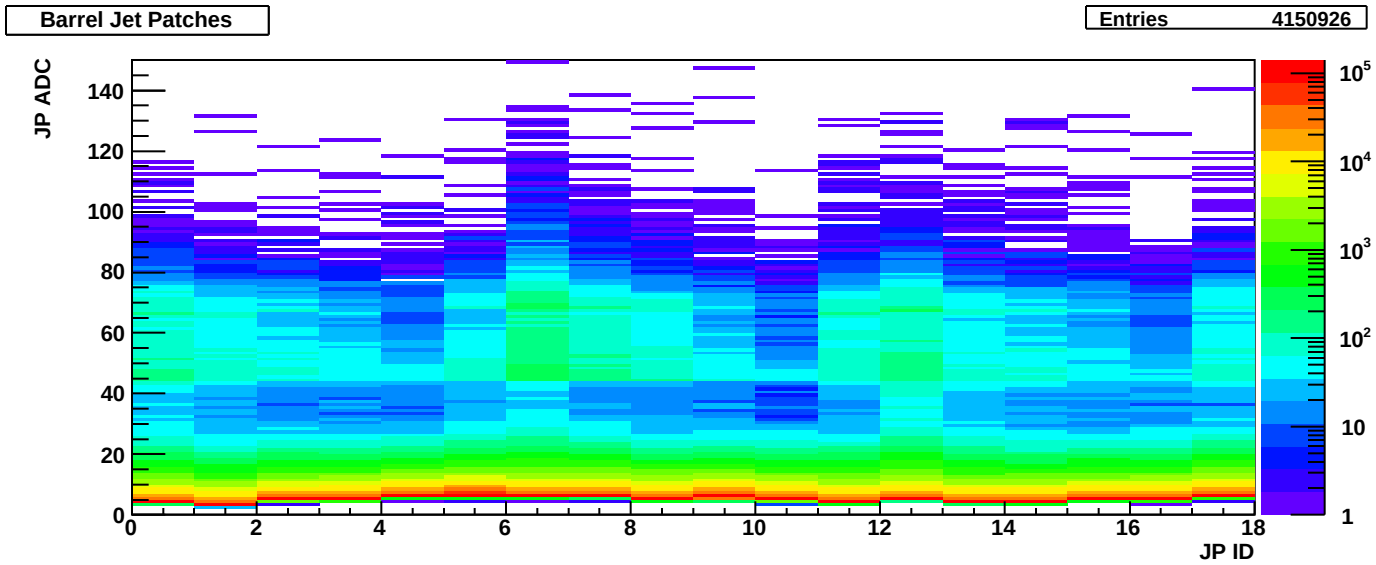

BunchId7Bit (BHT3)
Entries 9863

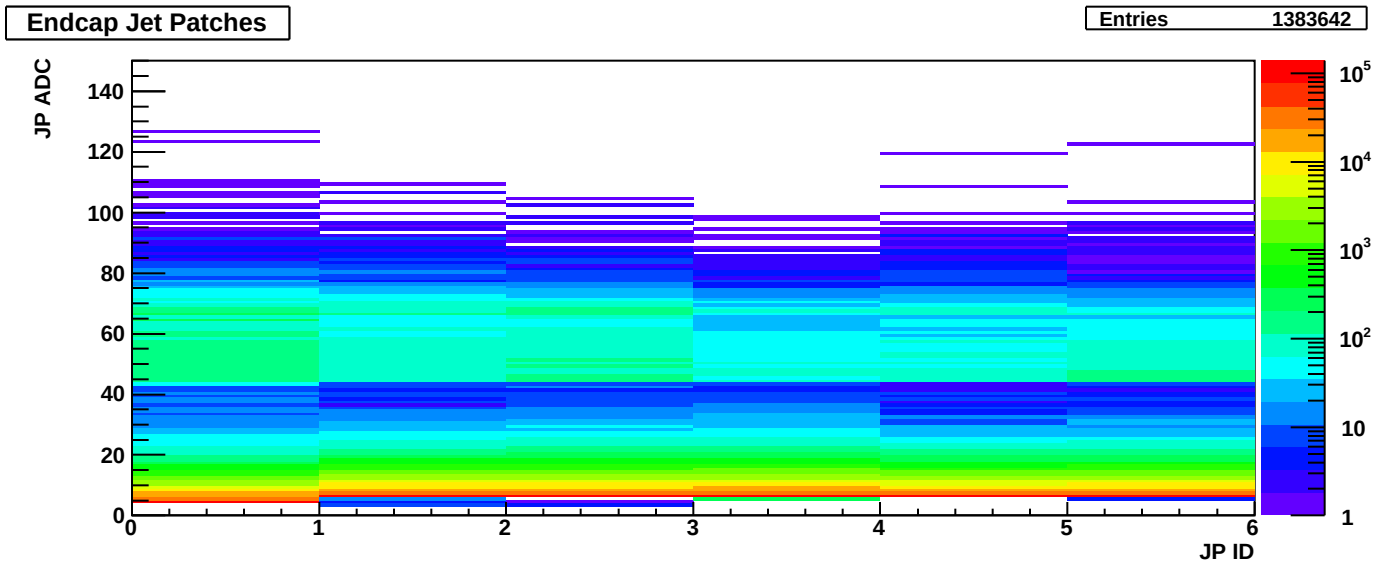

Barrel EMC L0 Input - High Tower
Entries 6.91821e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

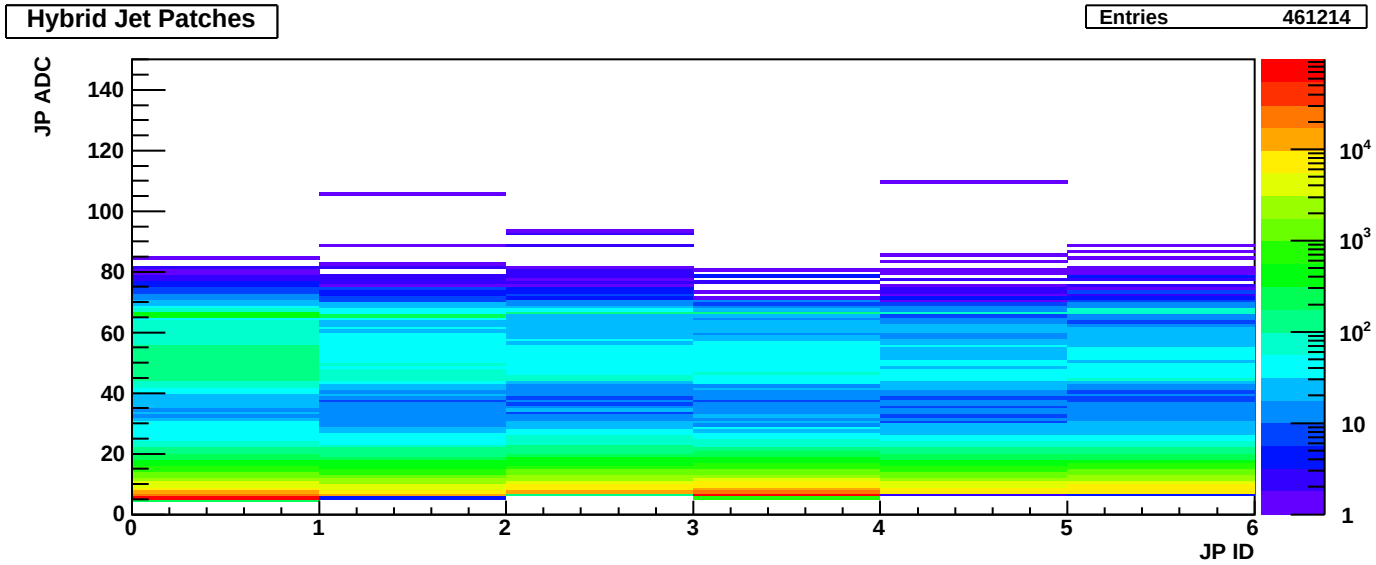

Barrel EMC L0 Input - Patch Sum
Entries 6.91821e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

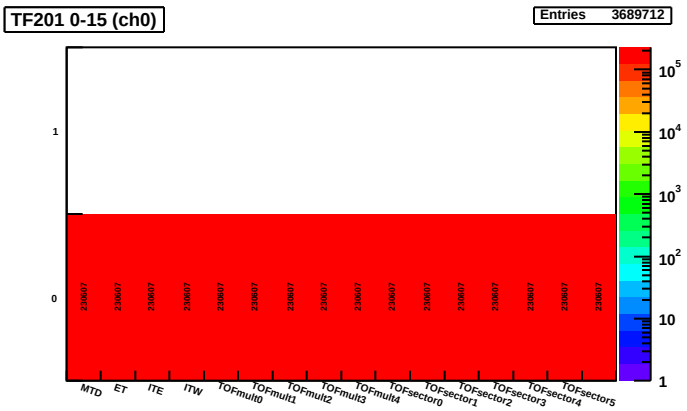

Endcap EMC L0 Input - High Tower
Entries 2.075463e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

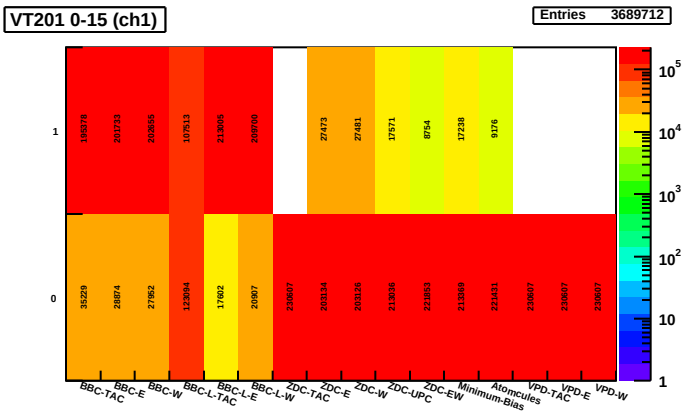

Endcap EMC L0 Input - Patch Sum
Entries 2.075463e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

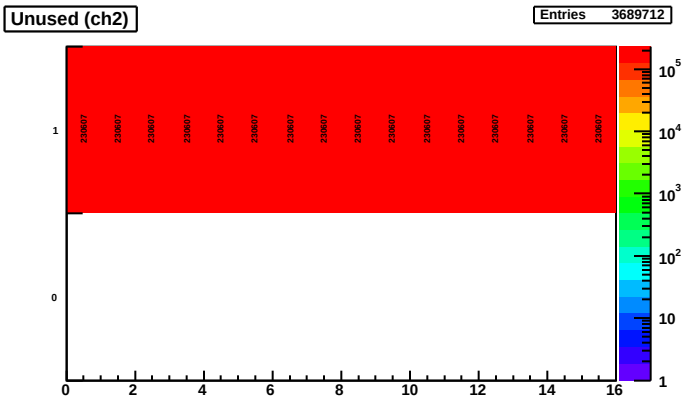

Barrel Jet Patches
Entries 4150926
JP ADC
JP ID

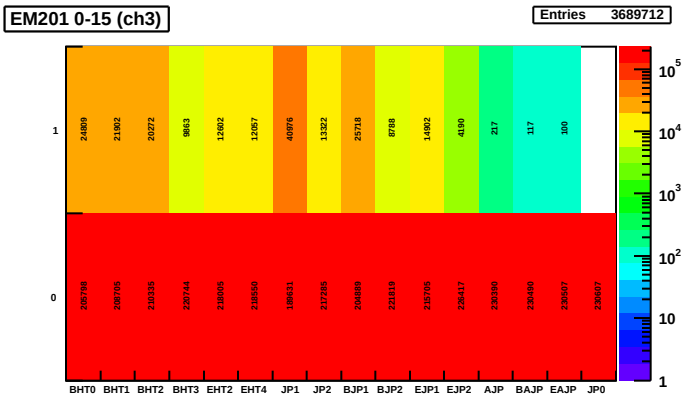

Endcap Jet Patches
Entries 1383642
JP ADC
JP ID

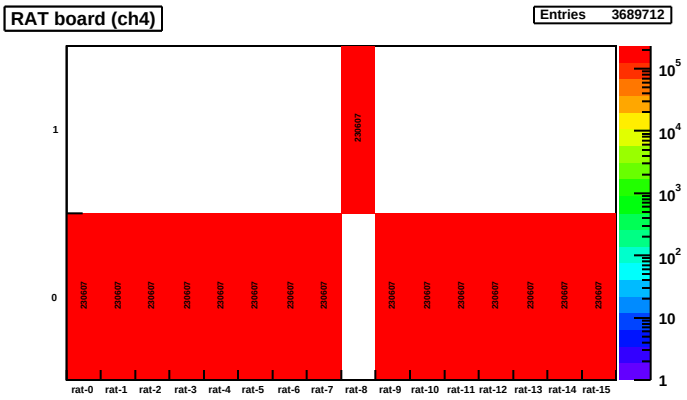

Hybrid Jet Patches
Entries 461214
JP ADC
JP ID

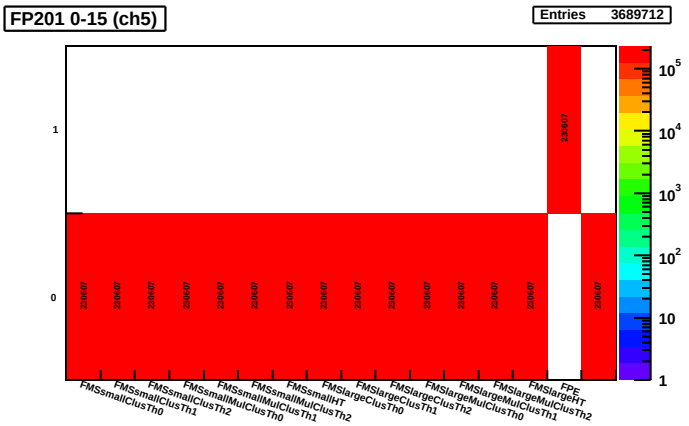

TF201 0-15 (ch0)
Entries 3689712
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

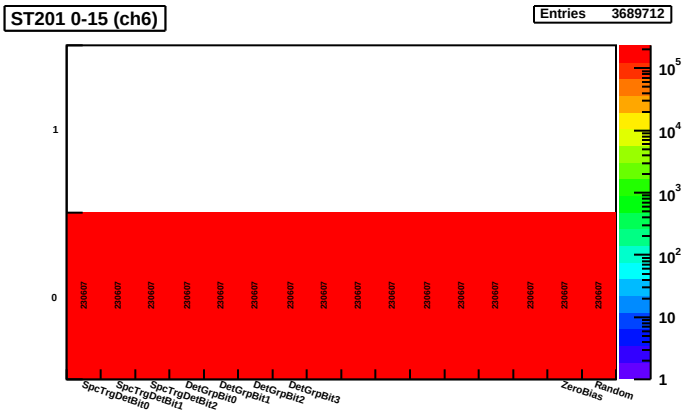

VT201 0-15 (ch1)
Entries 3689712
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

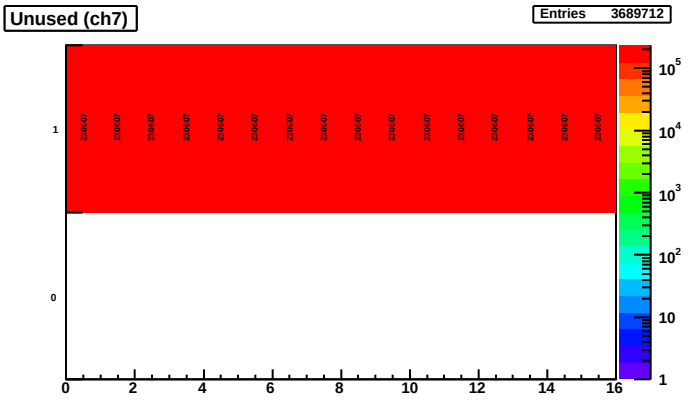

Unused (ch2)
Entries 3689712

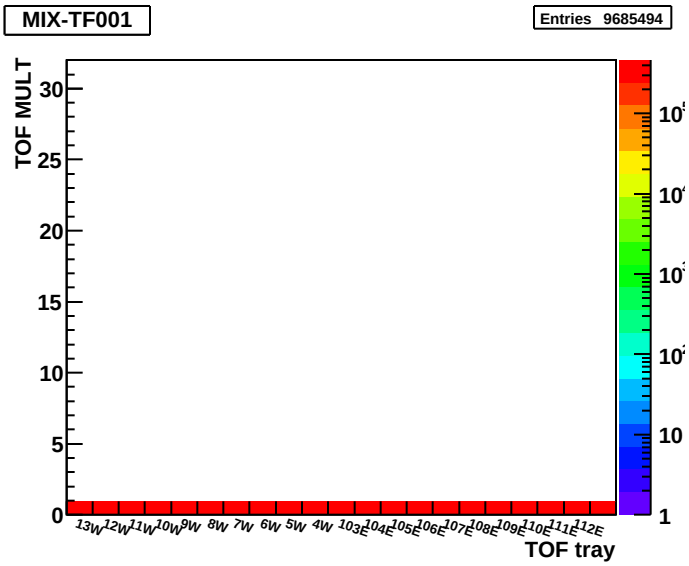

EM201 0-15 (ch3)
Entries 3689712
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

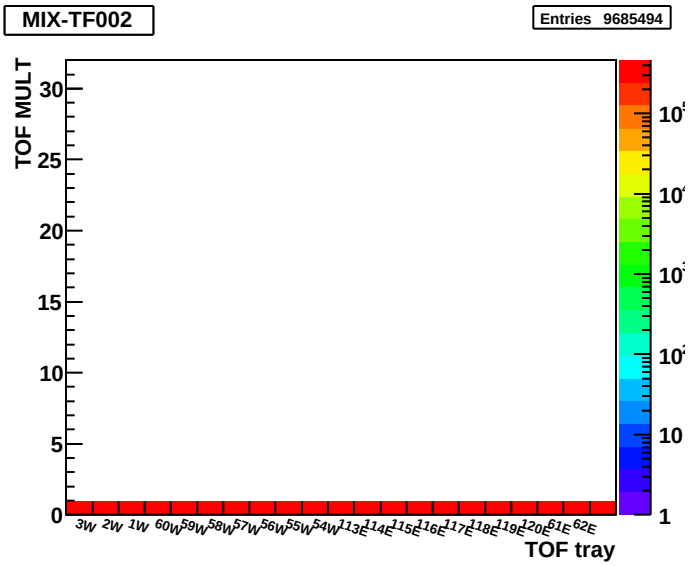

RAT board (ch4)
Entries 3689712
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

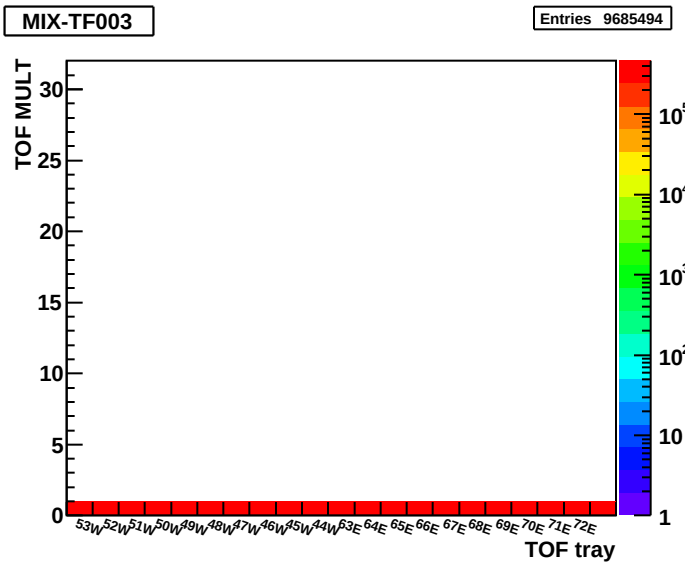

FP201 0-15 (ch5)
Entries 3689712
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

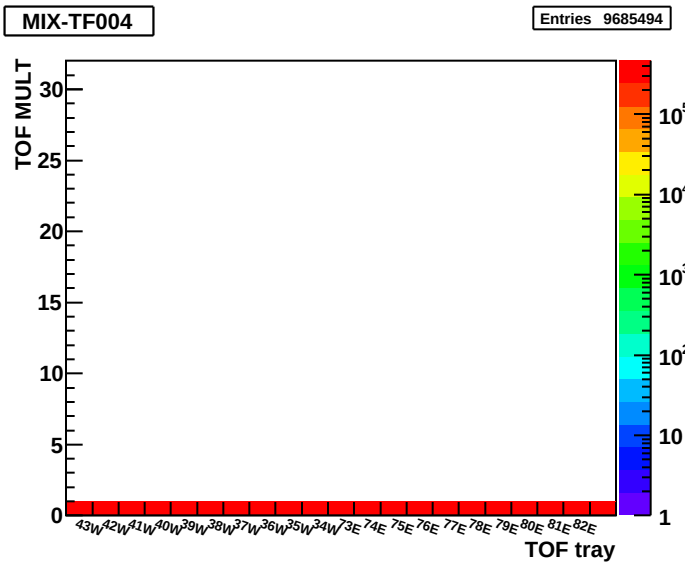

ST201 0-15 (ch6)
Entries 3689712
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

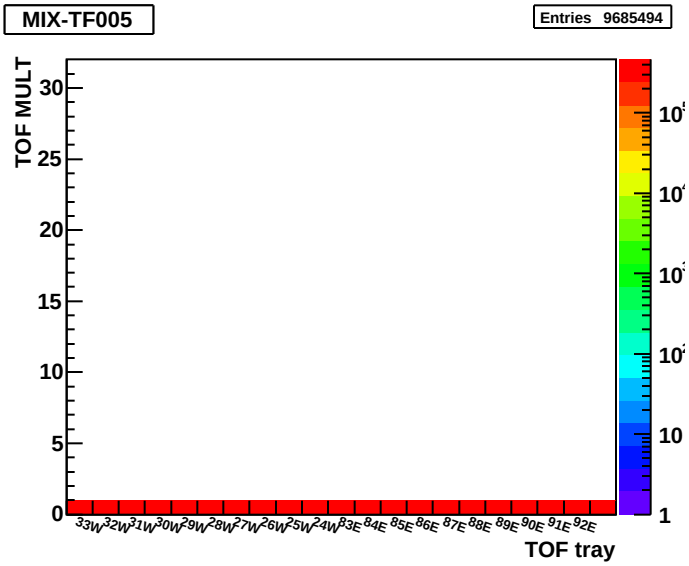

Unused (ch7)
Entries 3689712

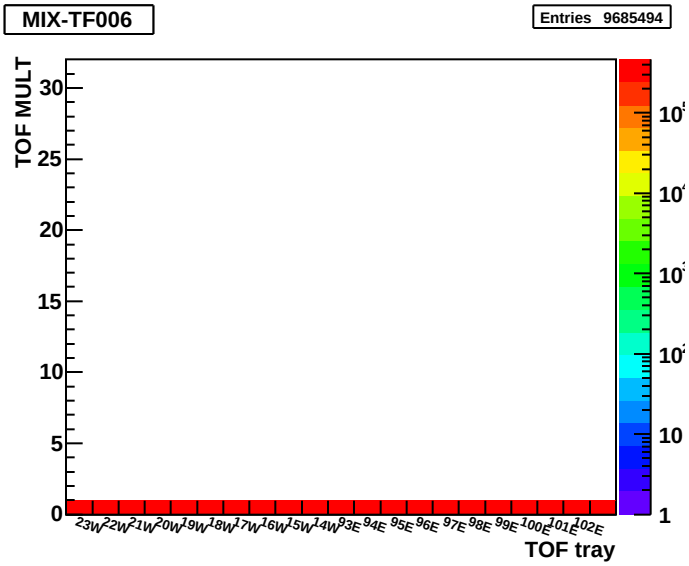

MIX-TF001
Entries 9685494
TOF MULT
TOF tray

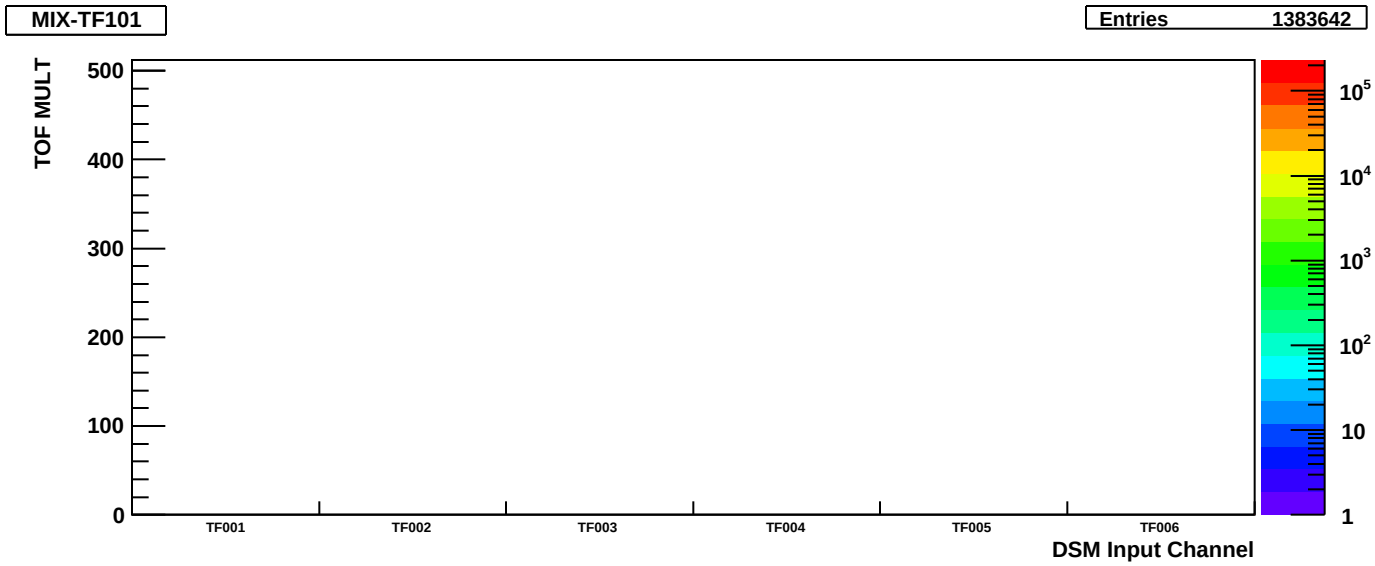

MIX-TF002
Entries 9685494
TOF MULT
TOF tray

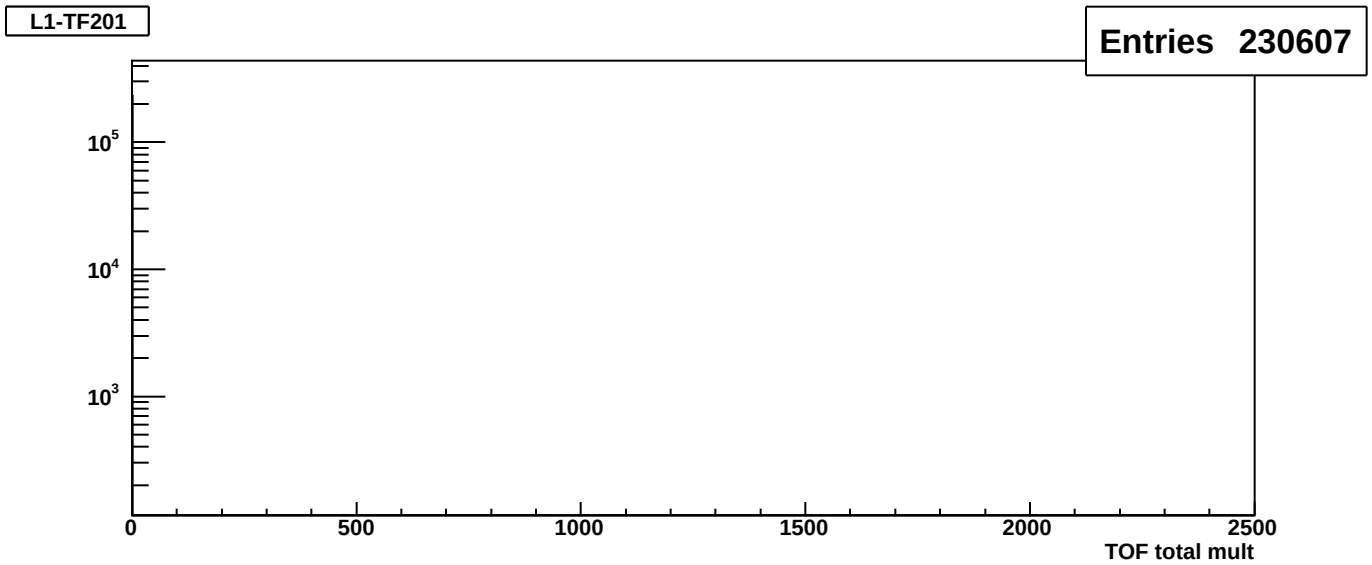

MIX-TF003
Entries 9685494
TOF MULT
TOF tray

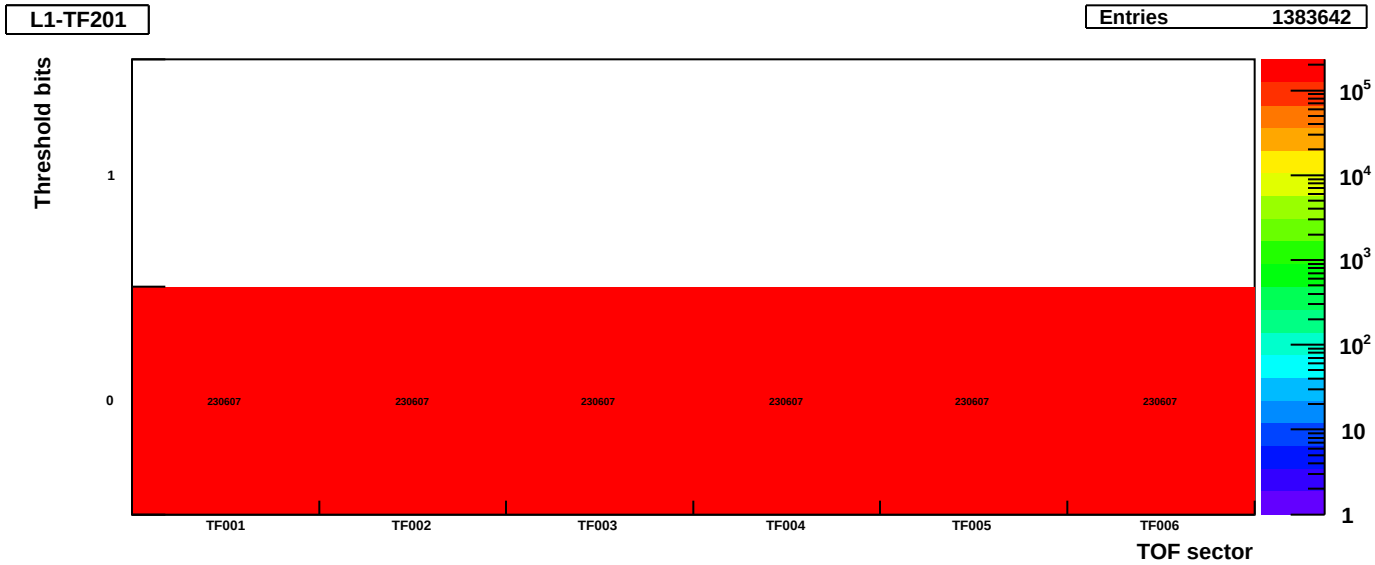

MIX-TF004
Entries 9685494
TOF MULT
TOF tray

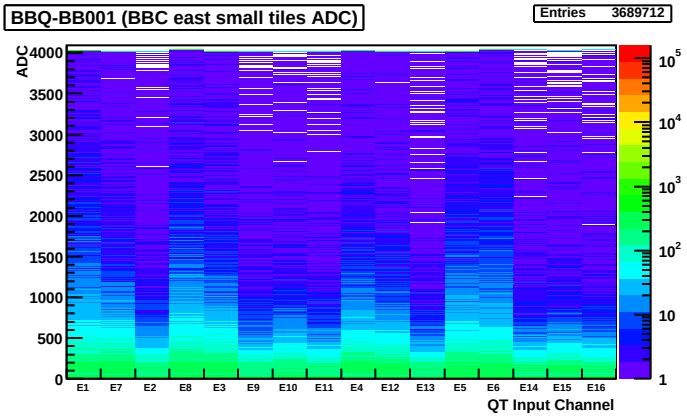

MIX-TF005
Entries 9685494
TOF MULT
TOF tray

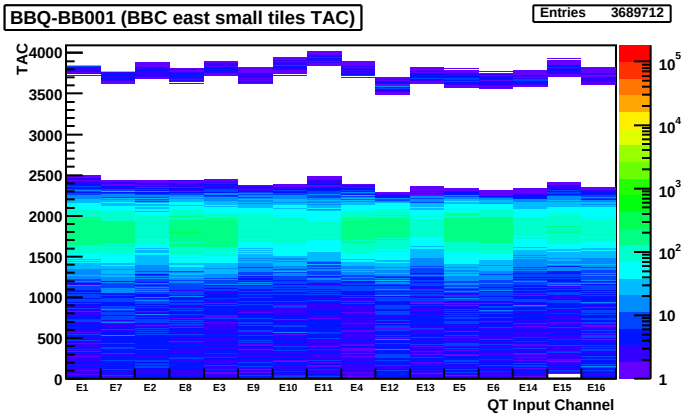

MIX-TF006
Entries 9685494
TOF MULT
TOF tray

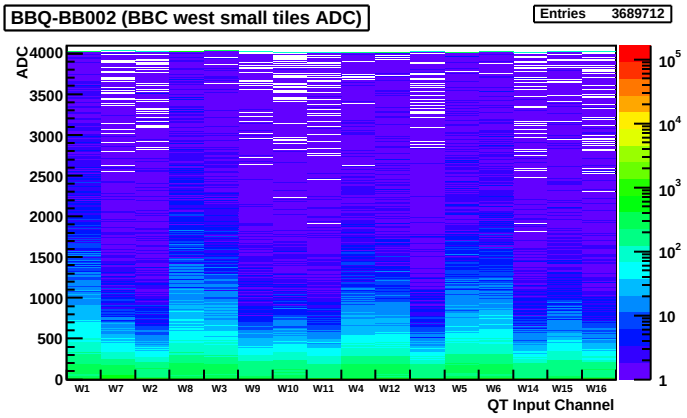

MIX-TF101
Entries 1383642
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

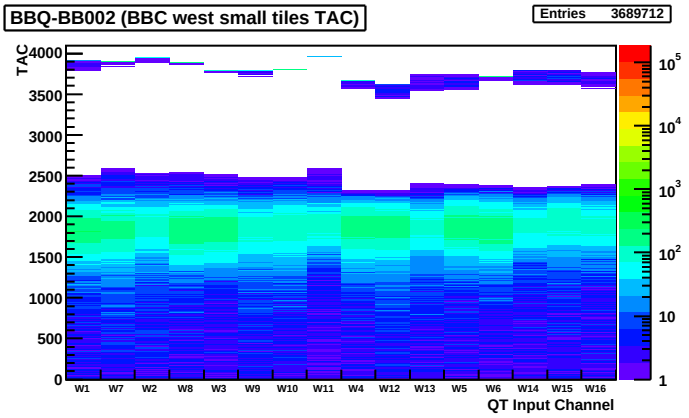

L1-TF201
Entries 230607
10^5
10^4
10^3
0
500
1000
1500
2000
2500
TOF total mult

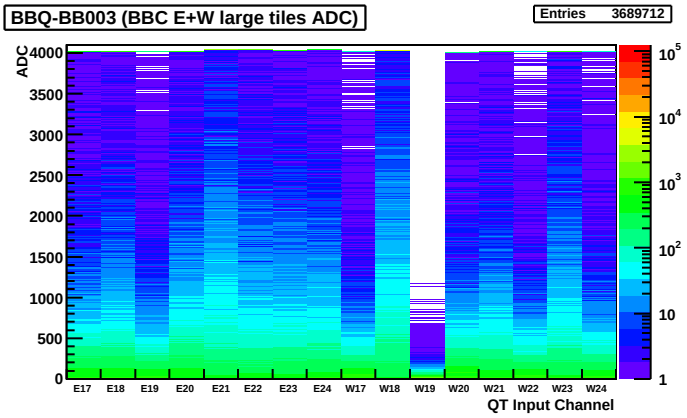

L1-TF201
Entries 1383642
Threshold bits
1
0
230607
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

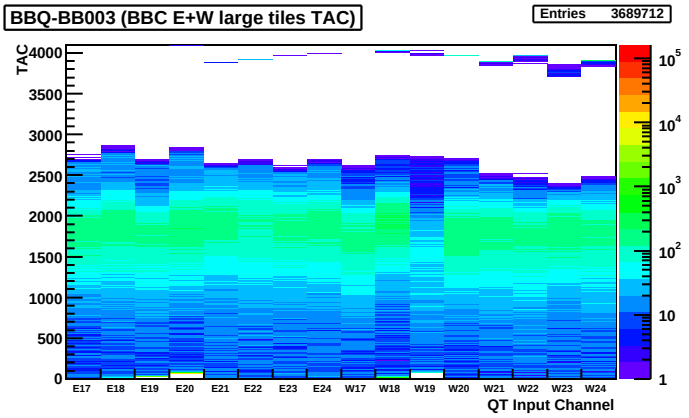

BBQ-BB001 (BBC east small tiles ADC)
Entries 3689712
ADC
QT Input Channel

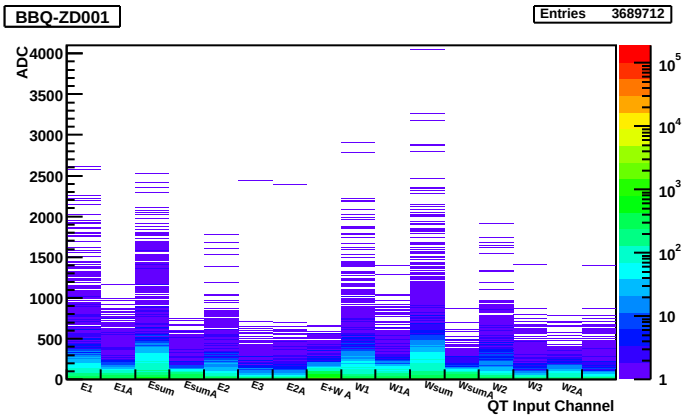

BBQ-BB001 (BBC east small tiles TAC)
Entries 3689712
TAC
QT Input Channel

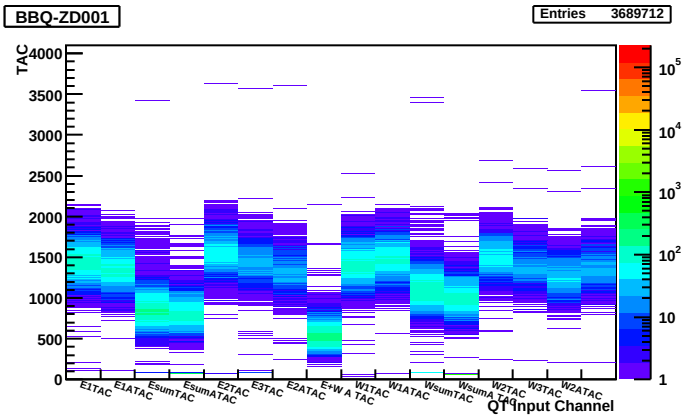

BBQ-BB002 (BBC west small tiles ADC)
Entries 3689712
ADC
QT Input Channel

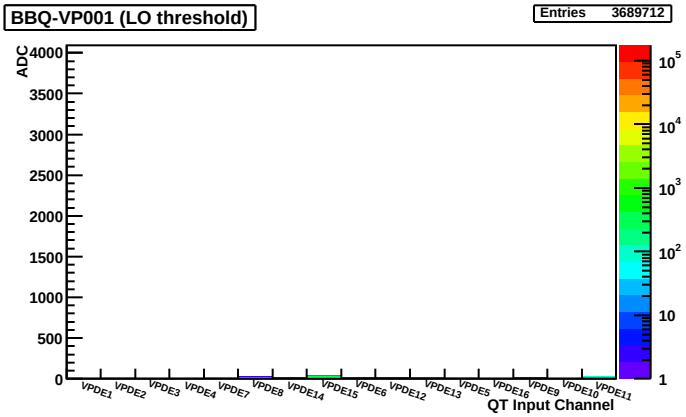

BBQ-BB002 (BBC west small tiles TAC)
Entries 3689712
TAC
QT Input Channel

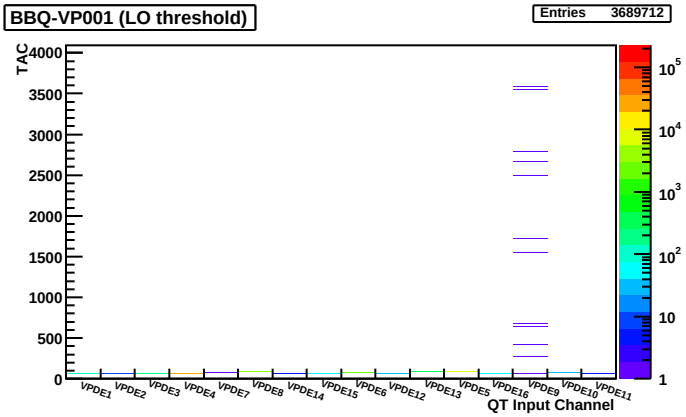

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3689712
ADC
QT Input Channel

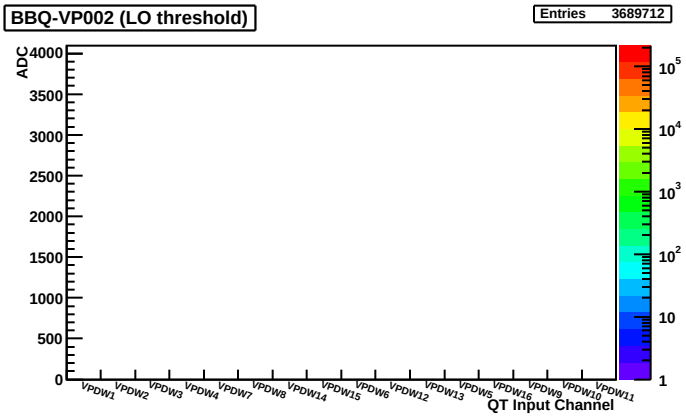

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3689712
TAC
QT Input Channel

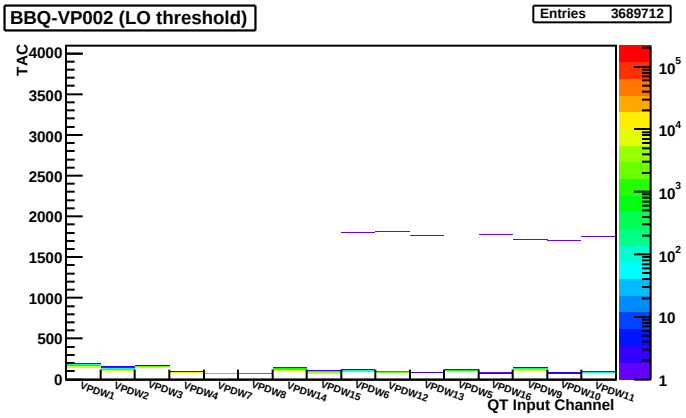

BBQ-ZD001
Entries 3689712
ADC
QT Input Channel

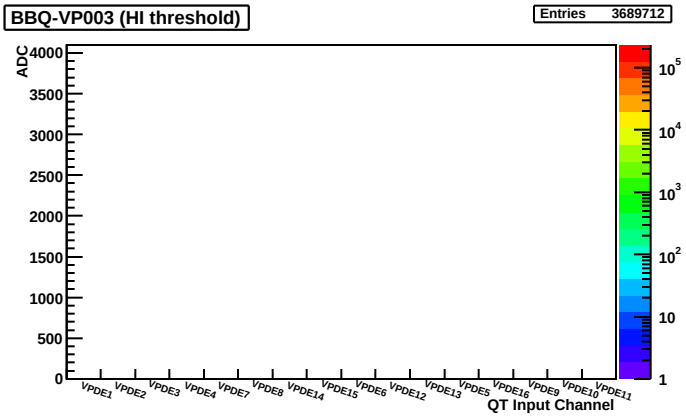

BBQ-ZD001
Entries 3689712
TAC
QT Input Channel

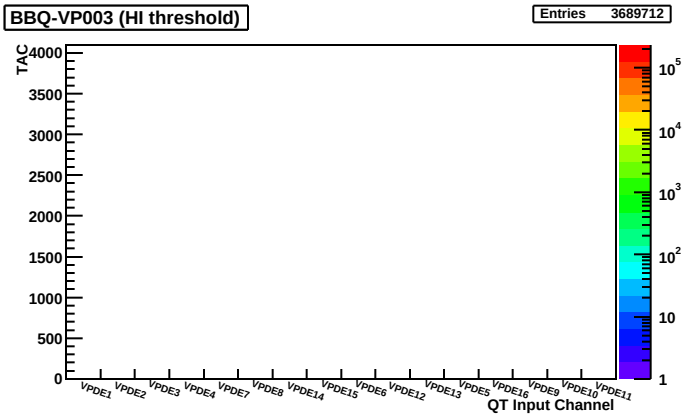

BBQ-VP001 (LO threshold)
Entries 3689712
ADC
QT Input Channel

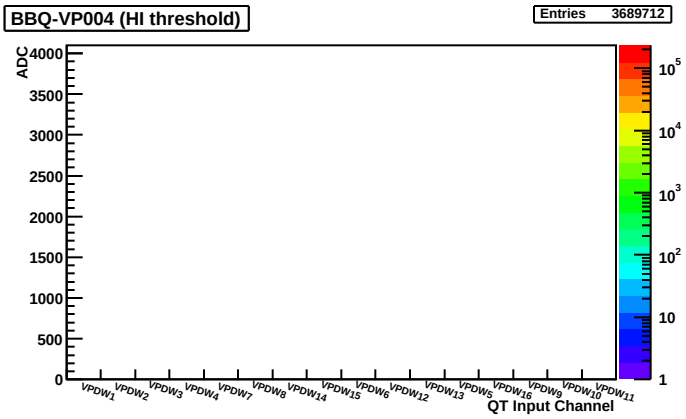

BBQ-VP001 (LO threshold)
Entries 3689712
TAC
QT Input Channel

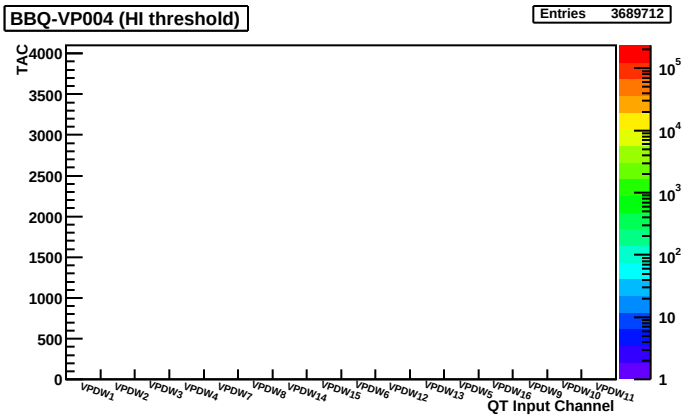

BBQ-VP002 (LO threshold)
Entries 3689712
ADC
QT Input Channel

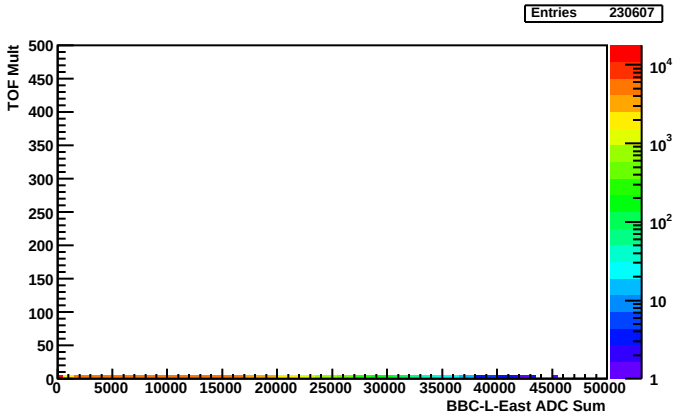

BBQ-VP002 (LO threshold)
Entries 3689712
TAC
QT Input Channel

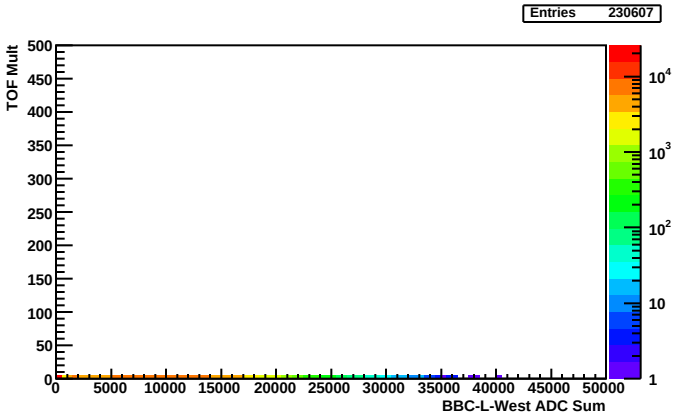

BBQ-VP003 (HI threshold)
Entries 3689712
ADC
QT Input Channel

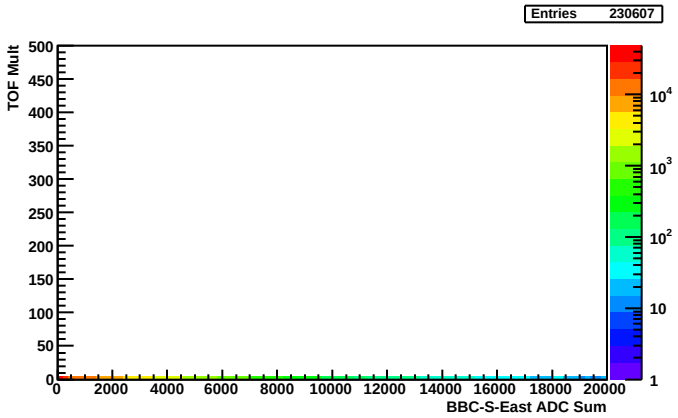

BBQ-VP003 (HI threshold)
Entries 3689712
TAC
QT Input Channel

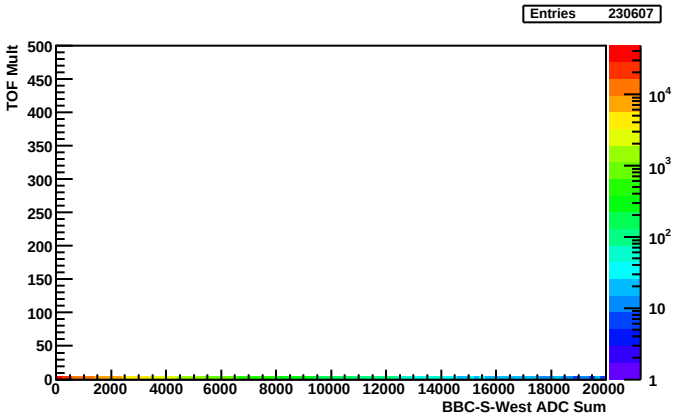

BBQ-VP004 (HI threshold)
Entries 3689712
ADC
QT Input Channel

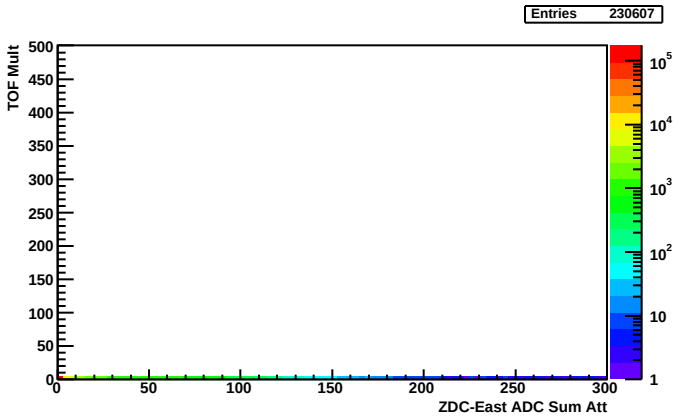

BBQ-VP004 (HI threshold)
Entries 3689712
TAC
QT Input Channel

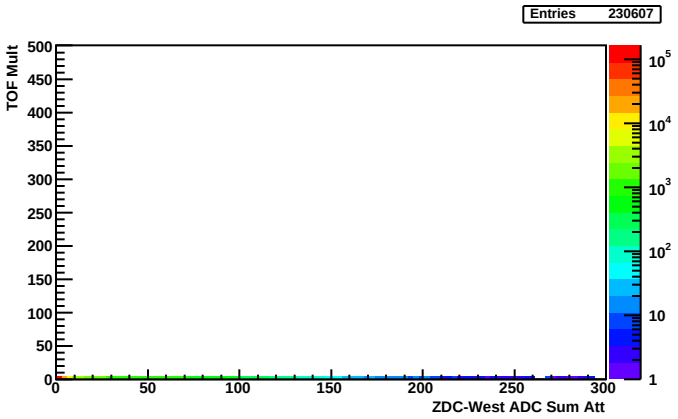

Entries 230607
TOF Mult
BBC-L-East ADC Sum

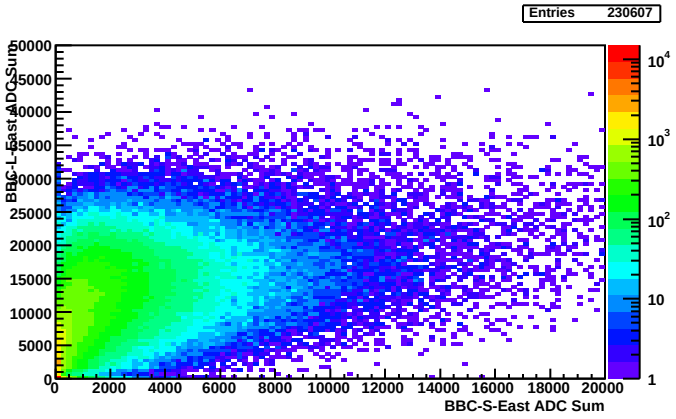

Entries 230607
TOF Mult
BBC-L-West ADC Sum

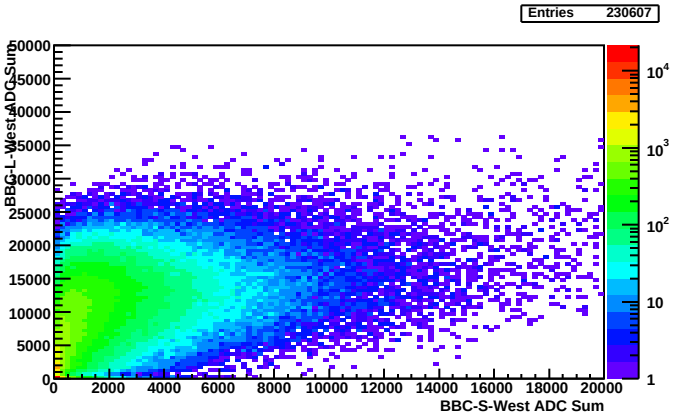

Entries 230607
TOF Mult
BBC-S-East ADC Sum

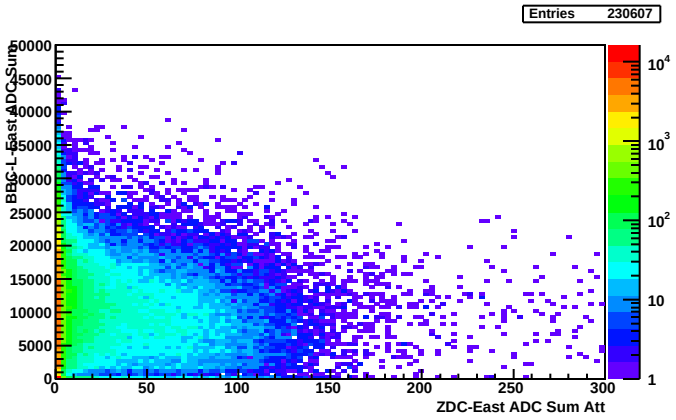

Entries 230607
TOF Mult
BBC-S-West ADC Sum

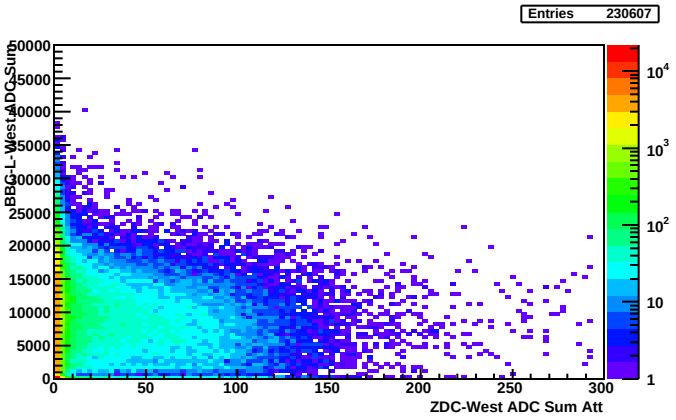

Entries 230607
TOF Mult
ZDC-East ADC Sum Att

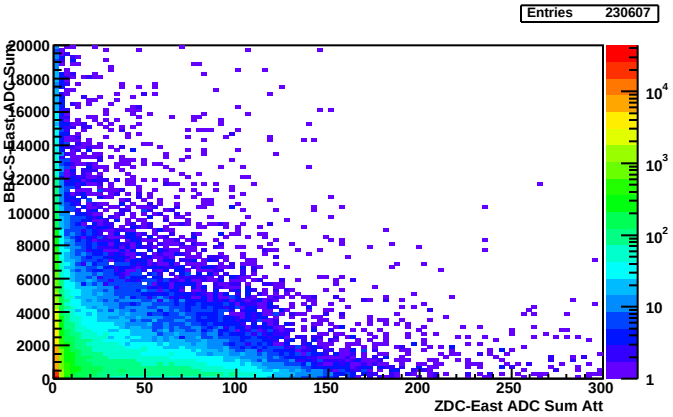

Entries 230607
TOF Mult
ZDC-West ADC Sum Att

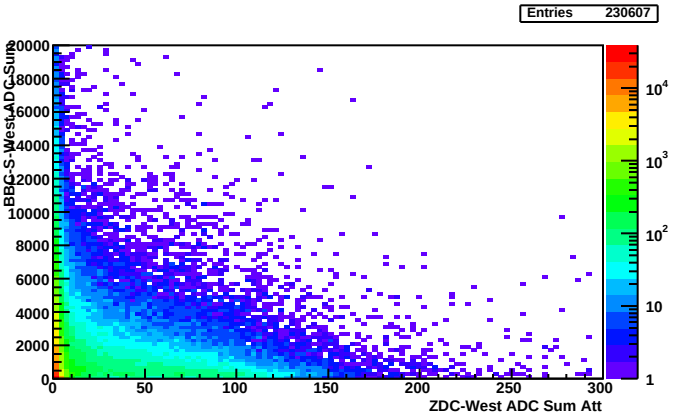

Entries 230607
BBC-L-East ADC Sum
BBC-S-East ADC Sum

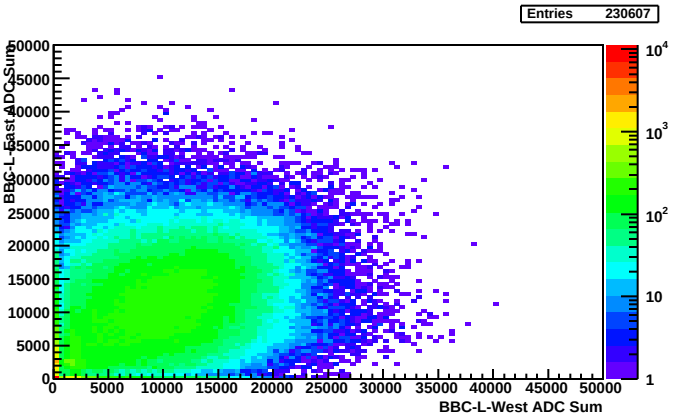

Entries 230607
BBC-L-West ADC Sum
BBC-S-West ADC Sum

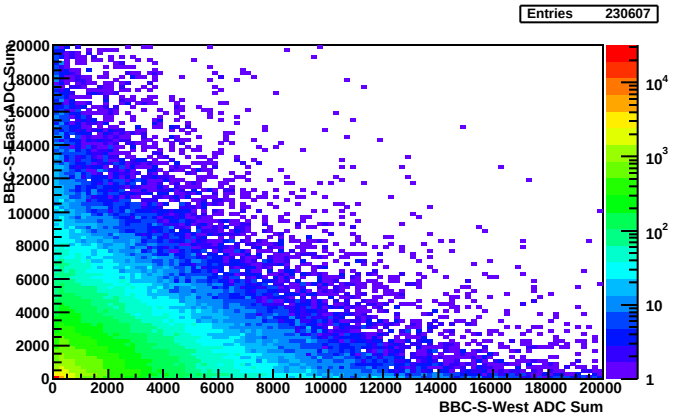

Entries 230607
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

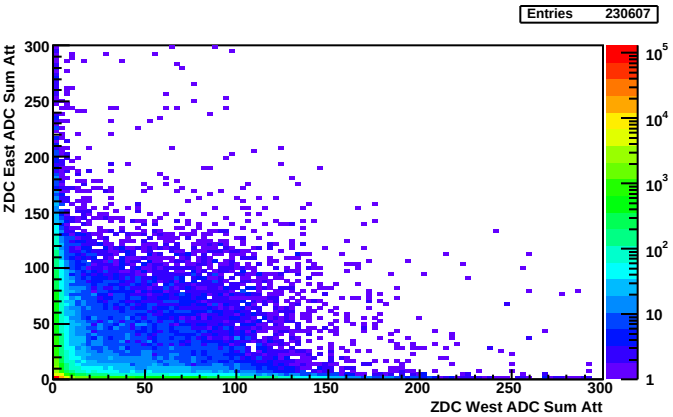

Entries 230607
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

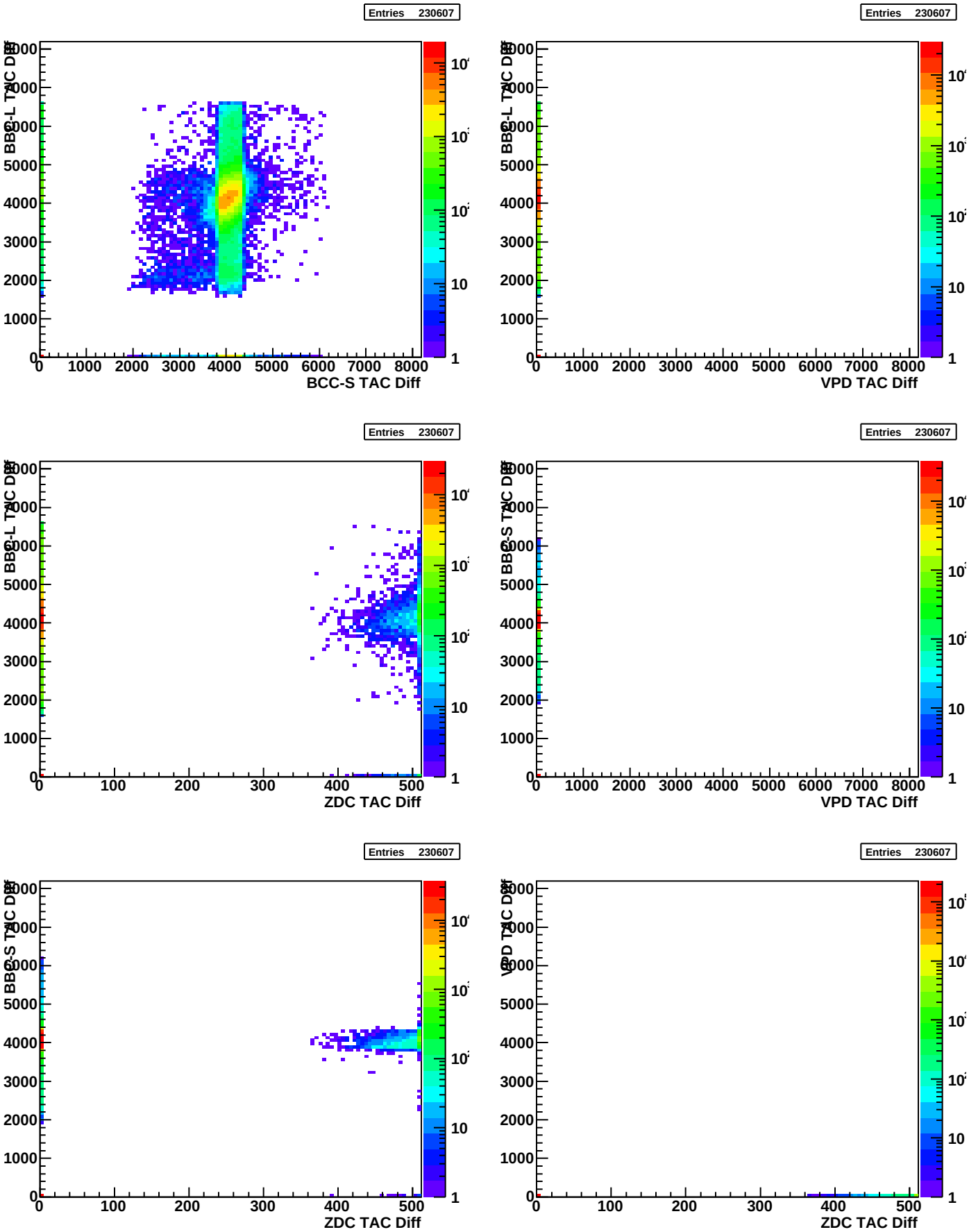

Entries 230607
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

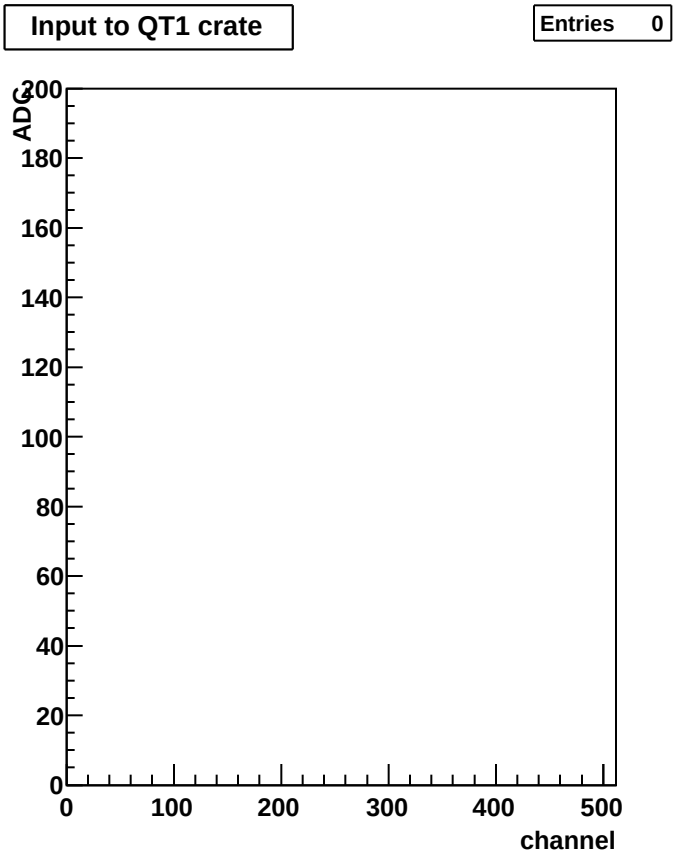

Entries 230607
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

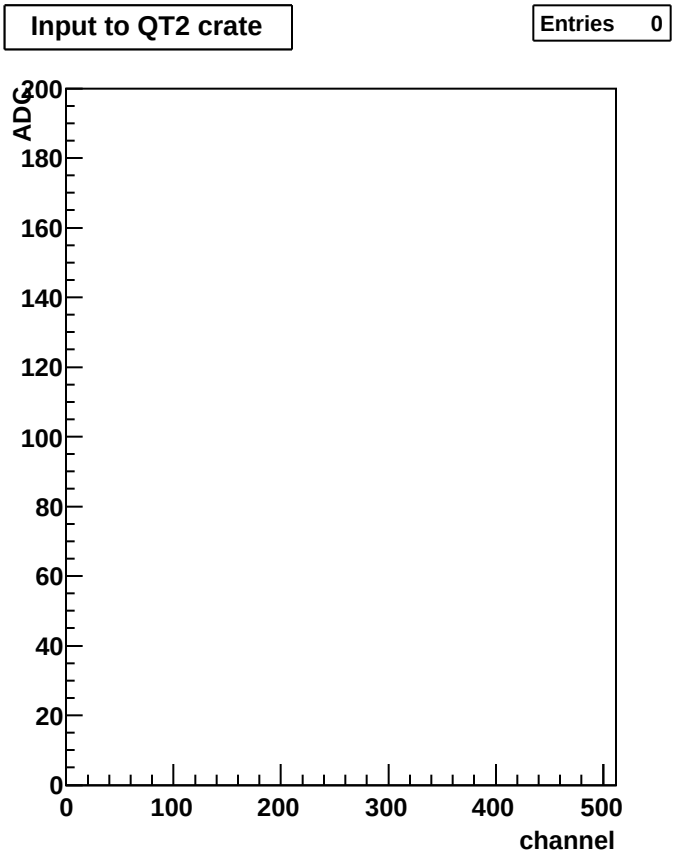

Entries 230607
BBC-L-East ADC Sum
BBC-L-West ADC Sum

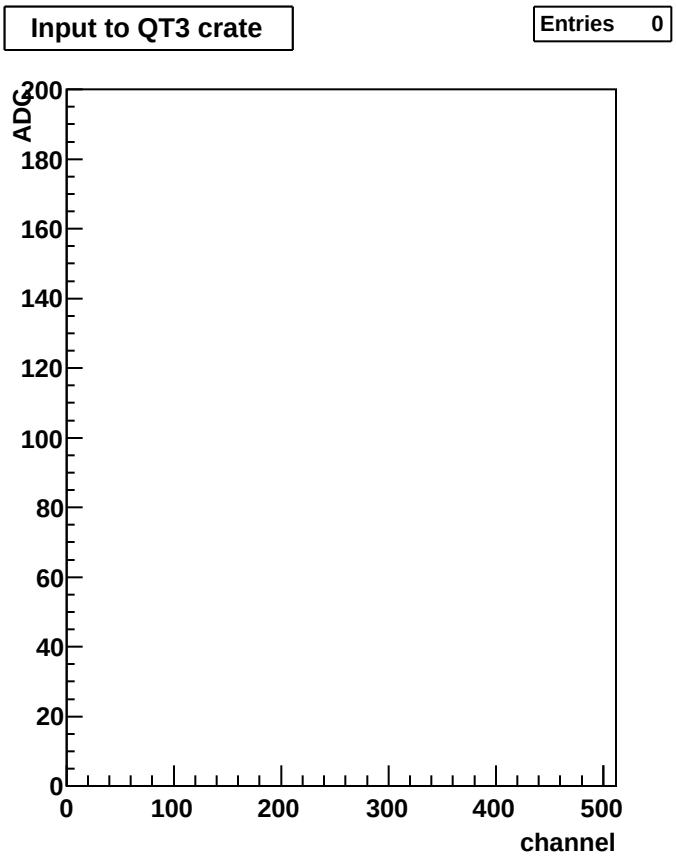

Entries 230607
BBC-S-East ADC Sum
BBC-S-West ADC Sum

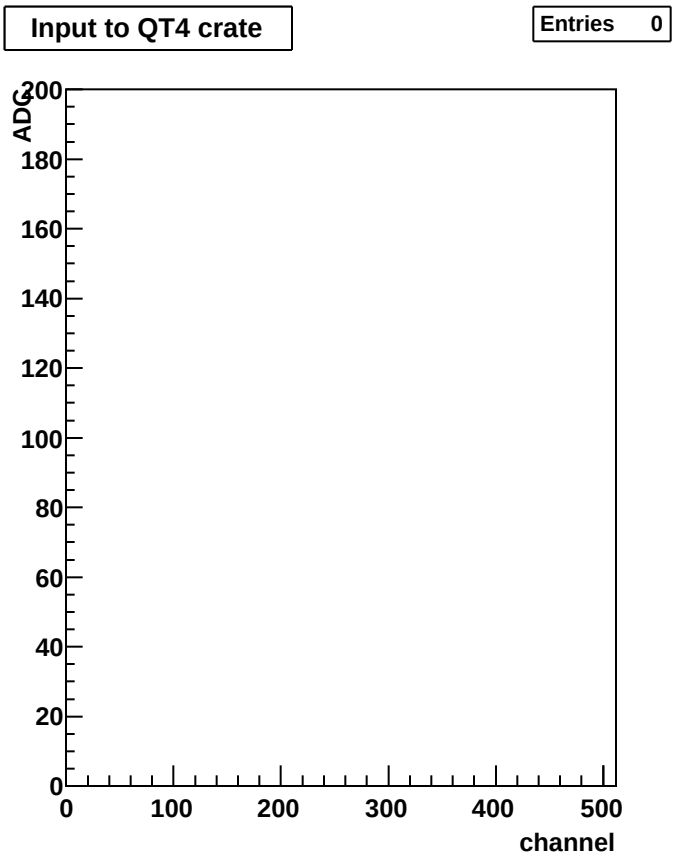

Entries 230607
ZDC East ADC Sum Att
ZDC West ADC Sum Att

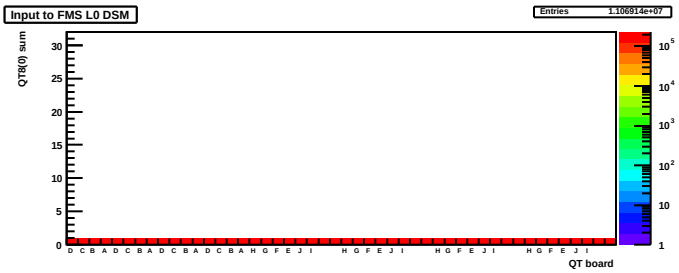

Entries 230607
BBC-L TAC Diff
BCC-S TAC Diff
Entries 230607
BBC-L TAC Diff
VPD TAC Diff
Entries 230607
BBC-L TAC Diff
ZDC TAC Diff
Entries 230607
BBC-S TAC Diff
VPD TAC Diff
Entries 230607
BBC-S TAC Diff
ZDC TAC Diff
Entries 230607
VPD TAC Diff
ZDC TAC Diff

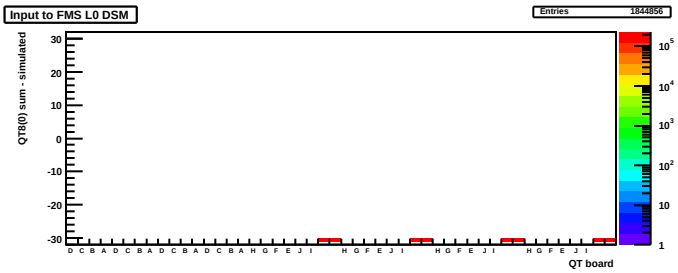

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

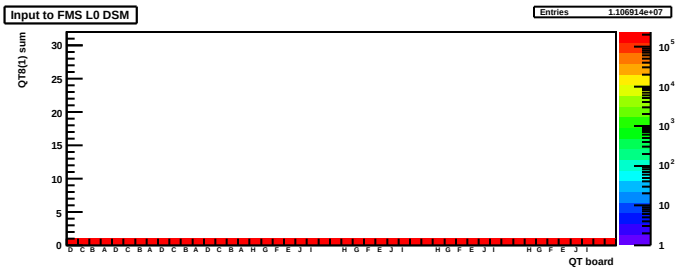

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

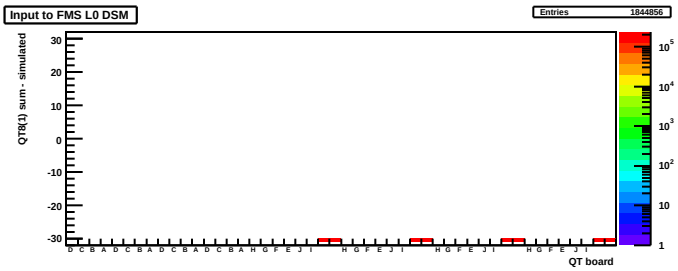

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

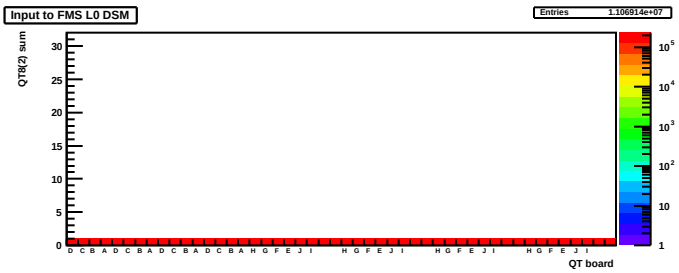

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

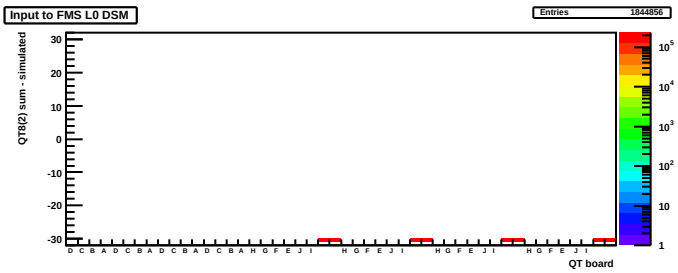

Input to FMS L0 DSM
Entries 1.106914e+07
QT8(0) sum
QT board

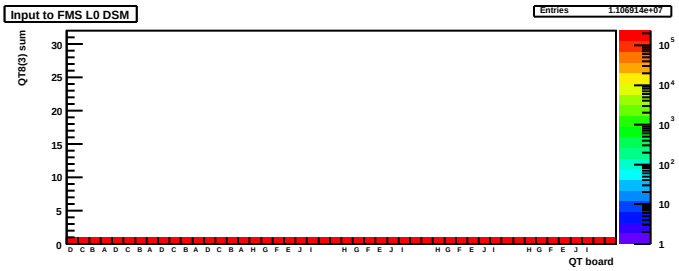

Input to FMS L0 DSM
Entries 1844856
QT8(0) sum - simulated
QT board

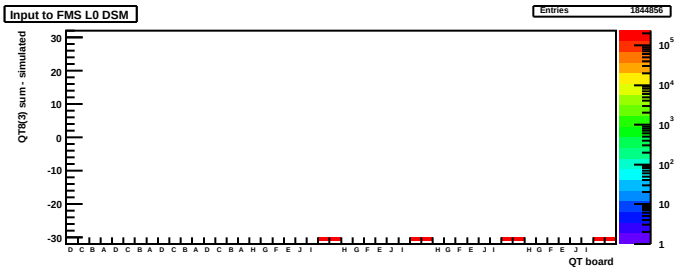

Input to FMS L0 DSM
Entries 1.106914e+07
QT8(1) sum
QT board

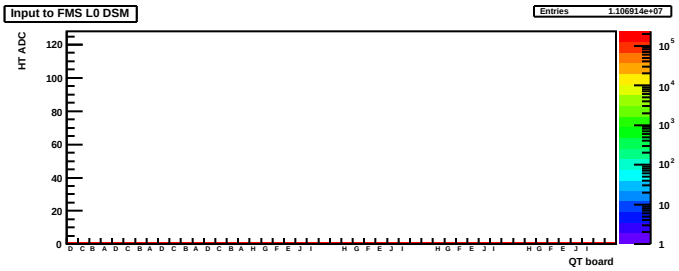

Input to FMS L0 DSM
Entries 1844856
QT8(1) sum - simulated
QT board

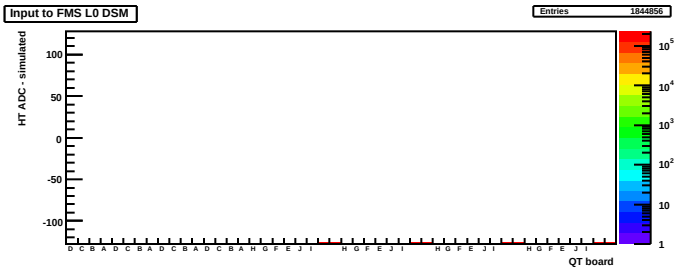

Input to FMS L0 DSM
Entries 1.106914e+07
QT8(2) sum
QT board

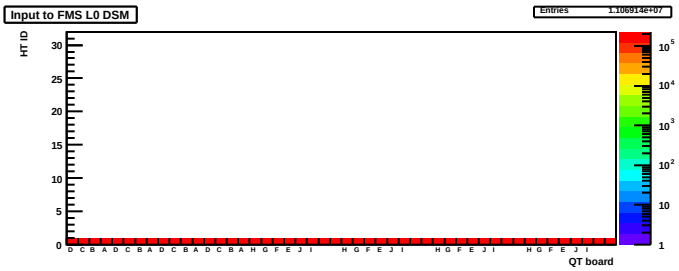

Input to FMS L0 DSM
Entries 1844856
QT8(2) sum - simulated
QT board

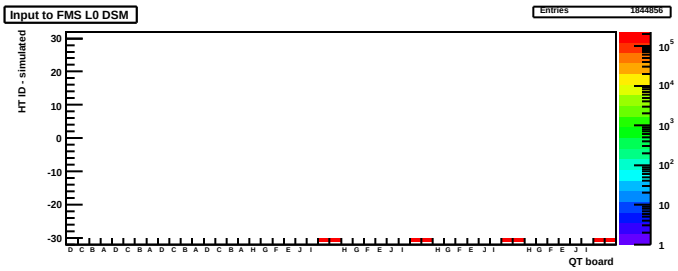

Input to FMS L0 DSM
Entries 1.106914e+07
QT8(3) sum
QT board

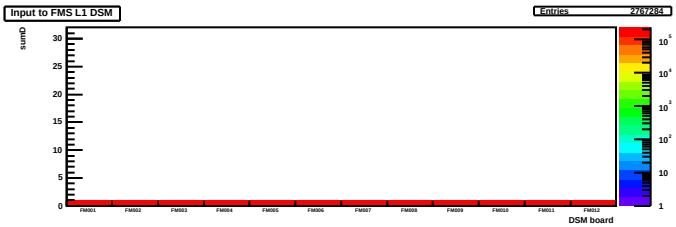

Input to FMS L0 DSM
Entries 1844856
QT8(3) sum - simulated
QT board

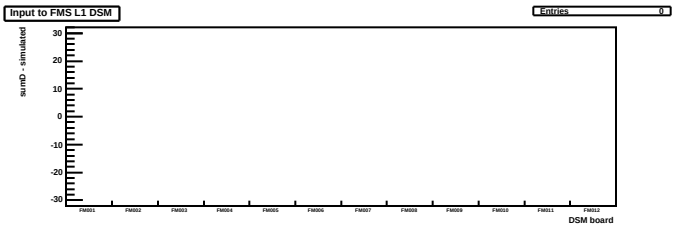

Input to FMS L0 DSM
Entries 1.106914e+07
HT ADC
QT board

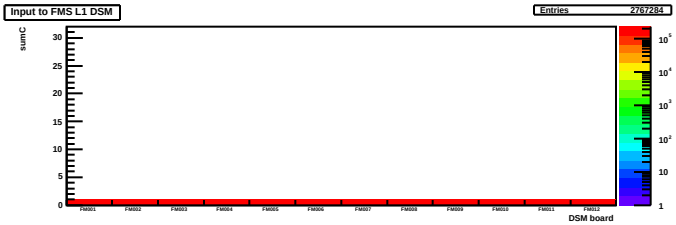

Input to FMS L0 DSM
Entries 1844856
HT ADC - simulated
QT board

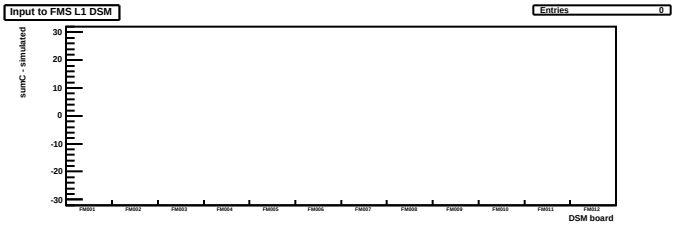

Input to FMS L0 DSM
Entries 1.106914e+07
HT ID
QT board

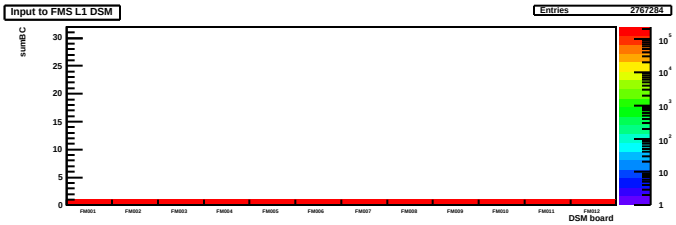

Input to FMS L0 DSM
Entries 1844856
HT ID - simulated
QT board

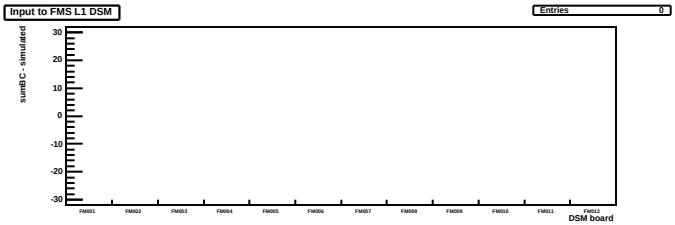

Input to FMS L1 DSM
Entries 2767284
sumD
DSM board

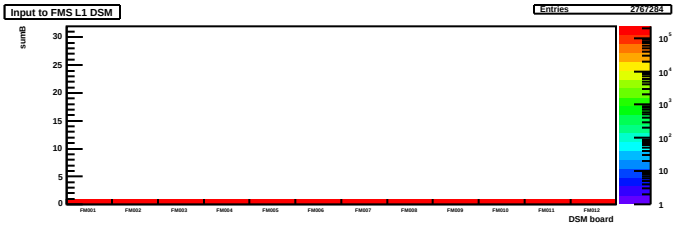

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

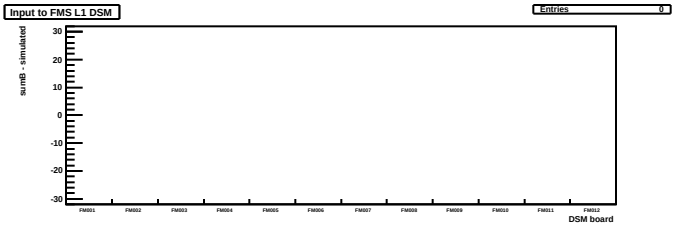

Input to FMS L1 DSM
Entries 2767284
sumC
DSM board

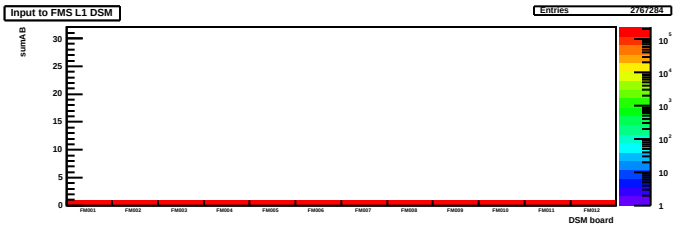

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

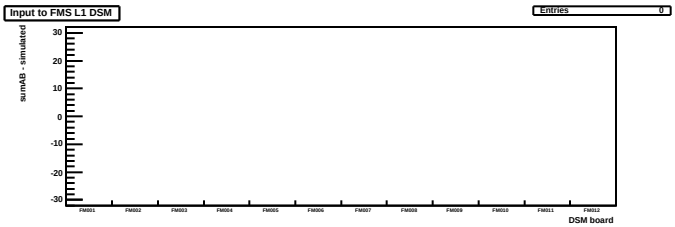

Input to FMS L1 DSM
Entries 2767284
sumBC
DSM board

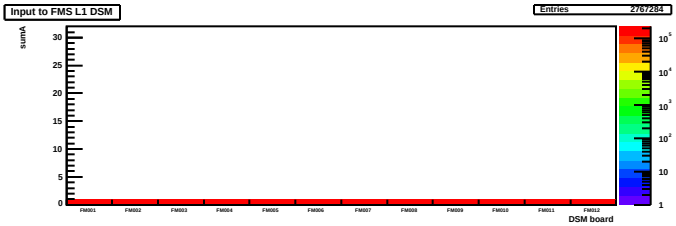

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

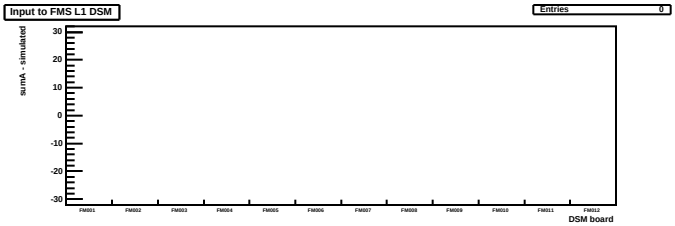

Input to FMS L1 DSM
Entries 2767284
sumB
DSM board

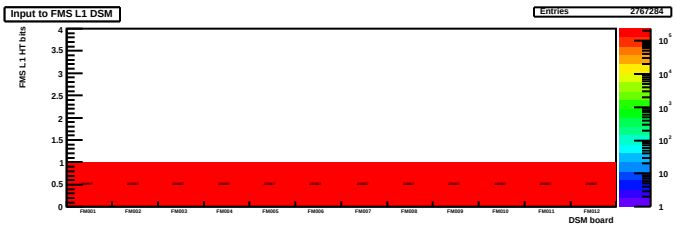

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

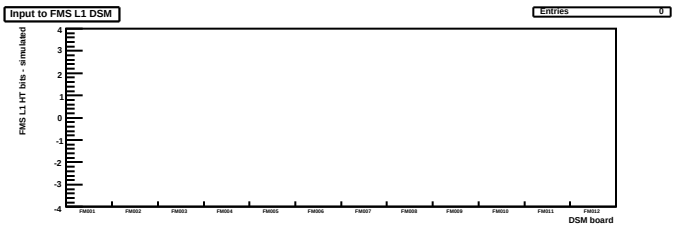

Input to FMS L1 DSM
Entries 2767284
sumAB
DSM board

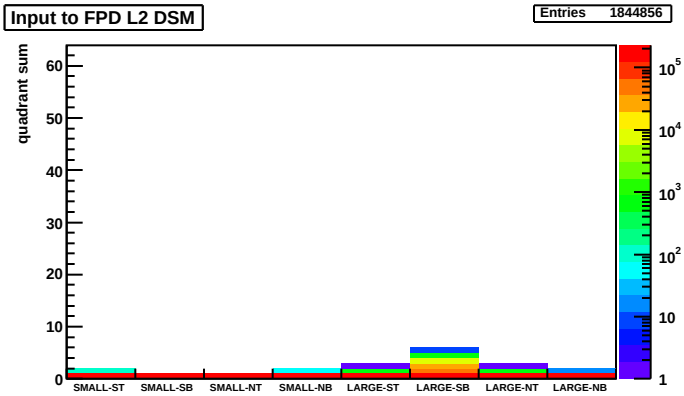

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

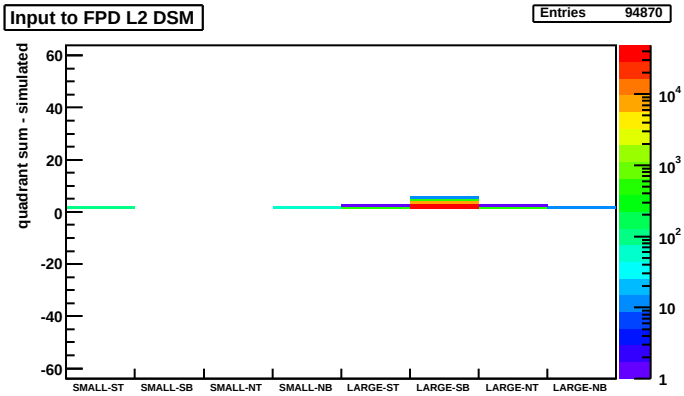

Input to FMS L1 DSM
Entries 2767284
sumA
DSM board

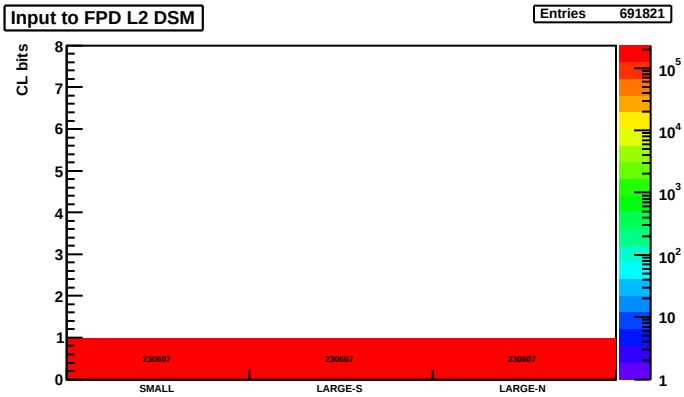

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

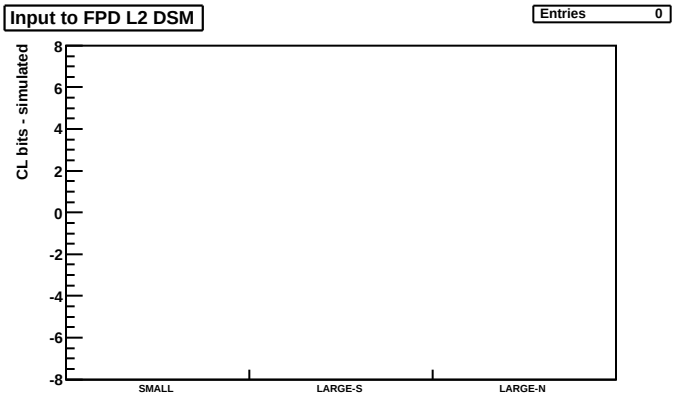

Input to FMS L1 DSM
Entries 2767284
FMS L1 HT bits
DSM board

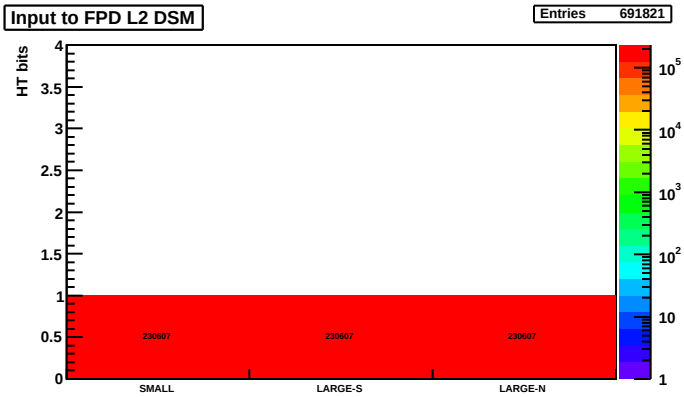

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

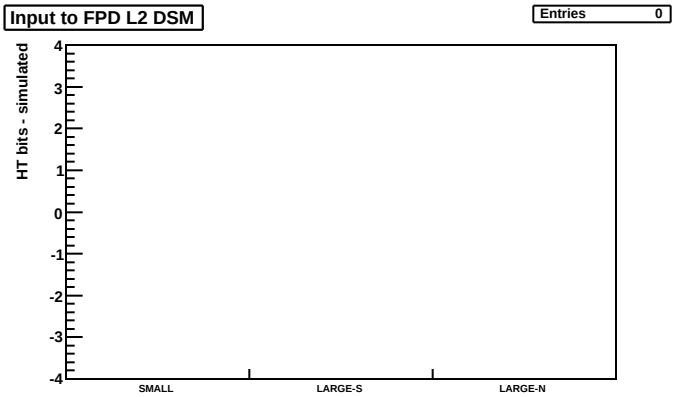

Input to FPD L2 DSM
Entries 1844856
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

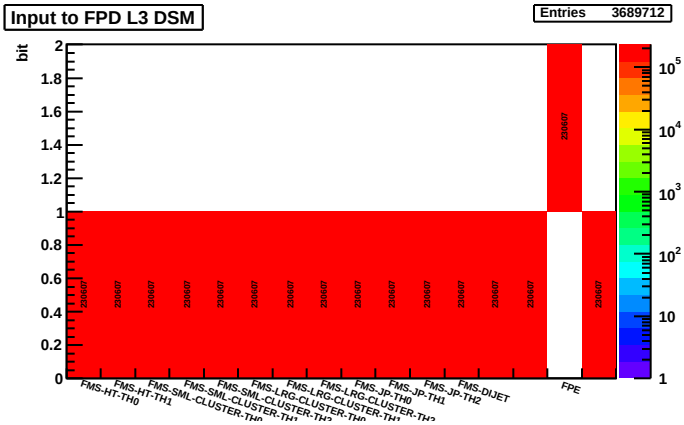

Input to FPD L2 DSM
Entries 94870
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

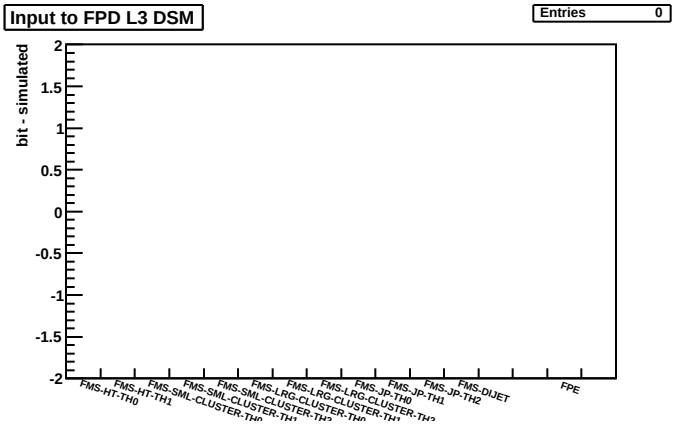

Input to FPD L2 DSM
Entries 691821
CL bits
230607
230607
230607
SMALL
LARGE-S
LARGE-N

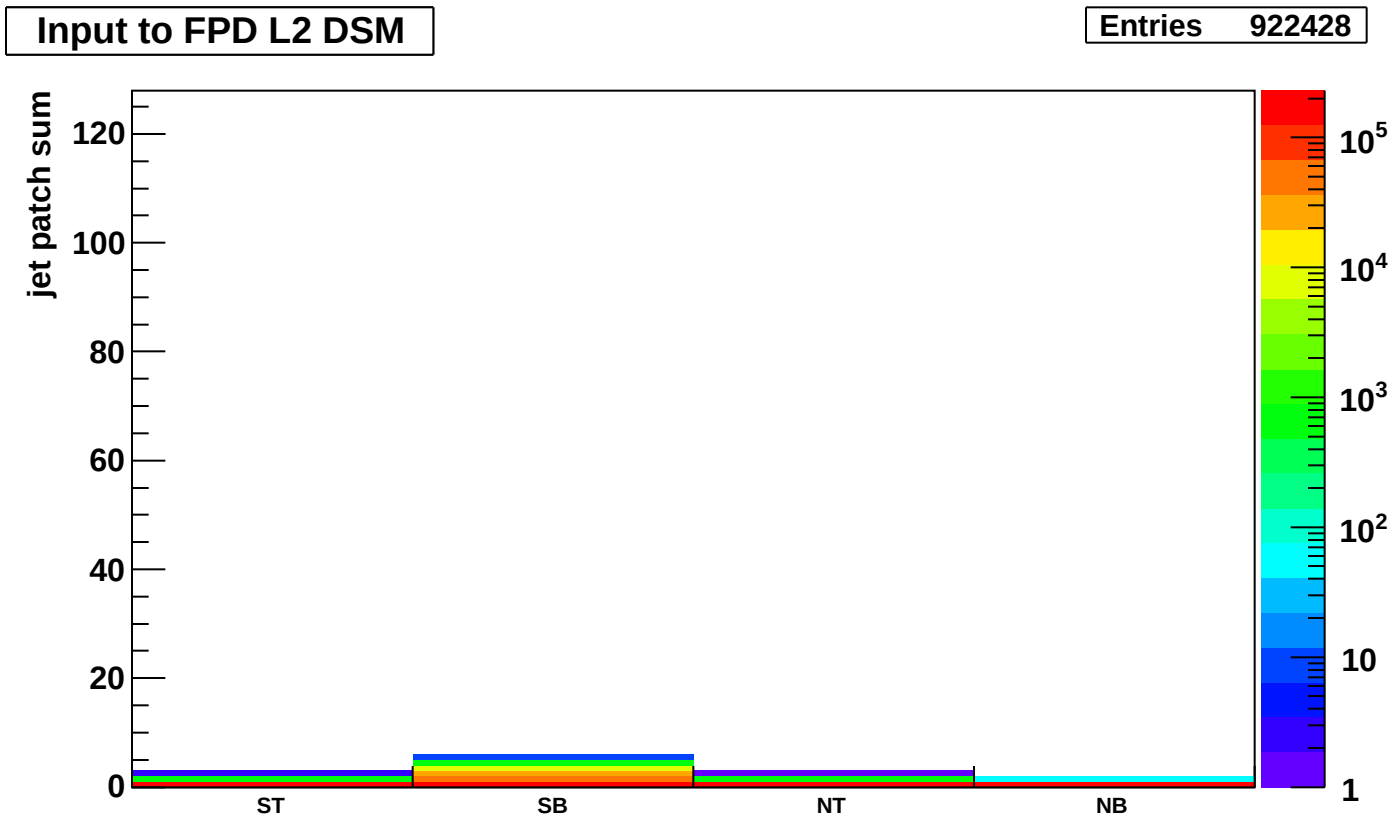

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

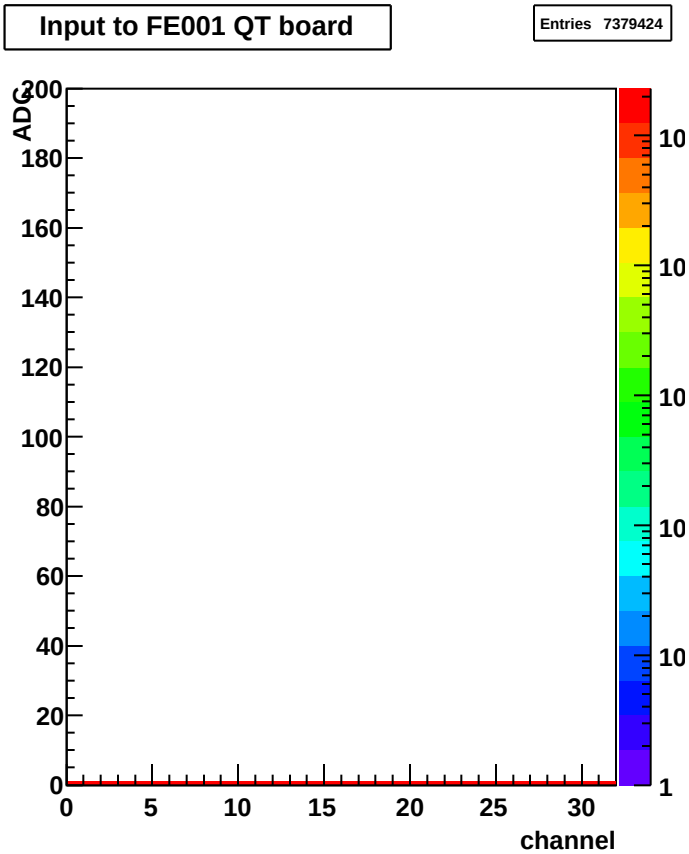

Input to FPD L2 DSM
Entries 691821
HT bits
230607
230607
230607
SMALL
LARGE-S
LARGE-N

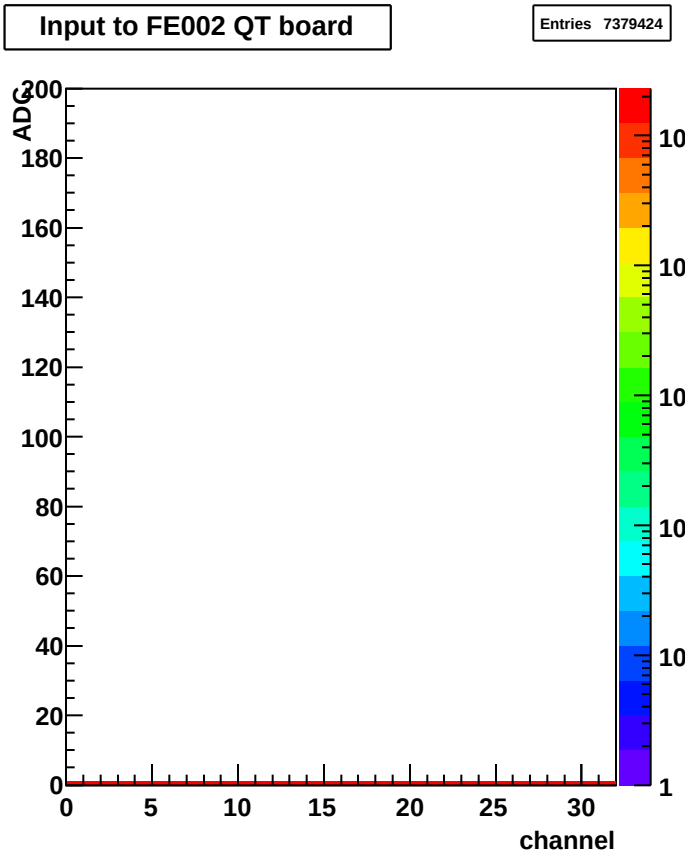

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

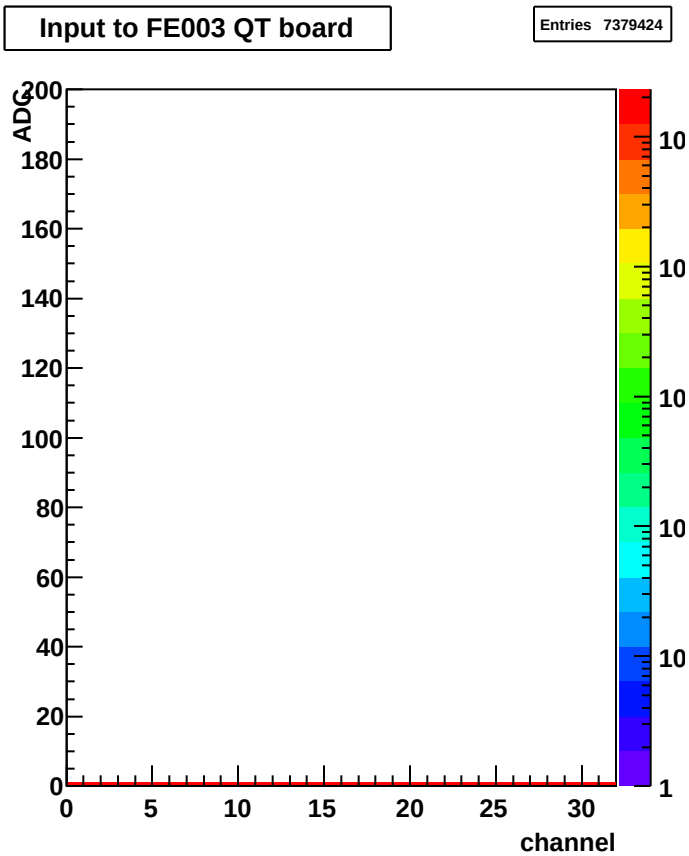

Input to FPD L3 DSM
Entries 3689712
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

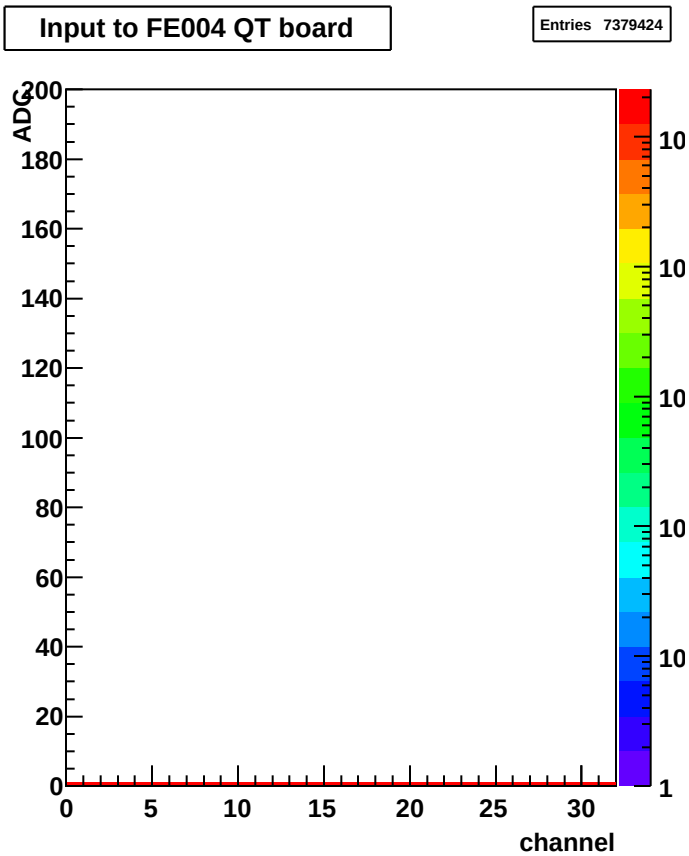

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

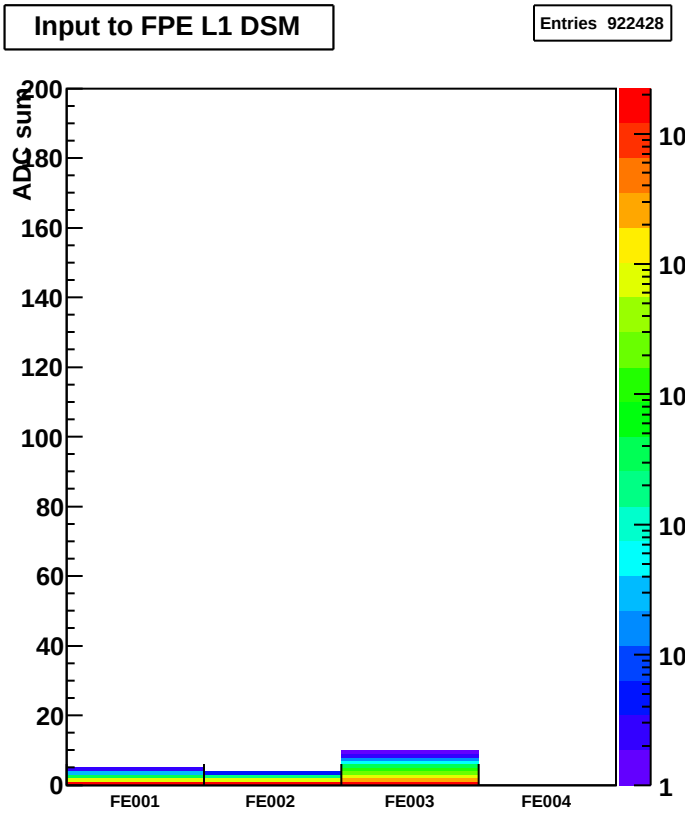

Input to FPD L2 DSM
Entries 922428
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

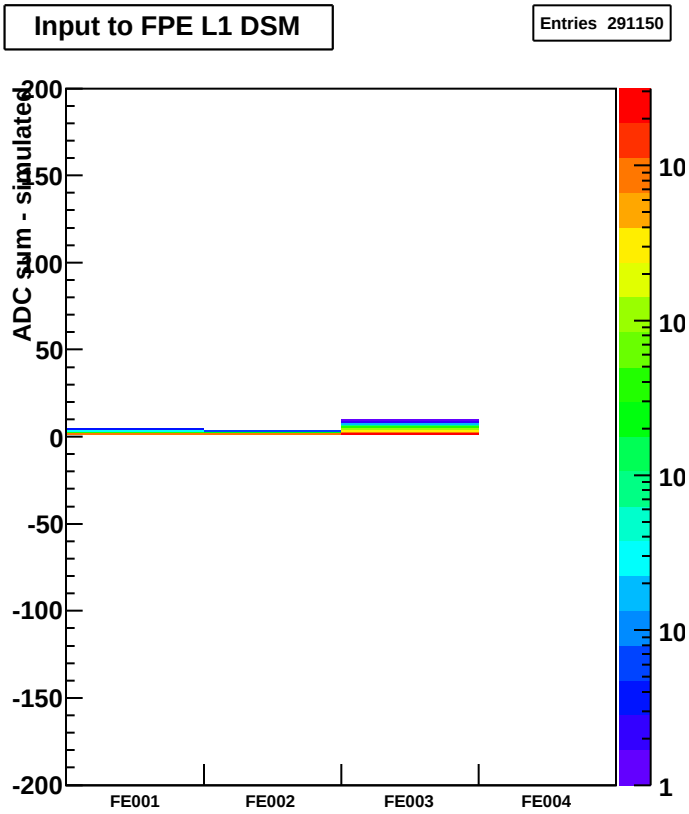

Input to FE001 QT board
Entries 7379424
ADC
channel

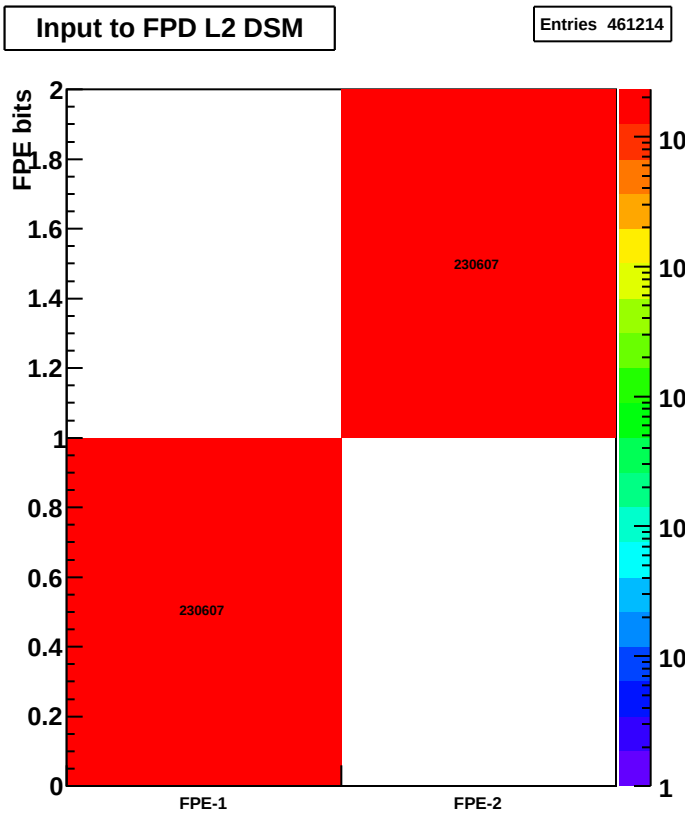

Input to FE002 QT board
Entries 7379424
ADC
channel

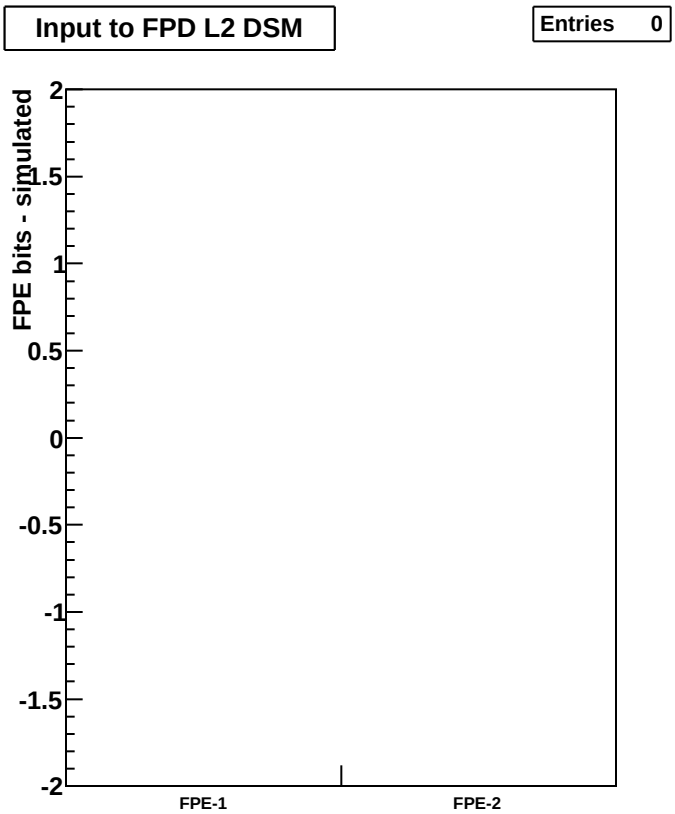

Input to FE003 QT board
Entries 7379424
ADC
channel


Input to FE004 QT board
Entries 7379424
ADC
channel


Input to FPE L1 DSM
Entries 922428
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 291150
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 461214
FPE bits
230607
230607
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2