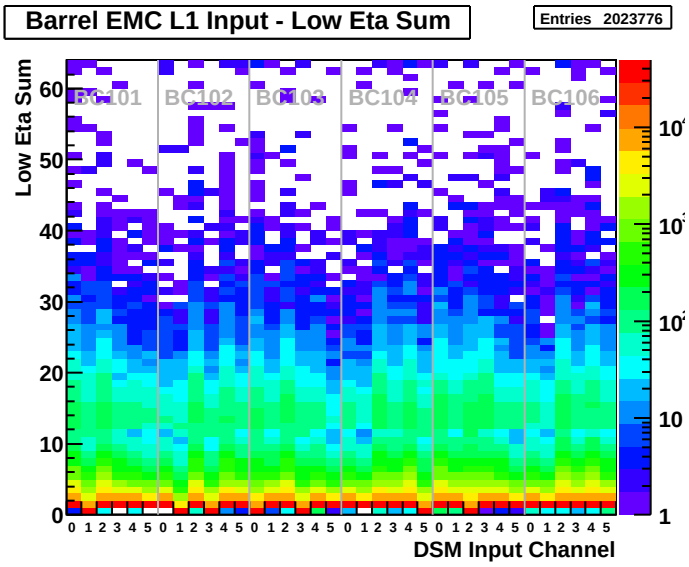
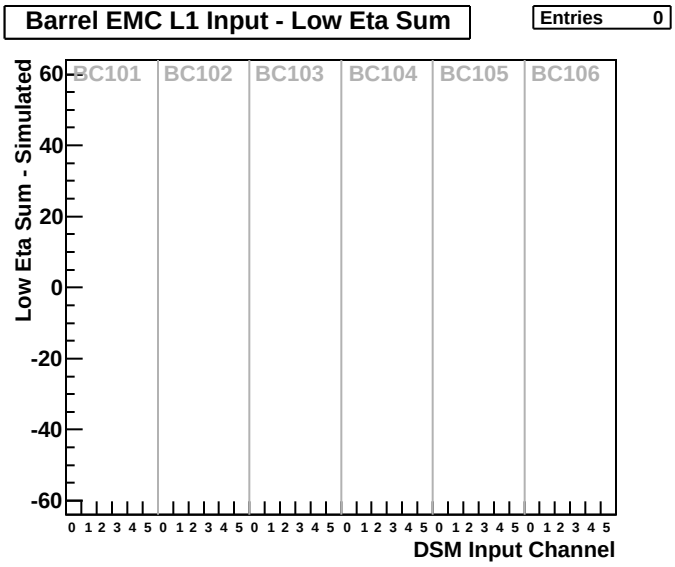
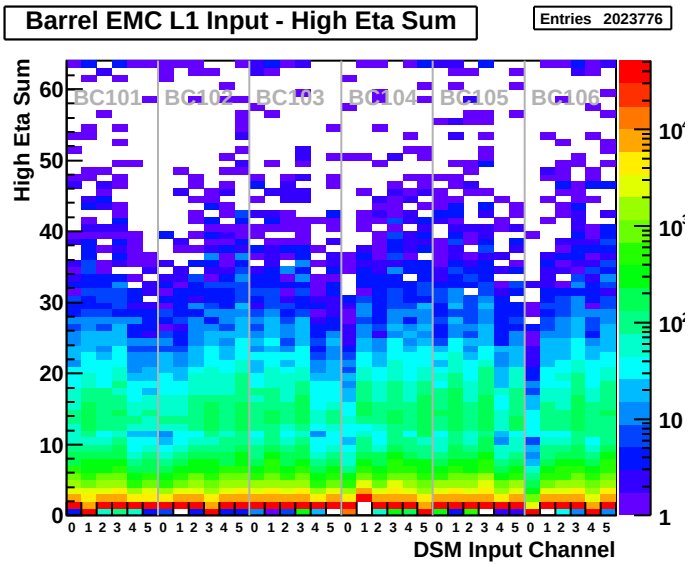
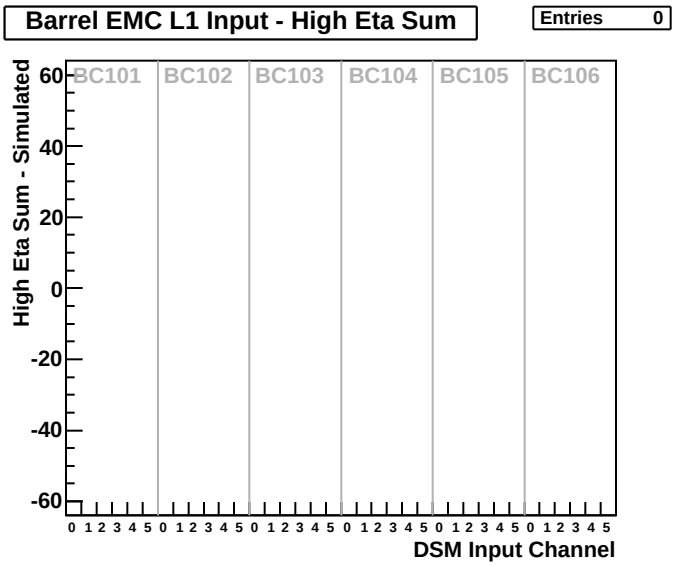
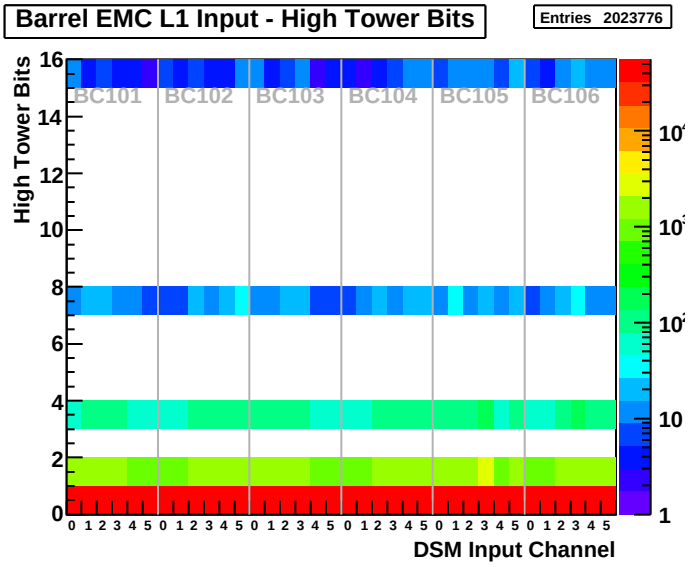
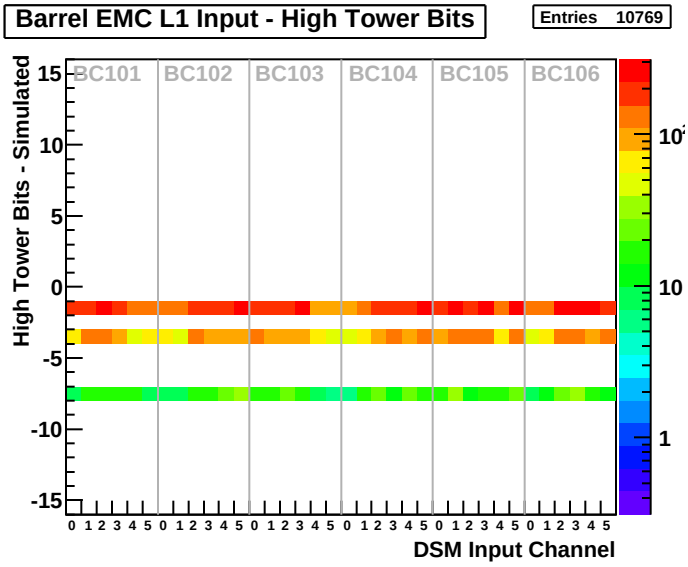

Barrel EMC L1 Input - Low Eta Sum
Entries 2023776
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

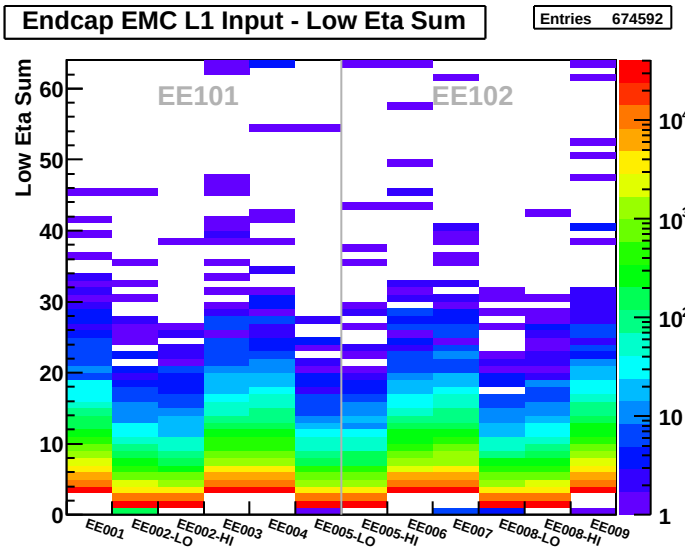

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

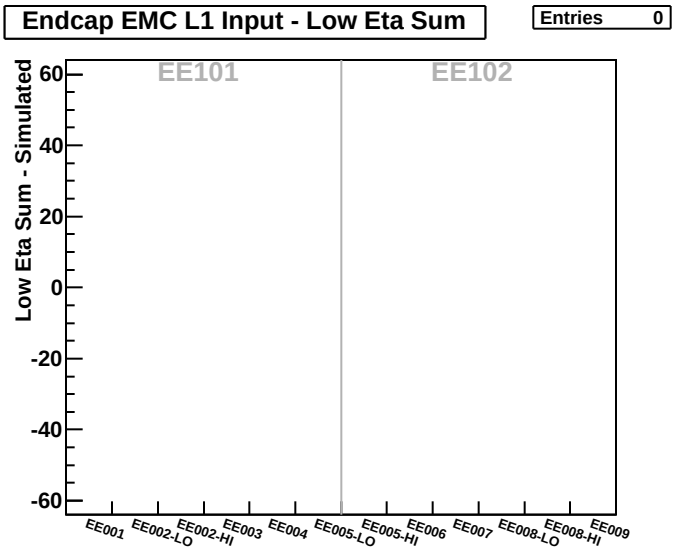

Barrel EMC L1 Input - High Eta Sum
Entries 2023776
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

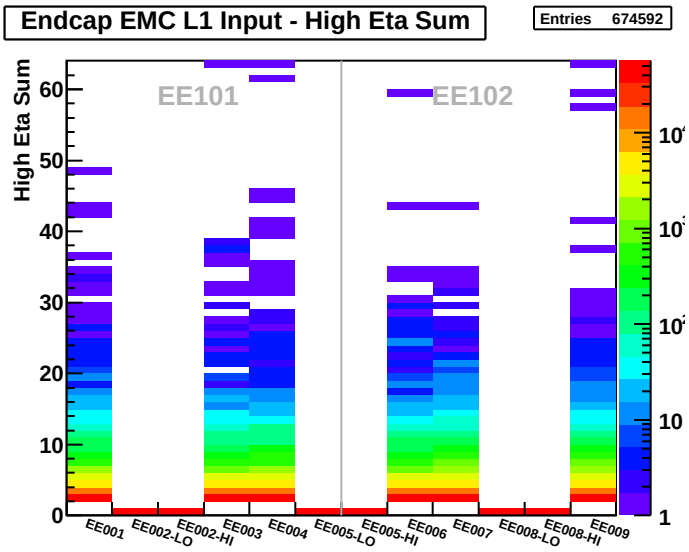

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

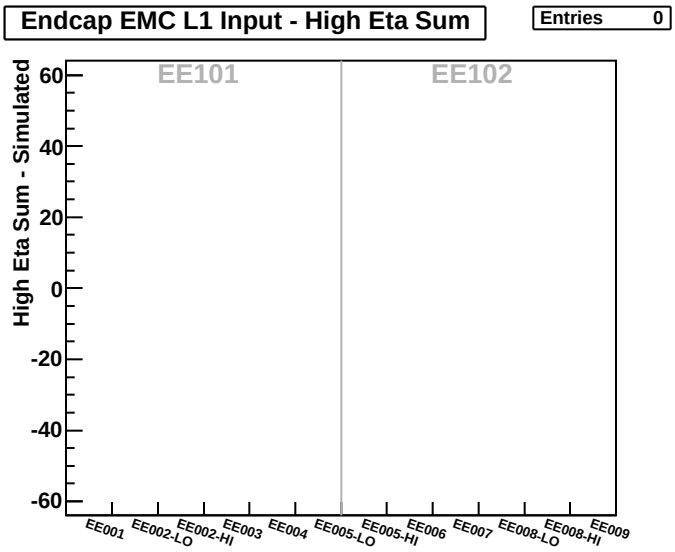

Barrel EMC L1 Input - High Tower Bits
Entries 2023776
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

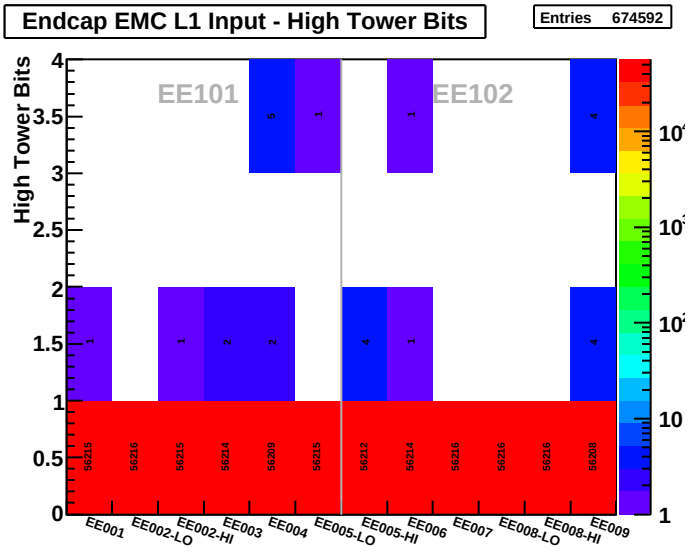

Barrel EMC L1 Input - High Tower Bits
Entries 10769
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

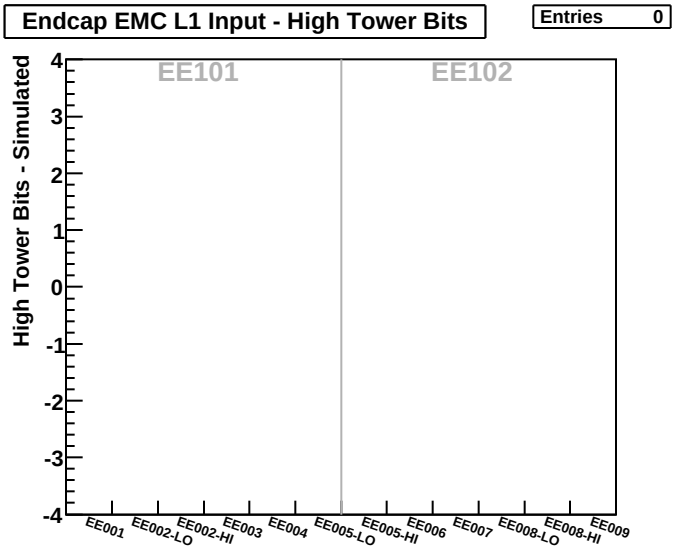

Endcap EMC L1 Input - Low Eta Sum
Entries 674592
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

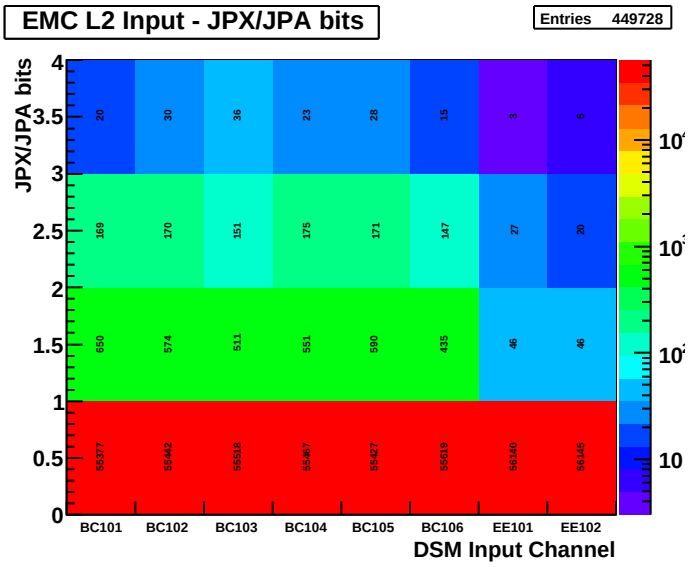

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

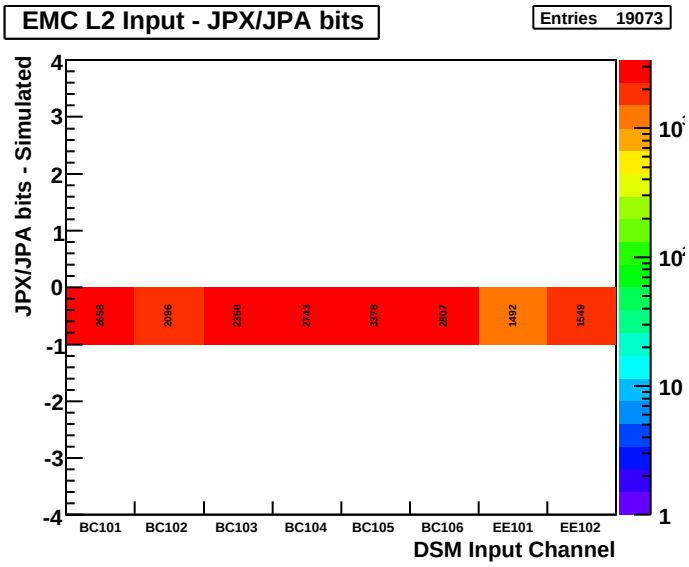

Endcap EMC L1 Input - High Eta Sum
Entries 674592
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

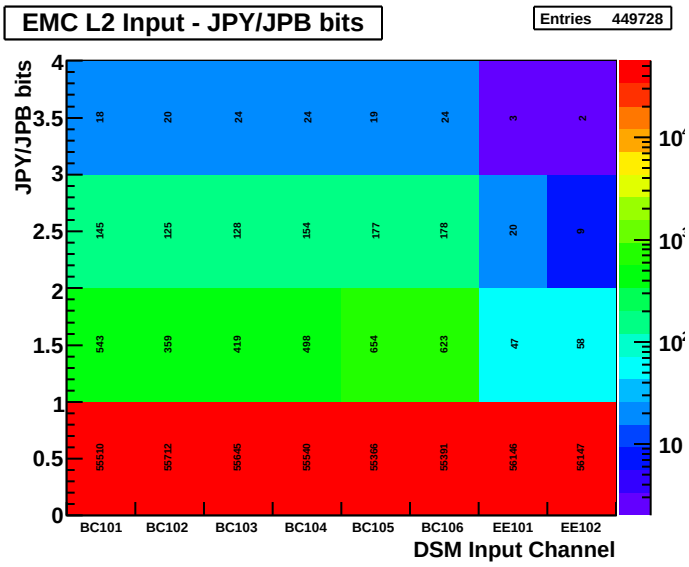

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

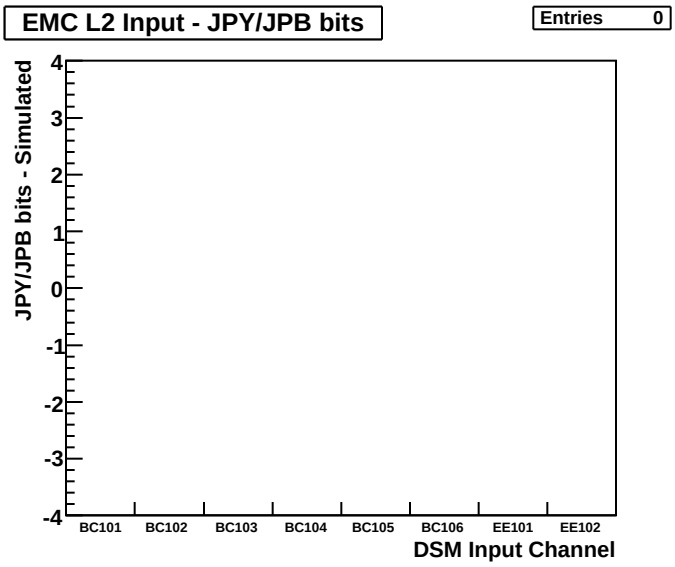

Endcap EMC L1 Input - High Tower Bits
Entries 674592
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

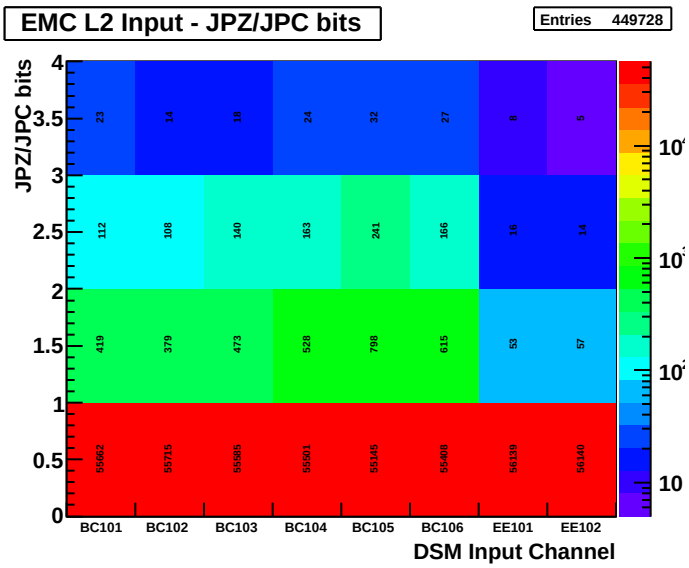

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

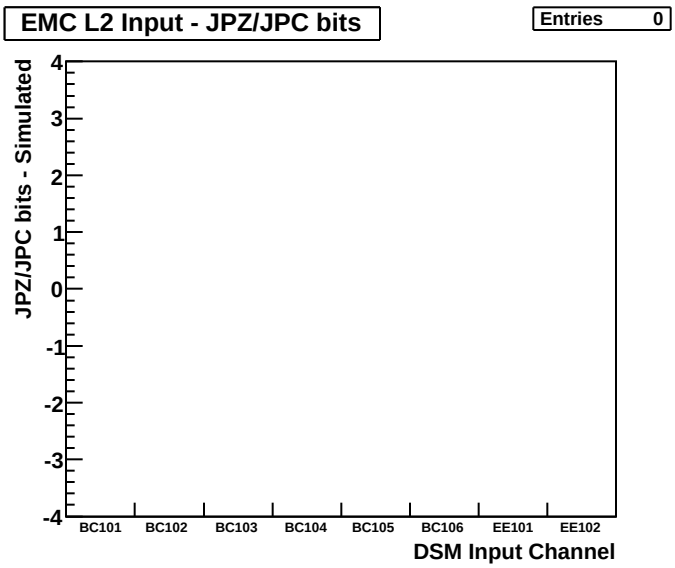

EMC L2 Input - JPX/JPA bits
Entries 449728
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
20 30 36 23 28 15 3 5
169 170 151 175 171 147 27 20
650 574 511 551 590 435 46 46
55377 55442 55518 55467 55427 55619 56140 56145

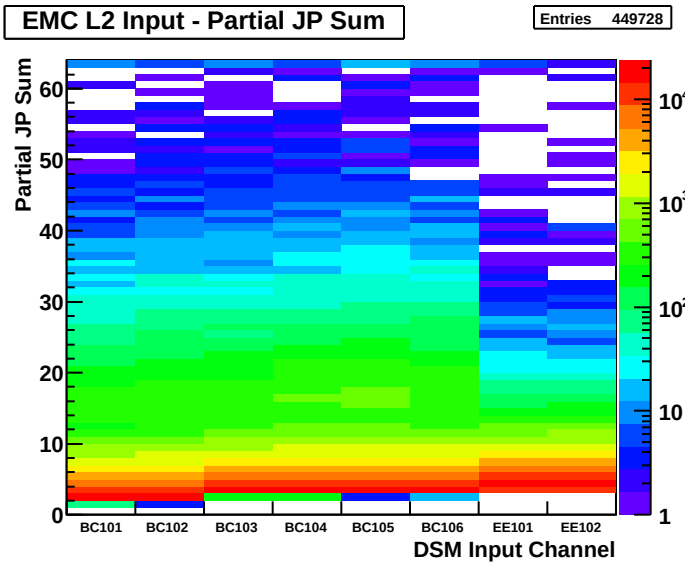

EMC L2 Input - JPX/JPA bits
Entries 19073
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2658 2096 2350 2743 3378 2807 1492 1549

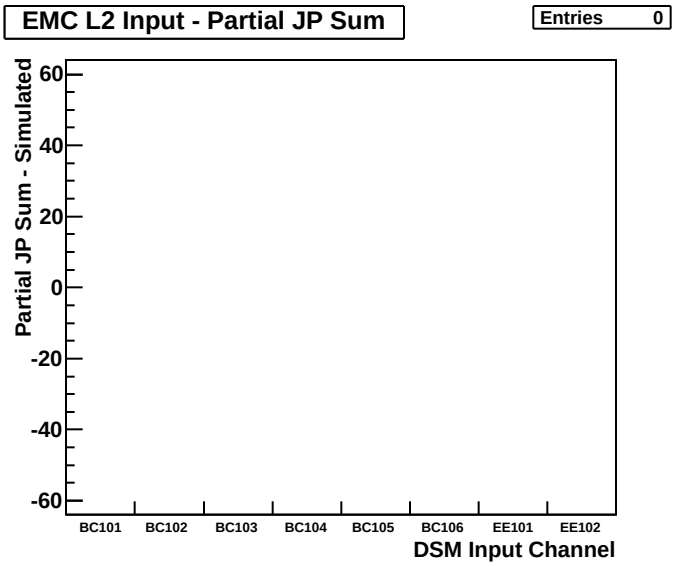

EMC L2 Input - JPY/JPB bits
Entries 449728
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
18 20 24 24 19 24 3 2
145 125 128 154 177 178 20 9
543 359 419 498 654 623 47 58
55510 55712 55646 55540 55306 55391 56146 56147

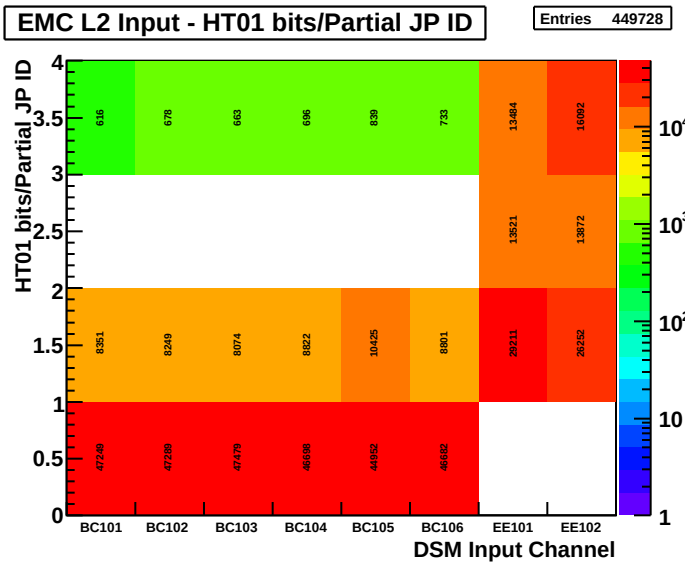

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

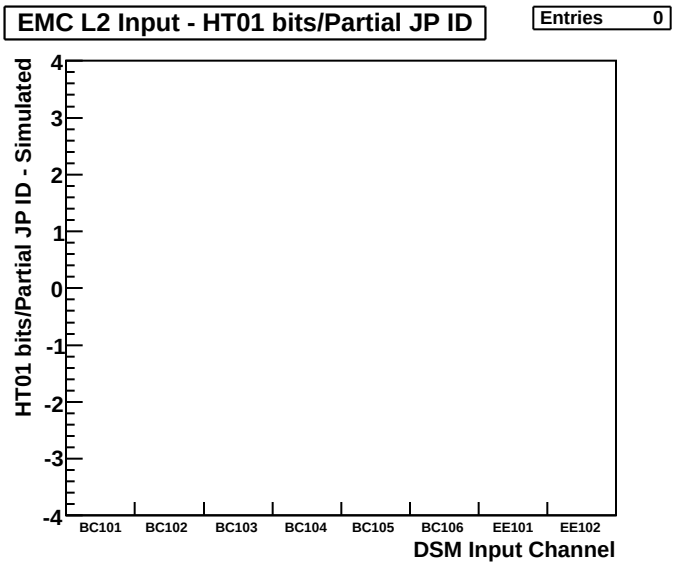

EMC L2 Input - JPZ/JPC bits
Entries 449728
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
23 14 18 24 32 27 8 5
112 108 140 163 241 166 16 14
419 379 473 528 798 615 53 57
55662 55715 55585 55501 55145 55408 56139 56140

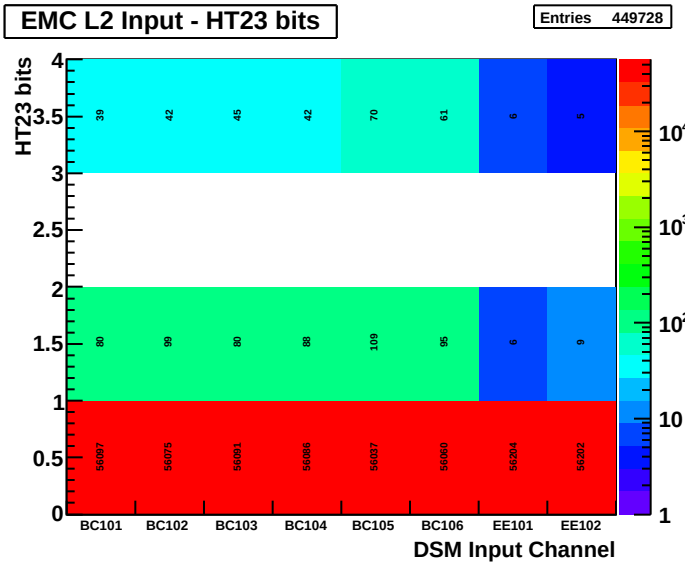

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

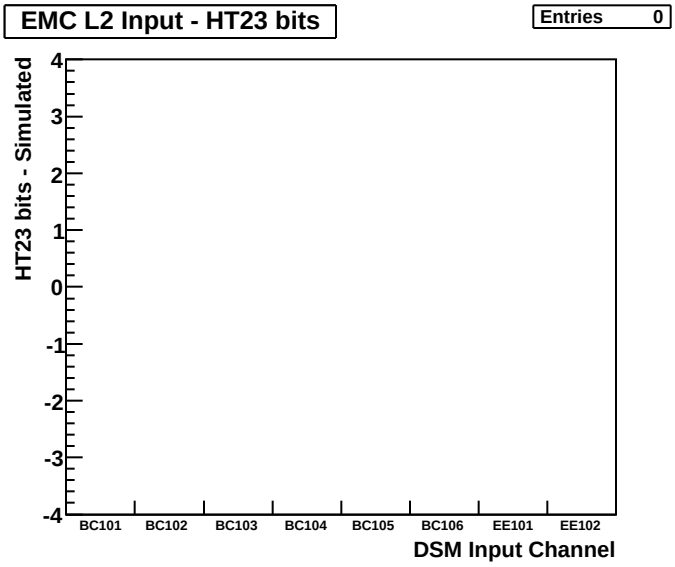

EMC L2 Input - Partial JP Sum
Entries 449728
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

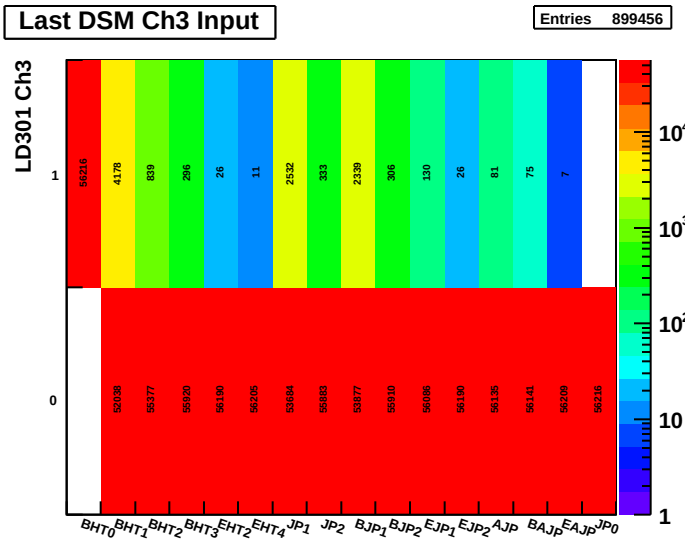

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

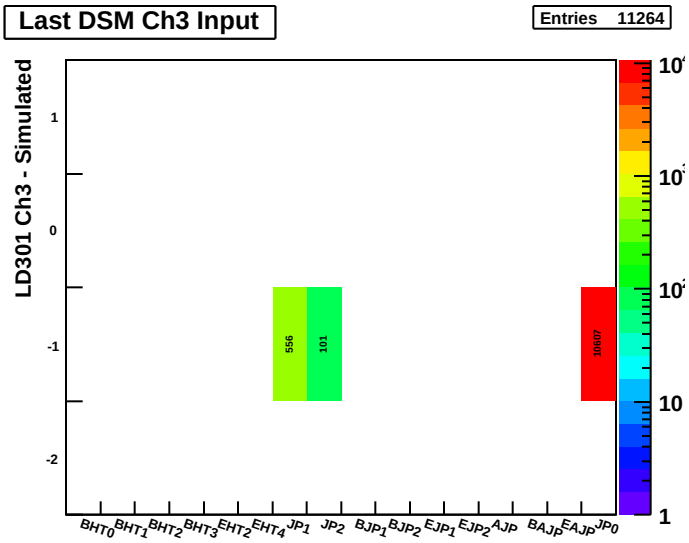

EMC L2 Input - HT01 bits/Partial JP ID
Entries 449728
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

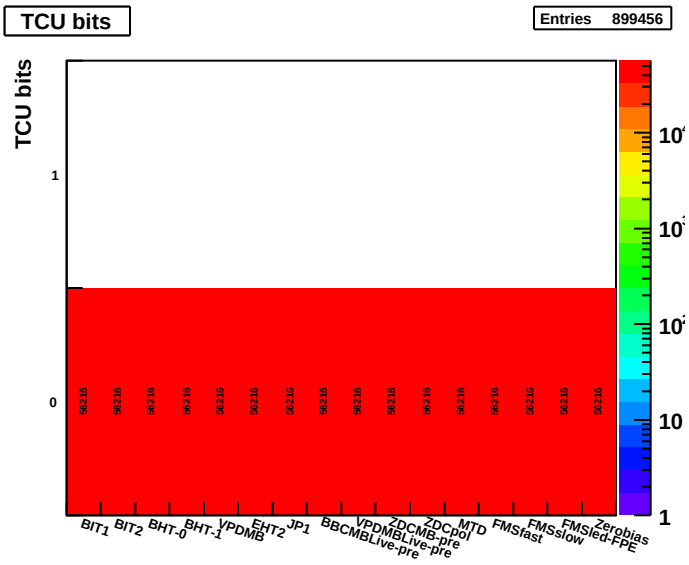

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

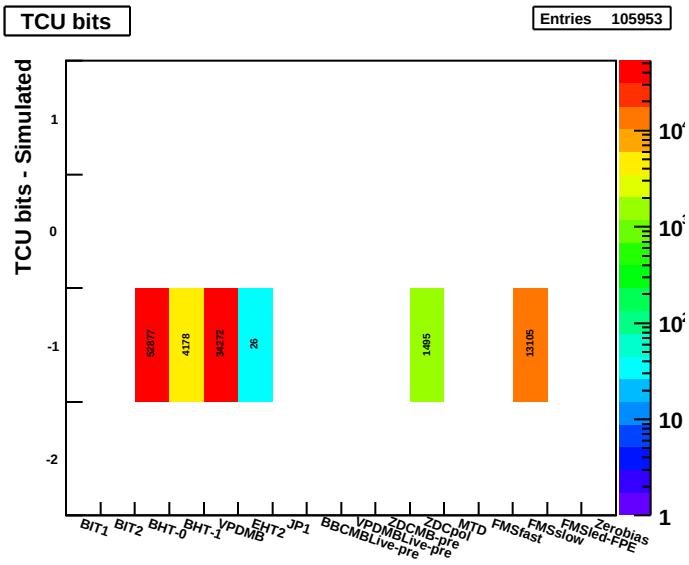

EMC L2 Input - HT23 bits
Entries 449728
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

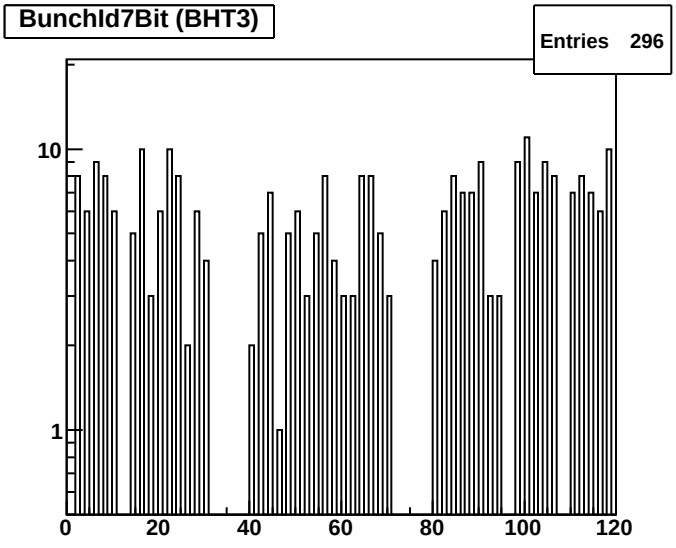

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

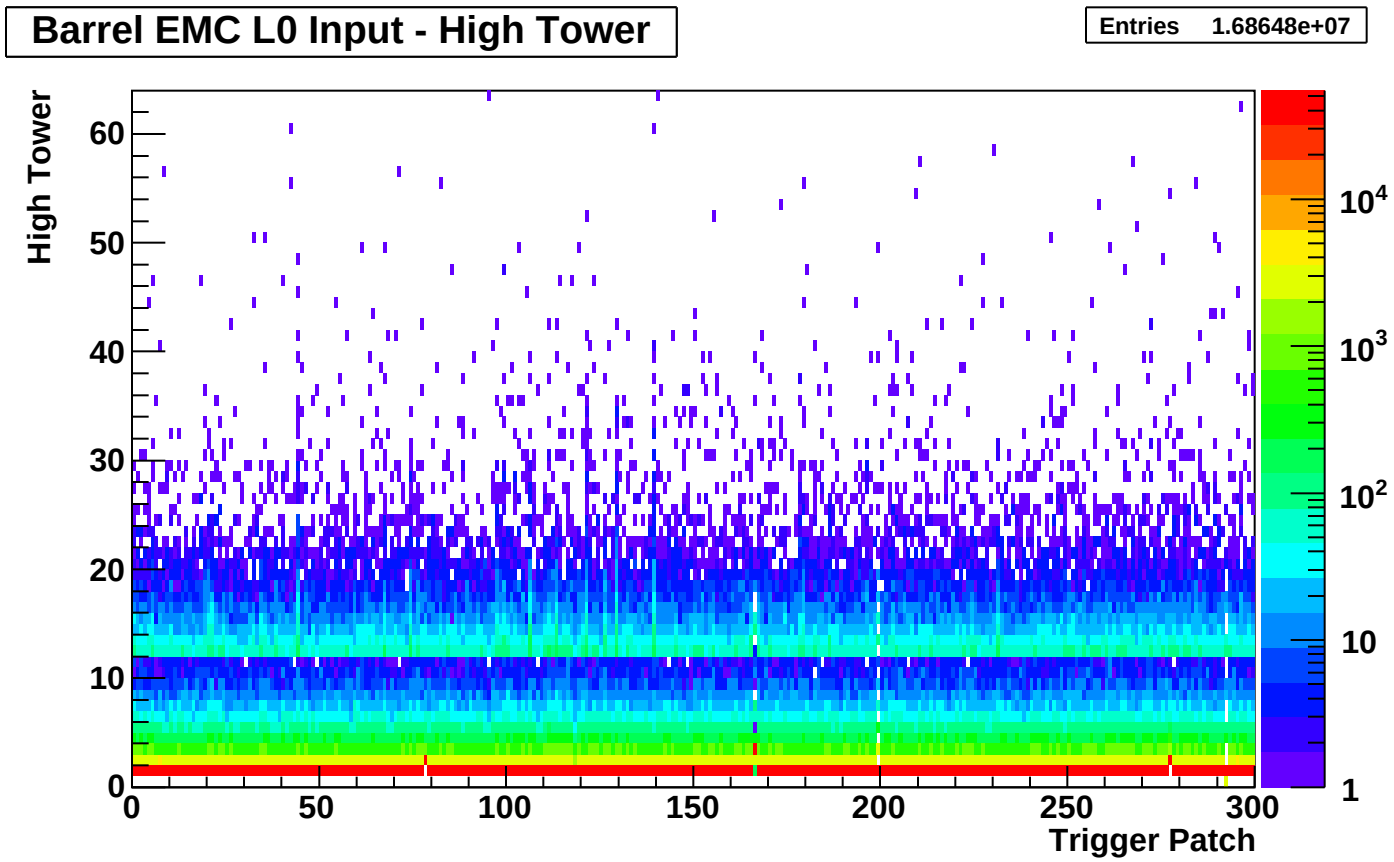

Last DSM Ch3 Input
Entries 899456
LD301 Ch3
56216 4178 839 296 26 11 2532 333 2339 306 130 26 81 75 7
52038 55377 55920 56190 56205 53684 55883 53877 55910 56086 56190 56135 56141 56209 56216
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

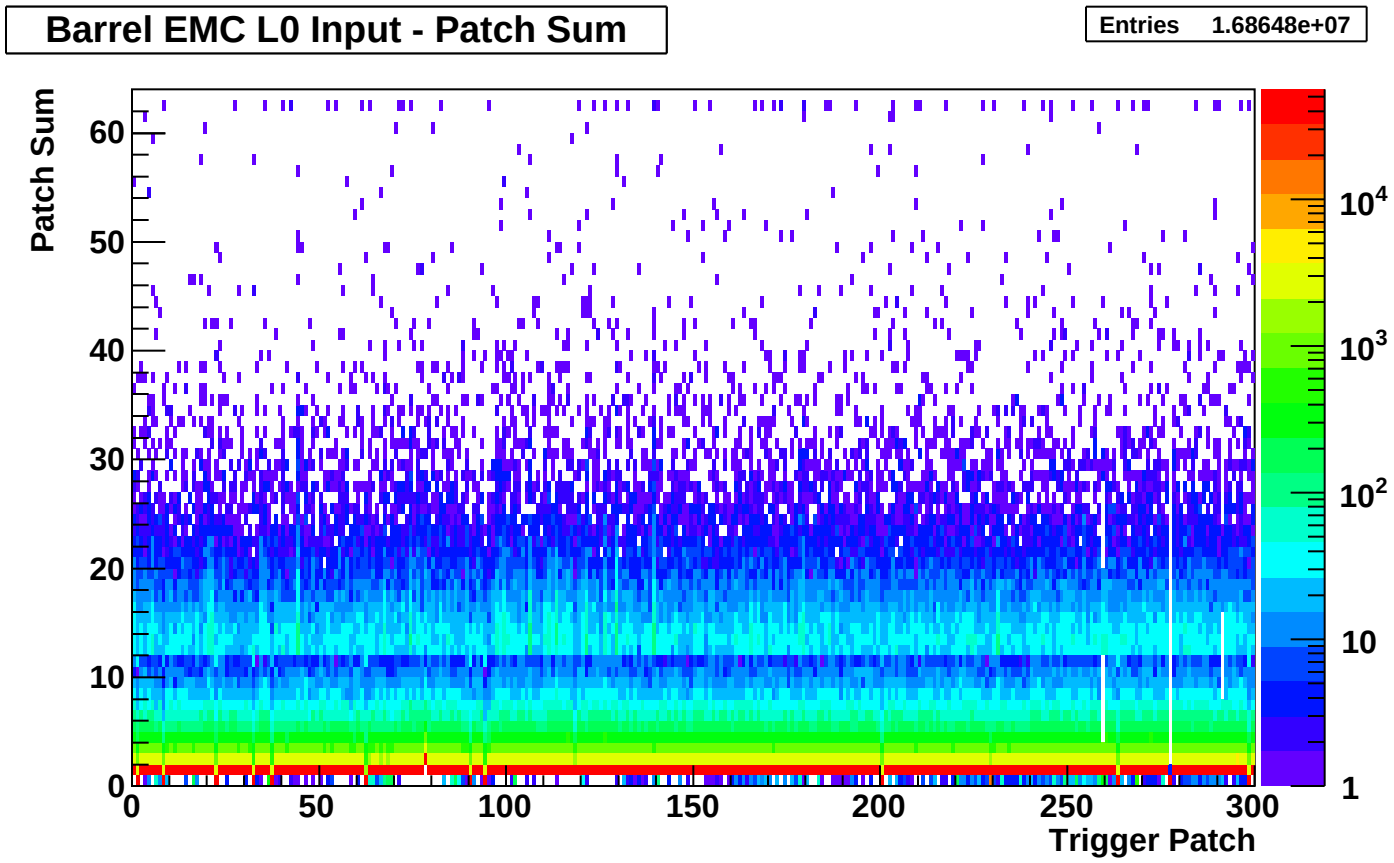

Last DSM Ch3 Input
Entries 11264
LD301 Ch3 - Simulated
556 101 10607
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

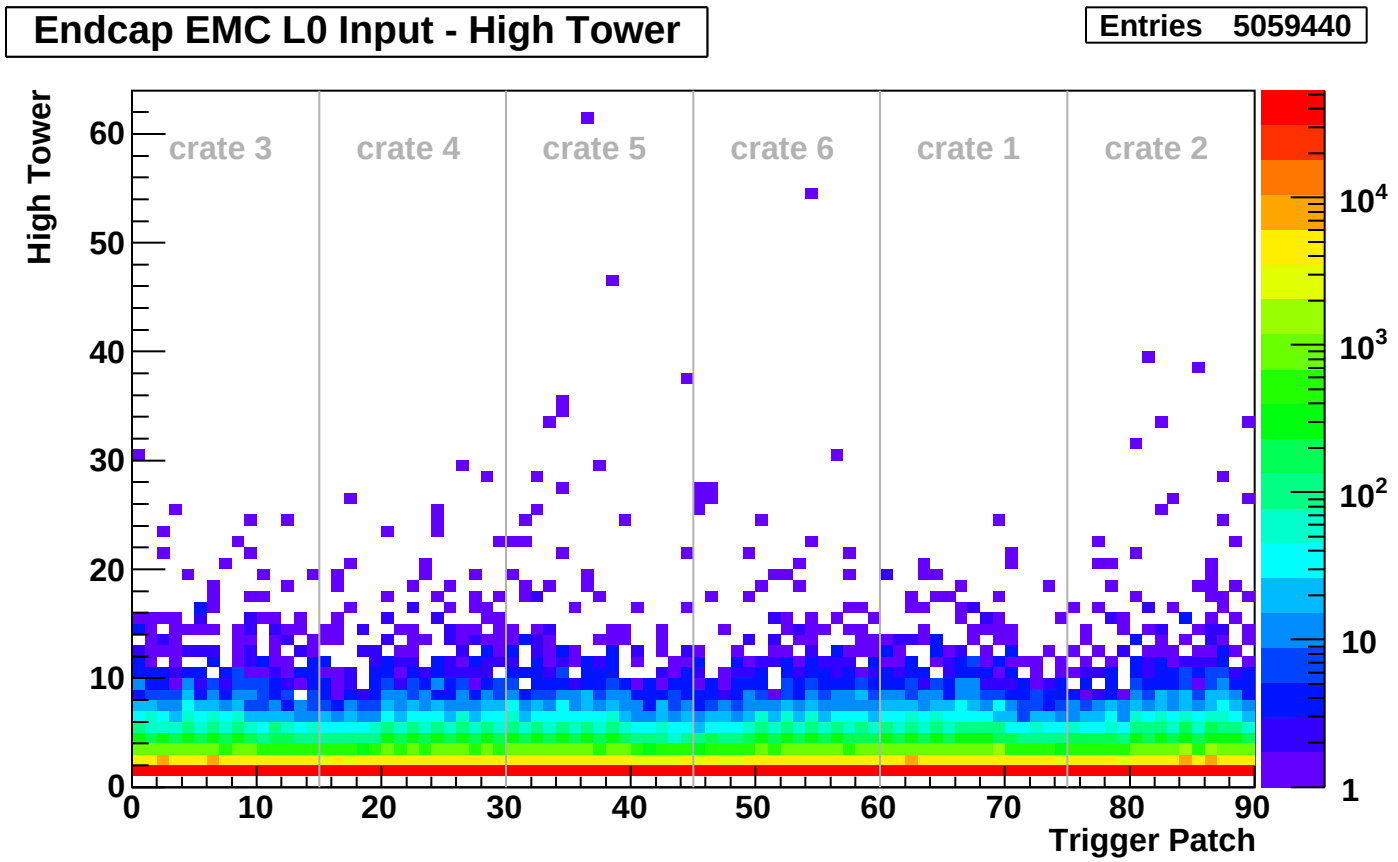

TCU bits
Entries 899456
TCU bits
56216
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

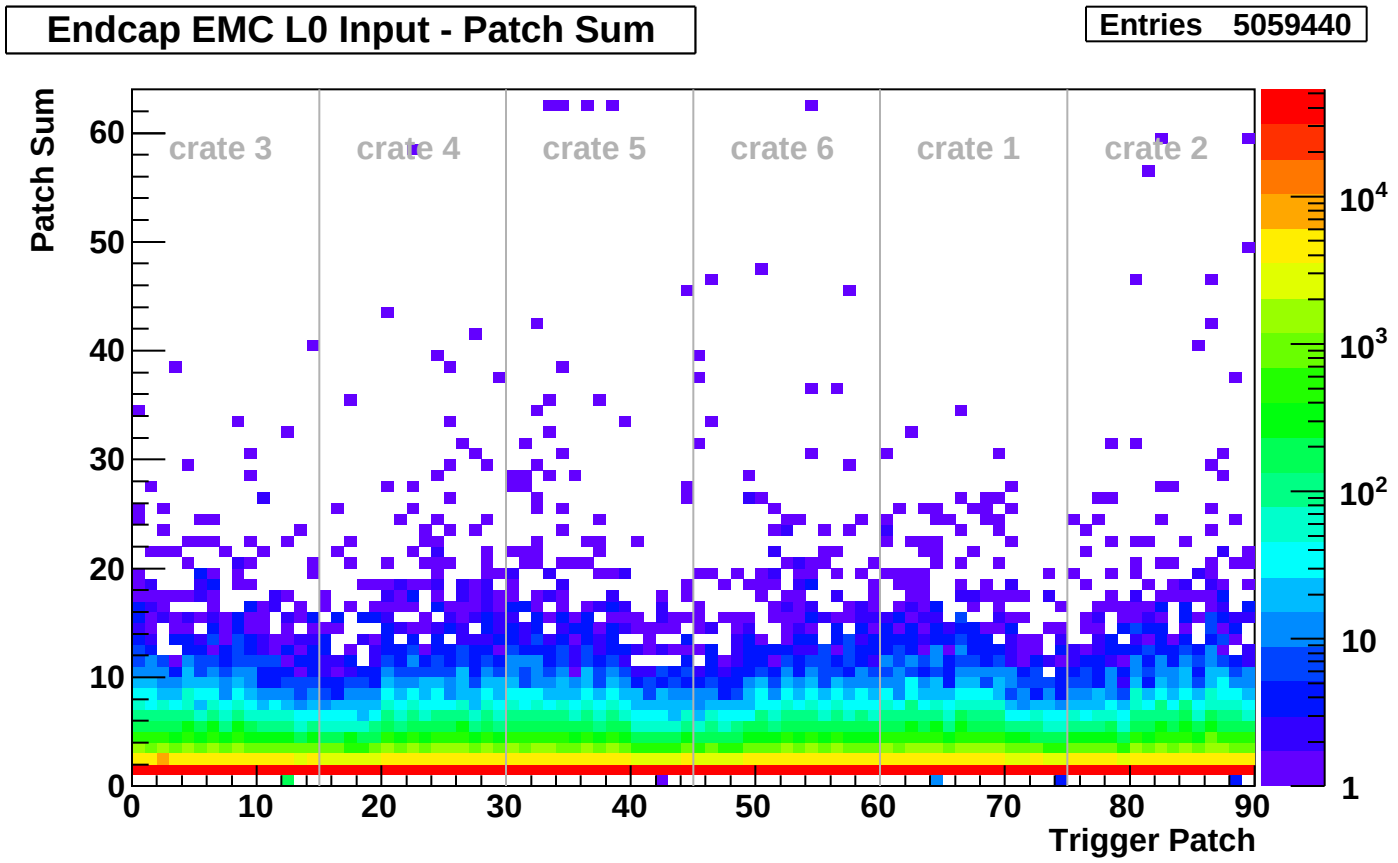

TCU bits
Entries 105953
TCU bits - Simulated
52877 4178 34272 26 1495 13105
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

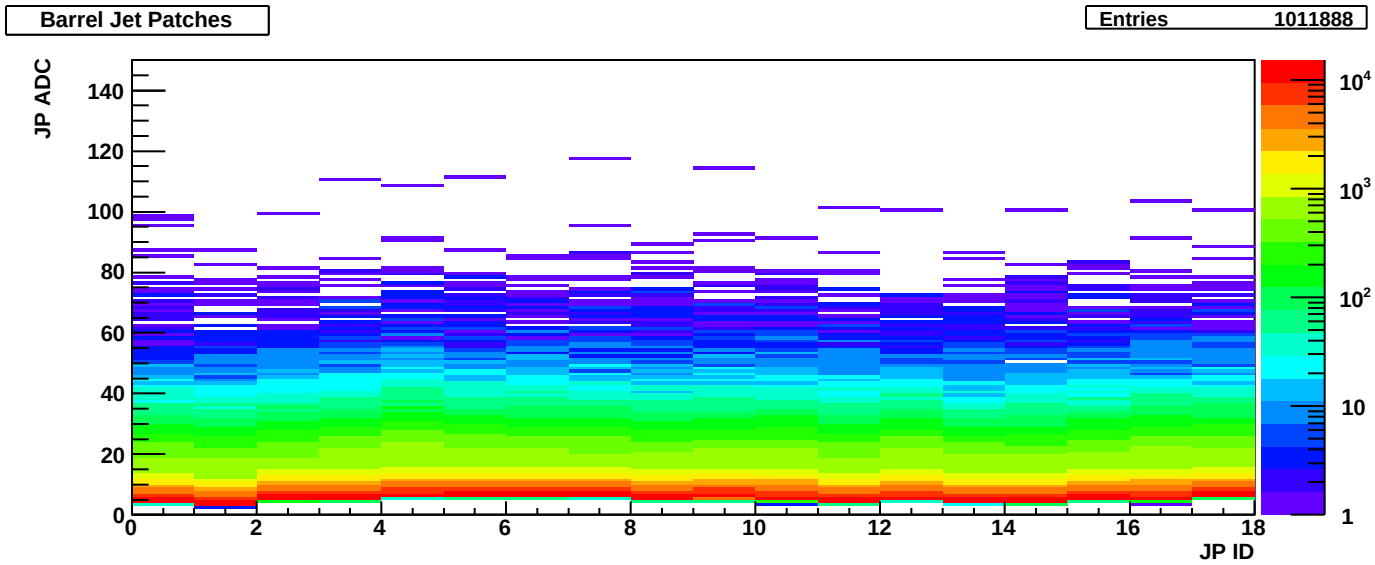

BunchId7Bit (BHT3)
Entries 296
0 20 40 60 80 100 120

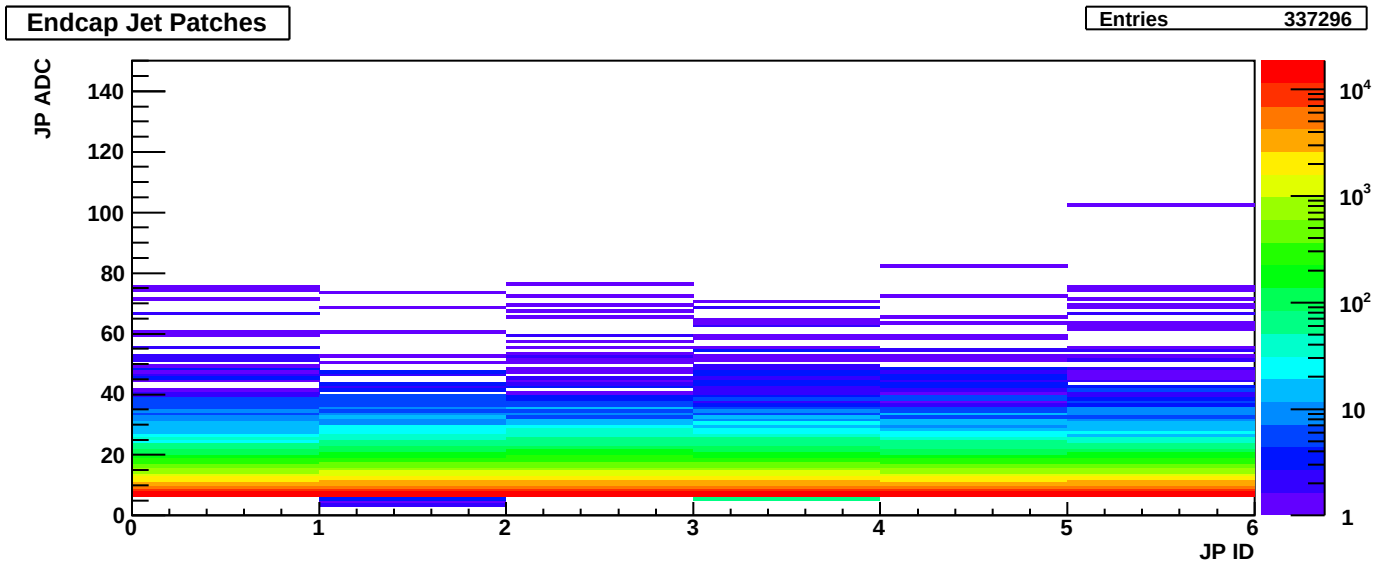

Barrel EMC L0 Input - High Tower
Entries 1.68648e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

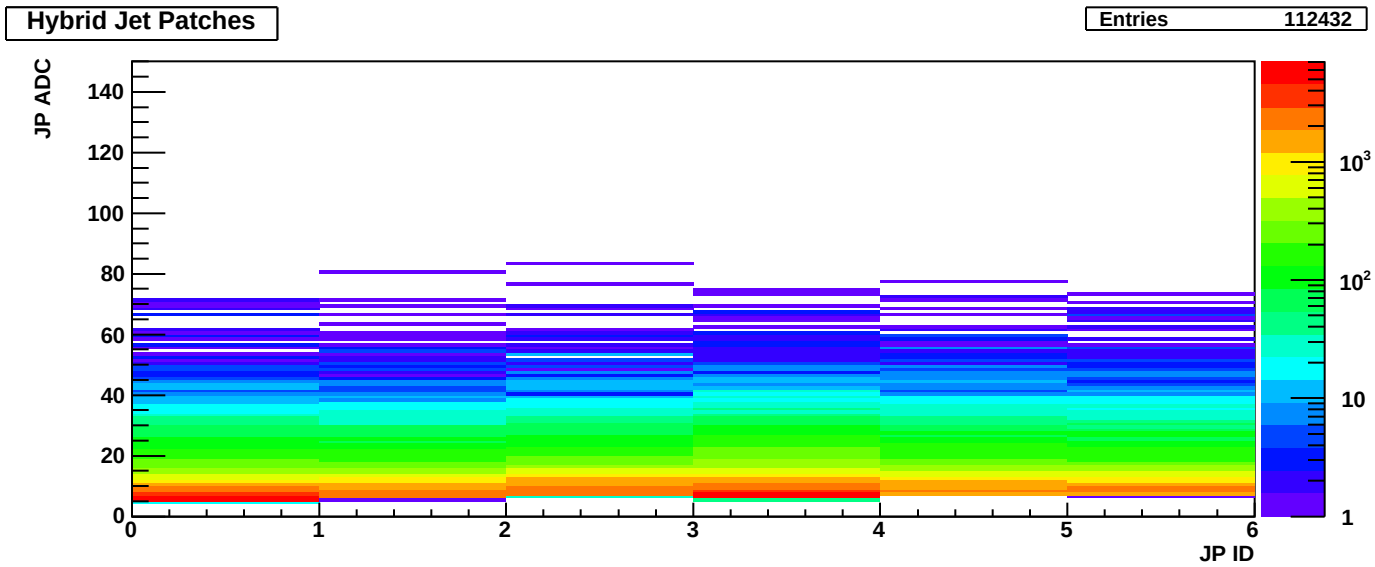

Barrel EMC L0 Input - Patch Sum
Entries 1.68648e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

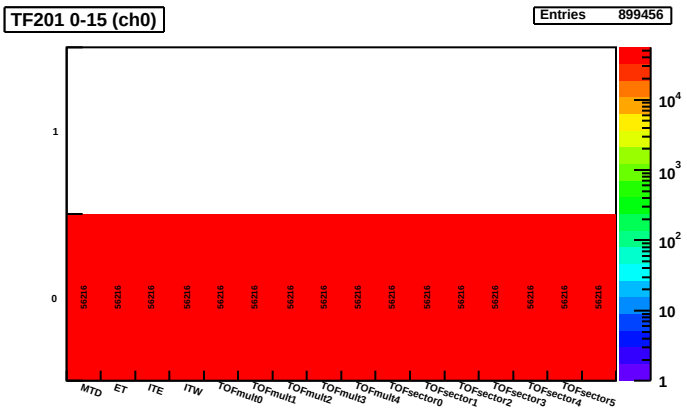

Endcap EMC L0 Input - High Tower
Entries 5059440
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

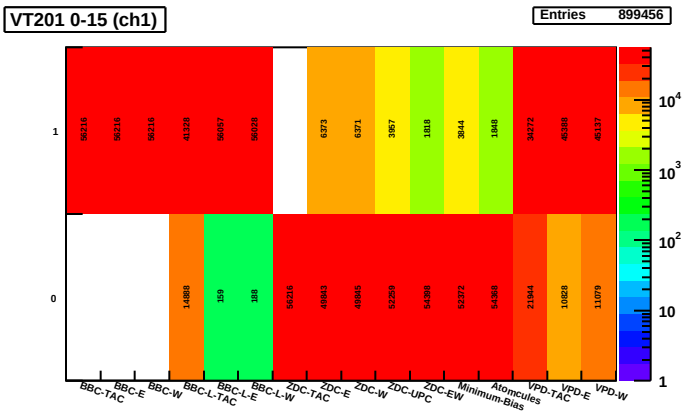

Endcap EMC L0 Input - Patch Sum
Entries 5059440
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

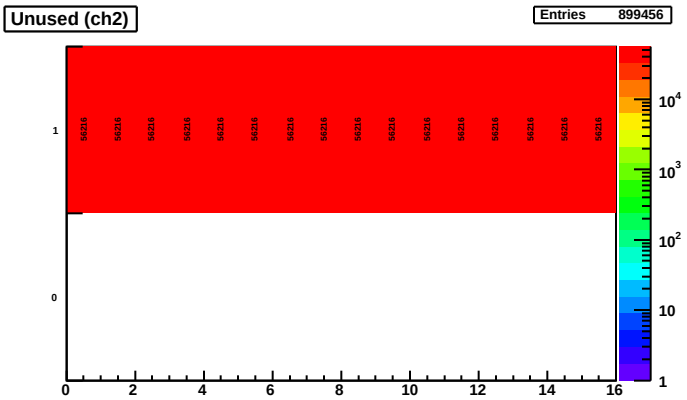

Barrel Jet Patches
Entries 1011888
JP ADC
JP ID

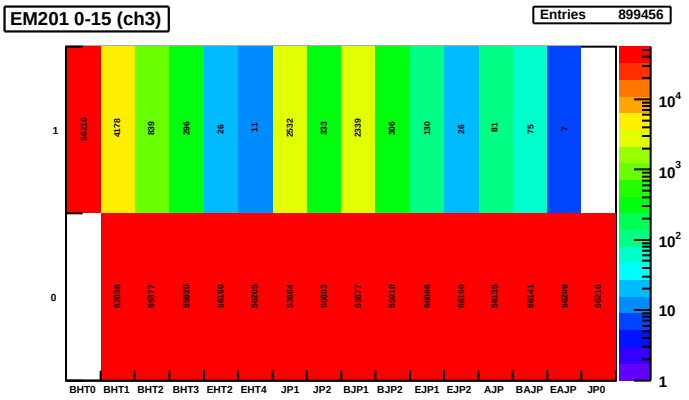

Endcap Jet Patches
Entries 337296
JP ADC
JP ID

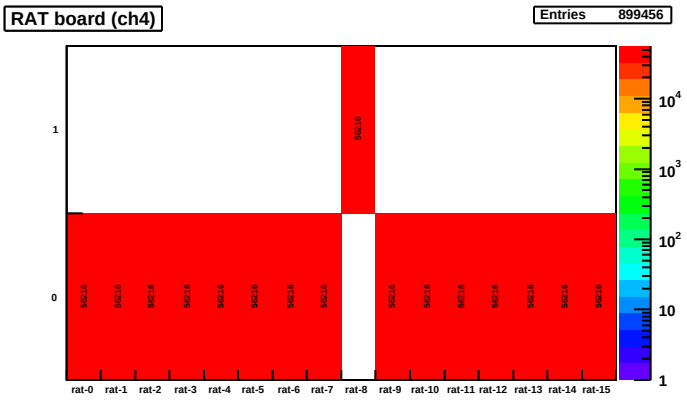

Hybrid Jet Patches
Entries 112432
JP ADC
JP ID

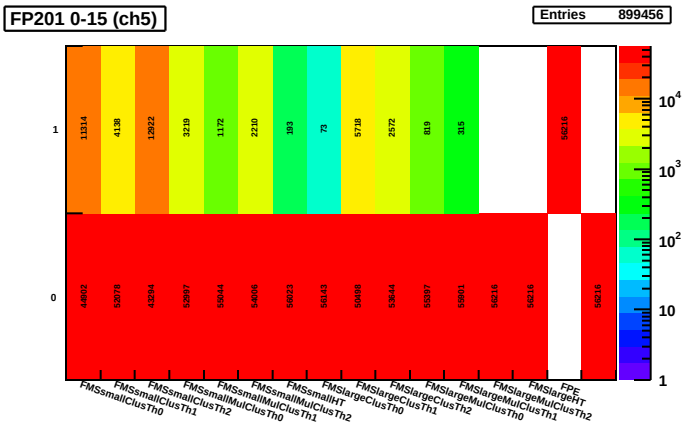

TF201 0-15 (ch0)
Entries 899456
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

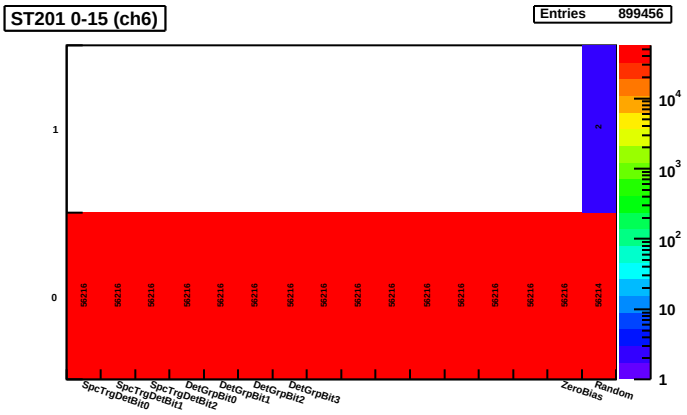

VT201 0-15 (ch1)
Entries 899456
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

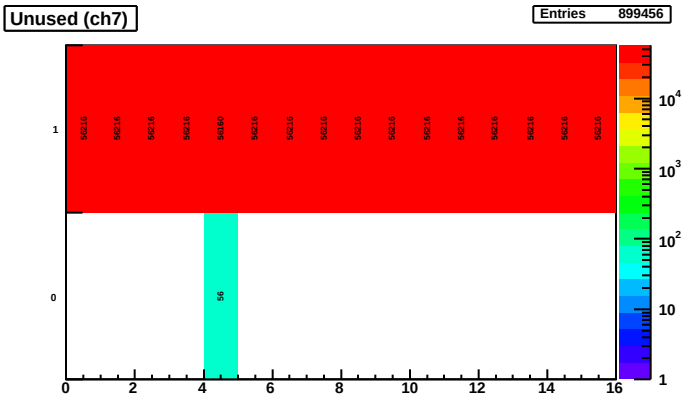

Unused (ch2)
Entries 899456

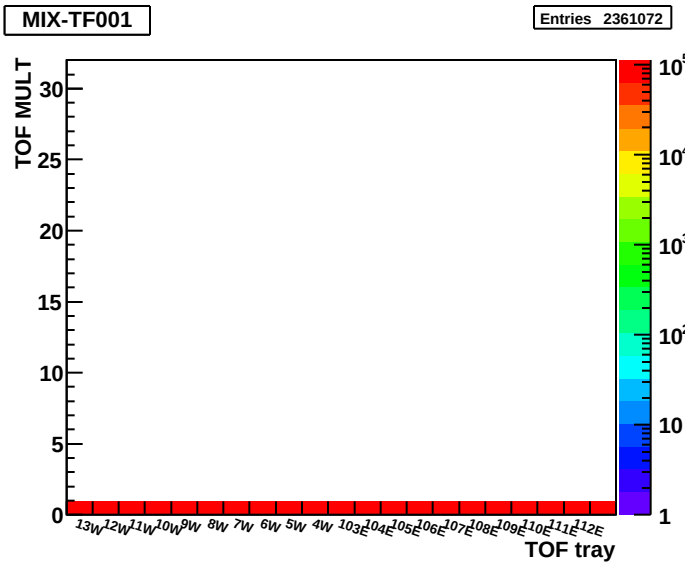

EM201 0-15 (ch3)
Entries 899456
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

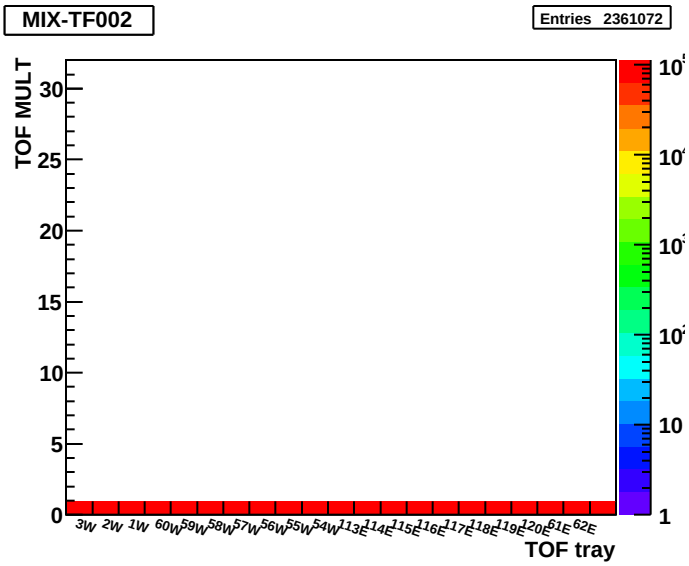

RAT board (ch4)
Entries 899456
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

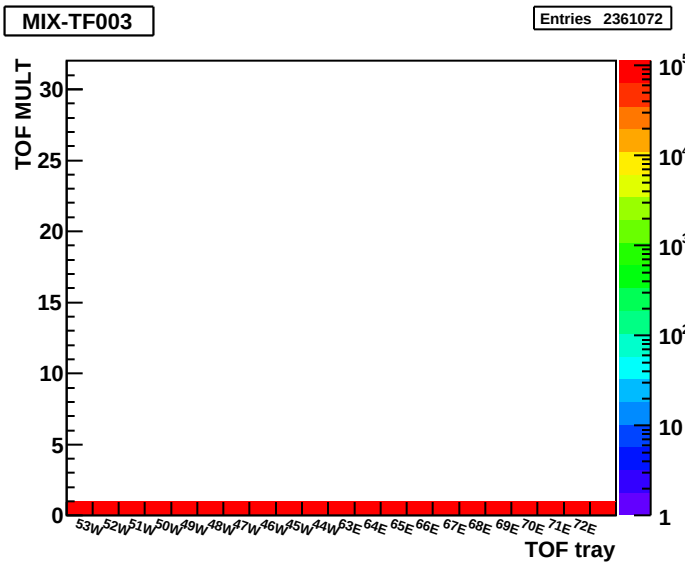

FP201 0-15 (ch5)
Entries 899456
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

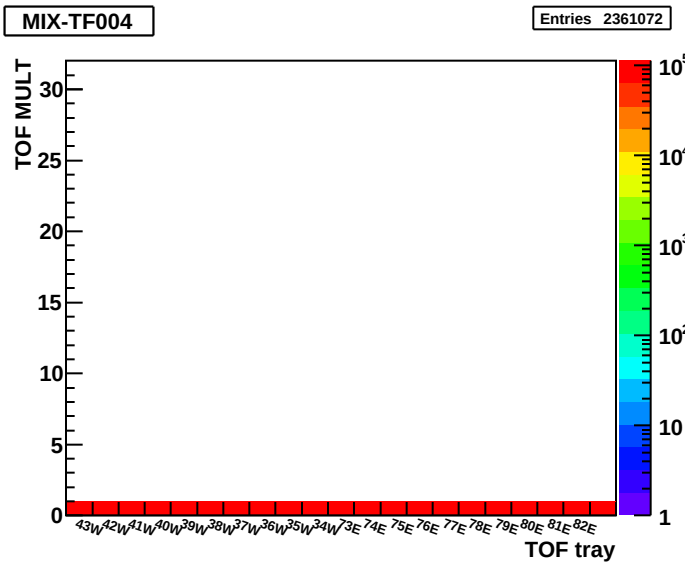

ST201 0-15 (ch6)
Entries 899456
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

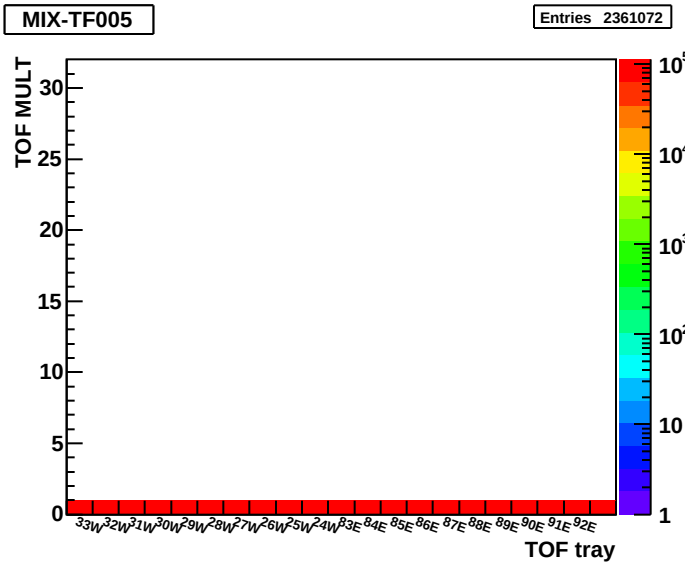

Unused (ch7)
Entries 899456

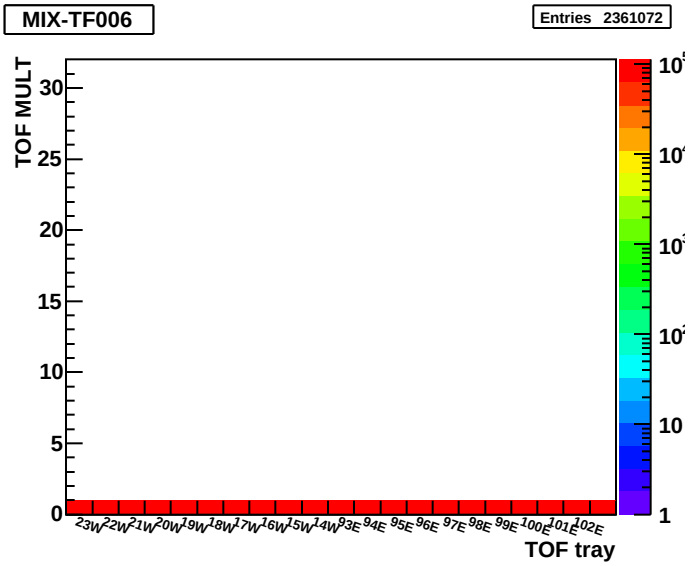

MIX-TF001
Entries 2361072
TOF MULT
TOF tray

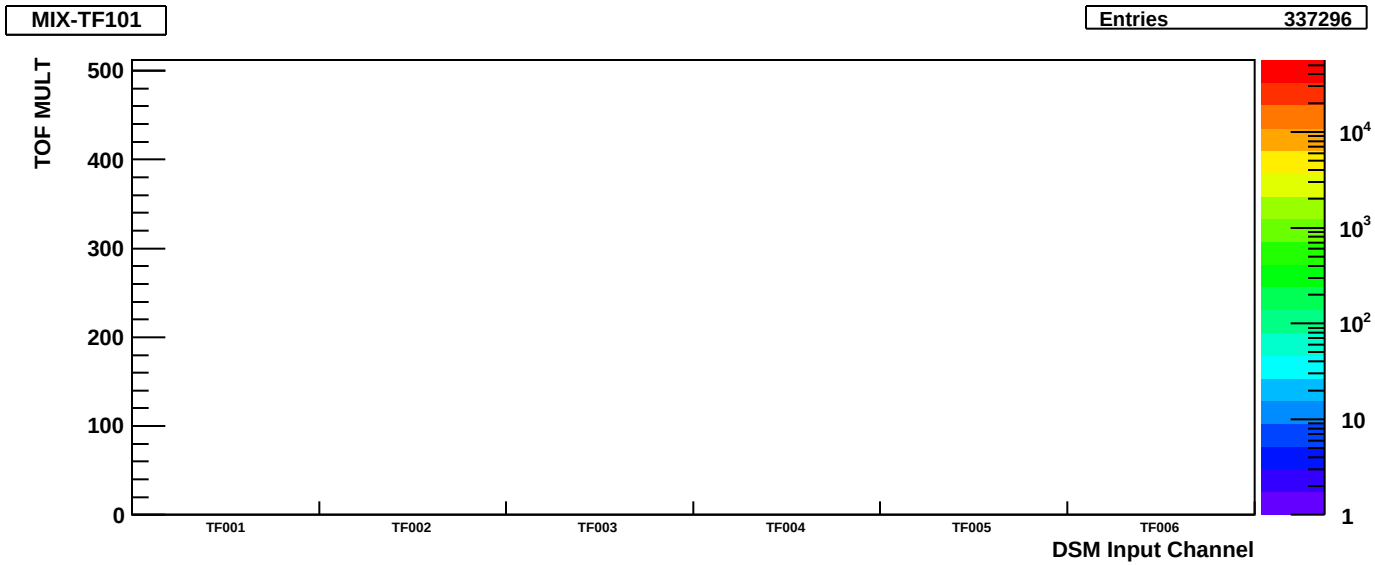

MIX-TF002
Entries 2361072
TOF MULT
TOF tray

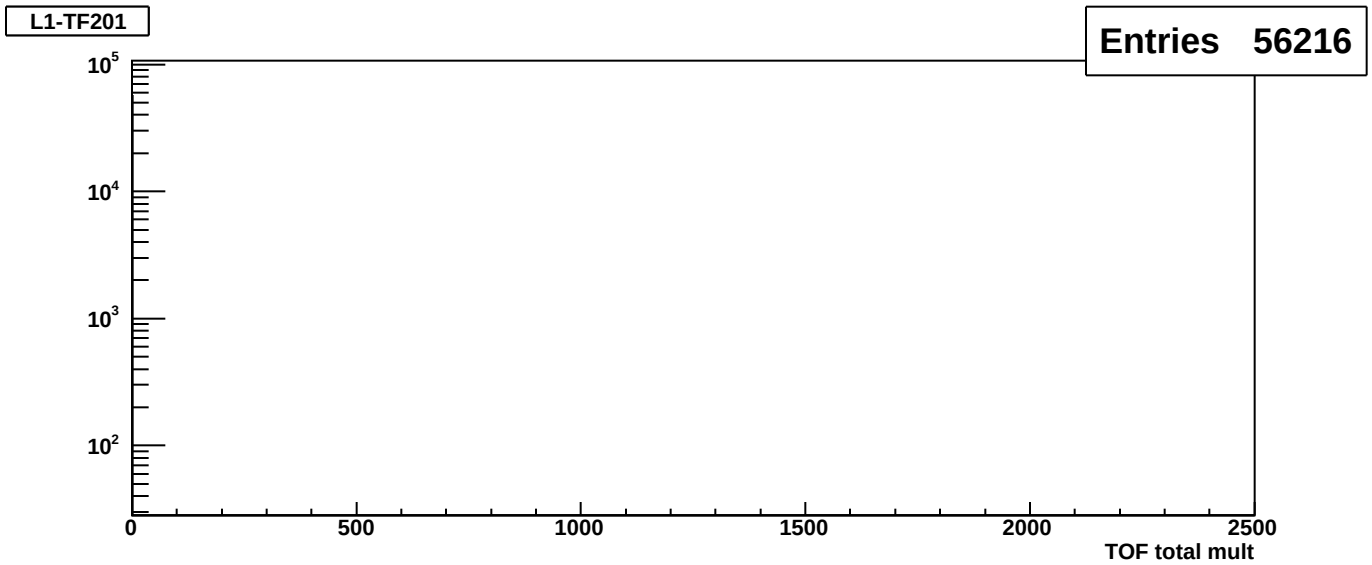

MIX-TF003
Entries 2361072
TOF MULT
TOF tray

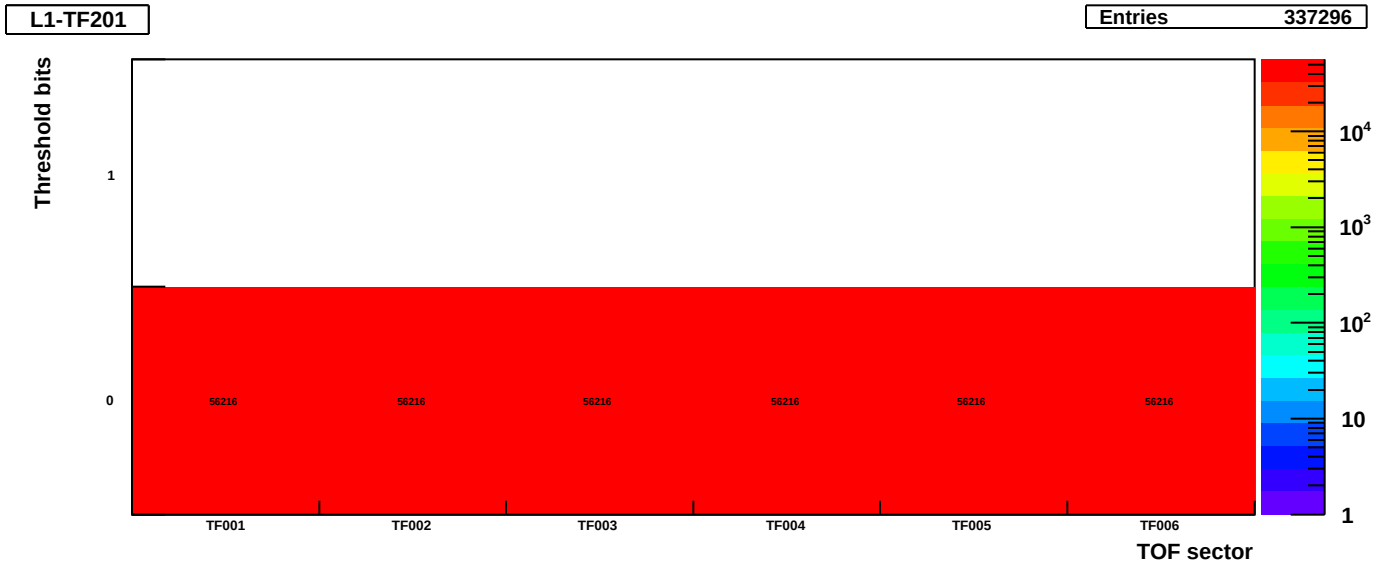

MIX-TF004
Entries 2361072
TOF MULT
TOF tray

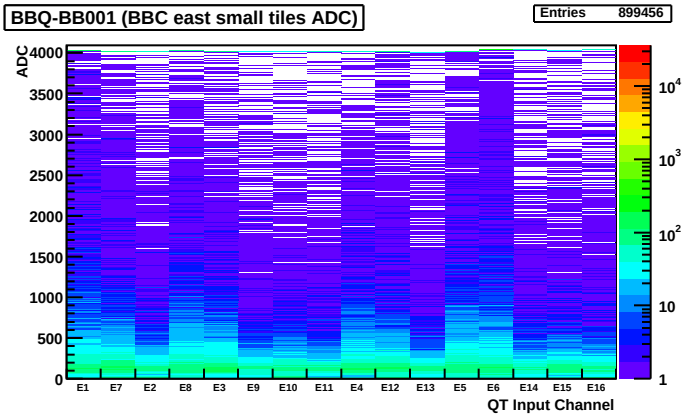

MIX-TF005
Entries 2361072
TOF MULT
TOF tray

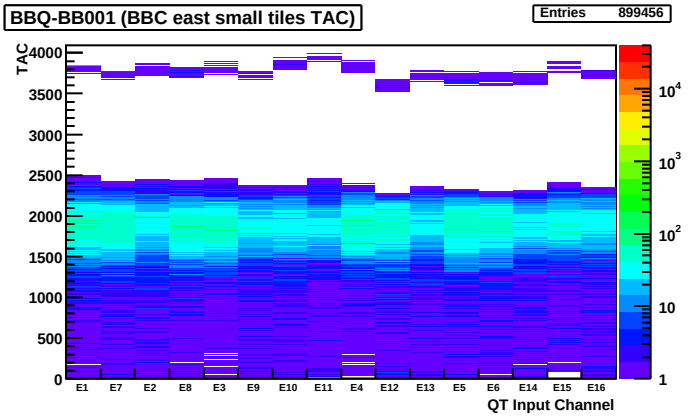

MIX-TF006
Entries 2361072
TOF MULT
TOF tray

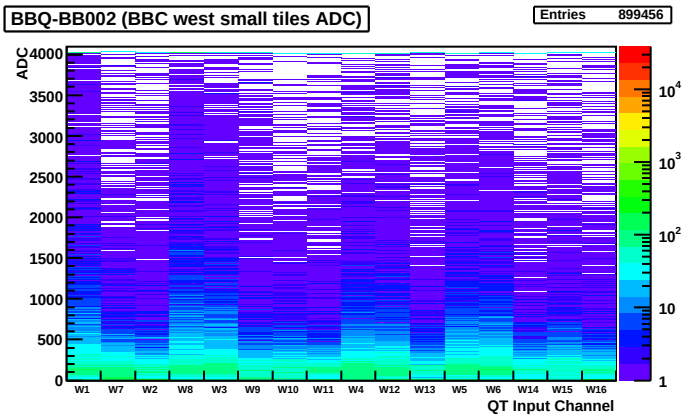

MIX-TF101
Entries 337296
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

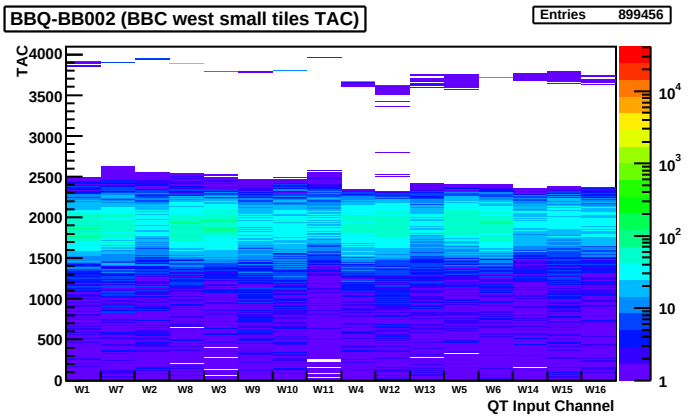

L1-TF201
Entries 56216
10^5
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

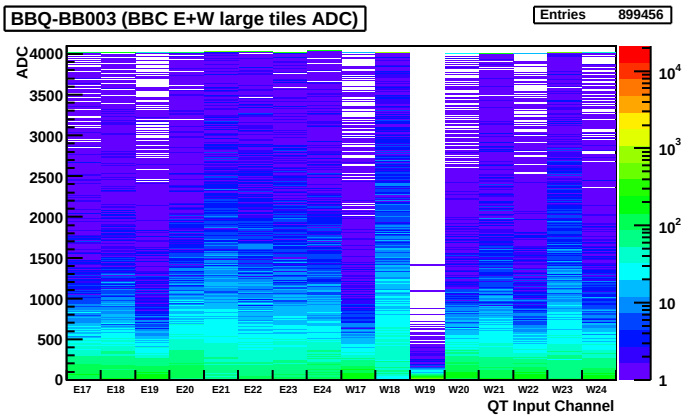

L1-TF201
Entries 337296
Threshold bits
1
0
56216
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

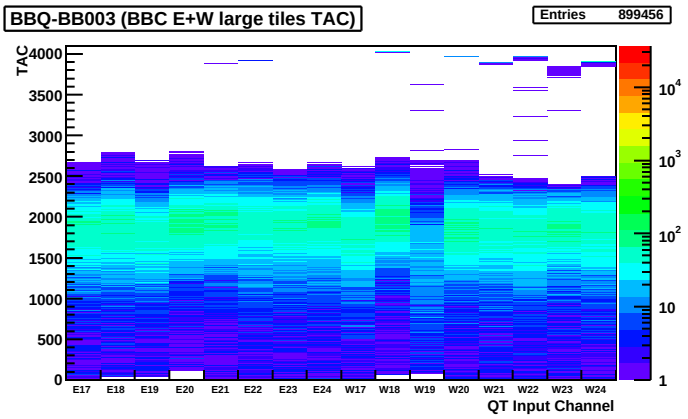

BBQ-BB001 (BBC east small tiles ADC)
Entries 899456
ADC
QT Input Channel

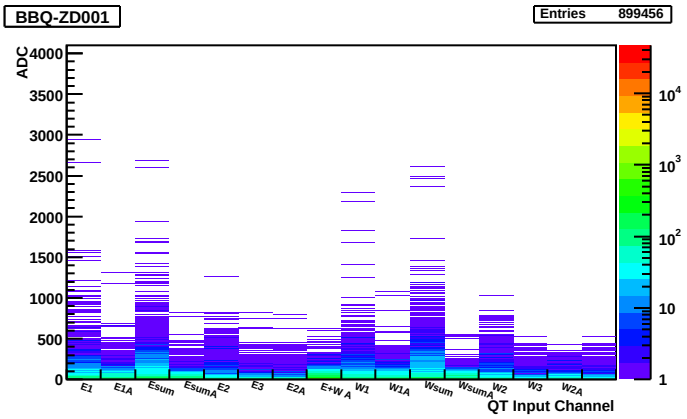

BBQ-BB001 (BBC east small tiles TAC)
Entries 899456
TAC
QT Input Channel

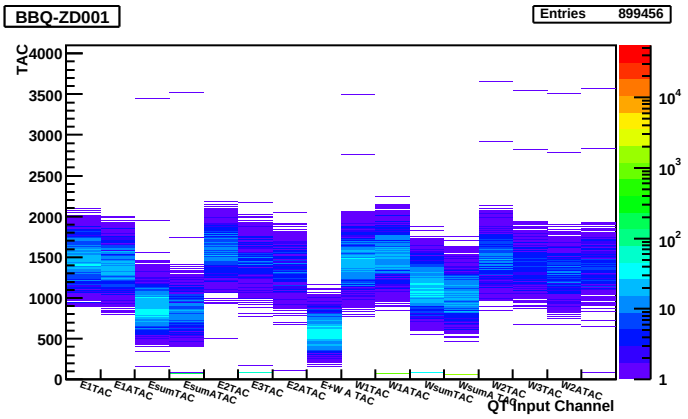

BBQ-BB002 (BBC west small tiles ADC)
Entries 899456
ADC
QT Input Channel

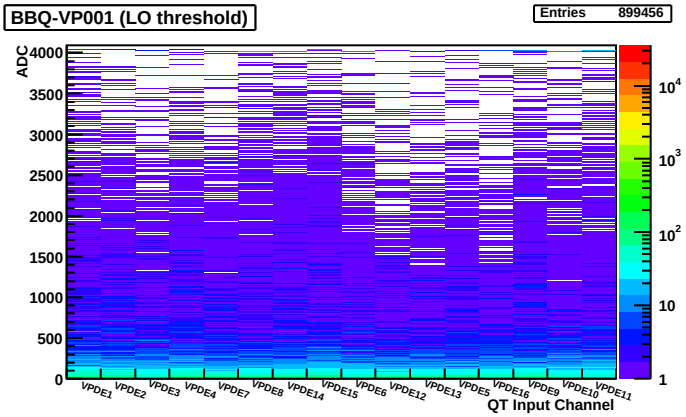

BBQ-BB002 (BBC west small tiles TAC)
Entries 899456
TAC
QT Input Channel

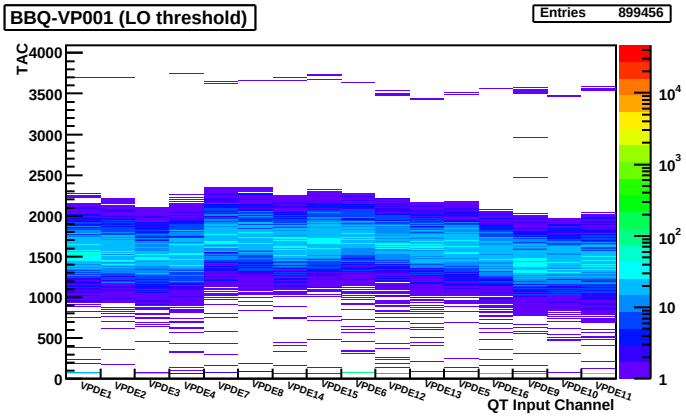

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 899456
ADC
QT Input Channel

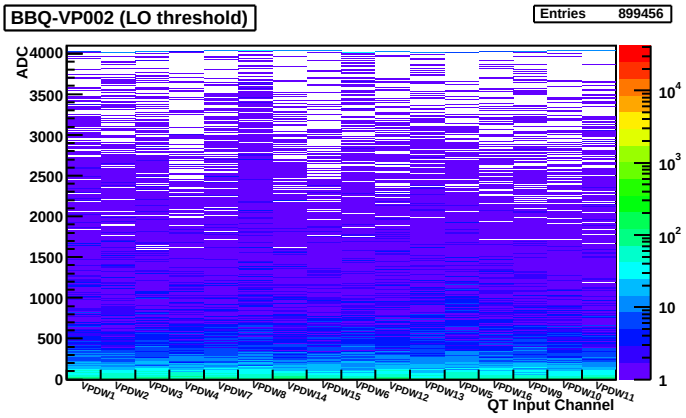

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 899456
TAC
QT Input Channel

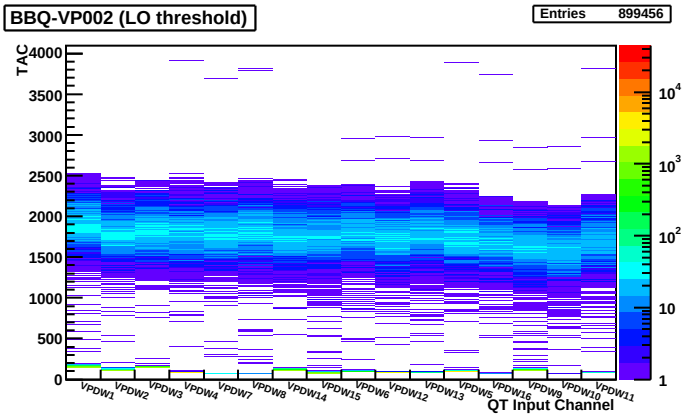

BBQ-ZD001
Entries 899456
ADC
QT Input Channel

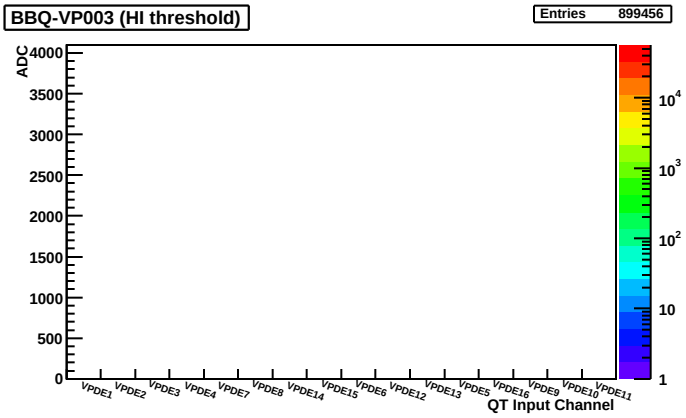

BBQ-ZD001
Entries 899456
TAC
QT Input Channel

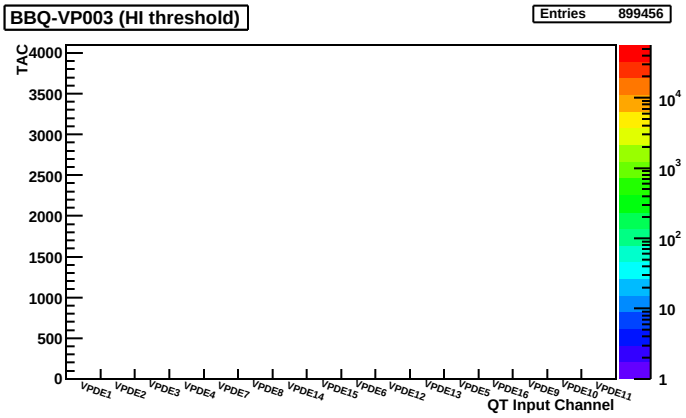

BBQ-VP001 (LO threshold)
Entries 899456
ADC
QT Input Channel

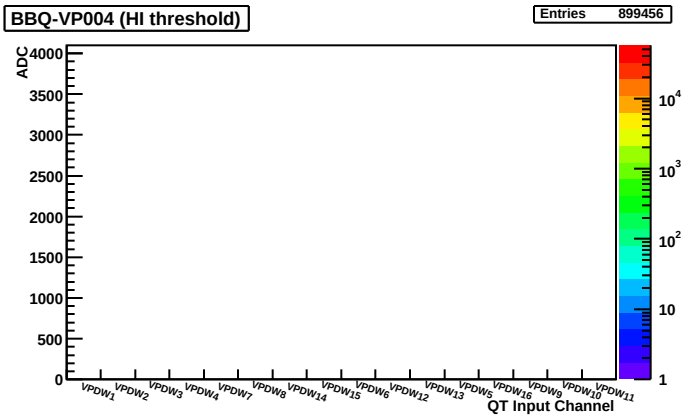

BBQ-VP001 (LO threshold)
Entries 899456
TAC
QT Input Channel

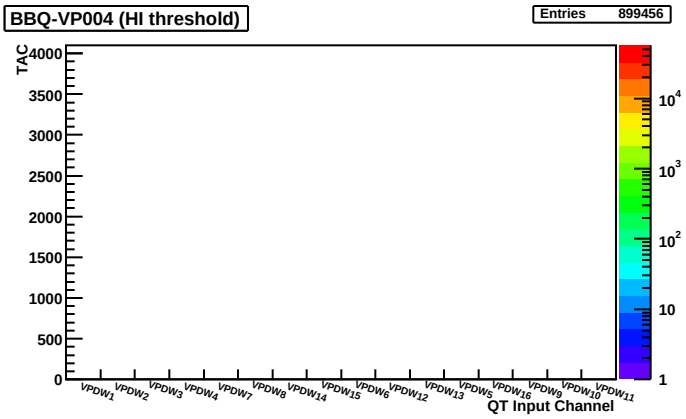

BBQ-VP002 (LO threshold)
Entries 899456
ADC
QT Input Channel

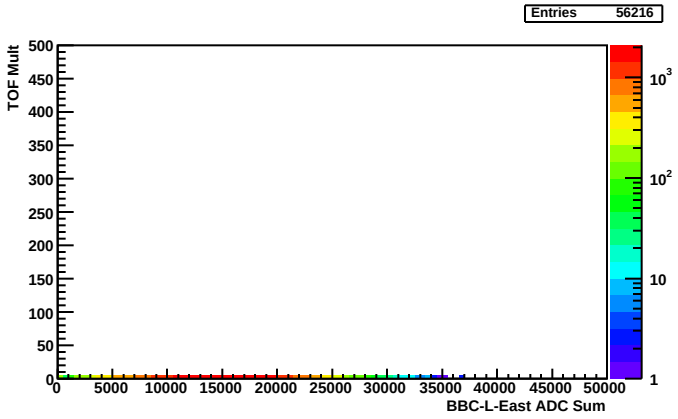

BBQ-VP002 (LO threshold)
Entries 899456
TAC
QT Input Channel

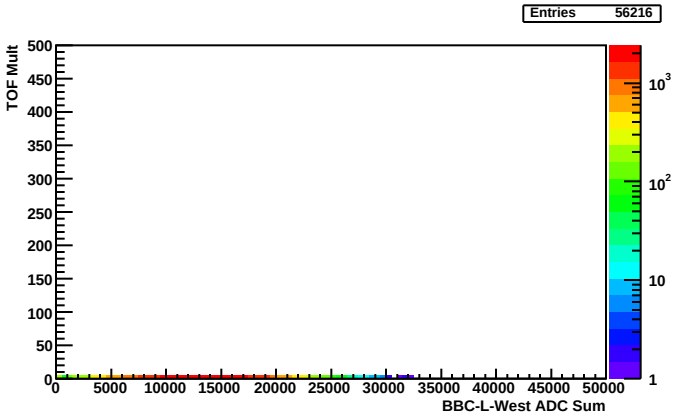

BBQ-VP003 (HI threshold)
Entries 899456
ADC
QT Input Channel

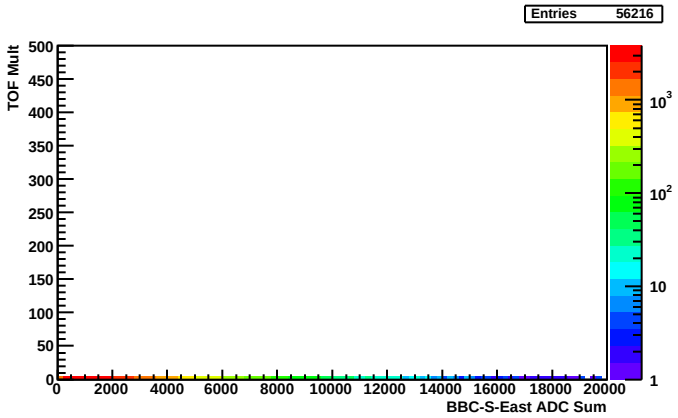

BBQ-VP003 (HI threshold)
Entries 899456
TAC
QT Input Channel

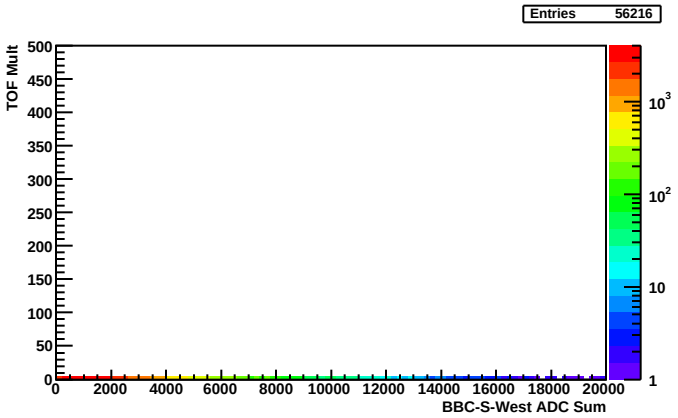

BBQ-VP004 (HI threshold)
Entries 899456
ADC
QT Input Channel

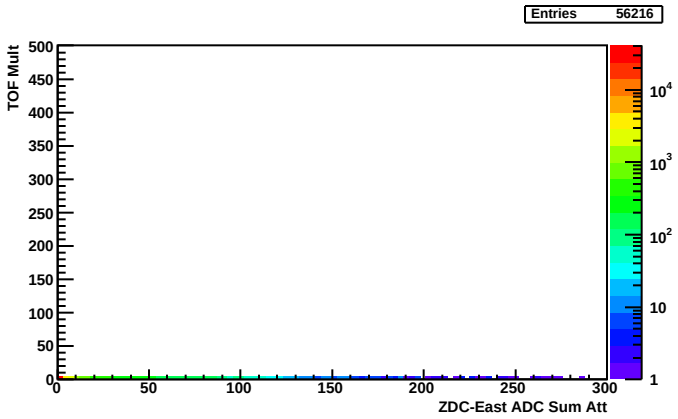

BBQ-VP004 (HI threshold)
Entries 899456
TAC
QT Input Channel

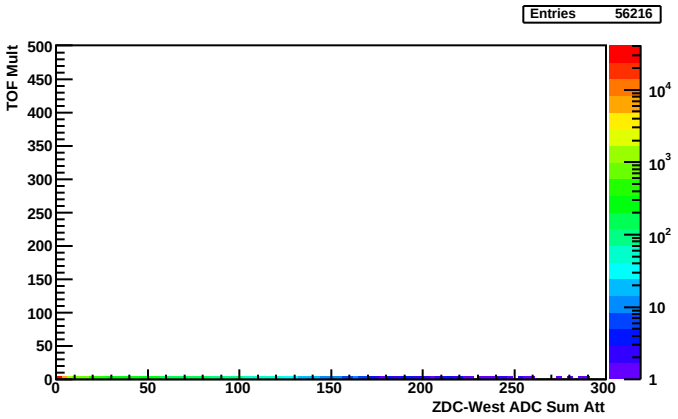

Entries 56216
TOF Mult
BBC-L-East ADC Sum

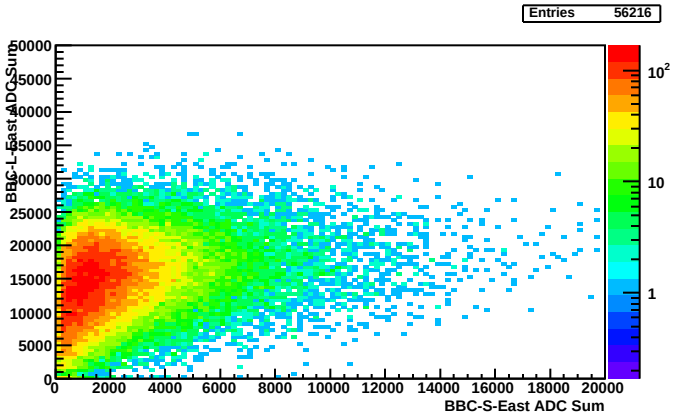

Entries 56216
TOF Mult
BBC-L-West ADC Sum

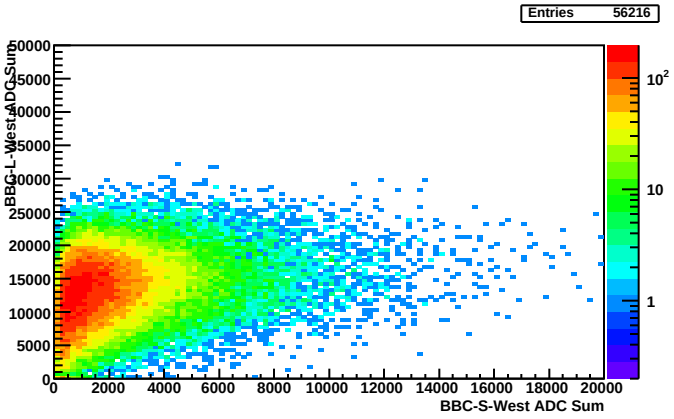

Entries 56216
TOF Mult
BBC-S-East ADC Sum

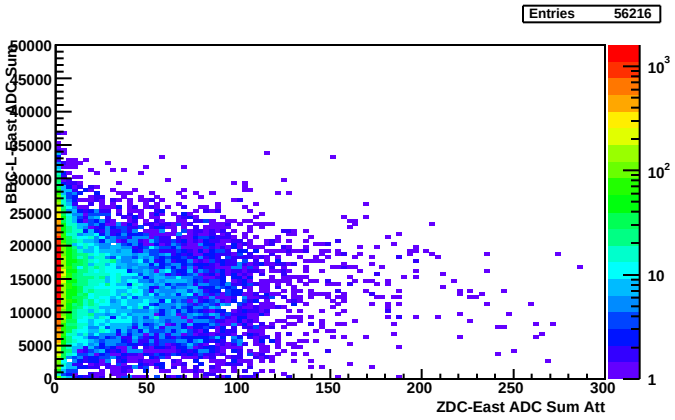

Entries 56216
TOF Mult
BBC-S-West ADC Sum

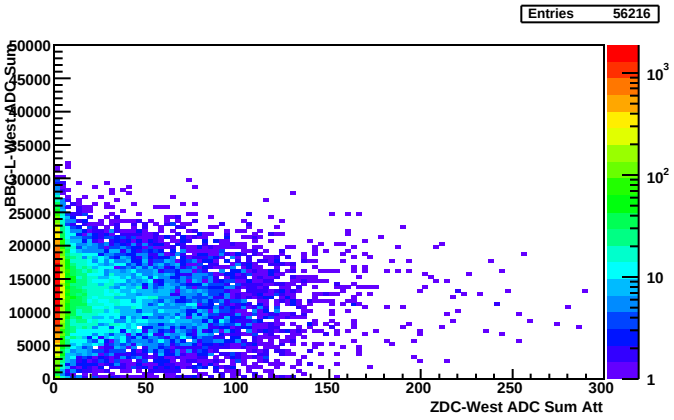

Entries 56216
TOF Mult
ZDC-East ADC Sum Att

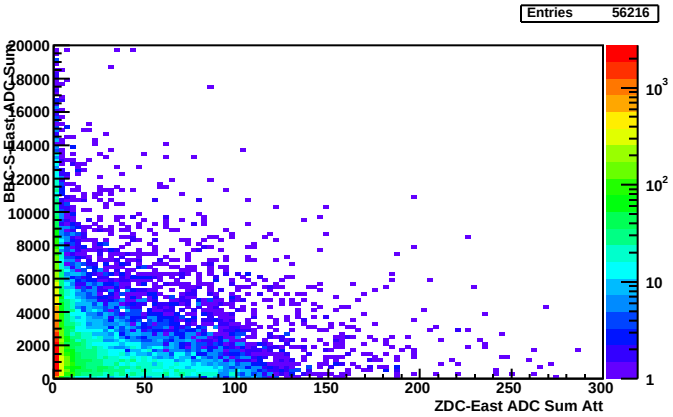

Entries 56216
TOF Mult
ZDC-West ADC Sum Att

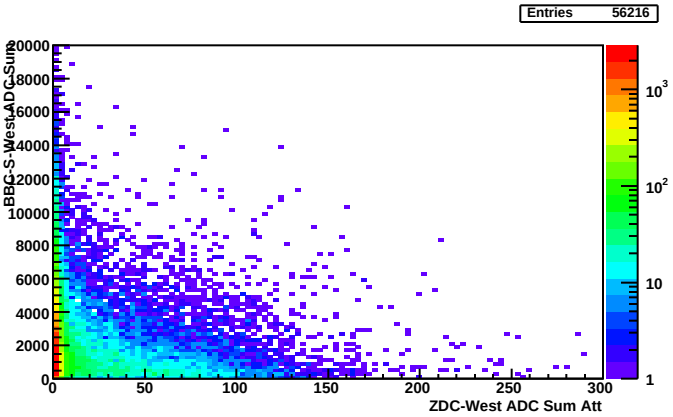

Entries 56216
BBC-L-East ADC Sum
BBC-S-East ADC Sum

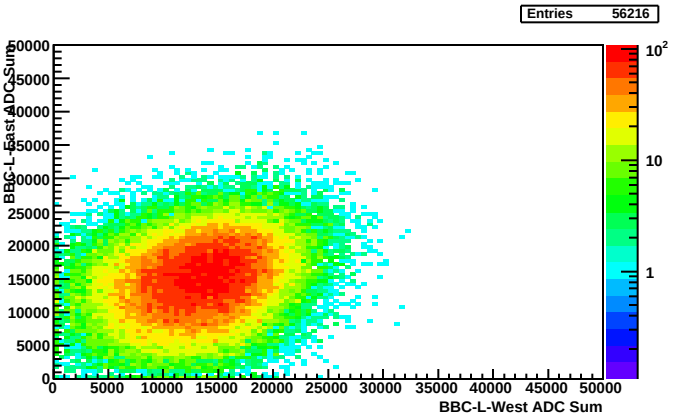

Entries 56216
BBC-L-West ADC Sum
BBC-S-West ADC Sum

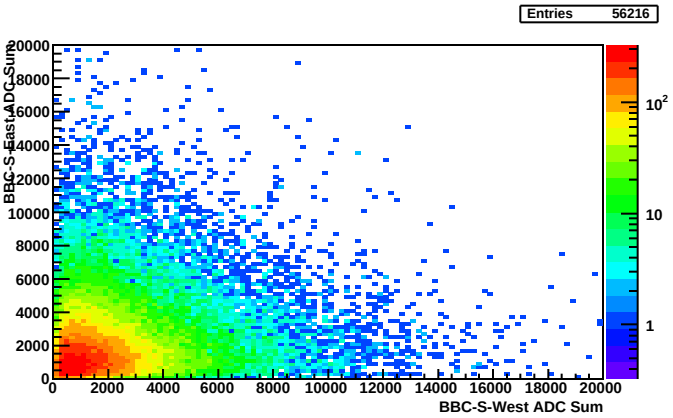

Entries 56216
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

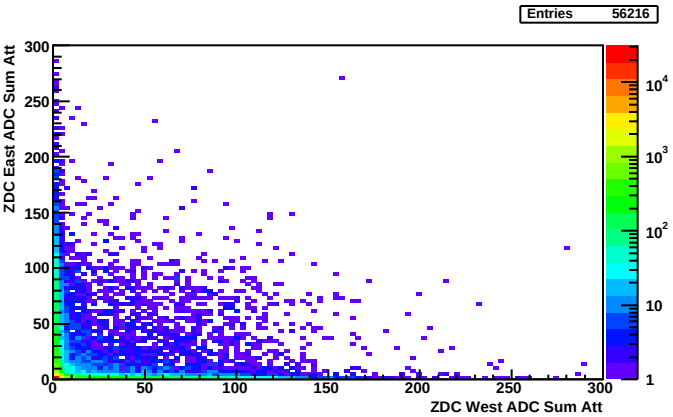

Entries 56216
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

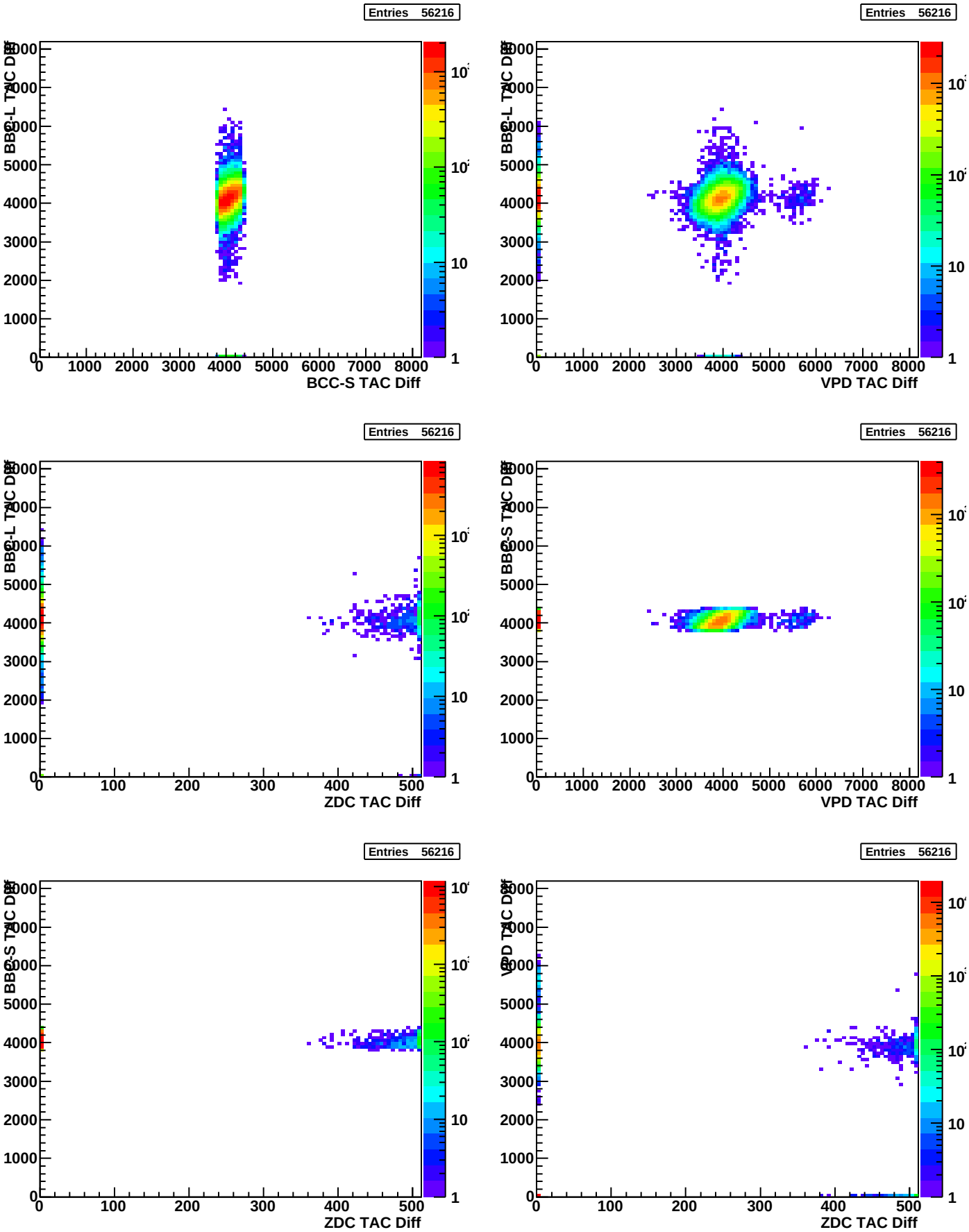

Entries 56216
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

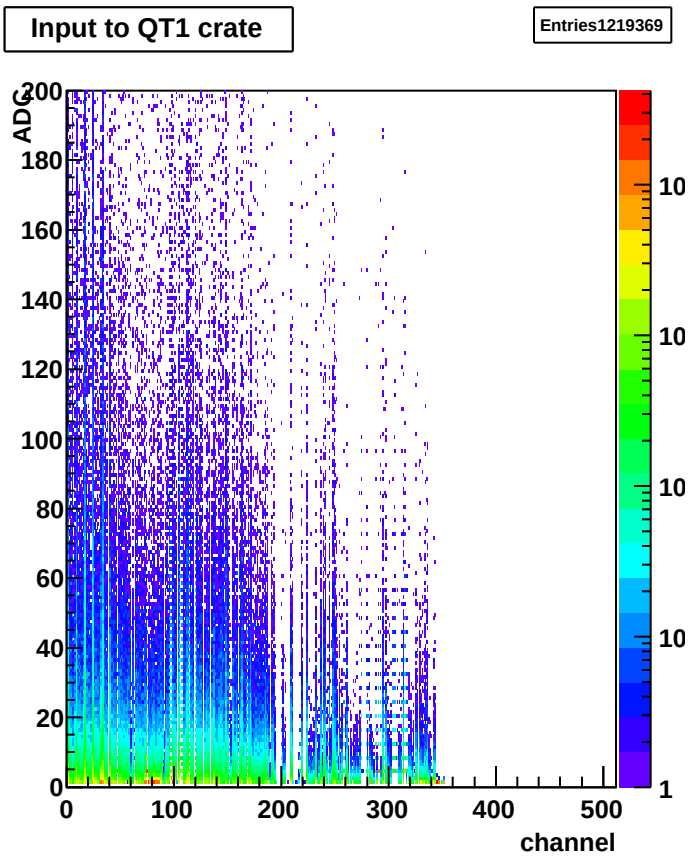

Entries 56216
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

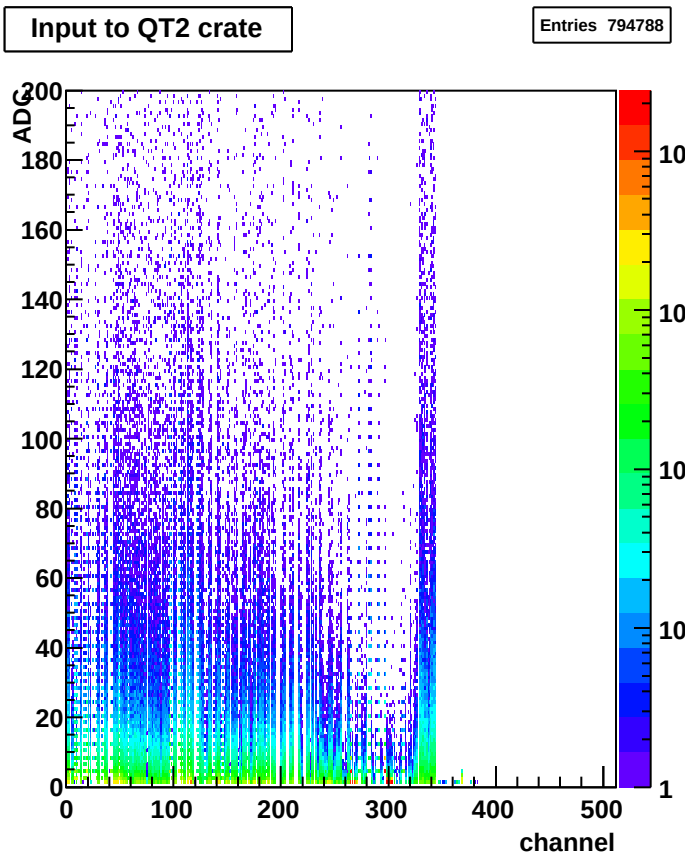

Entries 56216
BBC-L-East ADC Sum
BBC-L-West ADC Sum

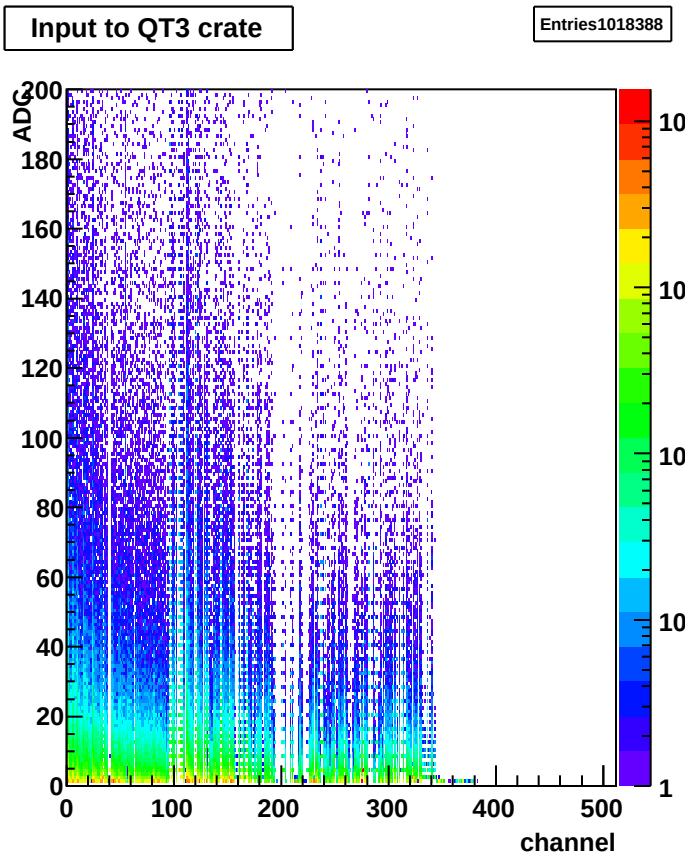

Entries 56216
BBC-S-East ADC Sum
BBC-S-West ADC Sum

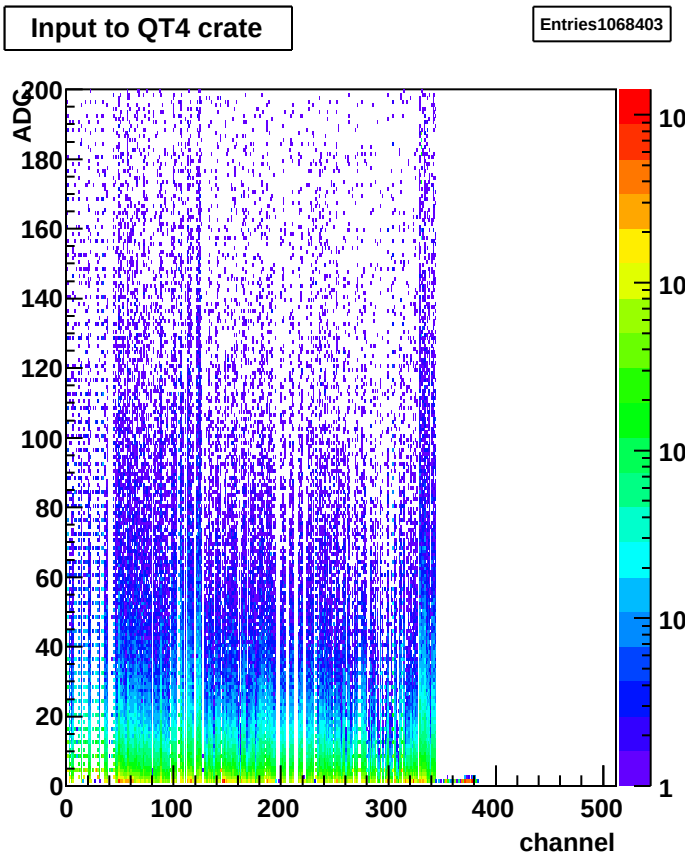

Entries 56216
ZDC East ADC Sum Att
ZDC West ADC Sum Att

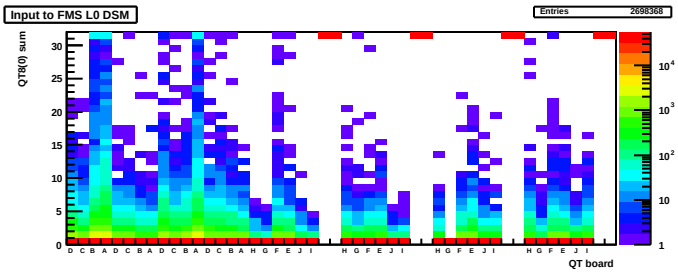

Entries 56216
BBC-L TAC Diff
BCC-S TAC Diff
Entries 56216
BBC-L TAC Diff
VPD TAC Diff
Entries 56216
BBC-L TAC Diff
ZDC TAC Diff
Entries 56216
BBC-S TAC Diff
VPD TAC Diff
Entries 56216
BBC-S TAC Diff
ZDC TAC Diff
Entries 56216
VPD TAC Diff
ZDC TAC Diff

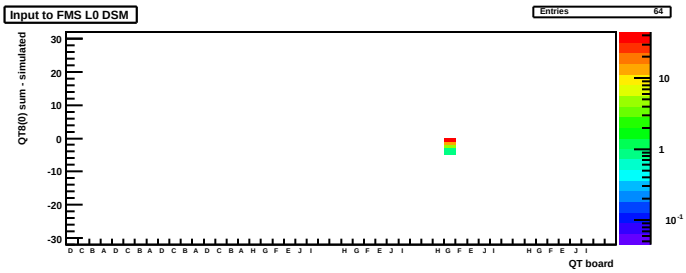

Input to QT1 crate
Entries1219369
ADC
channel

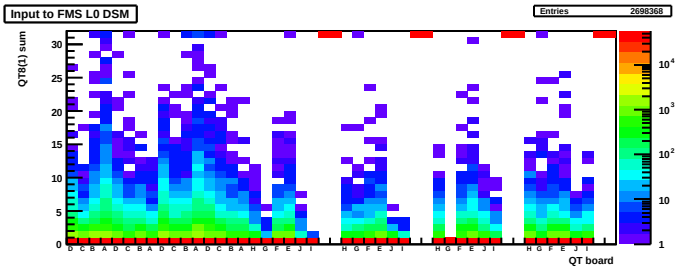

Input to QT2 crate
Entries 794788
ADC
channel

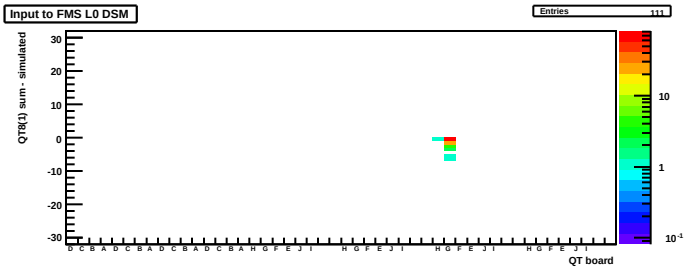

Input to QT3 crate
Entries1018388
ADC
channel

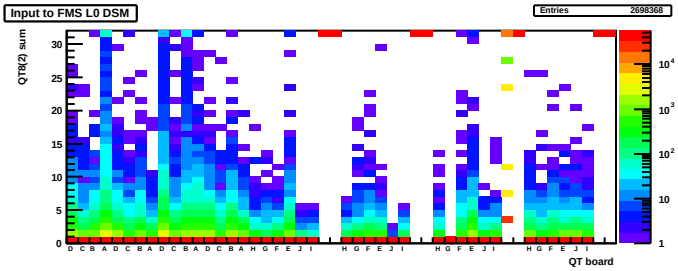

Input to QT4 crate
Entries1068403
ADC
channel

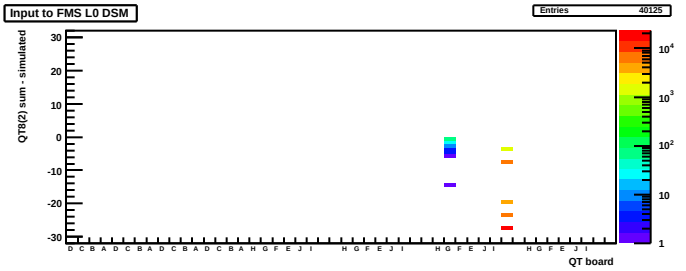

Input to FMS L0 DSM
Entries 2698368
QT8(0) sum
QT board

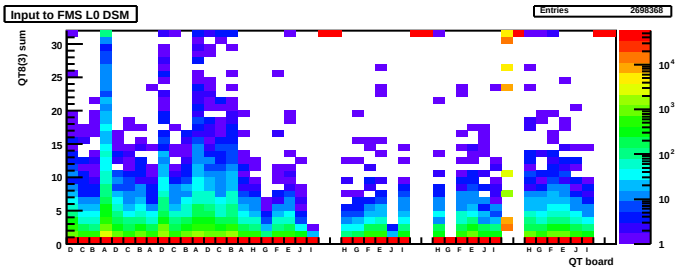

Input to FMS L0 DSM
Entries 64
QT8(0) sum - simulated
QT board

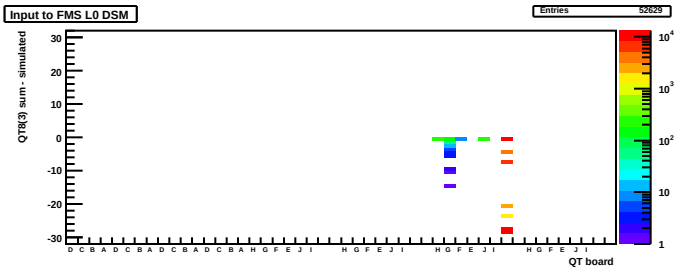

Input to FMS L0 DSM
Entries 2698368
QT8(1) sum
QT board

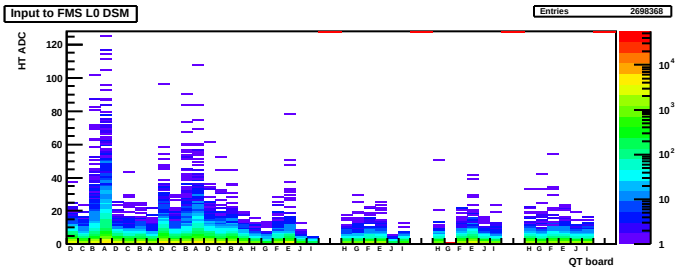

Input to FMS L0 DSM
Entries 111
QT8(1) sum - simulated
QT board

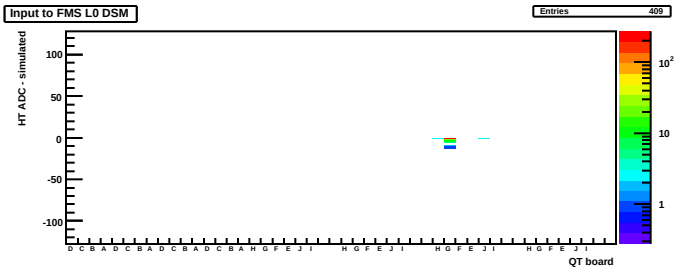

Input to FMS L0 DSM
Entries 2698368
QT8(2) sum
QT board

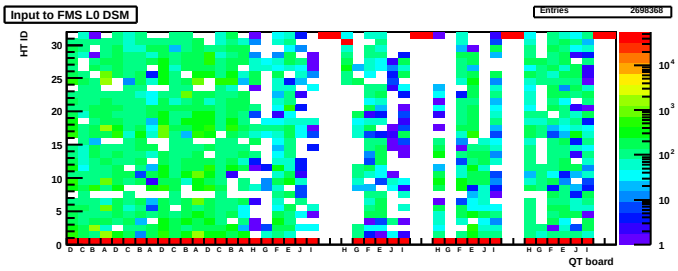

Input to FMS L0 DSM
Entries 40125
QT8(2) sum - simulated
QT board

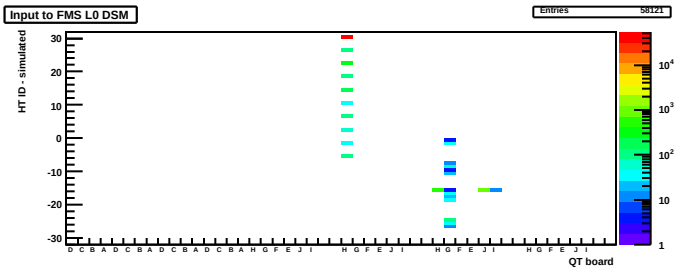

Input to FMS L0 DSM
Entries 2698368
QT8(3) sum
QT board

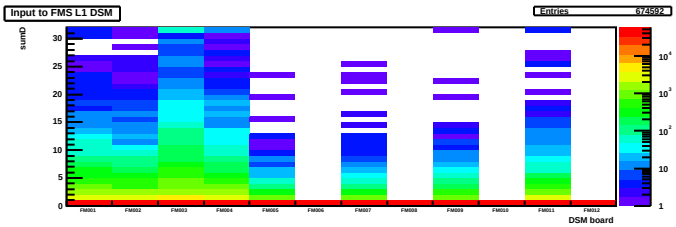

Input to FMS L0 DSM
Entries 52629
QT8(3) sum - simulated
QT board

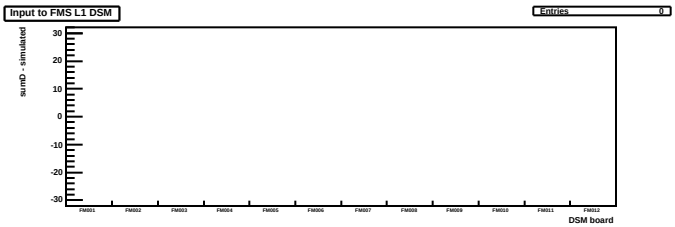

Input to FMS L0 DSM
Entries 2698368
HT ADC
QT board

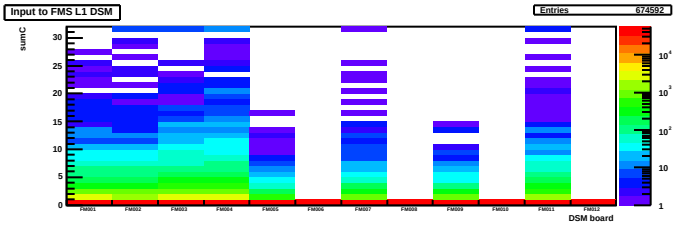

Input to FMS L0 DSM
Entries 409
HT ADC - simulated
QT board

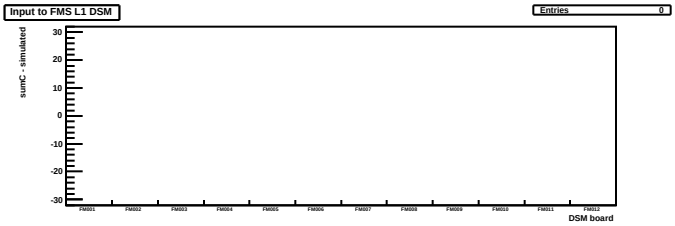

Input to FMS L0 DSM
Entries 2698368
HT ID
QT board

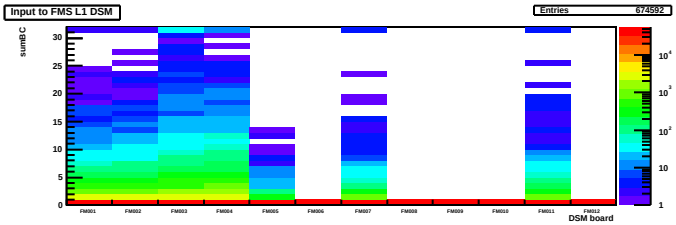

Input to FMS L0 DSM
Entries 58121
HT ID - simulated
QT board

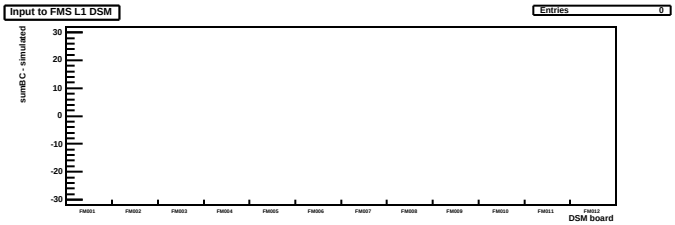

Input to FMS L1 DSM
Entries 674592
sumD
DSM board

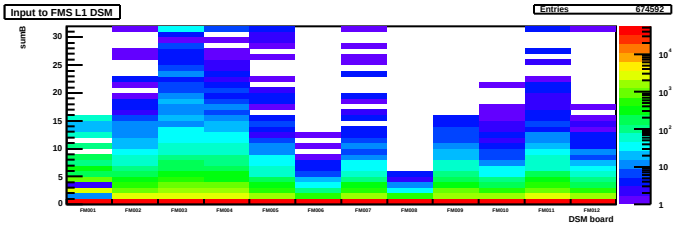

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

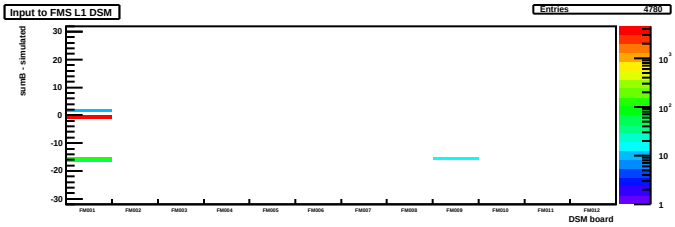

Input to FMS L1 DSM
Entries 674592
sumC
DSM board

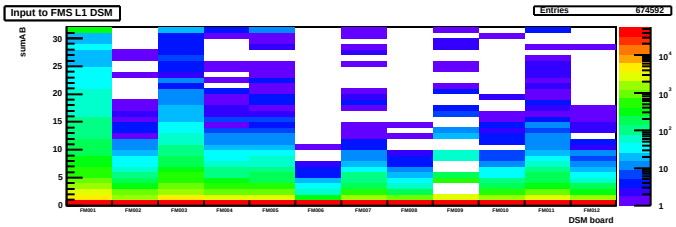

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

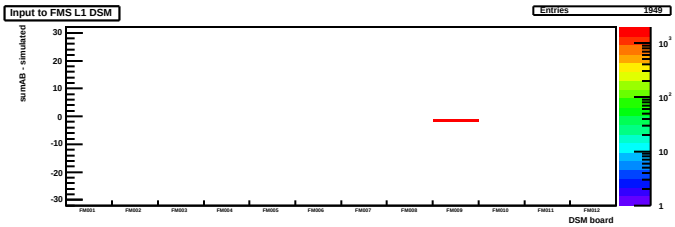

Input to FMS L1 DSM
Entries 674592
sumBC
DSM board

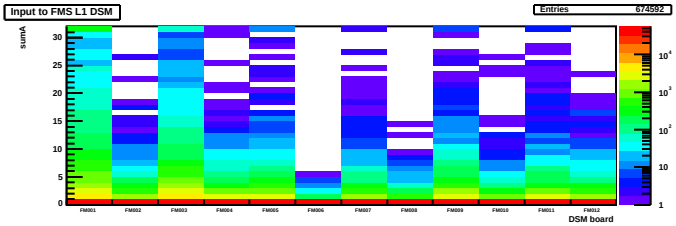

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

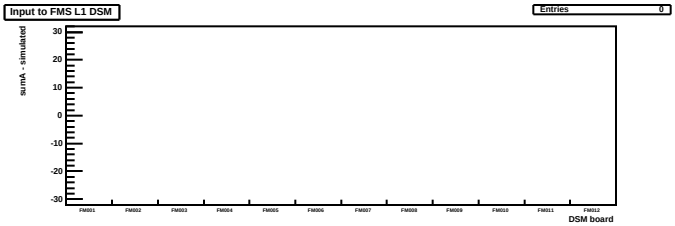

Input to FMS L1 DSM
Entries 674592
sumB
DSM board

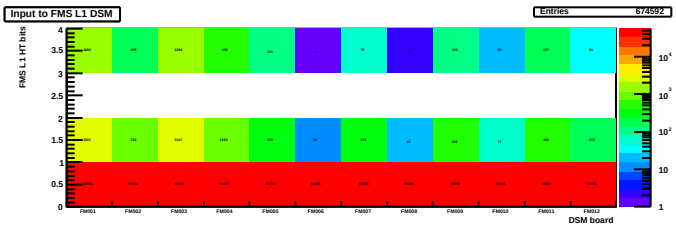

Input to FMS L1 DSM
Entries 4780
sumB - simulated
DSM board

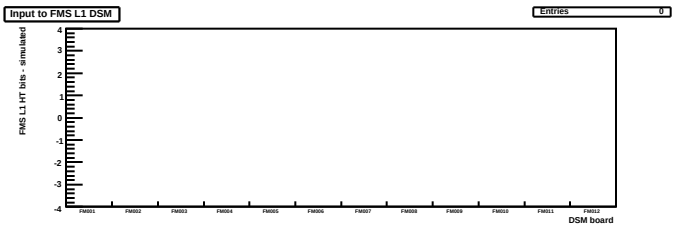

Input to FMS L1 DSM
Entries 674592
sumAB
DSM board

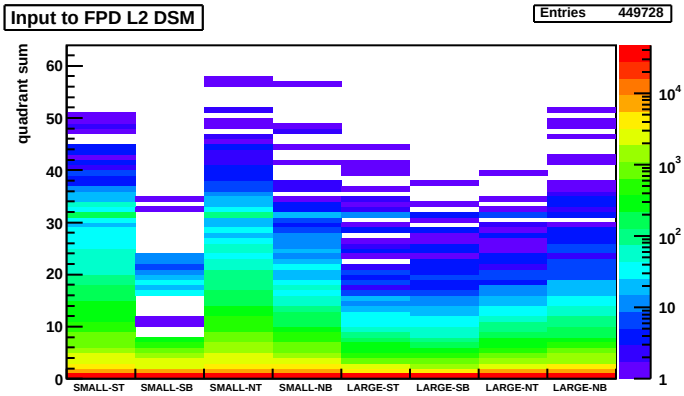

Input to FMS L1 DSM
Entries 1949
sumAB - simulated
DSM board

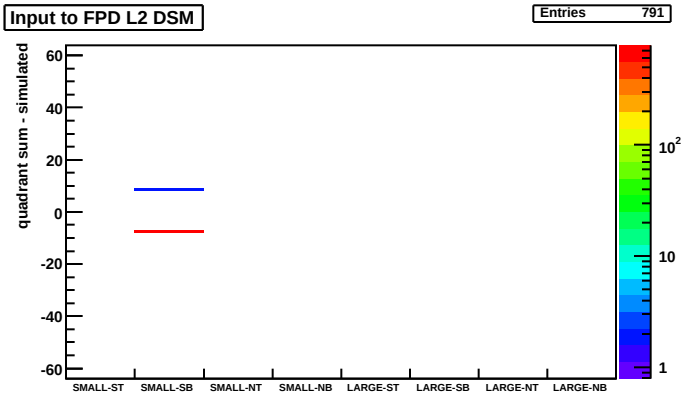

Input to FMS L1 DSM
Entries 674592
sumA
DSM board

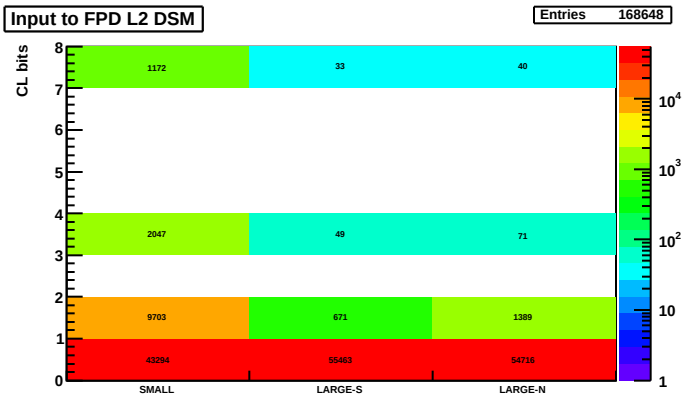

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

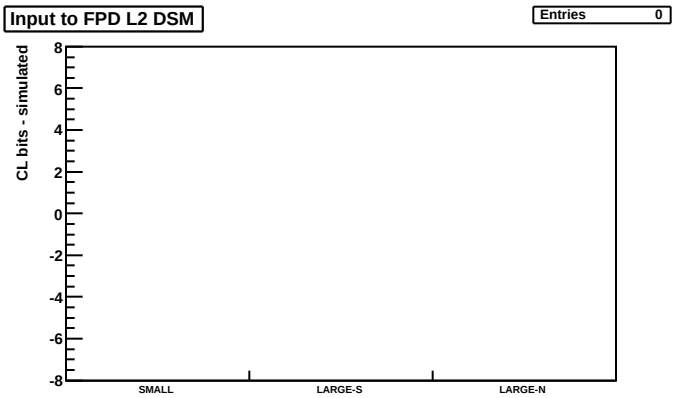

Input to FMS L1 DSM
Entries 674592
FMS L1 HT bits
DSM board

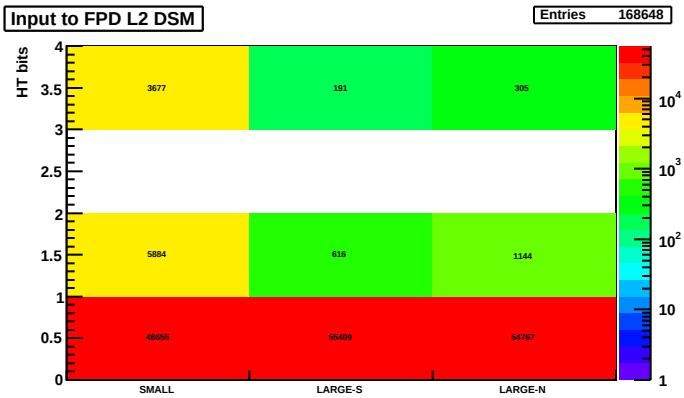

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

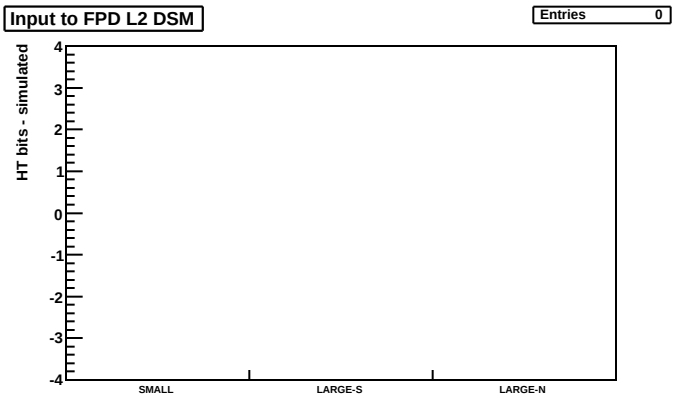

Input to FPD L2 DSM
Entries 449728
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

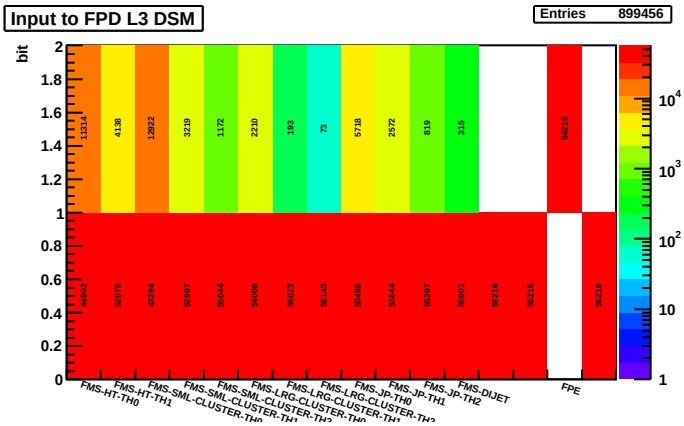

Input to FPD L2 DSM
Entries 791
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

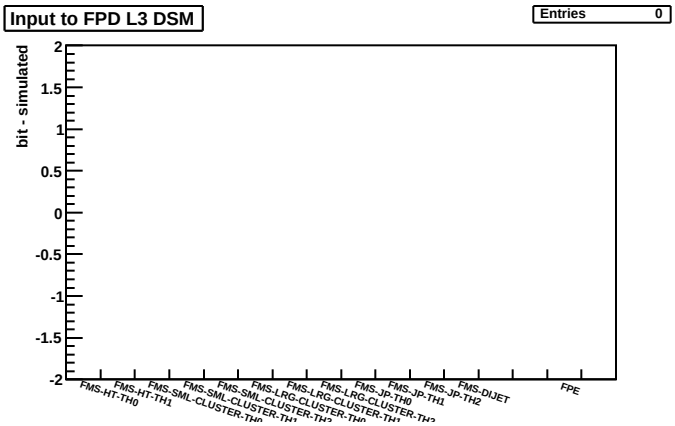

Input to FPD L2 DSM
Entries 168648
CL bits
1172
33
40
2047
49
71
9703
671
1389
43294
55463
54716
SMALL
LARGE-S
LARGE-N

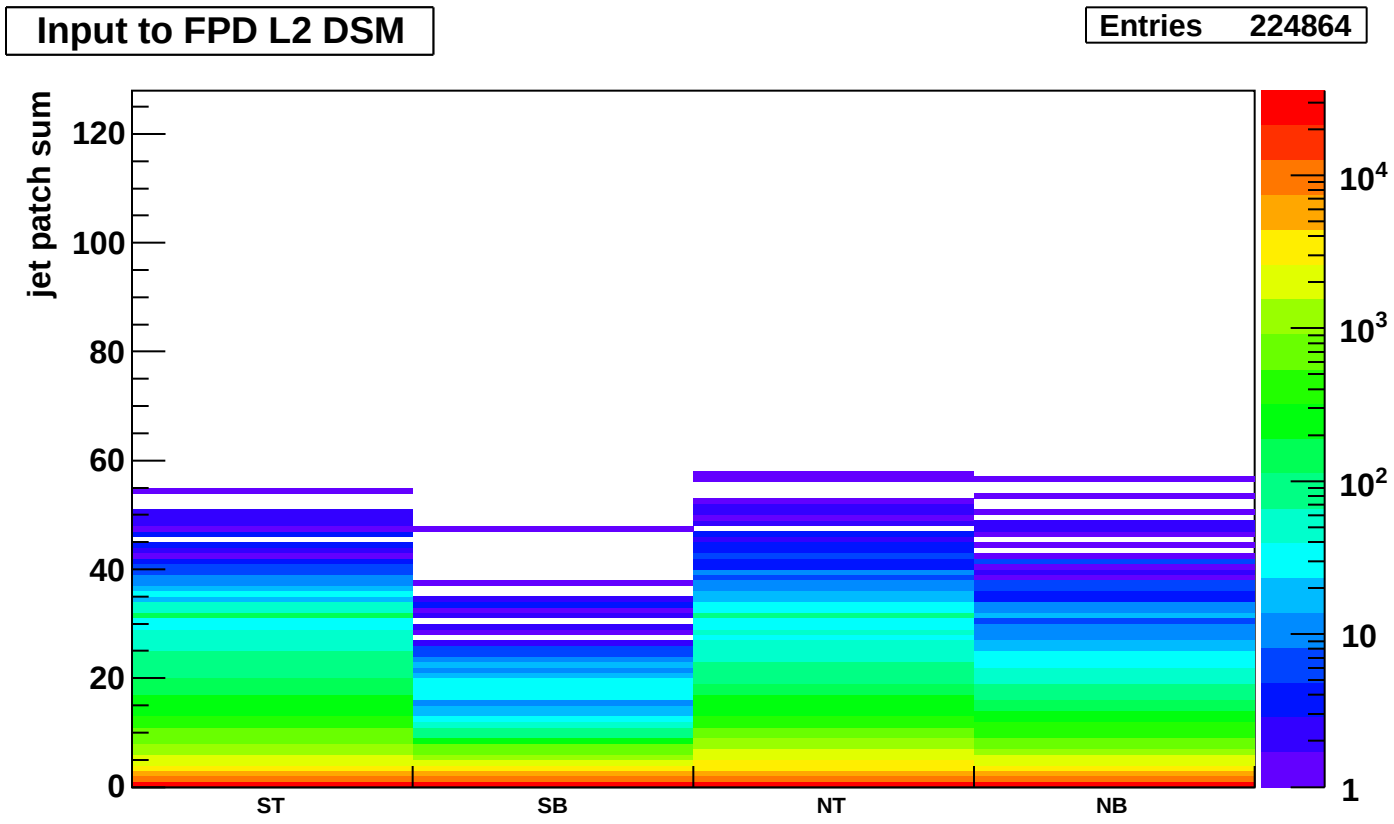

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

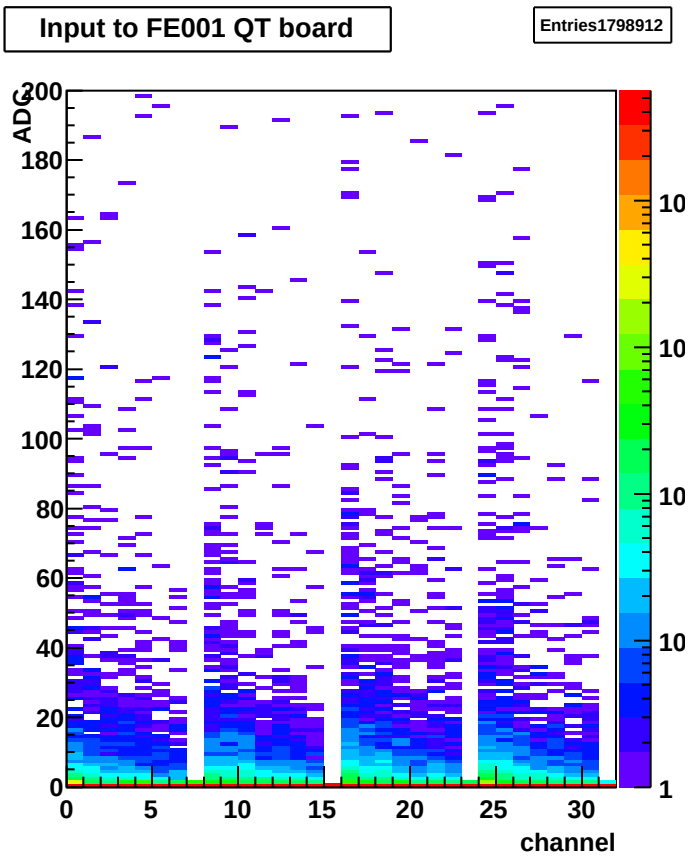

Input to FPD L2 DSM
Entries 168648
HT bits
3677
191
305
5884
616
1144
46655
55409
54767
SMALL
LARGE-S
LARGE-N

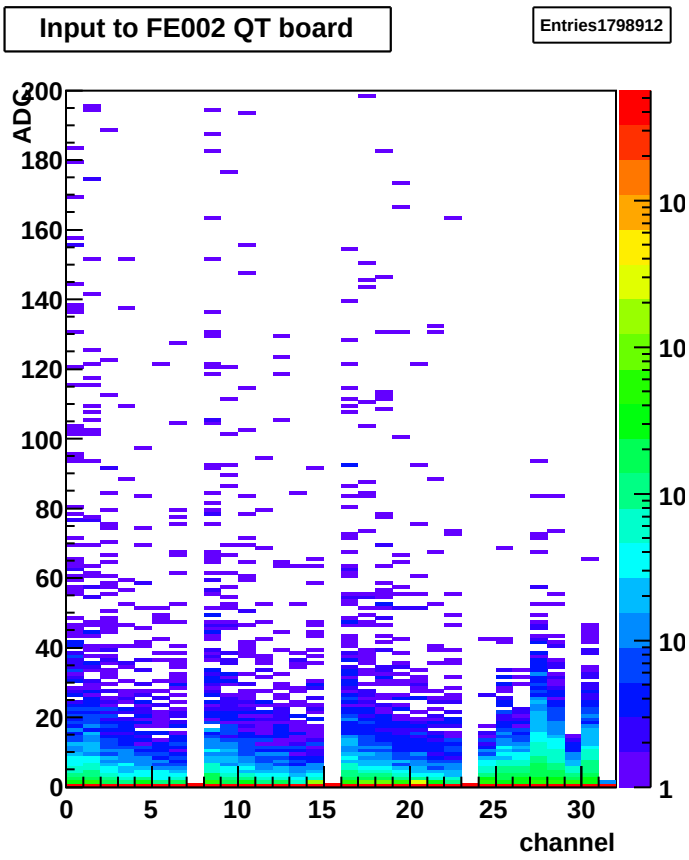

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

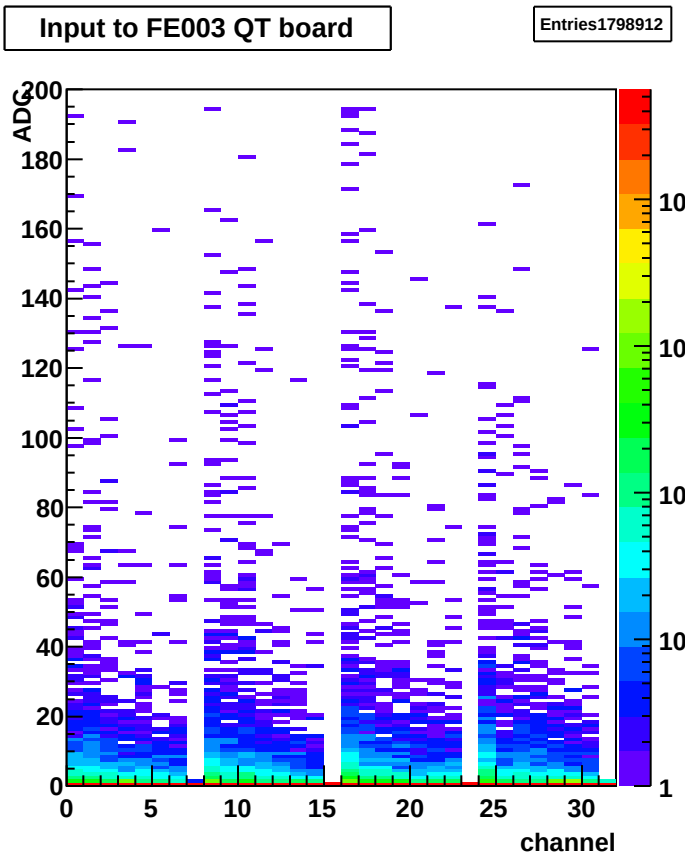

Input to FPD L3 DSM
Entries 899456
bit
13314
4138
12922
3219
1172
2210
193
73
5718
2572
819
315
56216
46902
52078
43294
52997
55044
54006
56023
55143
50498
53544
55397
55931
56216
56216
56216
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

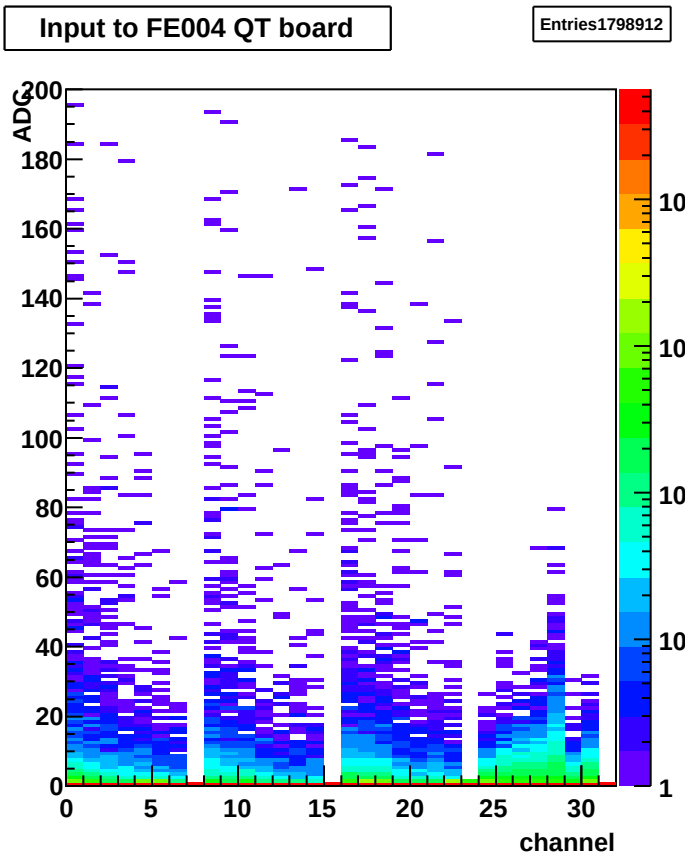

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

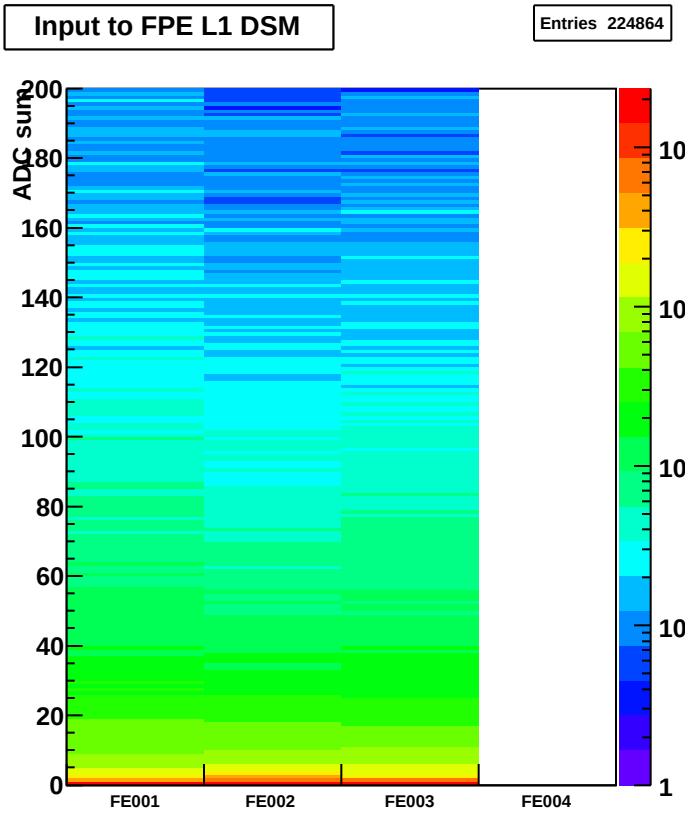

Input to FPD L2 DSM
Entries 224864
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

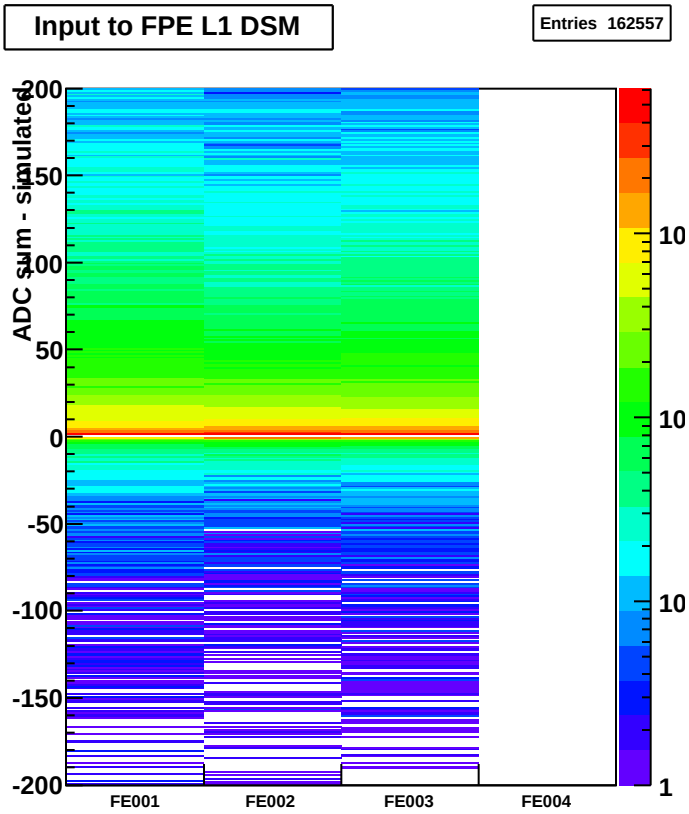

Input to FE001 QT board
Entries1798912
ADC
channel

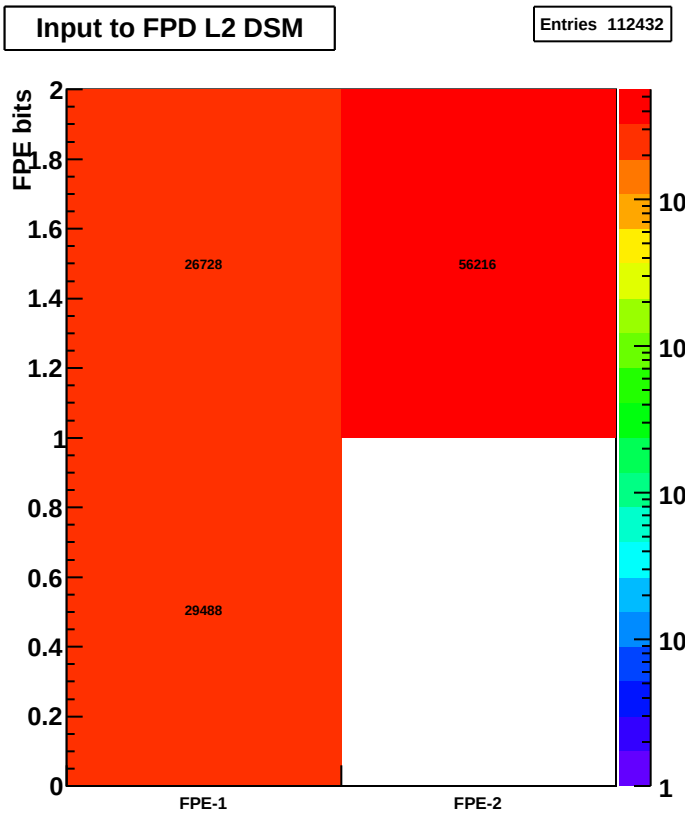

Input to FE002 QT board
Entries1798912
ADC
channel

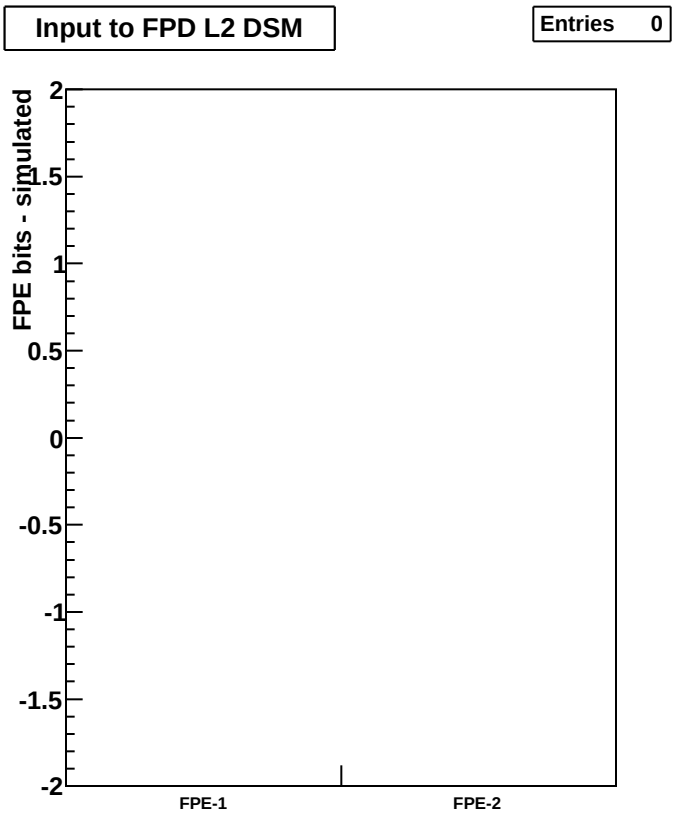

Input to FE003 QT board
Entries1798912
ADC
channel


Input to FE004 QT board
Entries1798912
ADC
channel


Input to FPE L1 DSM
Entries 224864
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 162557
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 112432
FPE bits
26728
56216
29488
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2