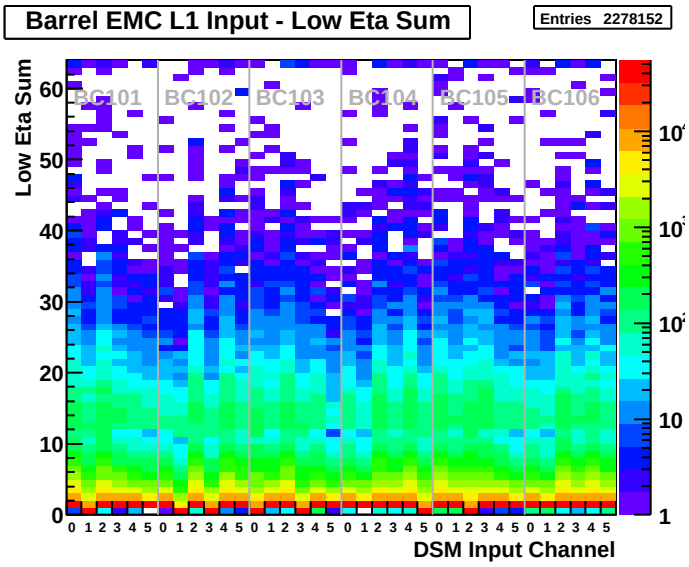
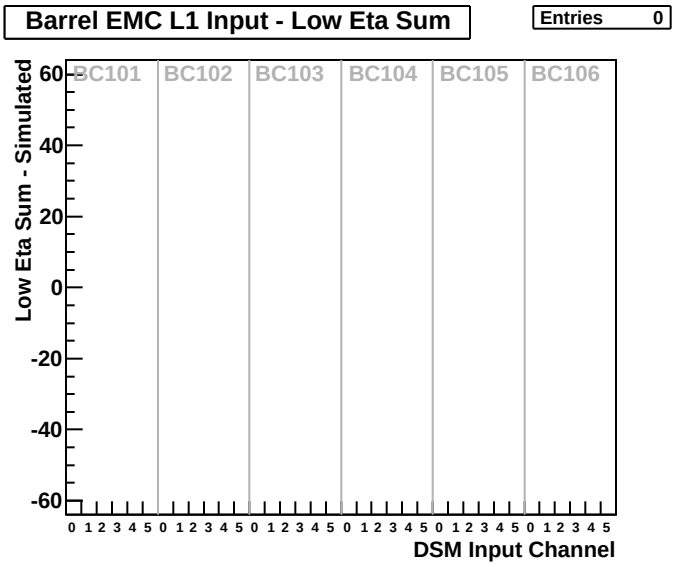
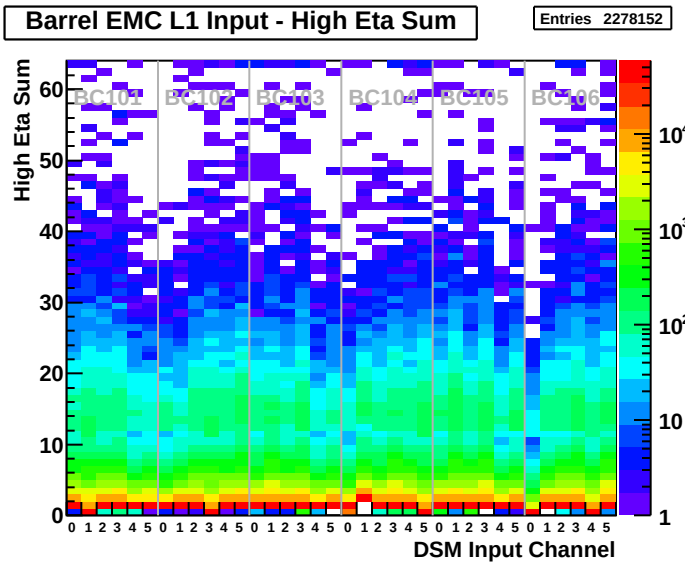
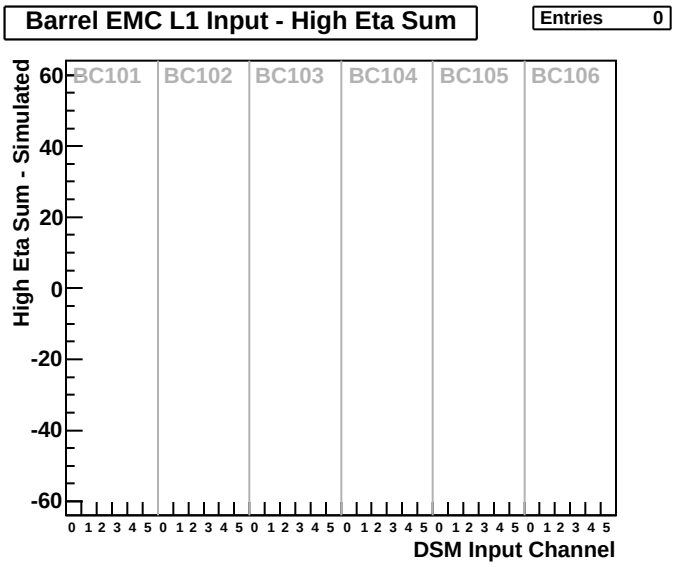
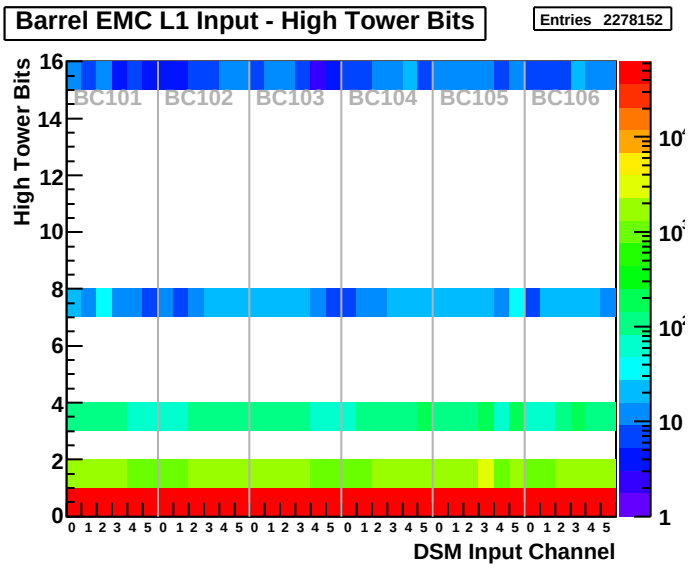
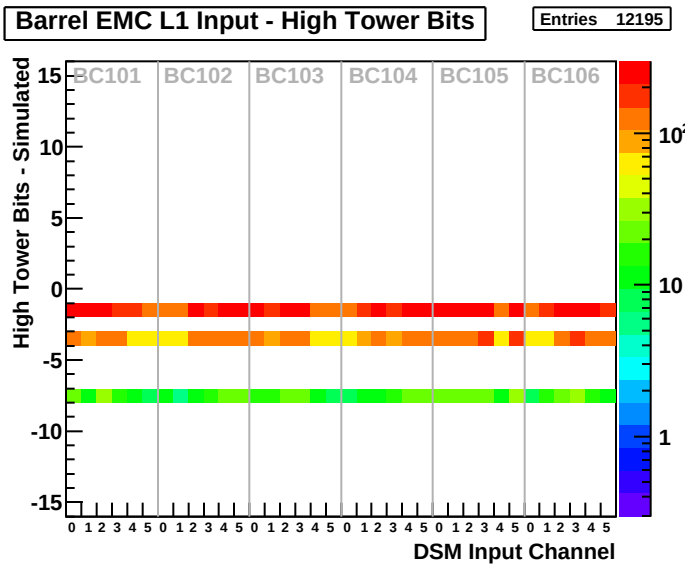

Barrel EMC L1 Input - Low Eta Sum
Entries 2278152
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

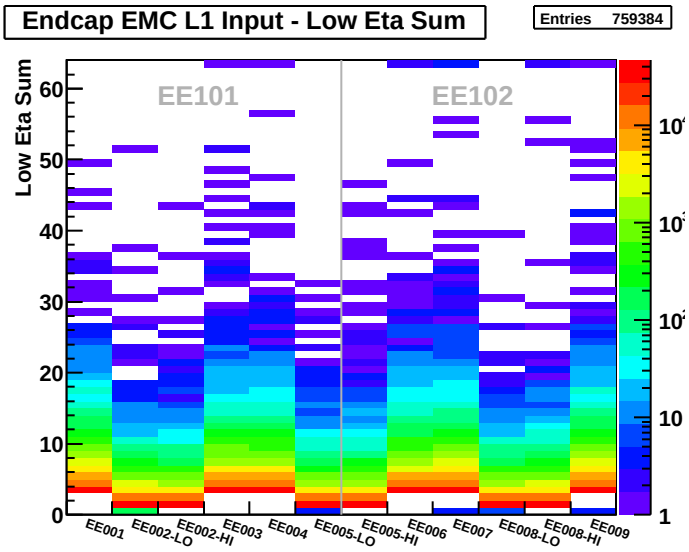

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

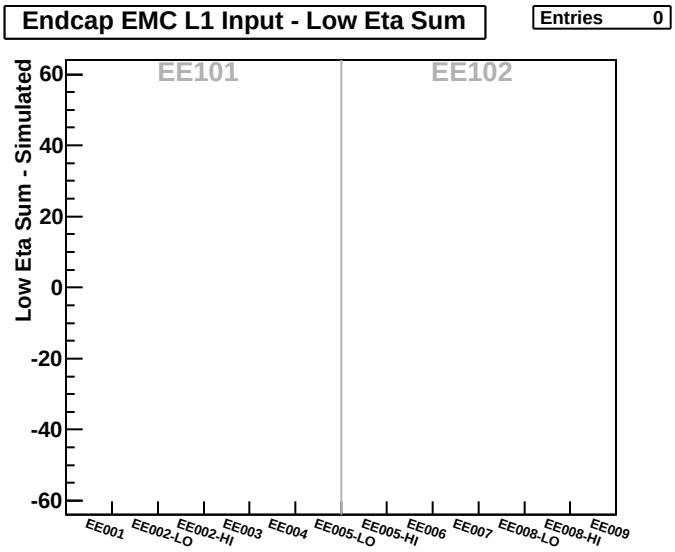

Barrel EMC L1 Input - High Eta Sum
Entries 2278152
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

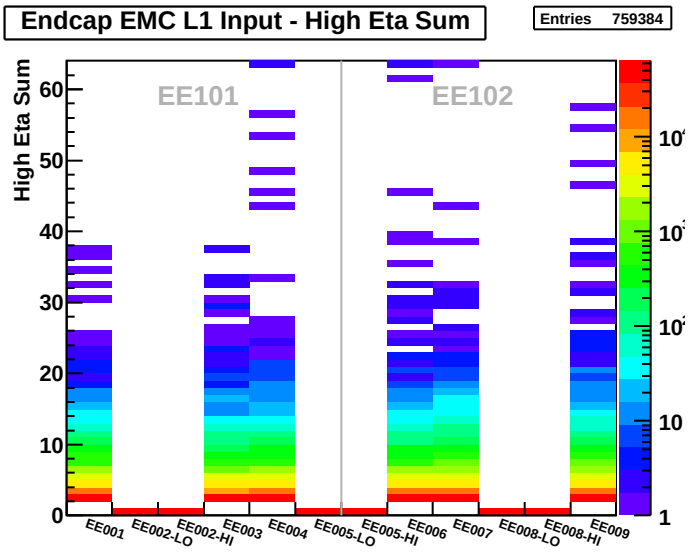

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

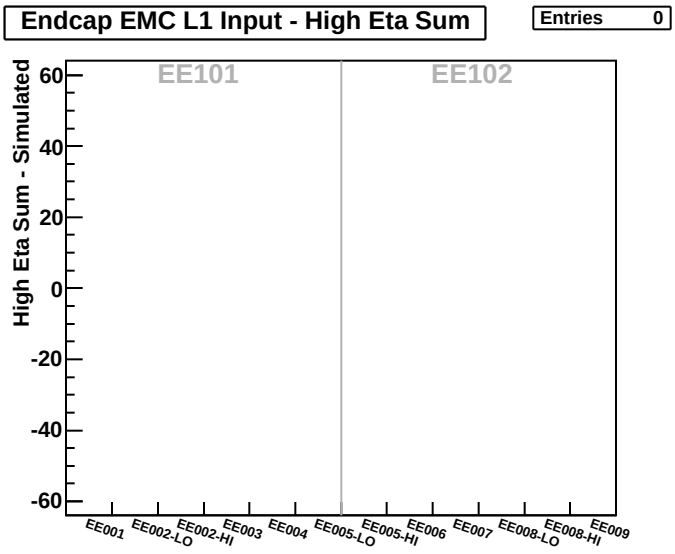

Barrel EMC L1 Input - High Tower Bits
Entries 2278152
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

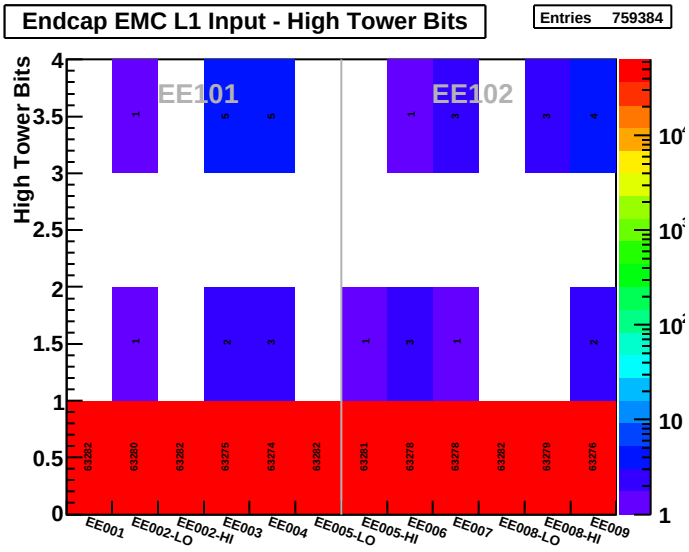

Barrel EMC L1 Input - High Tower Bits
Entries 12195
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

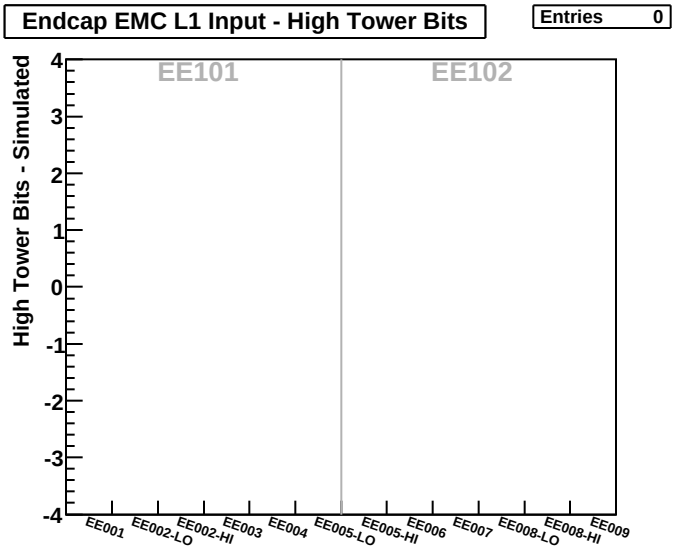

Endcap EMC L1 Input - Low Eta Sum
Entries 759384
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

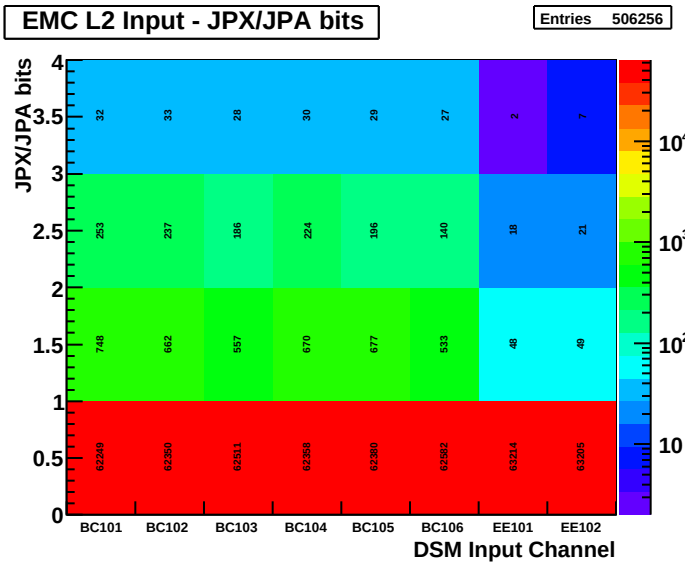

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

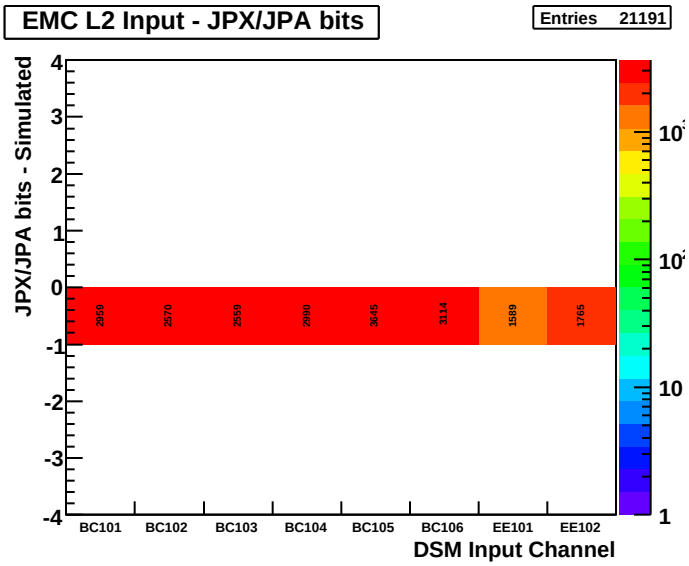

Endcap EMC L1 Input - High Eta Sum
Entries 759384
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

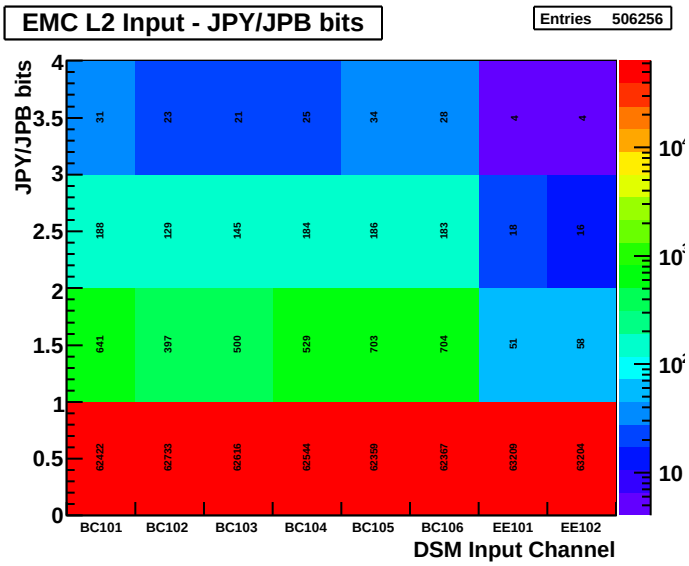

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

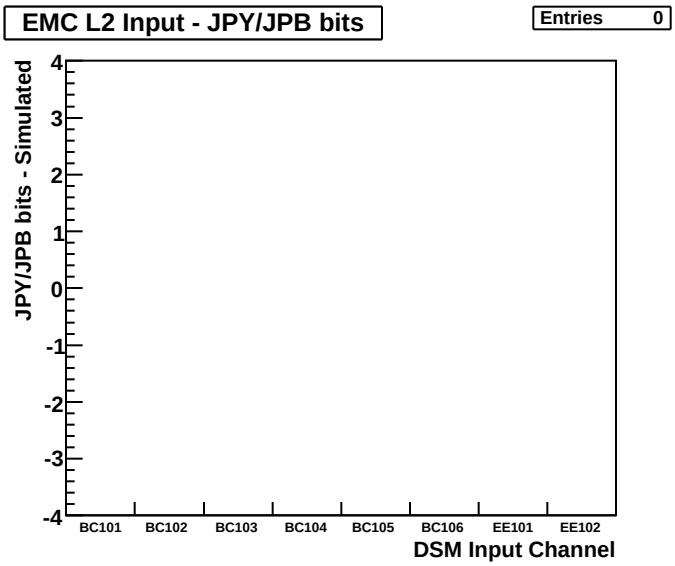

Endcap EMC L1 Input - High Tower Bits
Entries 759384
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

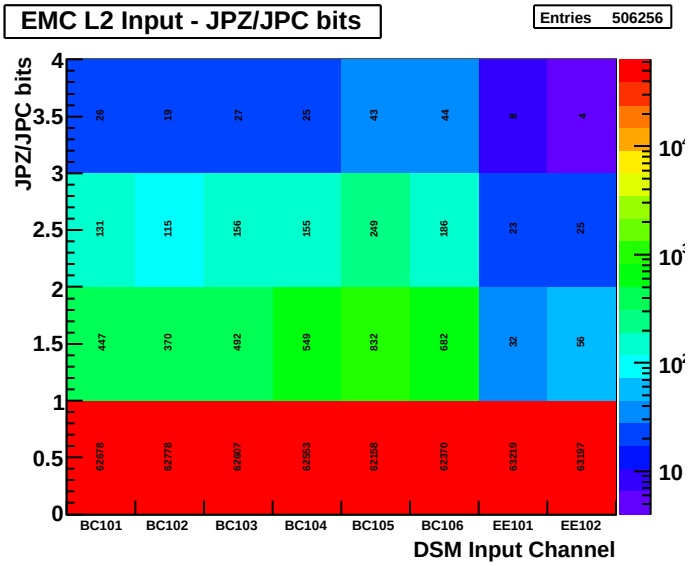

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

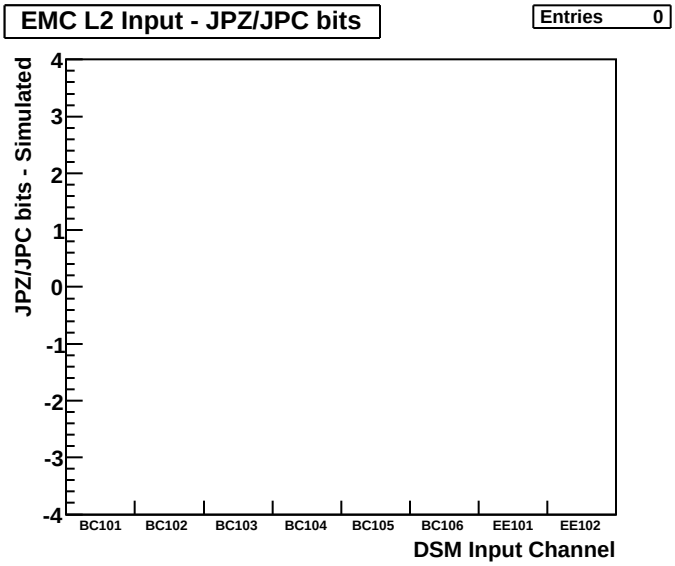

EMC L2 Input - JPX/JPA bits
Entries 506256
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
32 33 28 30 29 27 2 7
253 237 186 224 196 140 18 21
748 662 557 670 677 533 48 49
62249 62350 62511 62358 62380 62582 63214 63205

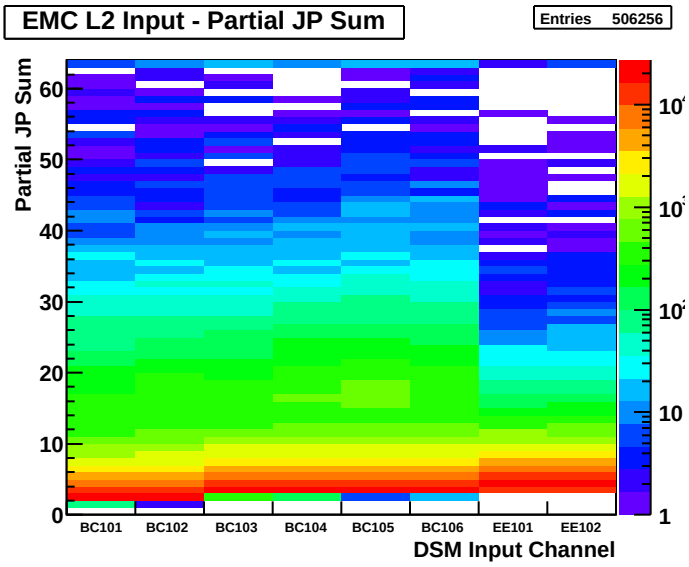

EMC L2 Input - JPX/JPA bits
Entries 21191
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2959 2570 2559 2990 3645 3114 1589 1765

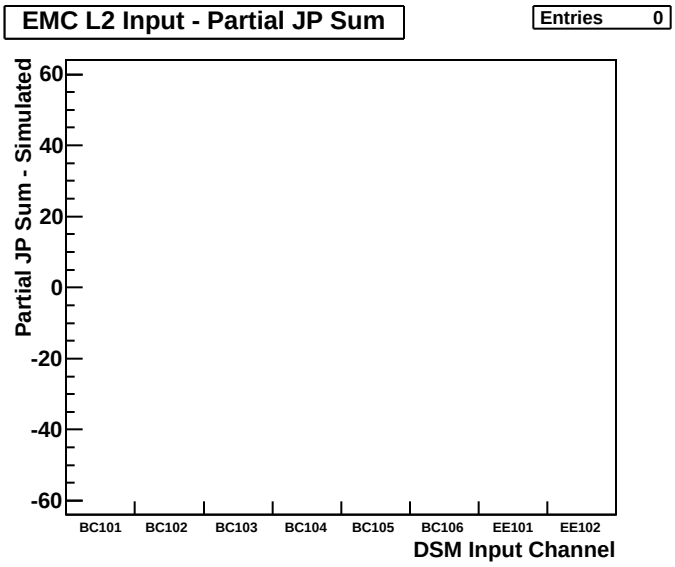

EMC L2 Input - JPY/JPB bits
Entries 506256
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
31 23 21 25 34 28 4 4
188 129 145 184 186 183 18 16
641 397 500 529 703 704 51 58
62422 62733 62616 62544 62359 62367 63209 63204

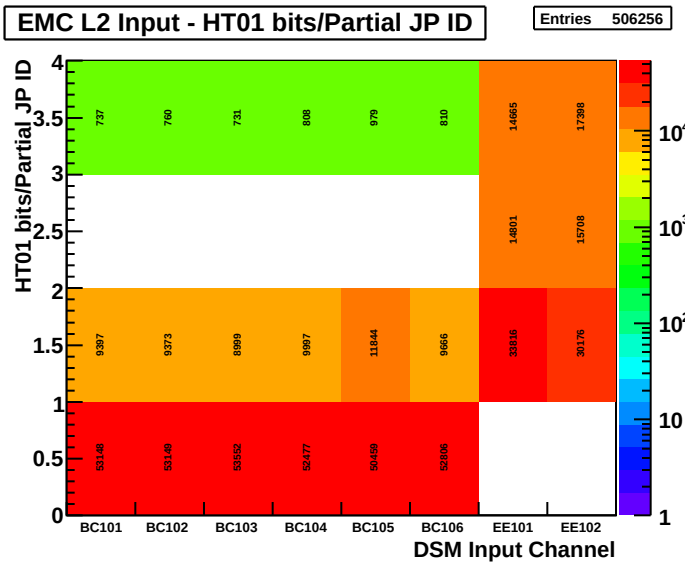

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

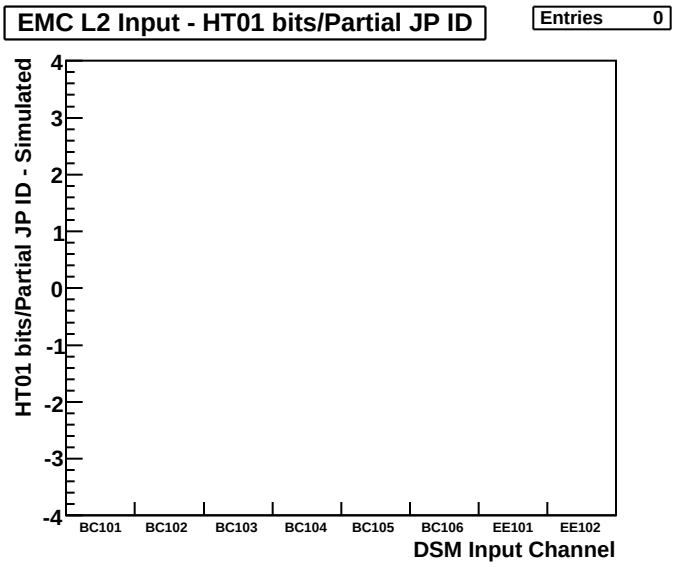

EMC L2 Input - JPZ/JPC bits
Entries 506256
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
26 19 27 25 43 44 8 4
131 115 156 155 249 186 23 25
447 370 492 549 832 682 32 56
62678 62778 62607 62553 62158 62370 63219 63197

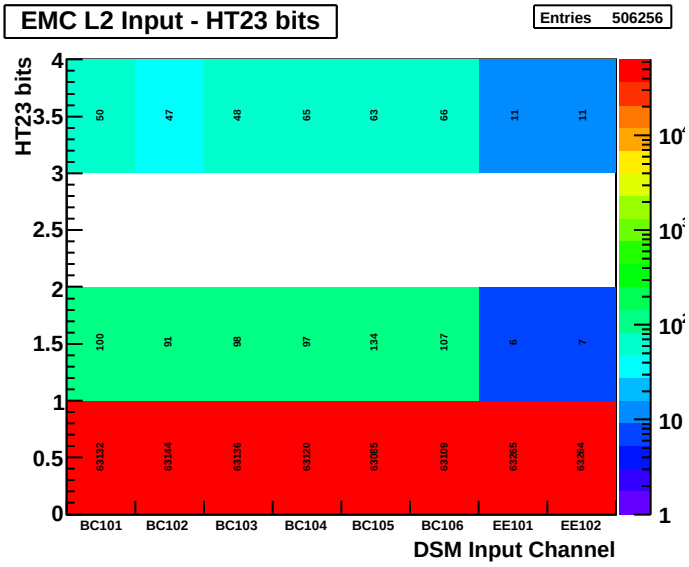

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

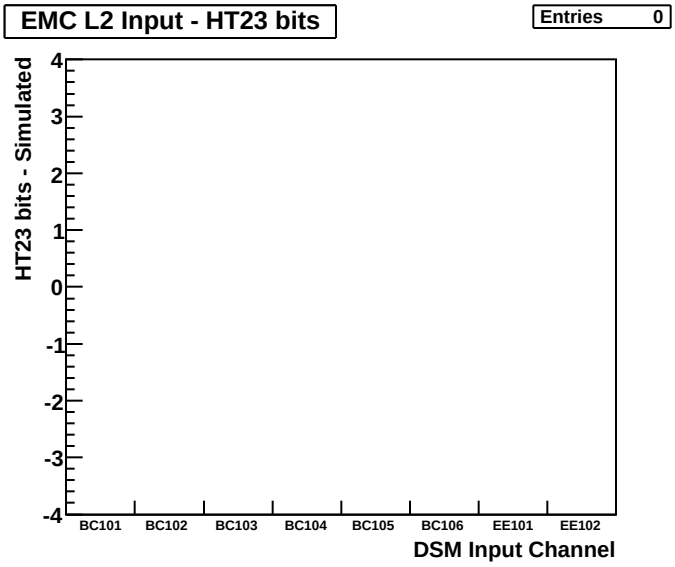

EMC L2 Input - Partial JP Sum
Entries 506256
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

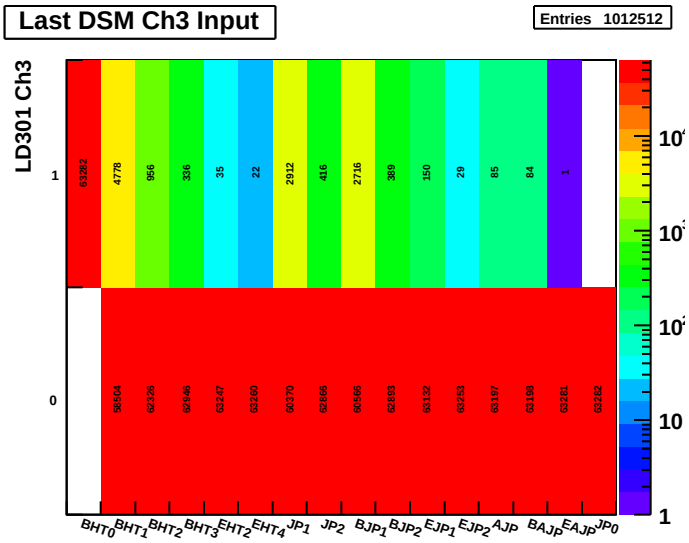

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

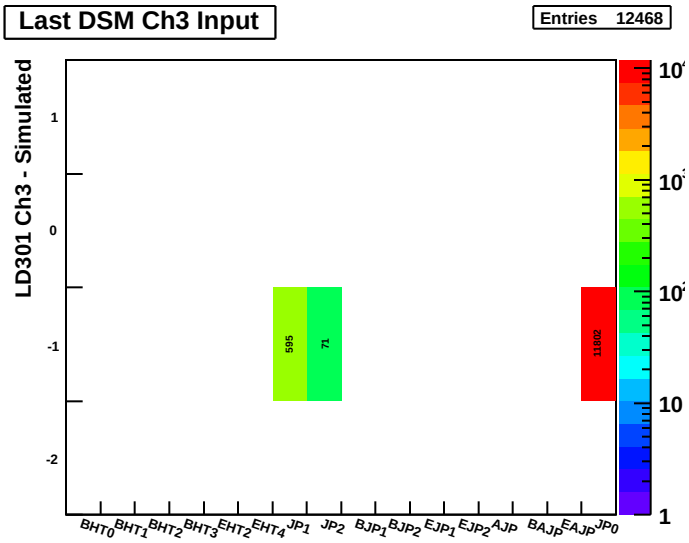

EMC L2 Input - HT01 bits/Partial JP ID
Entries 506256
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

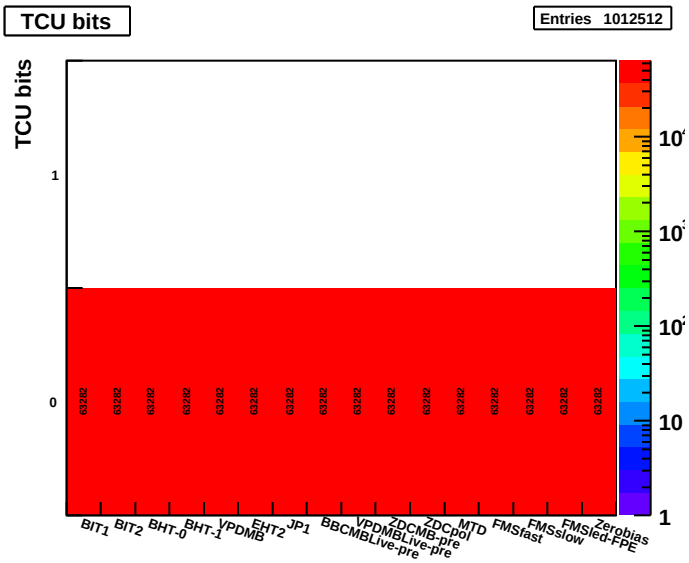

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

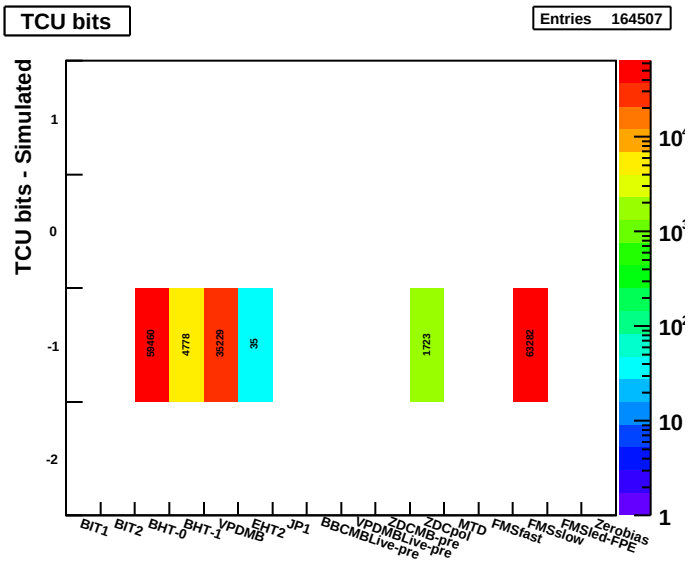

EMC L2 Input - HT23 bits
Entries 506256
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

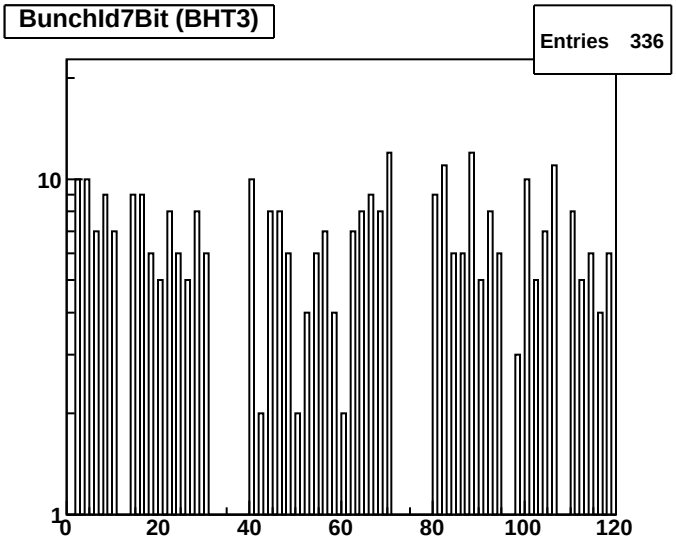

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

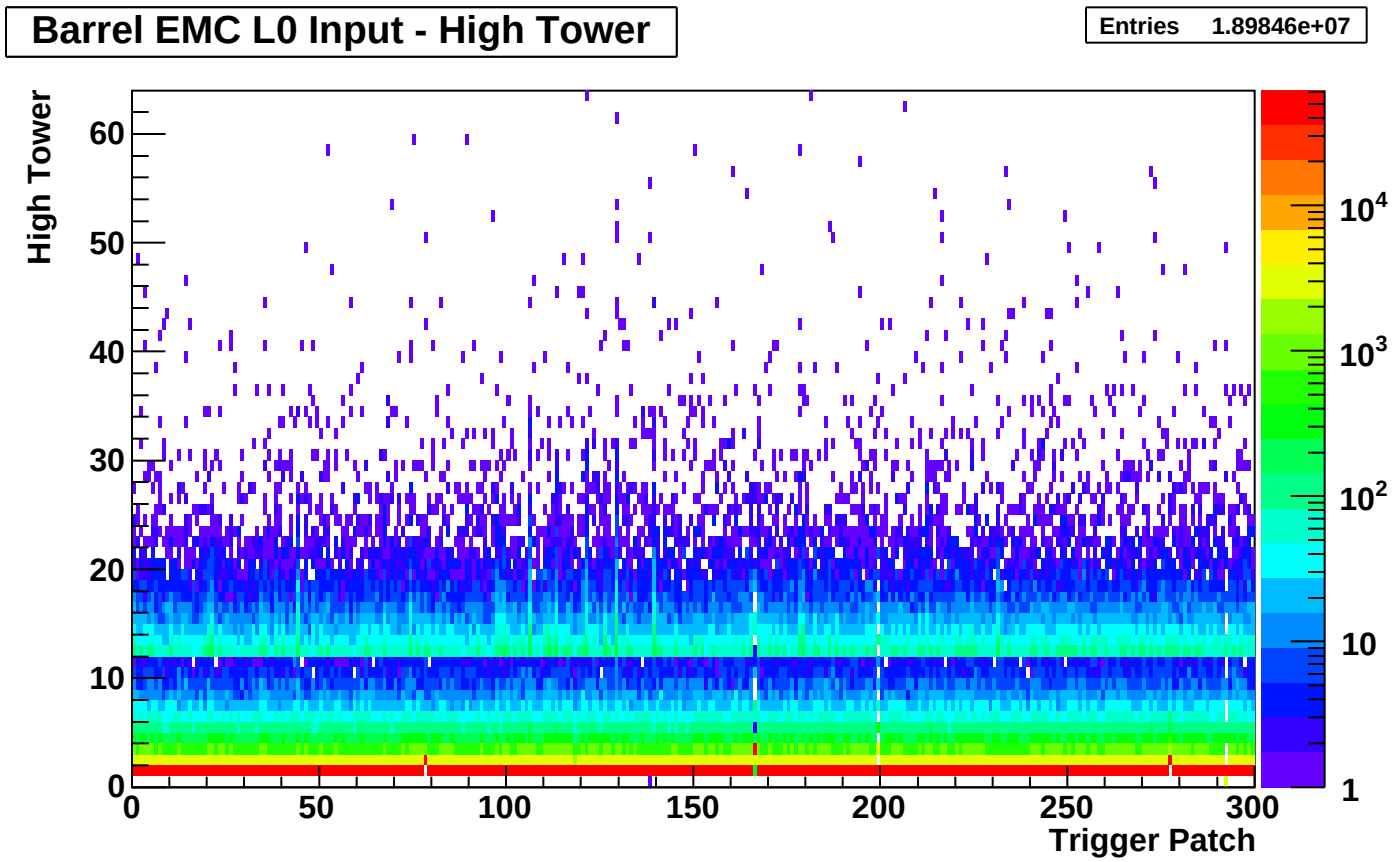

Last DSM Ch3 Input
Entries 1012512
LD301 Ch3
63282 4778 956 336 35 22 2912 416 2716 389 150 29 85 84 1
59504 62326 62946 63247 63260 60370 62866 60566 62893 63132 63253 63197 63198 63281 63282
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

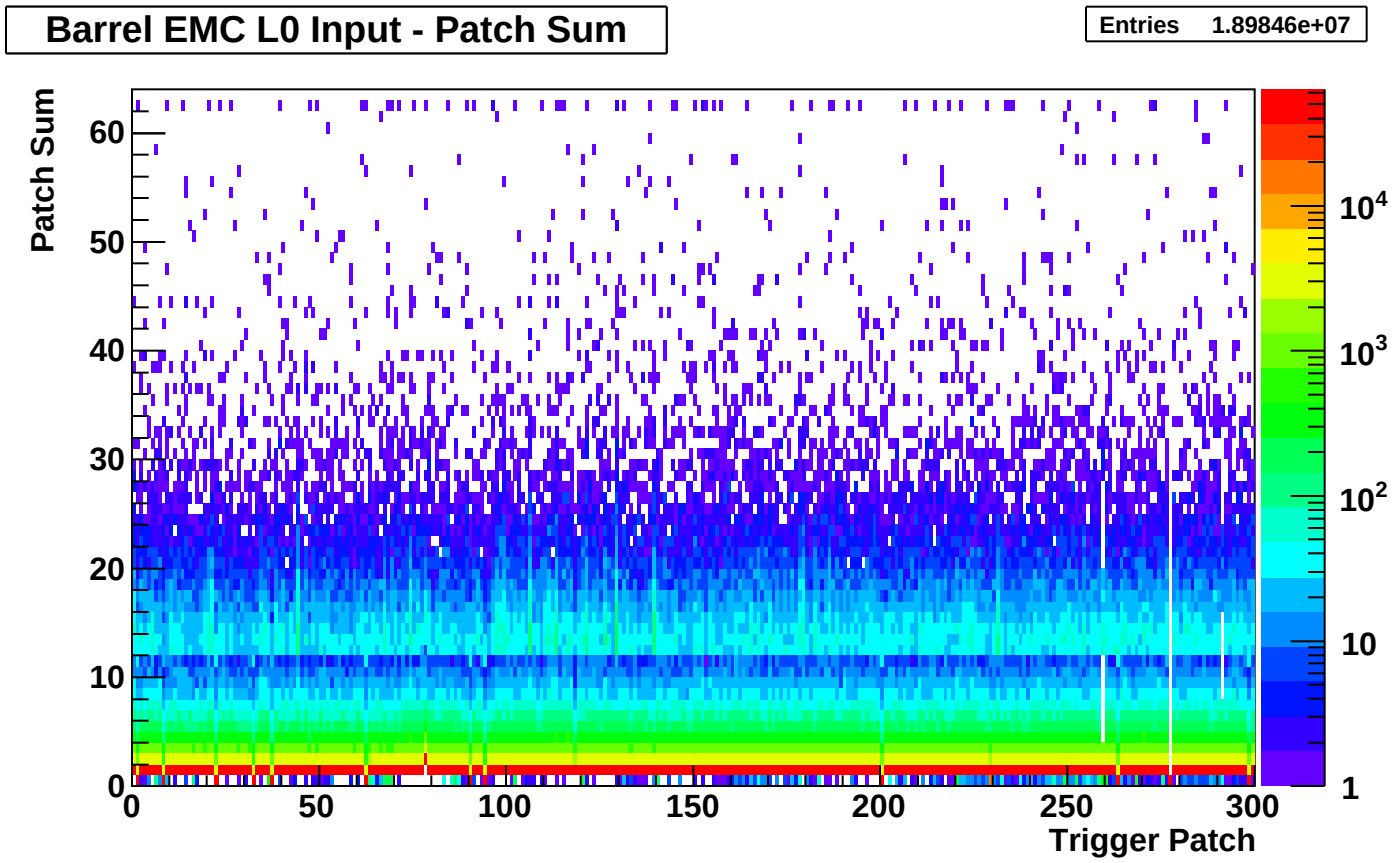

Last DSM Ch3 Input
Entries 12468
LD301 Ch3 - Simulated
595 71 11802
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

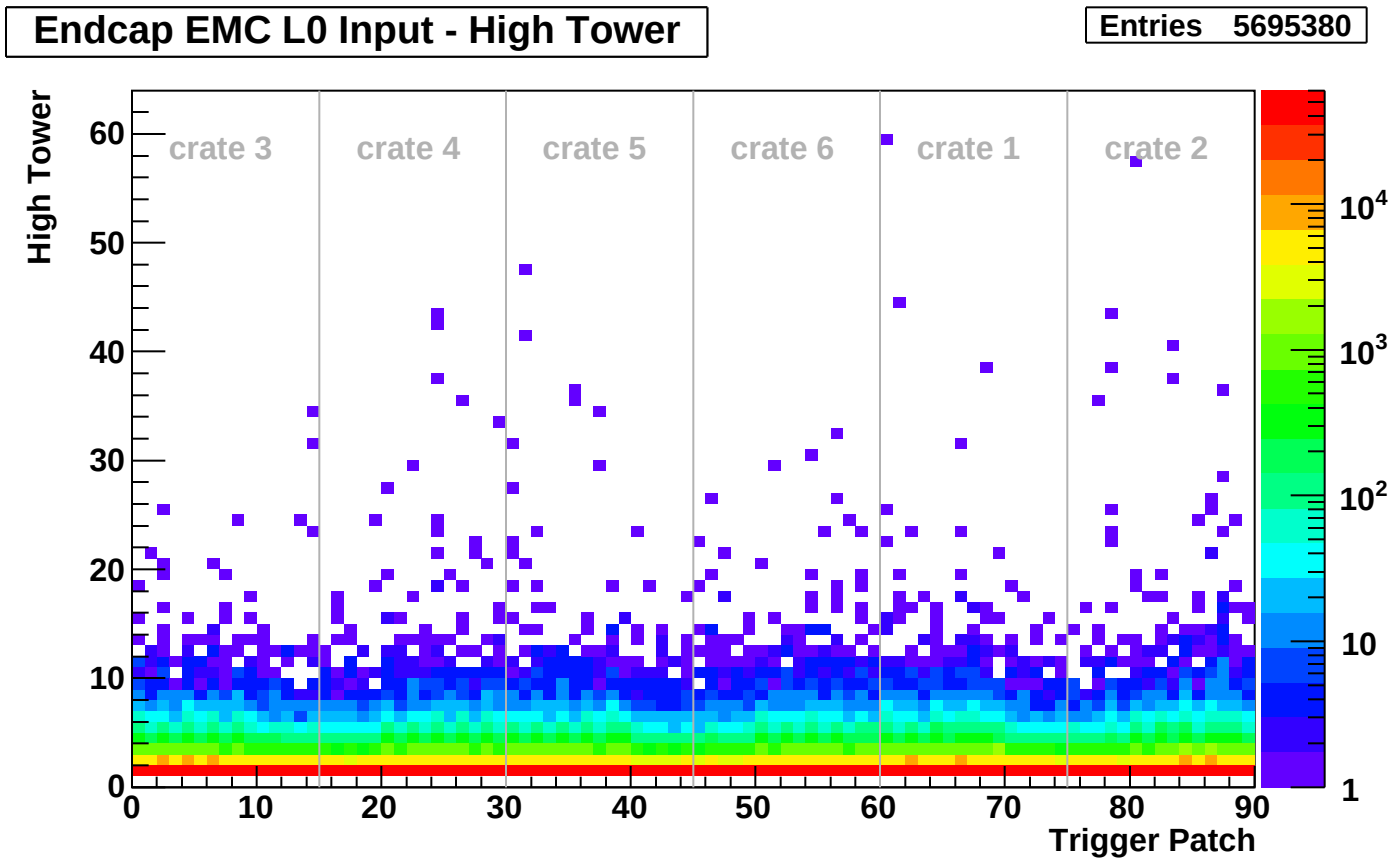

TCU bits
Entries 1012512
TCU bits
63282 63282 63282 63282 63282 63282 63282 63282 63282 63282 63282 63282 63282 63282 63282 63282
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

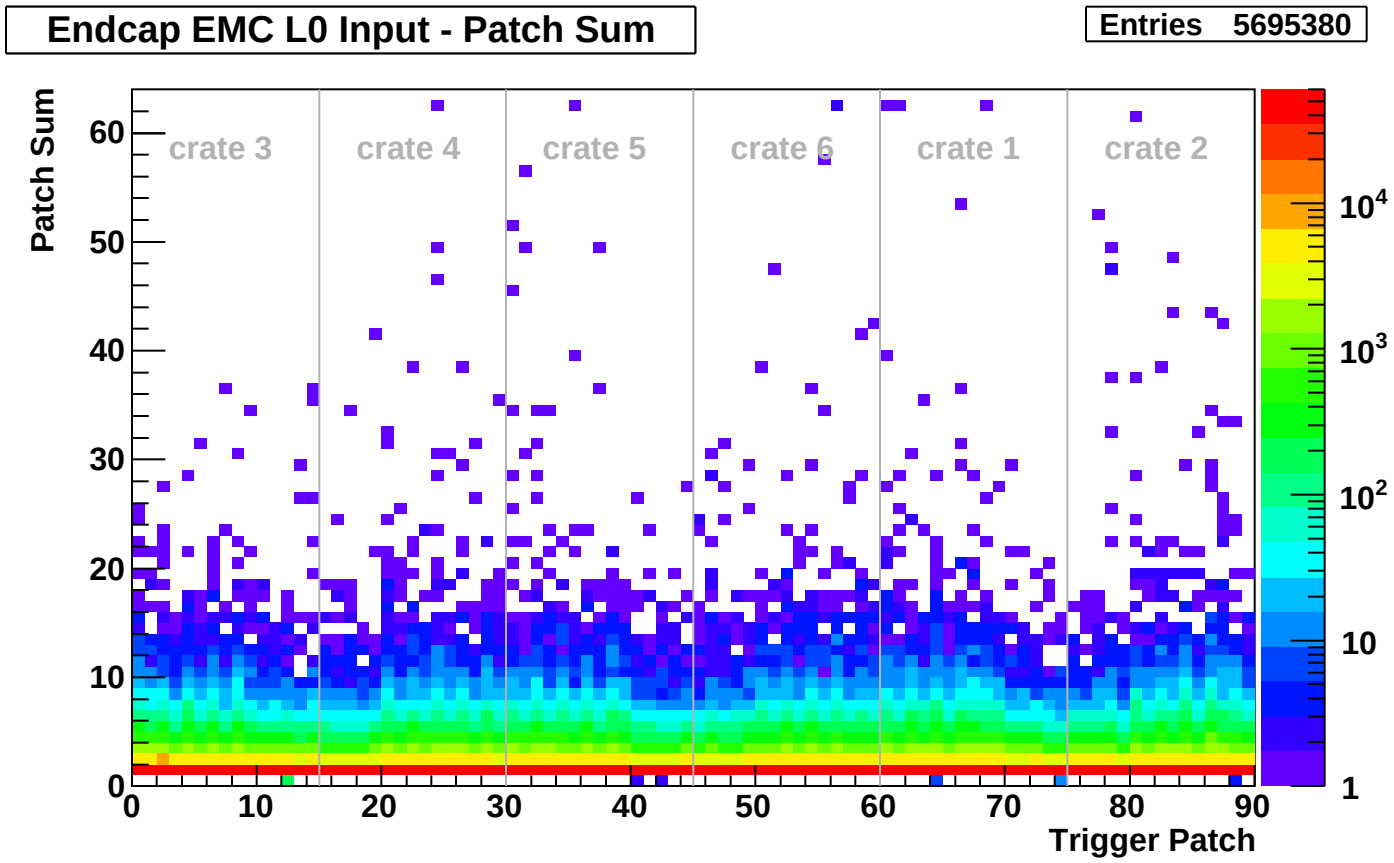

TCU bits
Entries 164507
TCU bits - Simulated
59460 4778 35229 35 1723 63282
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

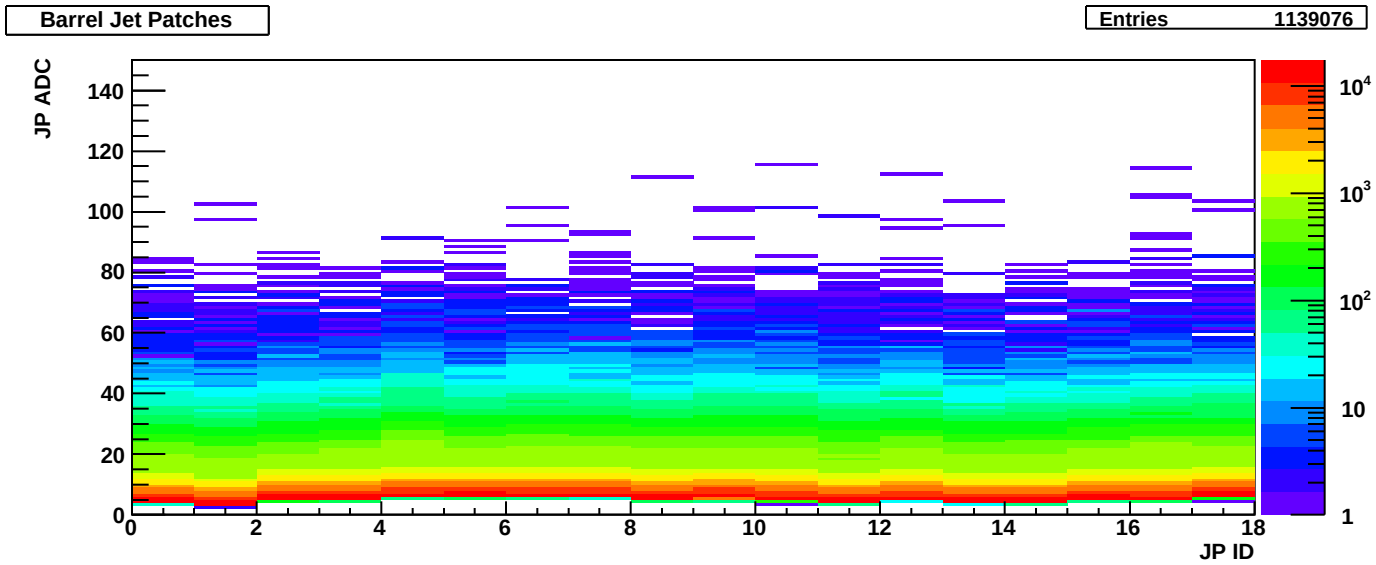

BunchId7Bit (BHT3)
Entries 336
0 20 40 60 80 100 120

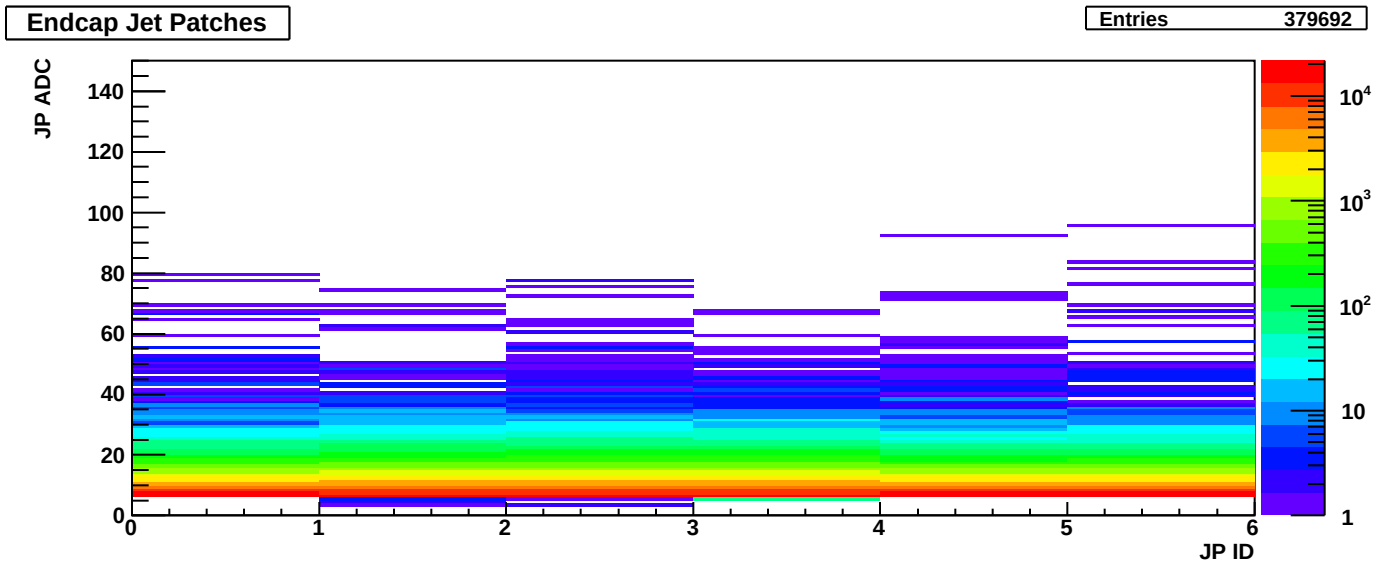

Barrel EMC L0 Input - High Tower
Entries 1.89846e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

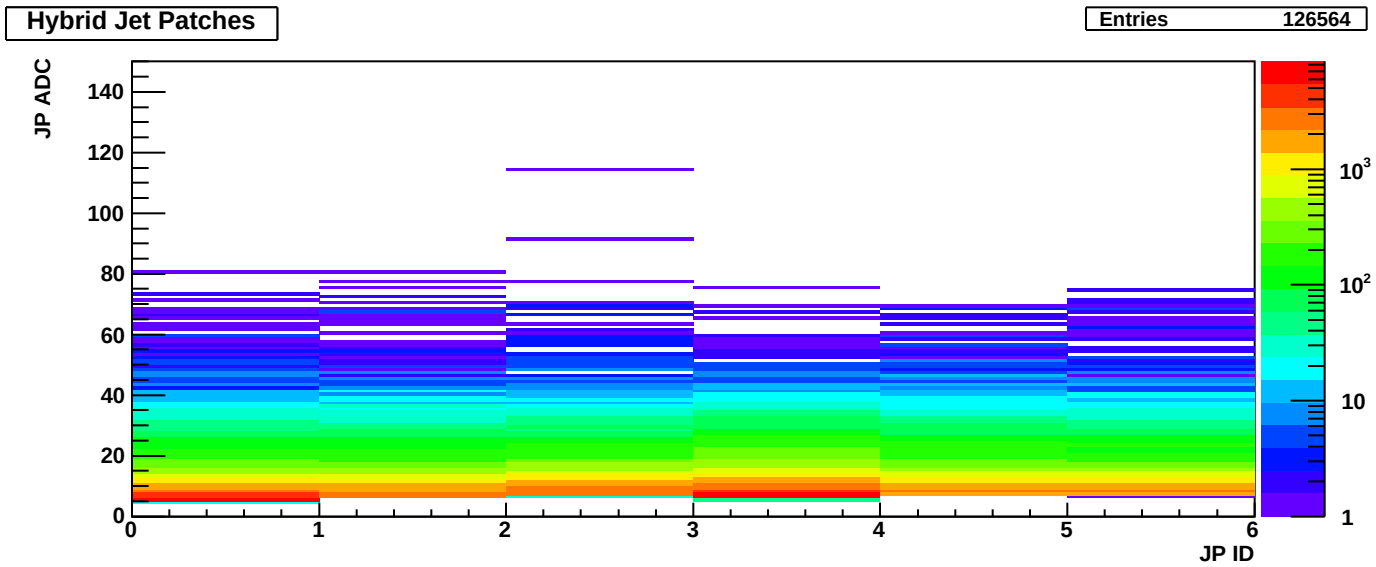

Barrel EMC L0 Input - Patch Sum
Entries 1.89846e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

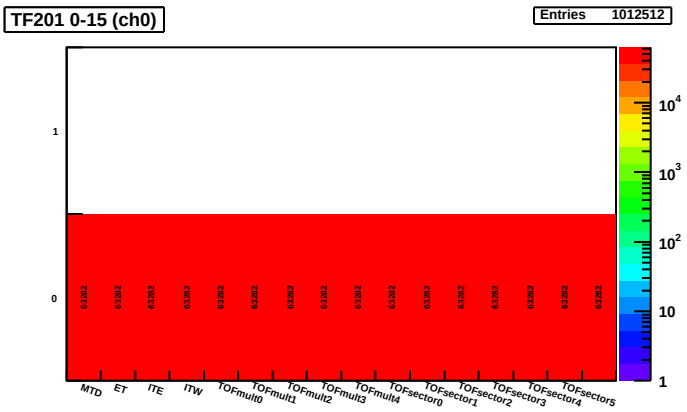

Endcap EMC L0 Input - High Tower
Entries 5695380
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

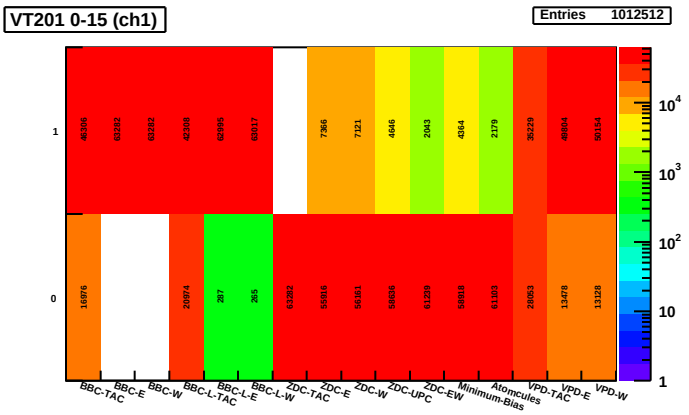

Endcap EMC L0 Input - Patch Sum
Entries 5695380
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

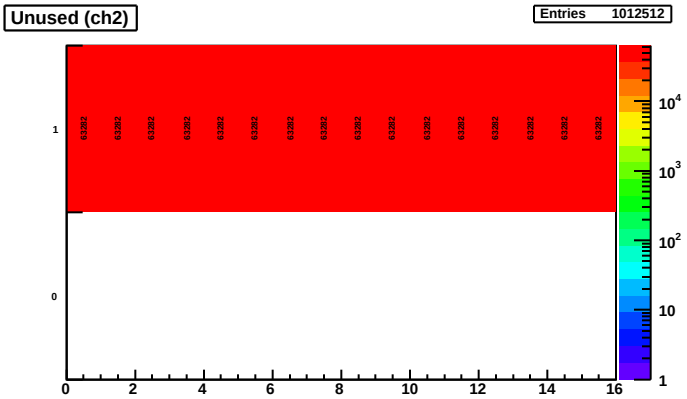

Barrel Jet Patches
Entries 1139076
JP ADC
JP ID

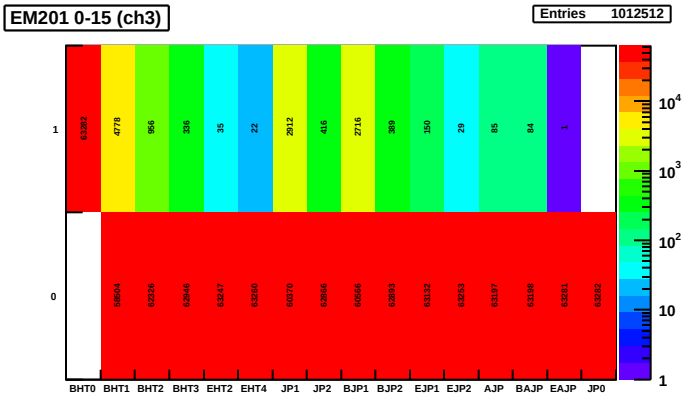

Endcap Jet Patches
Entries 379692
JP ADC
JP ID

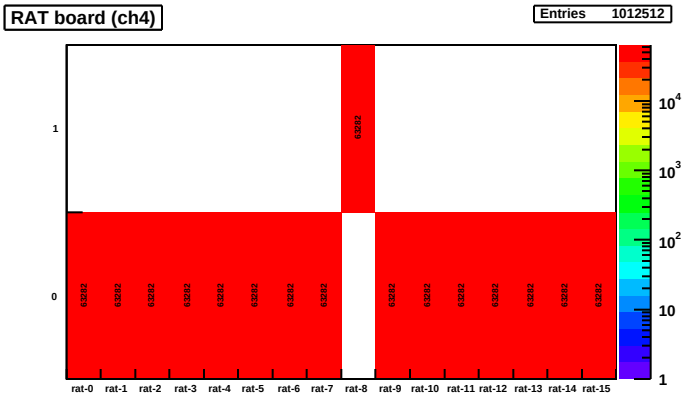

Hybrid Jet Patches
Entries 126564
JP ADC
JP ID

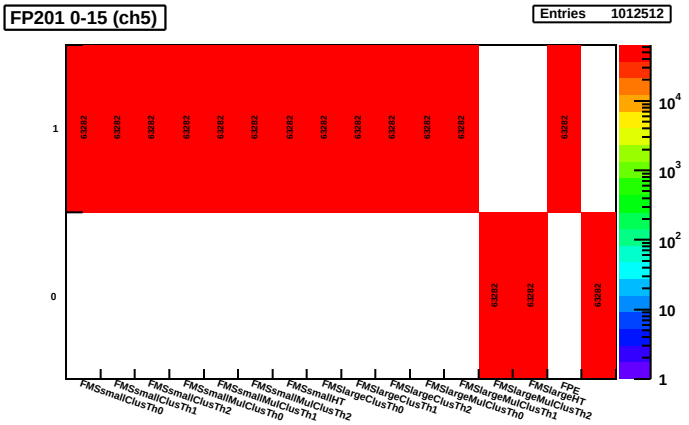

TF201 0-15 (ch0)
Entries 1012512
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

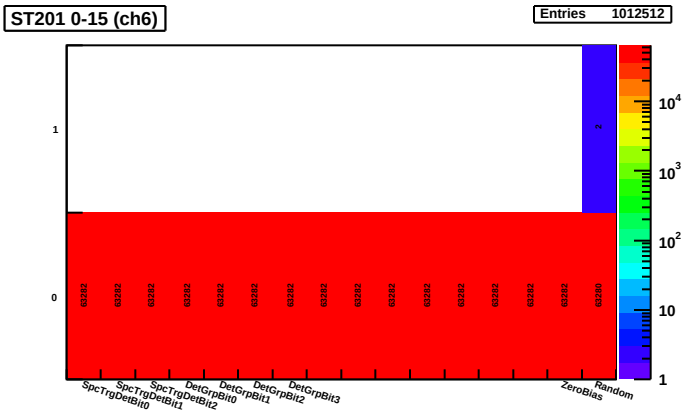

VT201 0-15 (ch1)
Entries 1012512
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

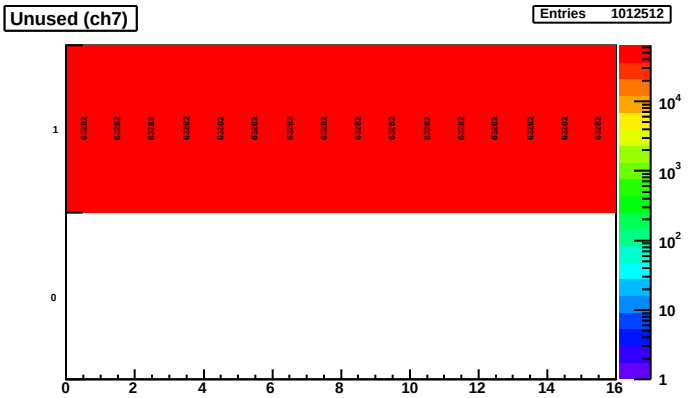

Unused (ch2)
Entries 1012512

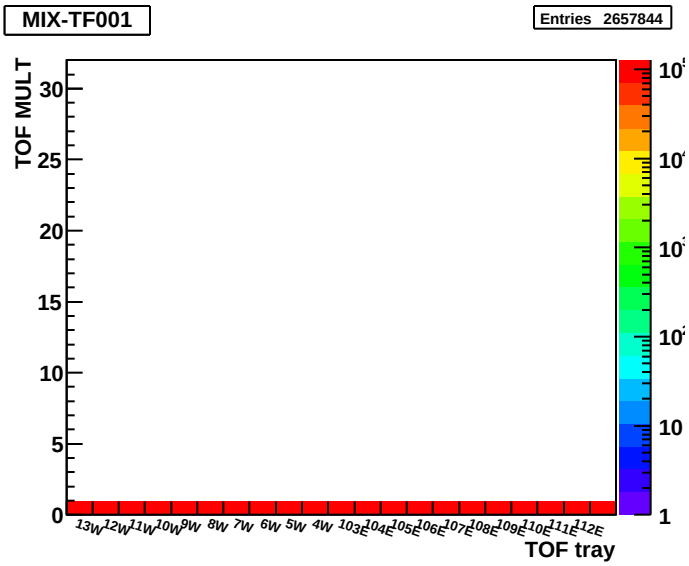

EM201 0-15 (ch3)
Entries 1012512
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

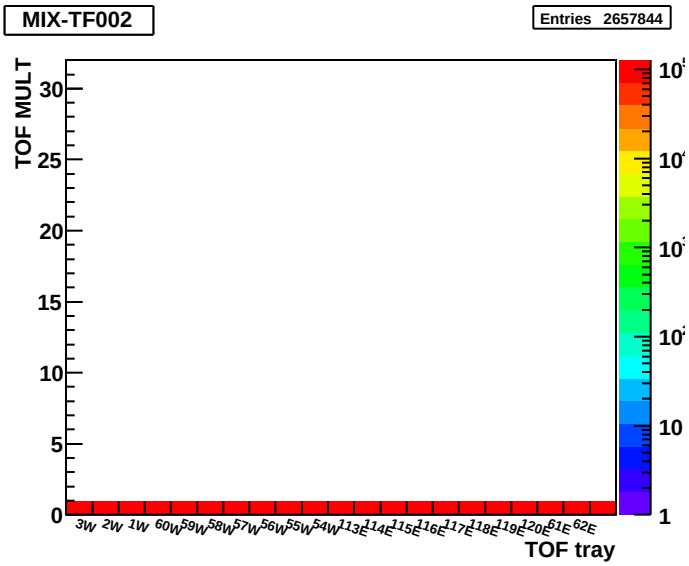

RAT board (ch4)
Entries 1012512
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

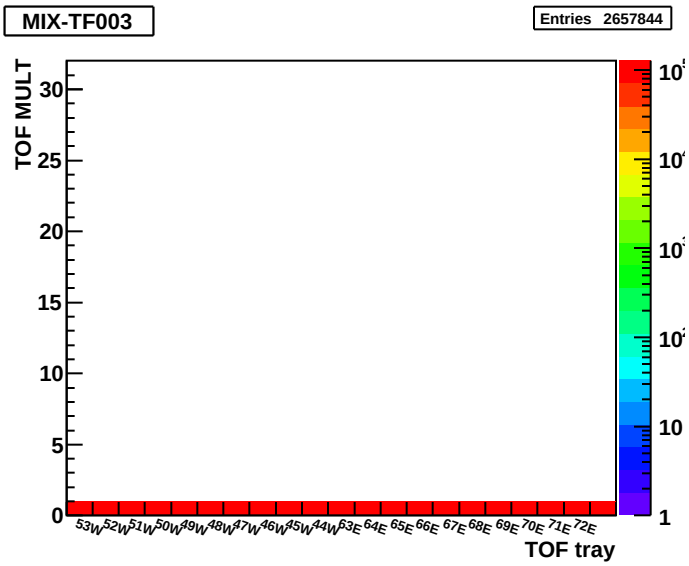

FP201 0-15 (ch5)
Entries 1012512
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

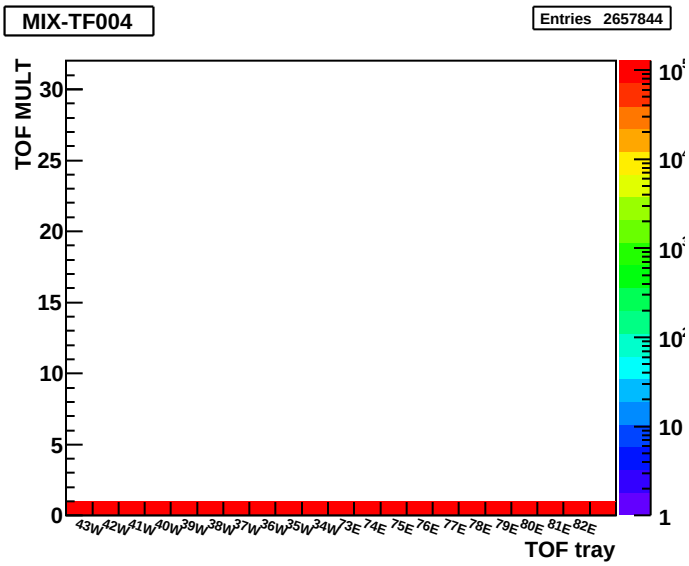

ST201 0-15 (ch6)
Entries 1012512
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

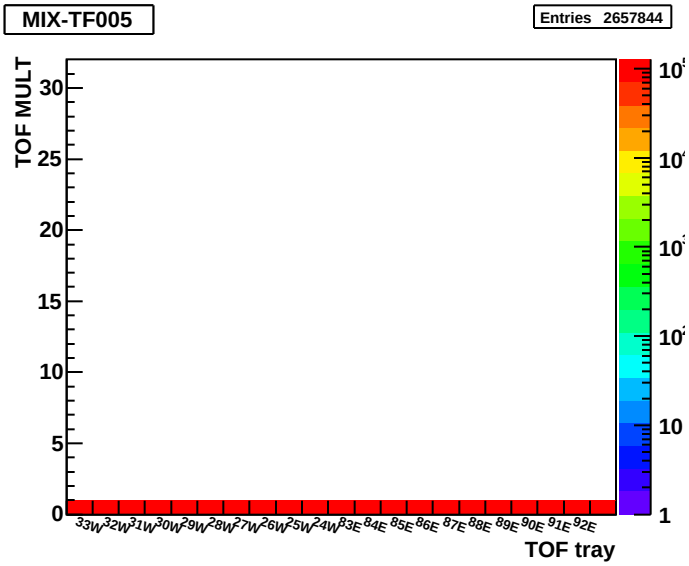

Unused (ch7)
Entries 1012512

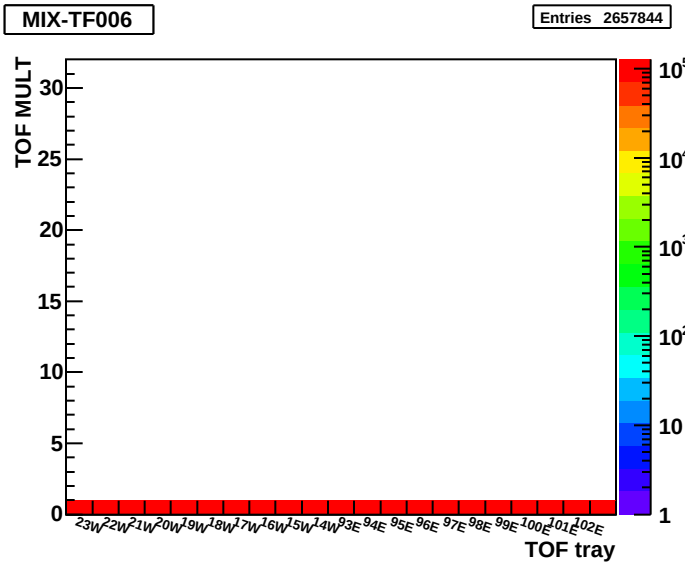

MIX-TF001
Entries 2657844
TOF MULT
TOF tray

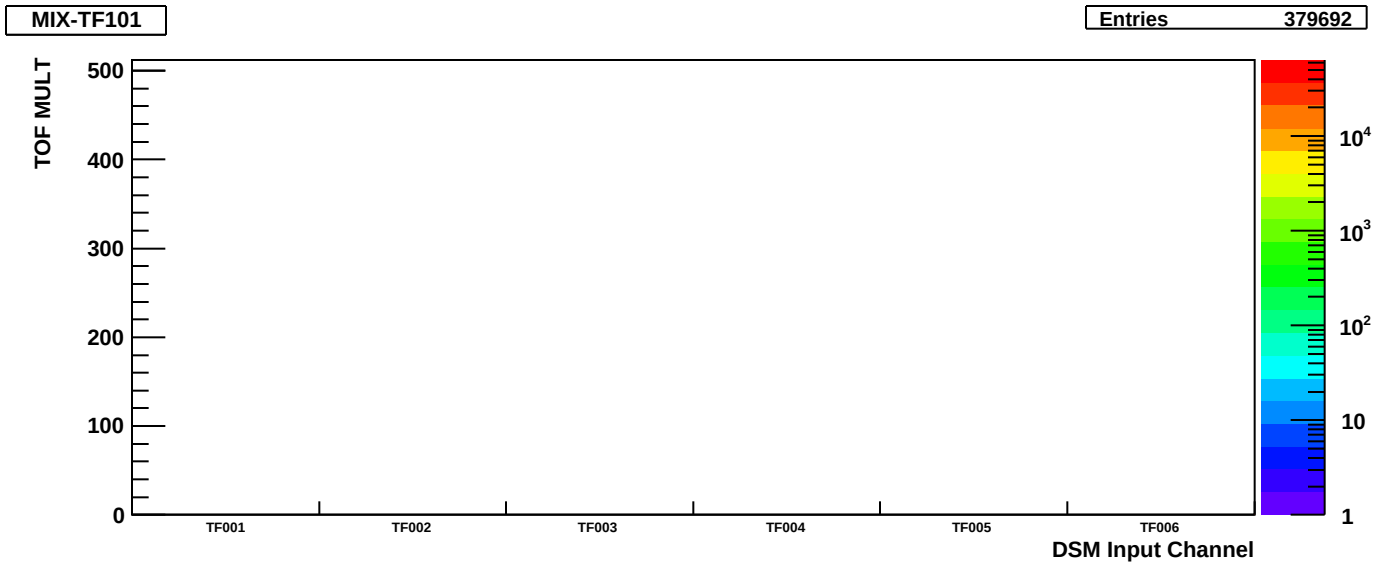

MIX-TF002
Entries 2657844
TOF MULT
TOF tray

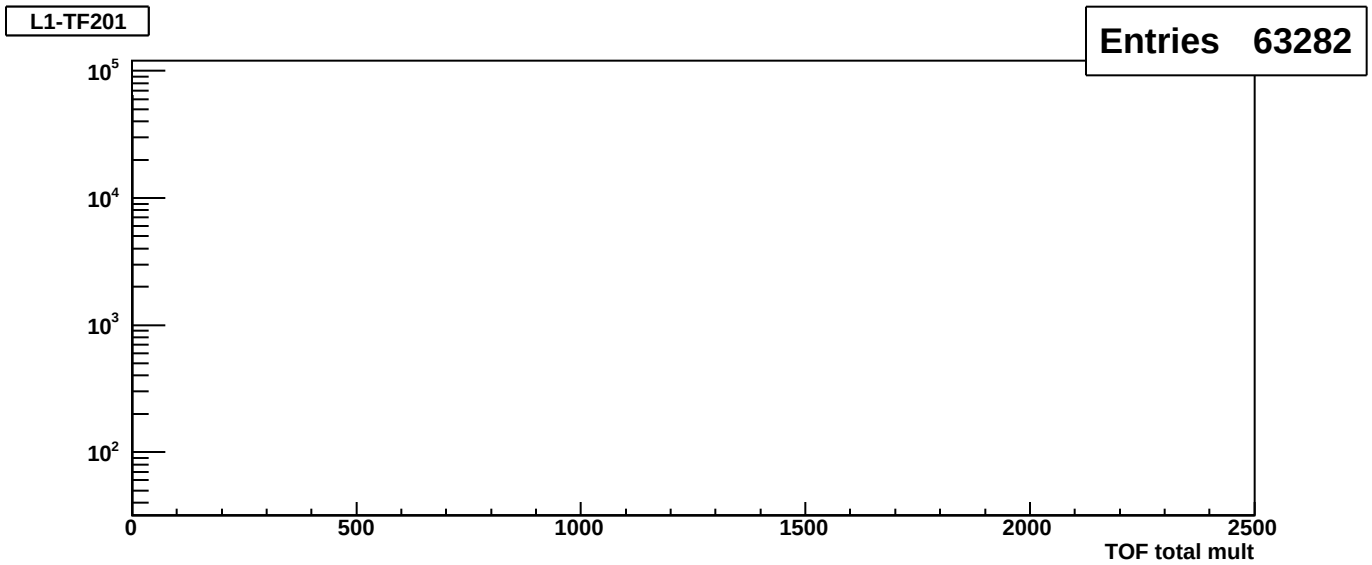

MIX-TF003
Entries 2657844
TOF MULT
TOF tray

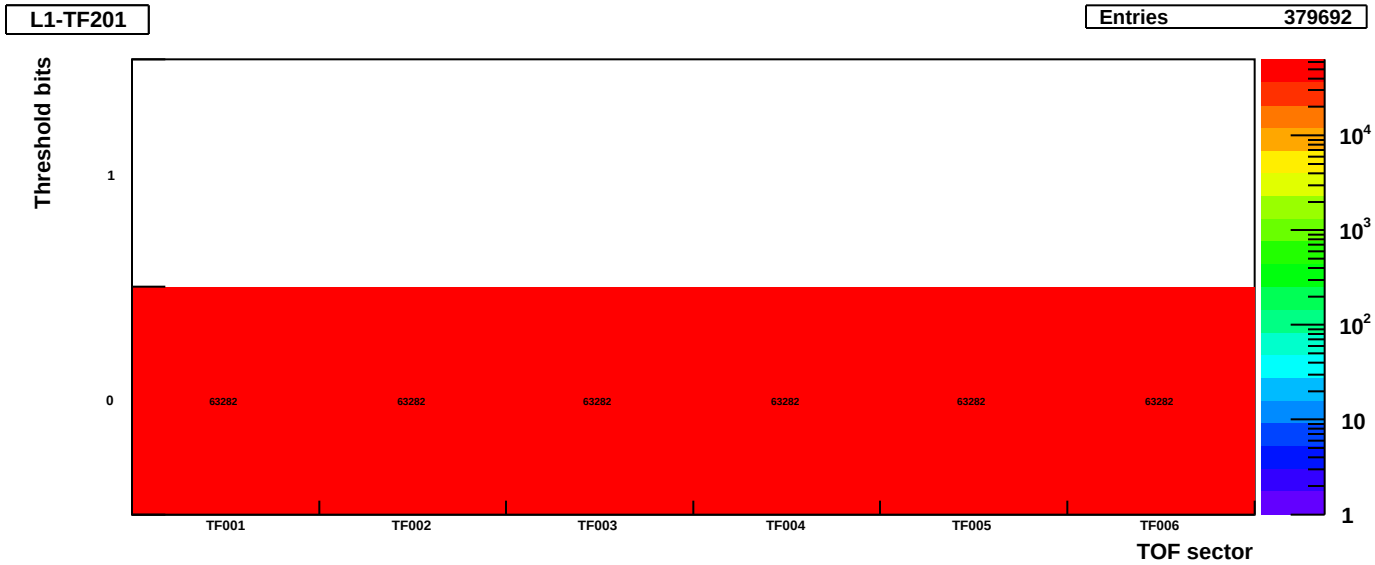

MIX-TF004
Entries 2657844
TOF MULT
TOF tray

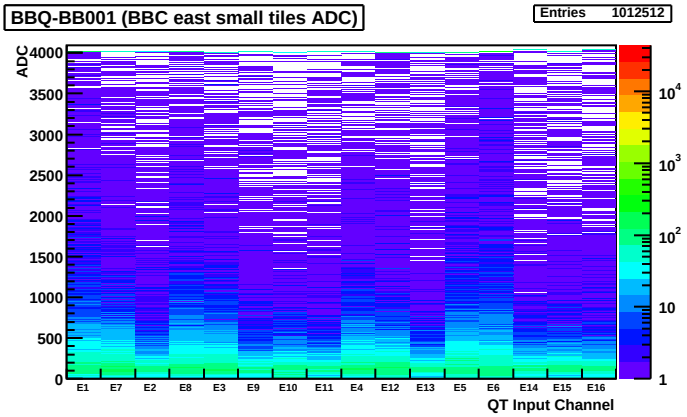

MIX-TF005
Entries 2657844
TOF MULT
TOF tray

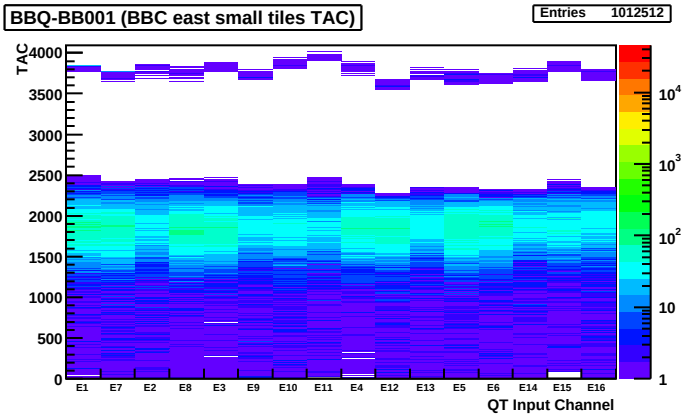

MIX-TF006
Entries 2657844
TOF MULT
TOF tray

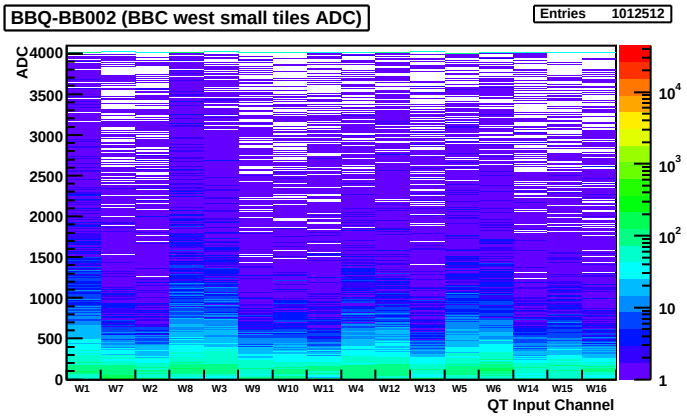

MIX-TF101
Entries 379692
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

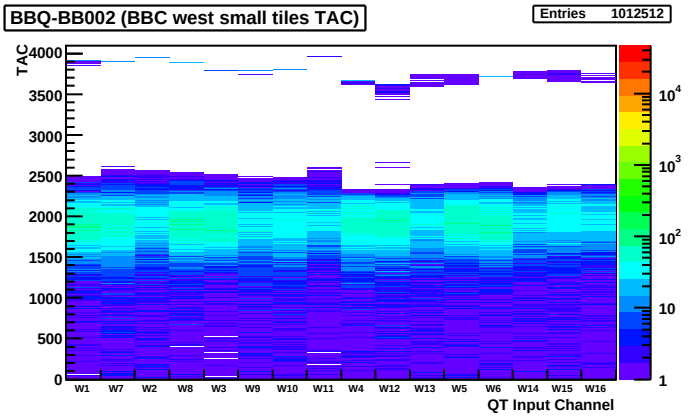

L1-TF201
Entries 63282
10^5
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

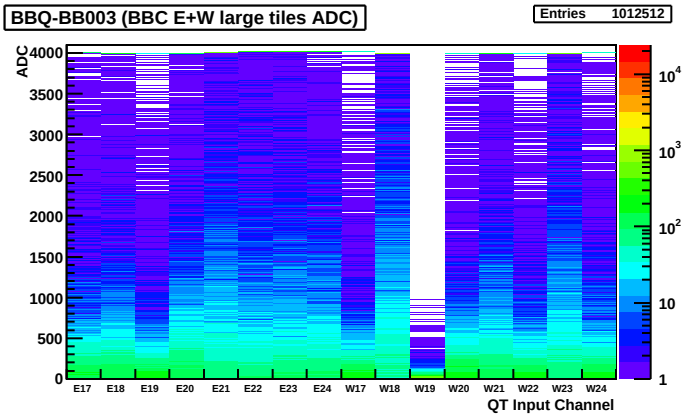

L1-TF201
Entries 379692
Threshold bits
1
0
63282
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

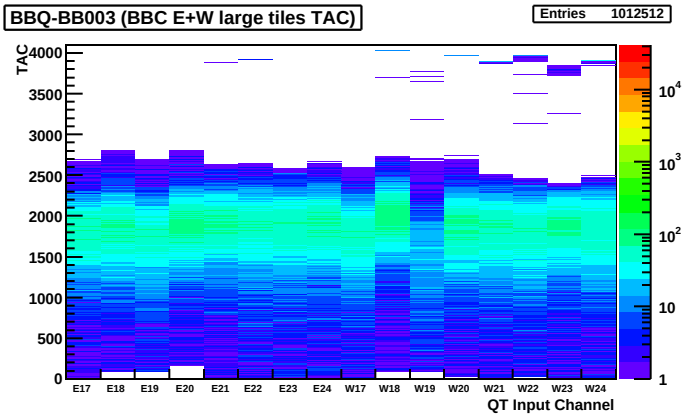

BBQ-BB001 (BBC east small tiles ADC)
Entries 1012512
ADC
QT Input Channel

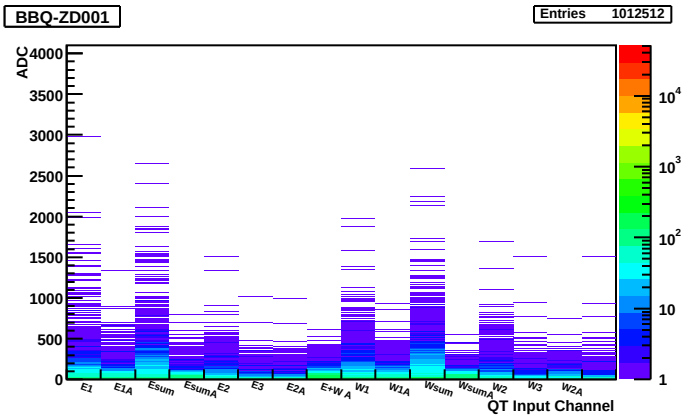

BBQ-BB001 (BBC east small tiles TAC)
Entries 1012512
TAC
QT Input Channel

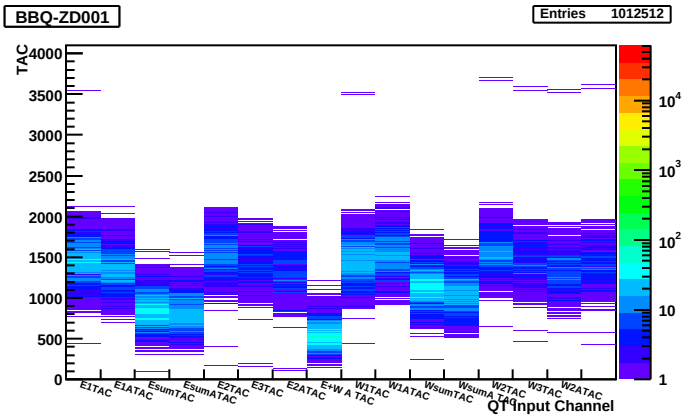

BBQ-BB002 (BBC west small tiles ADC)
Entries 1012512
ADC
QT Input Channel

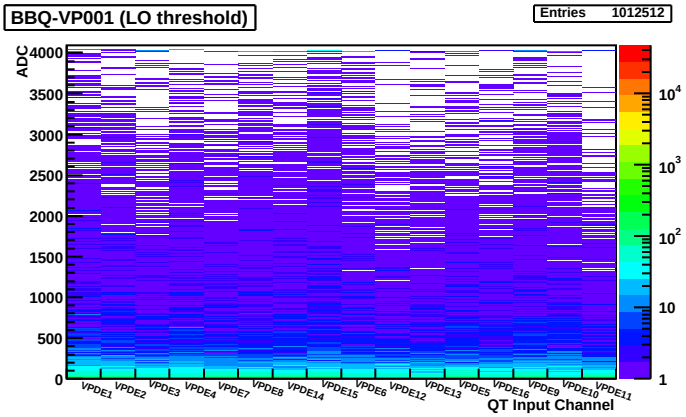

BBQ-BB002 (BBC west small tiles TAC)
Entries 1012512
TAC
QT Input Channel

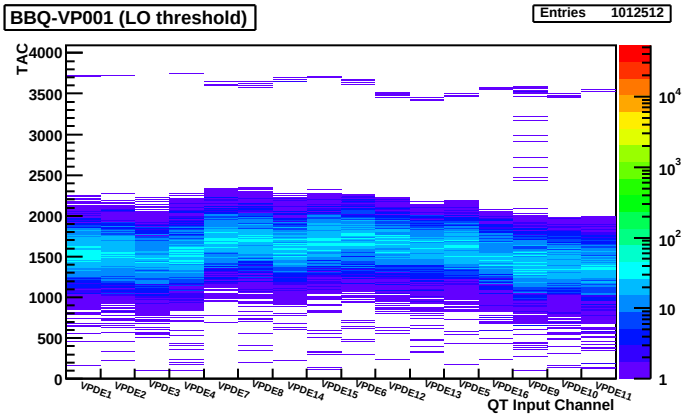

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1012512
ADC
QT Input Channel

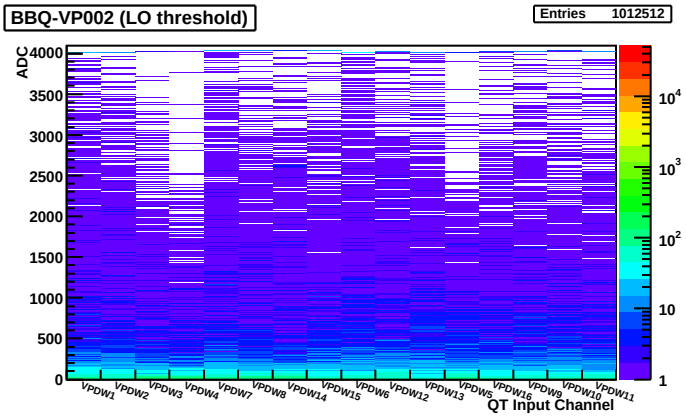

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1012512
TAC
QT Input Channel

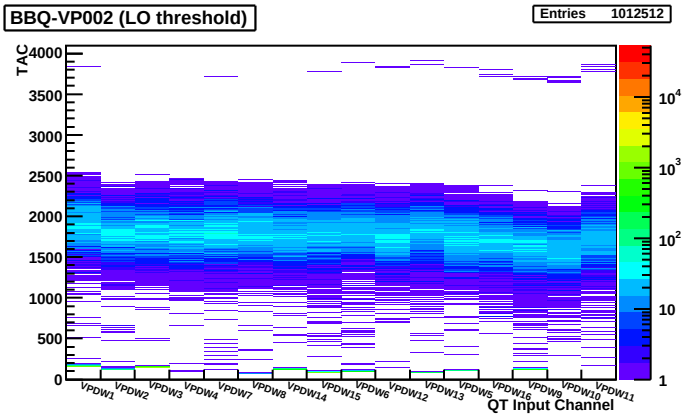

BBQ-ZD001
Entries 1012512
ADC
QT Input Channel

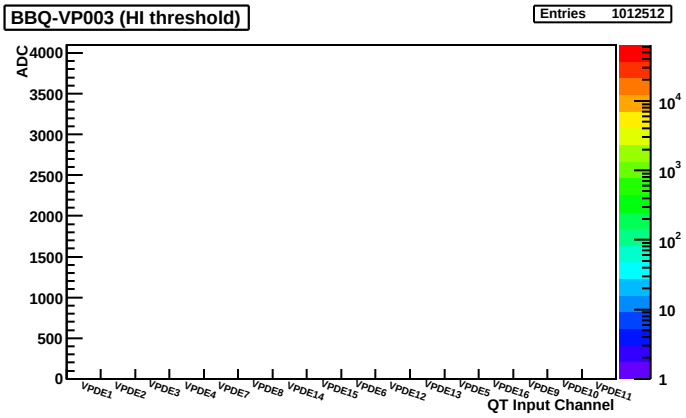

BBQ-ZD001
Entries 1012512
TAC
QT Input Channel

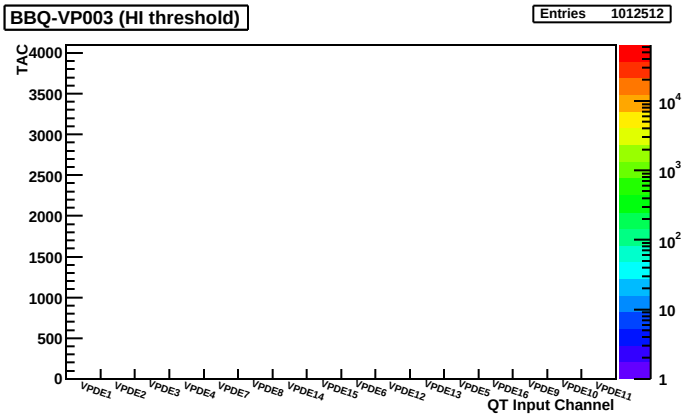

BBQ-VP001 (LO threshold)
Entries 1012512
ADC
QT Input Channel

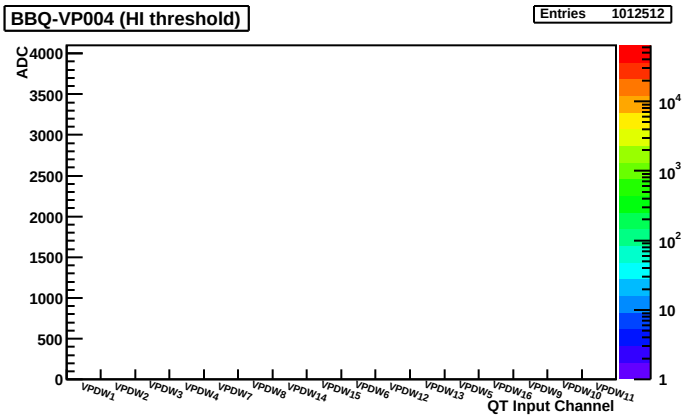

BBQ-VP001 (LO threshold)
Entries 1012512
TAC
QT Input Channel

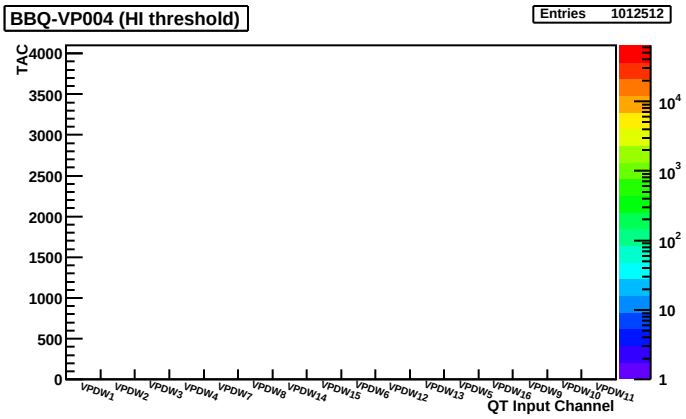

BBQ-VP002 (LO threshold)
Entries 1012512
ADC
QT Input Channel

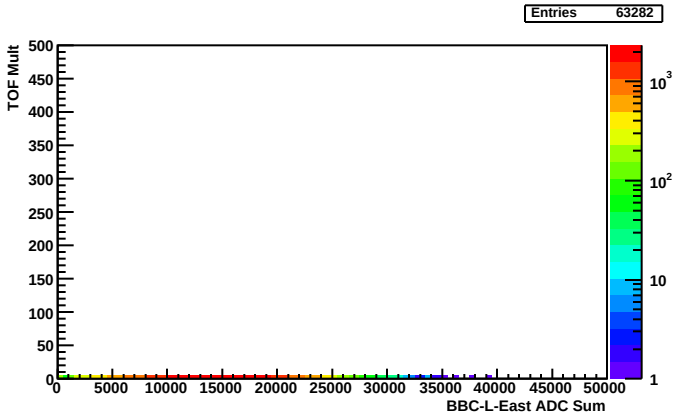

BBQ-VP002 (LO threshold)
Entries 1012512
TAC
QT Input Channel

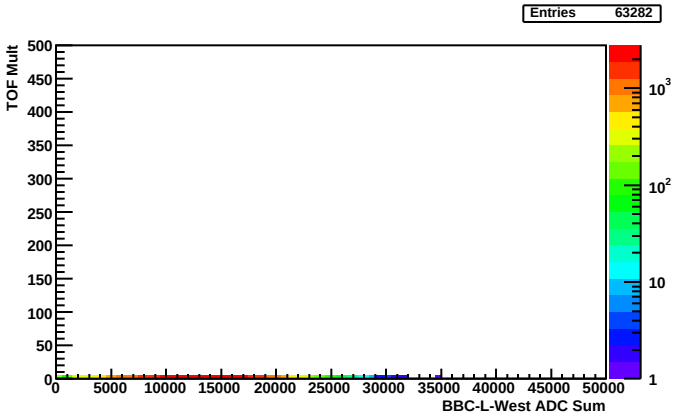

BBQ-VP003 (HI threshold)
Entries 1012512
ADC
QT Input Channel

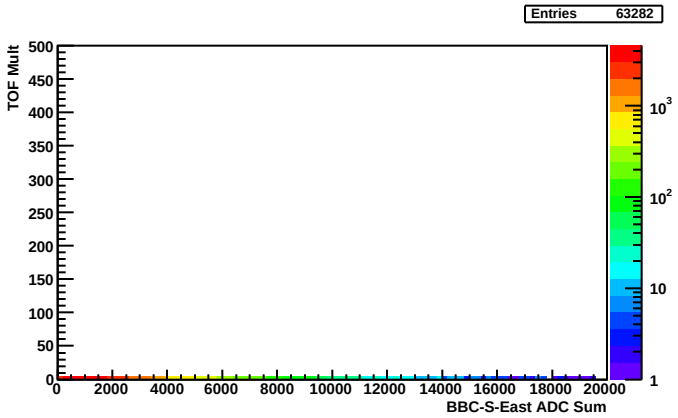

BBQ-VP003 (HI threshold)
Entries 1012512
TAC
QT Input Channel

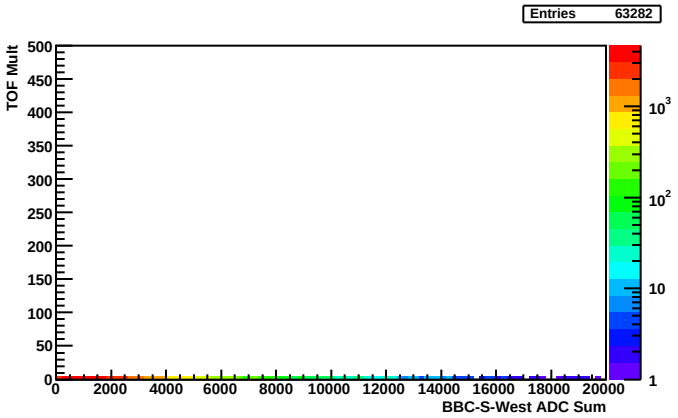

BBQ-VP004 (HI threshold)
Entries 1012512
ADC
QT Input Channel

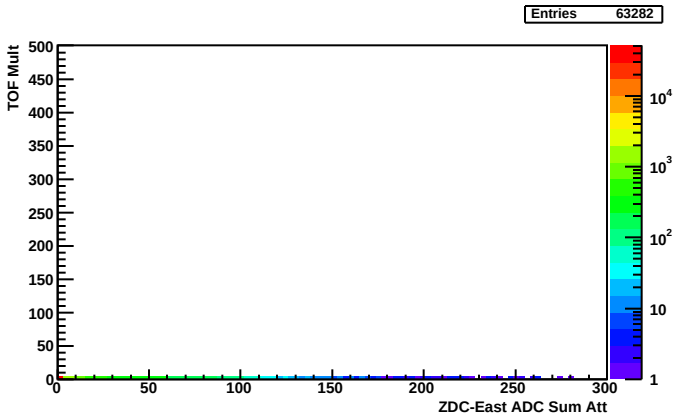

BBQ-VP004 (HI threshold)
Entries 1012512
TAC
QT Input Channel

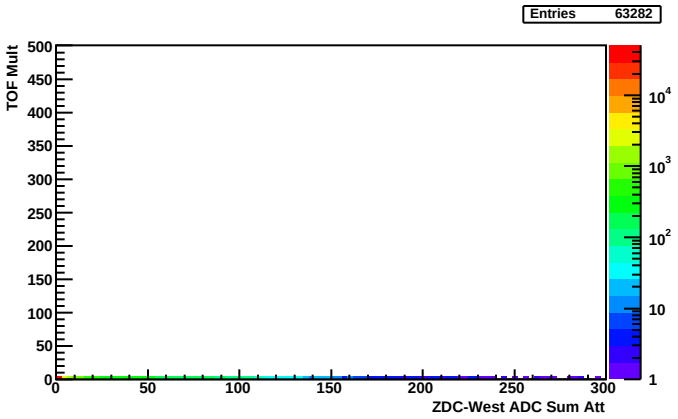

Entries 63282
TOF Mult
BBC-L-East ADC Sum

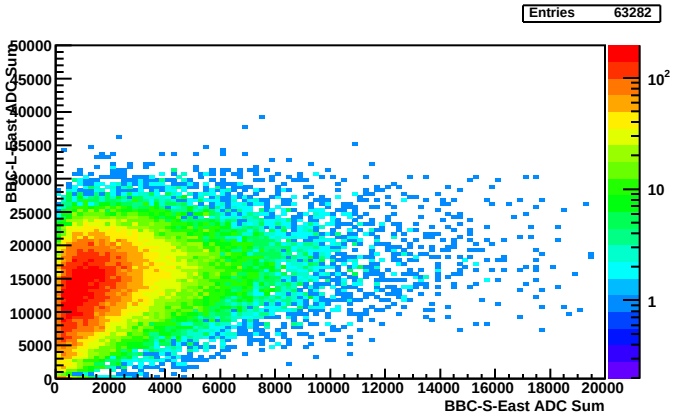

Entries 63282
TOF Mult
BBC-L-West ADC Sum

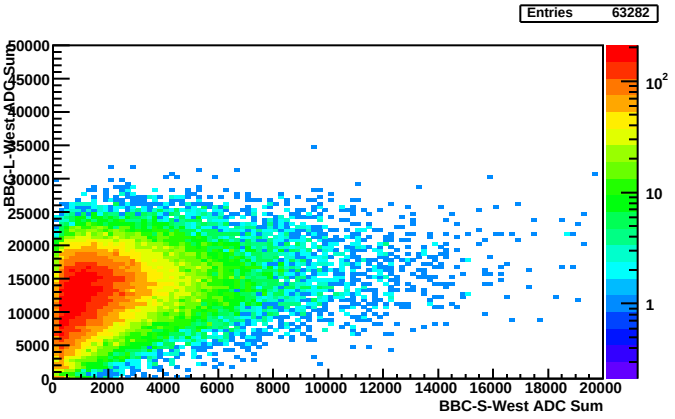

Entries 63282
TOF Mult
BBC-S-East ADC Sum

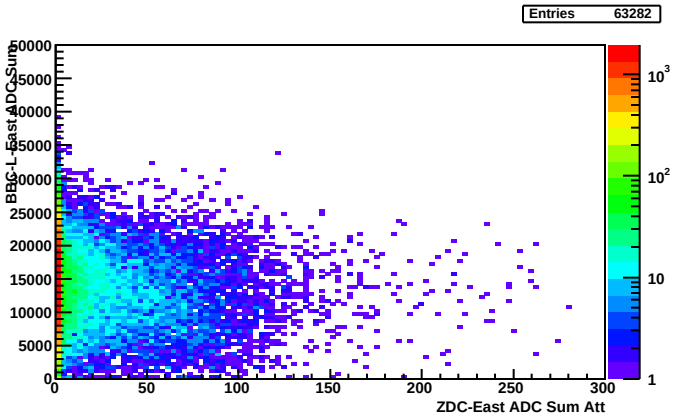

Entries 63282
TOF Mult
BBC-S-West ADC Sum

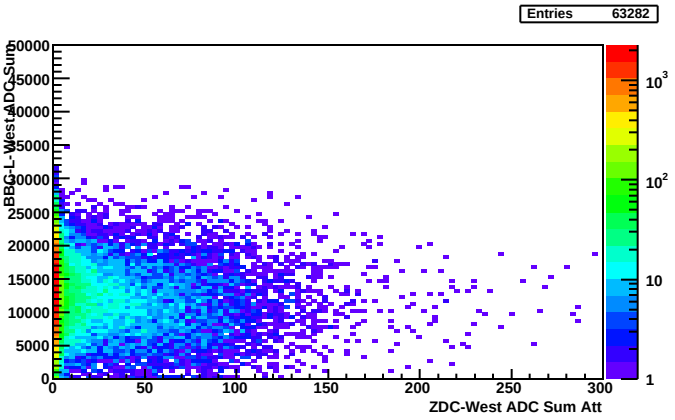

Entries 63282
TOF Mult
ZDC-East ADC Sum Att

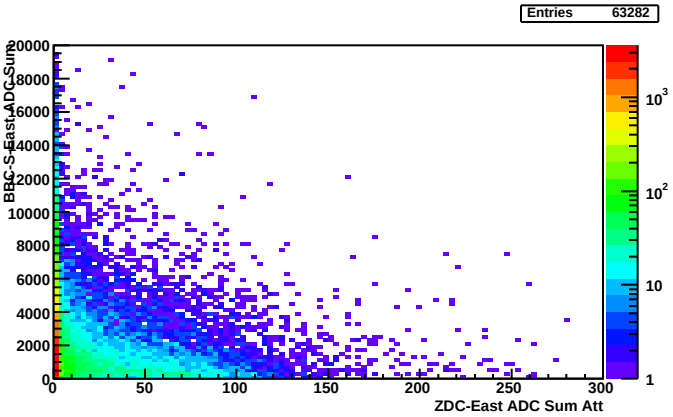

Entries 63282
TOF Mult
ZDC-West ADC Sum Att

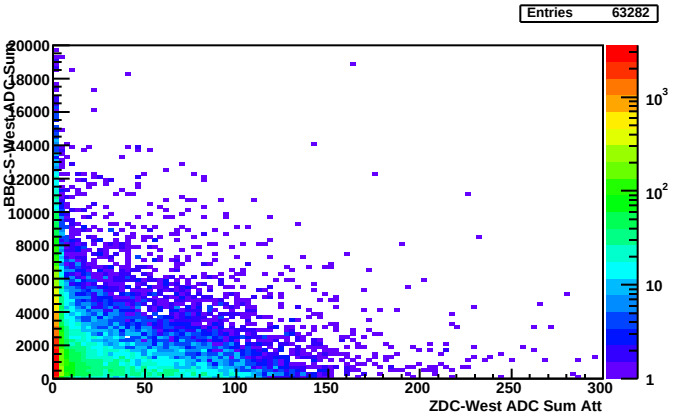

Entries 63282
BBC-L-East ADC Sum
BBC-S-East ADC Sum

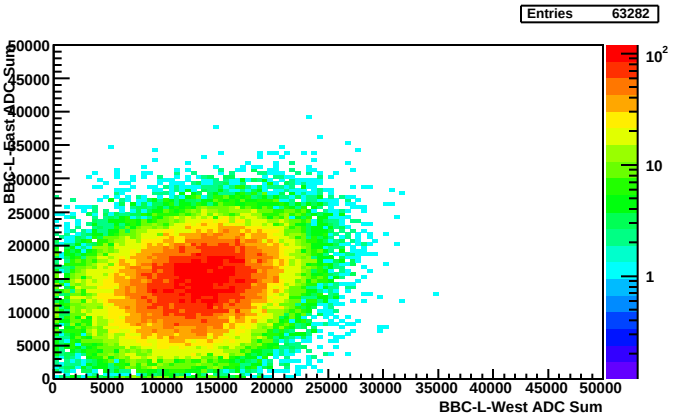

Entries 63282
BBC-L-West ADC Sum
BBC-S-West ADC Sum

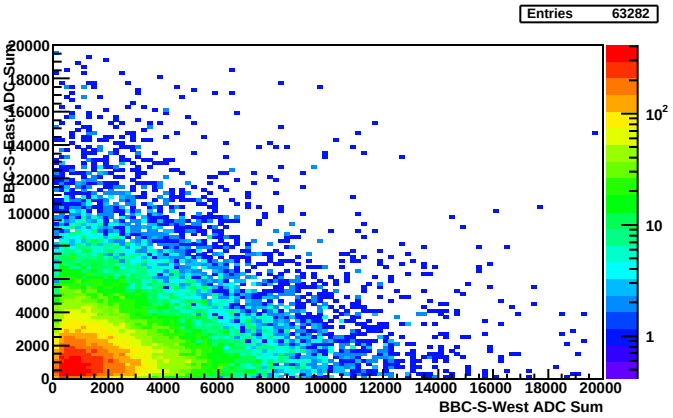

Entries 63282
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

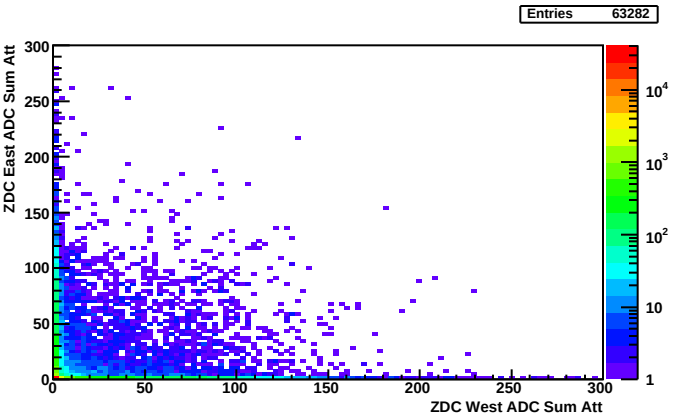

Entries 63282
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

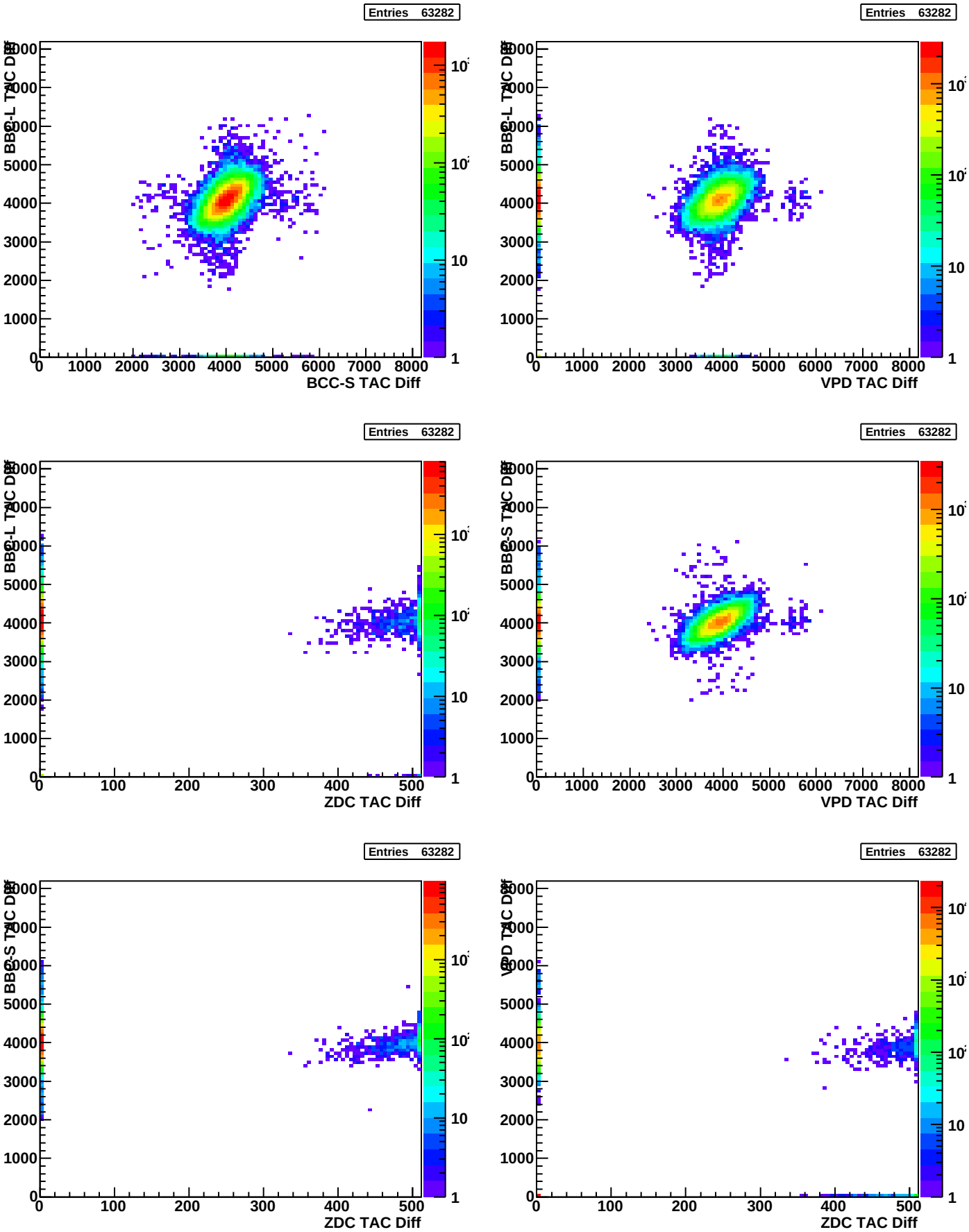

Entries 63282
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

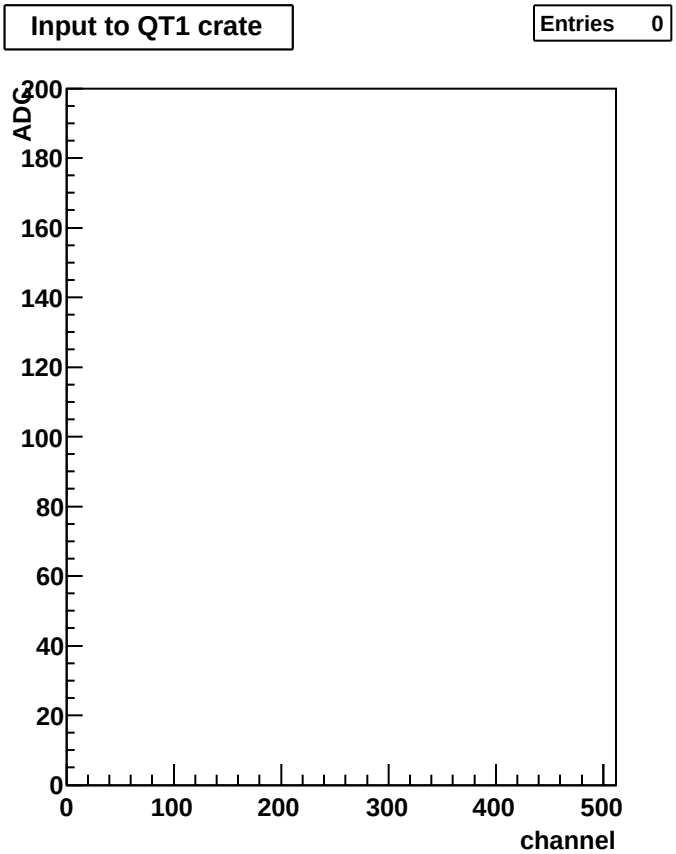

Entries 63282
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

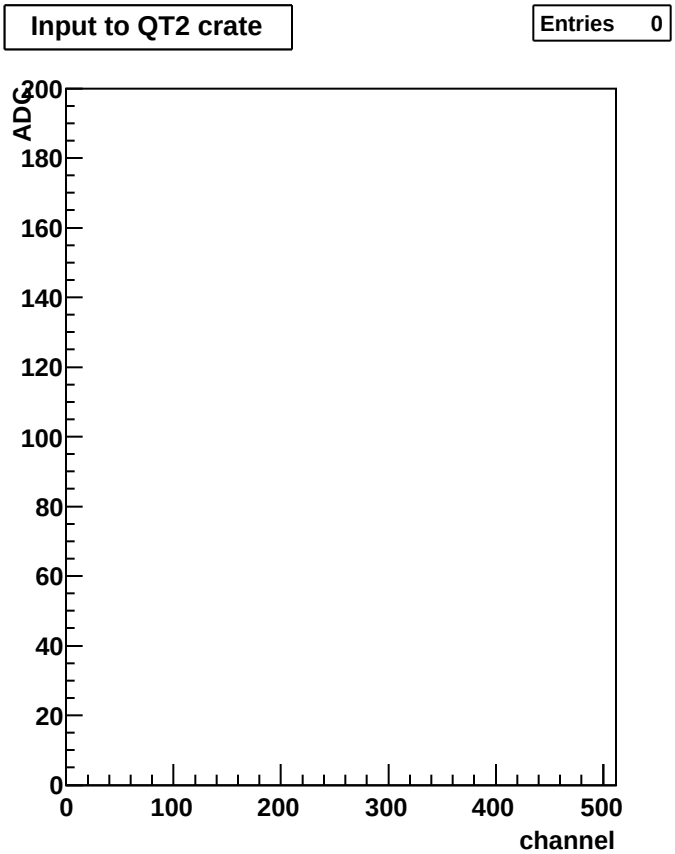

Entries 63282
BBC-L-East ADC Sum
BBC-L-West ADC Sum

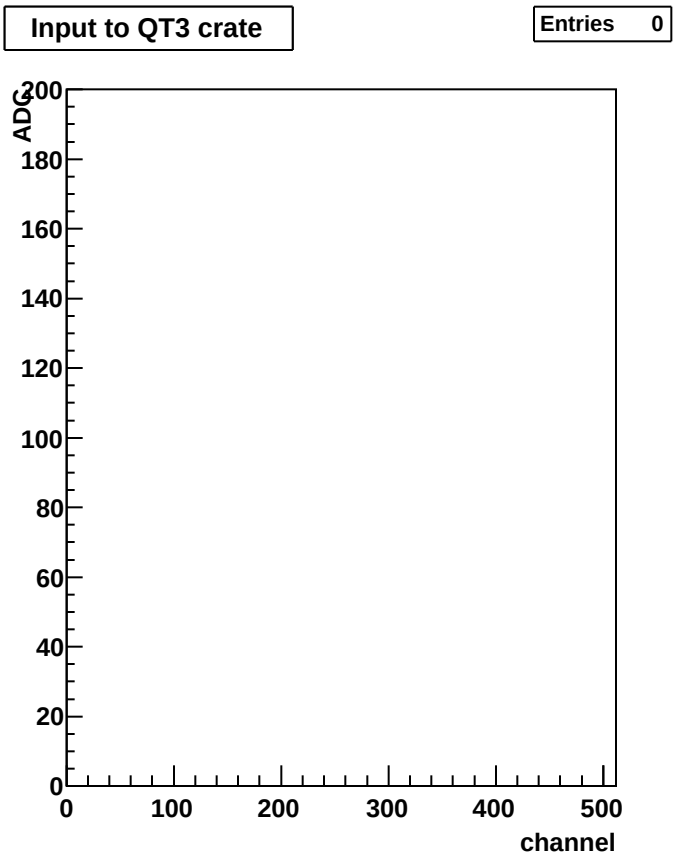

Entries 63282
BBC-S-East ADC Sum
BBC-S-West ADC Sum

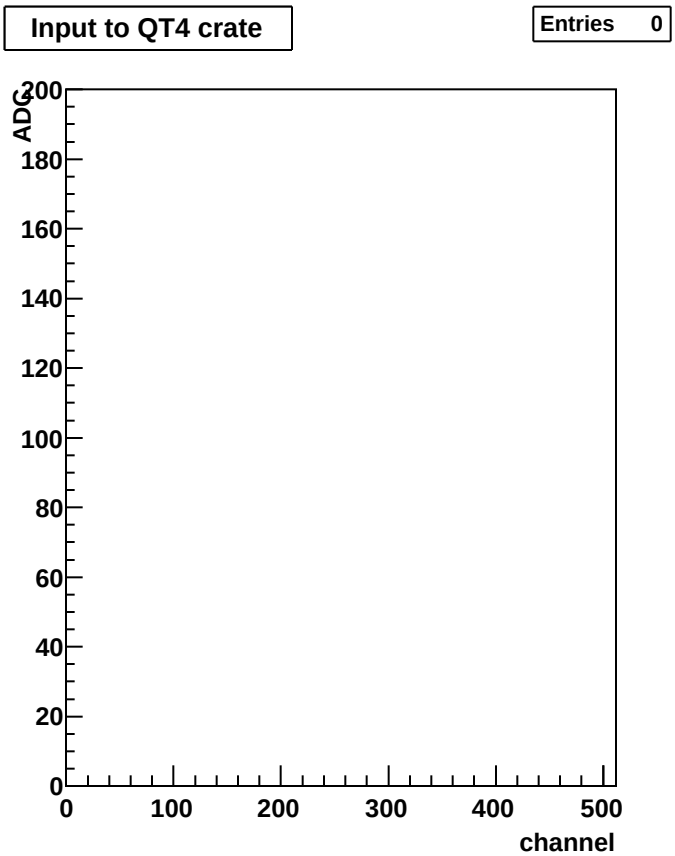

Entries 63282
ZDC East ADC Sum Att
ZDC West ADC Sum Att

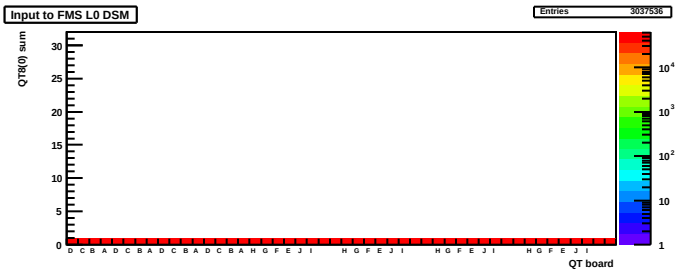

Entries 63282
BBC-L TAC Diff
BCC-S TAC Diff
Entries 63282
BBC-L TAC Diff
VPD TAC Diff
Entries 63282
BBC-L TAC Diff
ZDC TAC Diff
Entries 63282
BBC-S TAC Diff
VPD TAC Diff
Entries 63282
BBC-S TAC Diff
ZDC TAC Diff
Entries 63282
VPD TAC Diff
ZDC TAC Diff

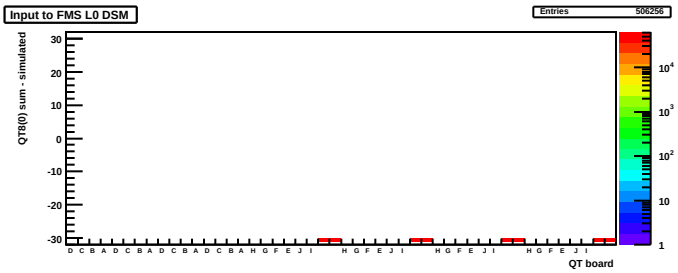

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

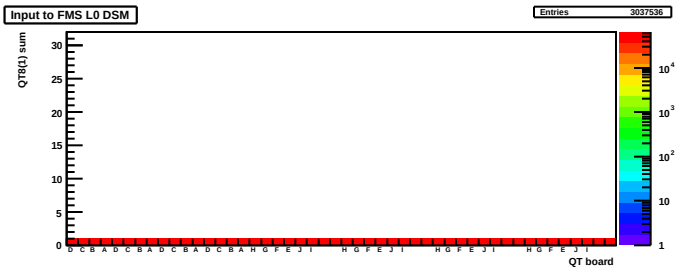

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

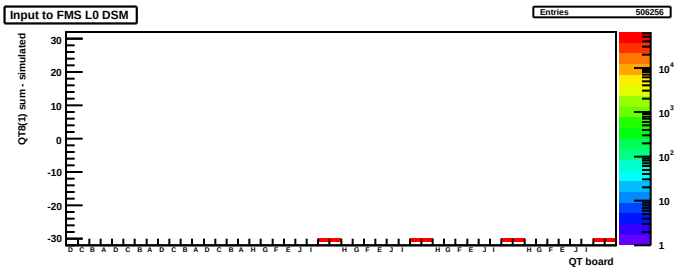

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

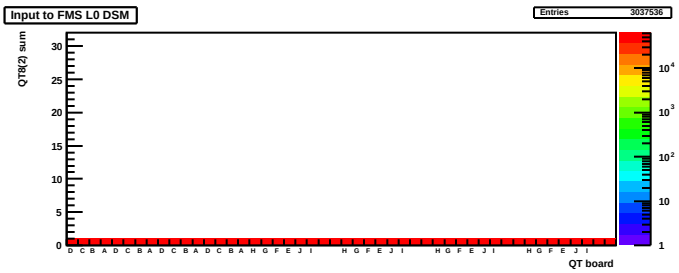

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

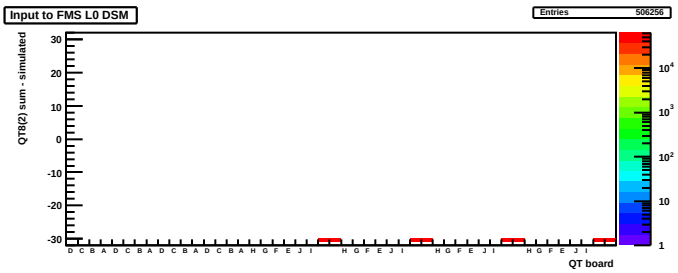

Input to FMS L0 DSM
Entries 3037536
QT8(0) sum
QT board

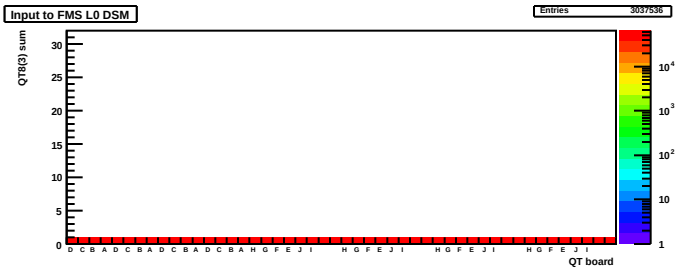

Input to FMS L0 DSM
Entries 506256
QT8(0) sum - simulated
QT board

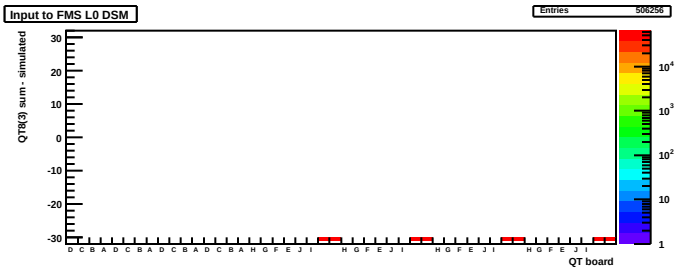

Input to FMS L0 DSM
Entries 3037536
QT8(1) sum
QT board

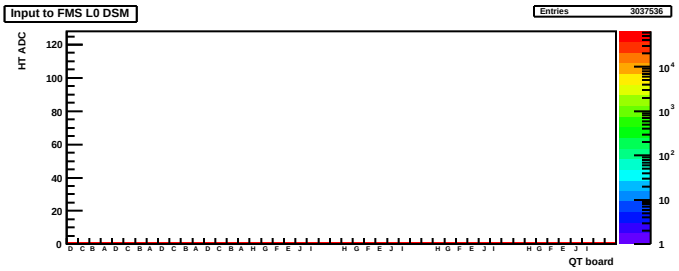

Input to FMS L0 DSM
Entries 506256
QT8(1) sum - simulated
QT board

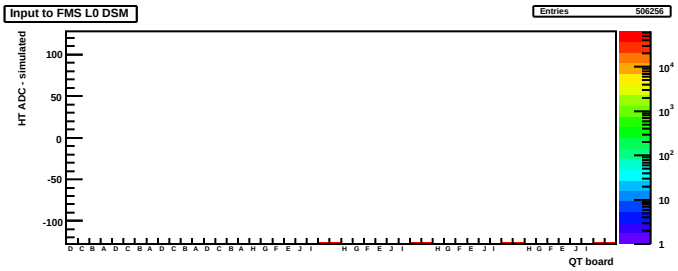

Input to FMS L0 DSM
Entries 3037536
QT8(2) sum
QT board

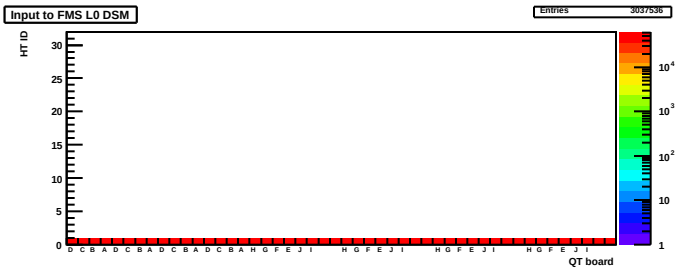

Input to FMS L0 DSM
Entries 506256
QT8(2) sum - simulated
QT board

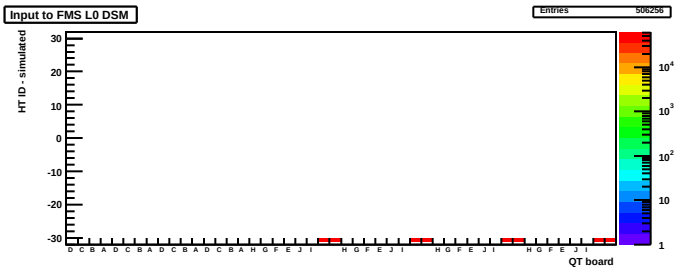

Input to FMS L0 DSM
Entries 3037536
QT8(3) sum
QT board

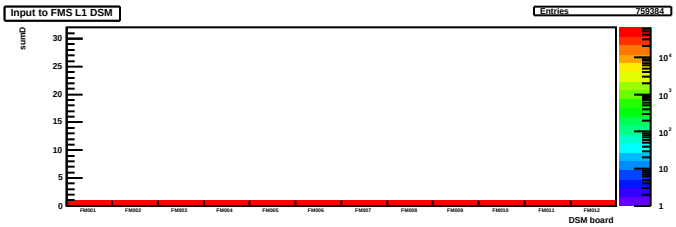

Input to FMS L0 DSM
Entries 506256
QT8(3) sum - simulated
QT board

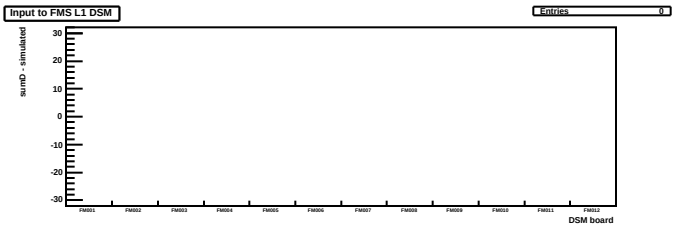

Input to FMS L0 DSM
Entries 3037536
HT ADC
QT board

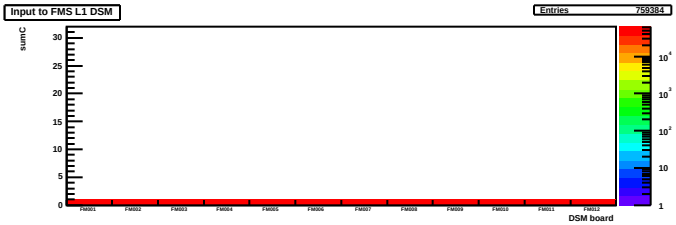

Input to FMS L0 DSM
Entries 506256
HT ADC - simulated
QT board

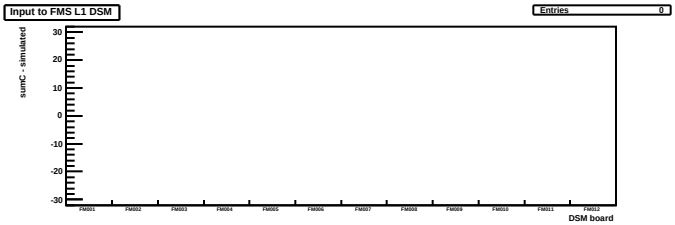

Input to FMS L0 DSM
Entries 3037536
HT ID
QT board

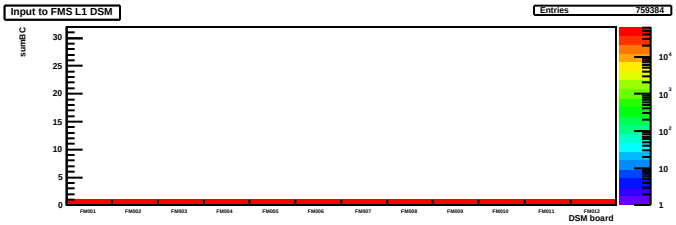

Input to FMS L0 DSM
Entries 506256
HT ID - simulated
QT board

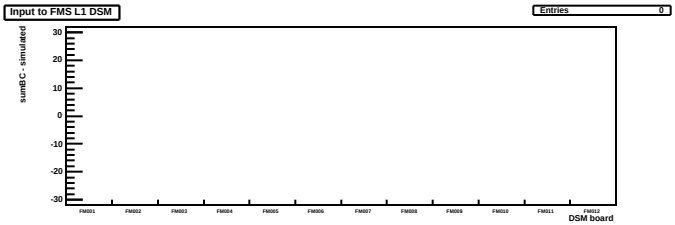

Input to FMS L1 DSM
Entries 759384
sumD
DSM board

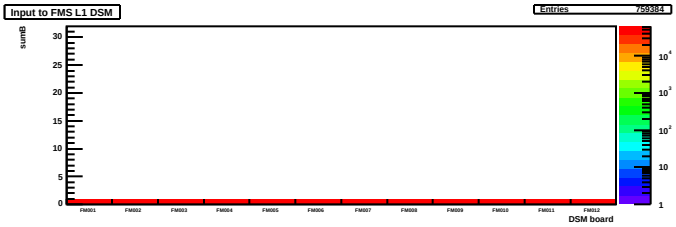

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

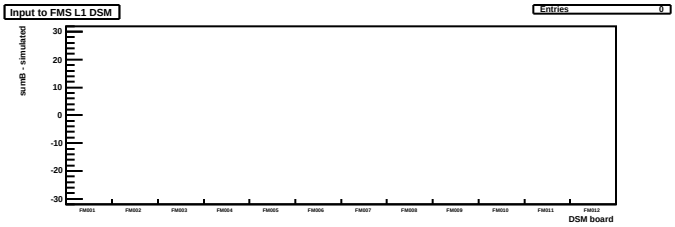

Input to FMS L1 DSM
Entries 759384
sumC
DSM board

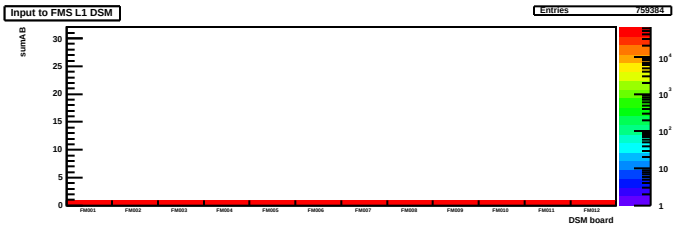

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

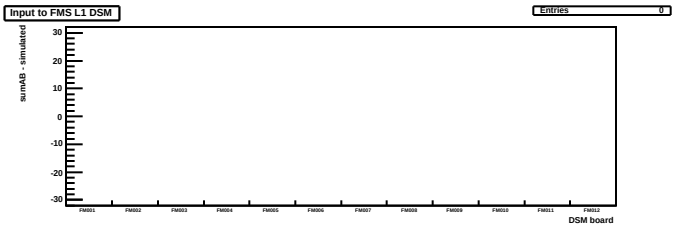

Input to FMS L1 DSM
Entries 759384
sumBC
DSM board

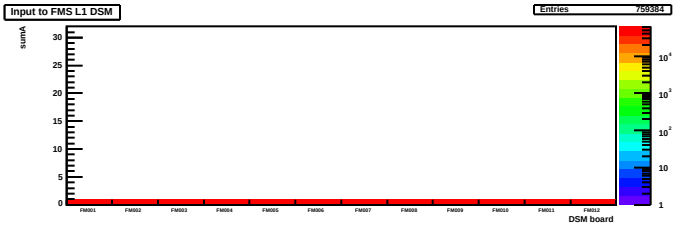

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

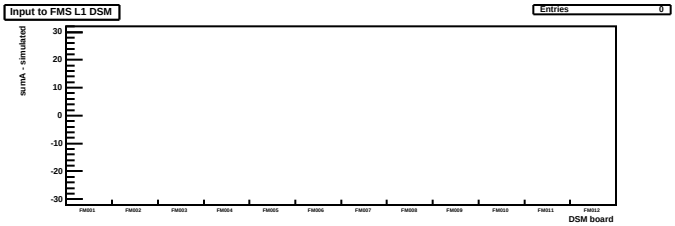

Input to FMS L1 DSM
Entries 759384
sumB
DSM board

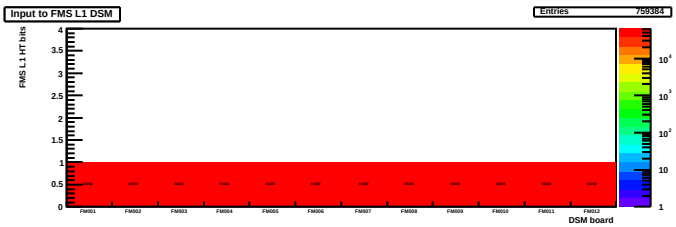

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

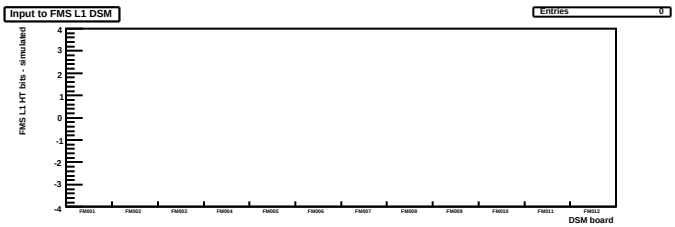

Input to FMS L1 DSM
Entries 759384
sumAB
DSM board

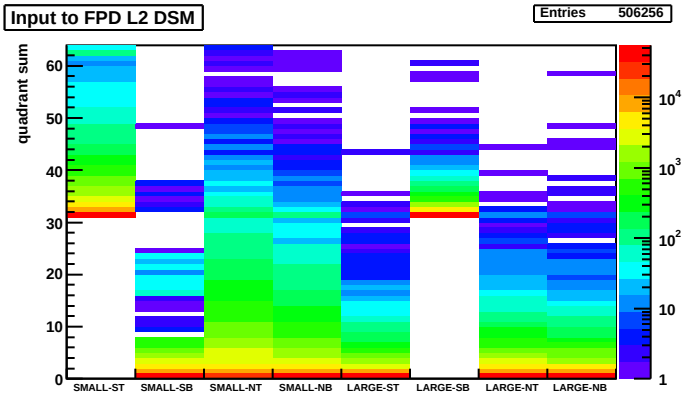

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

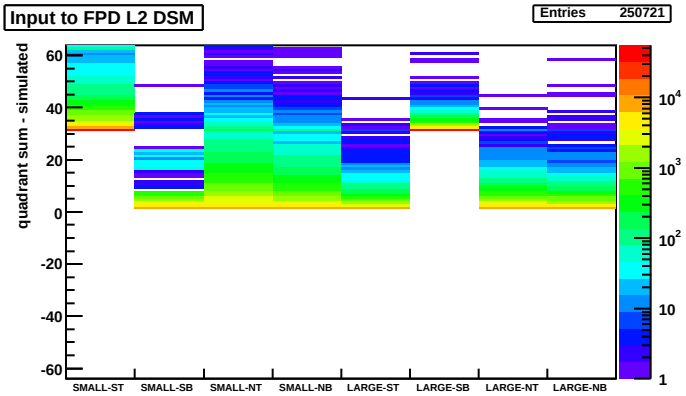

Input to FMS L1 DSM
Entries 759384
sumA
DSM board

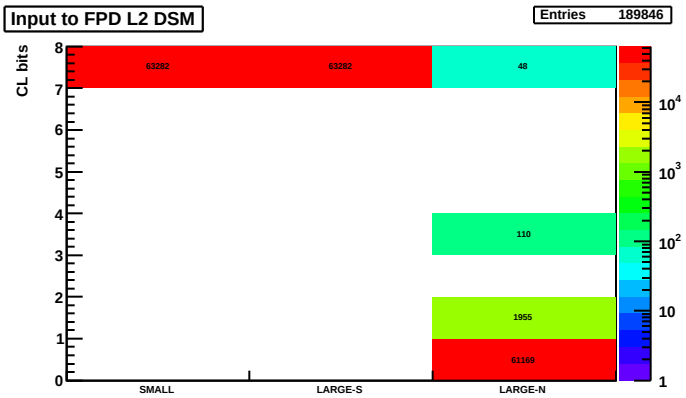

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

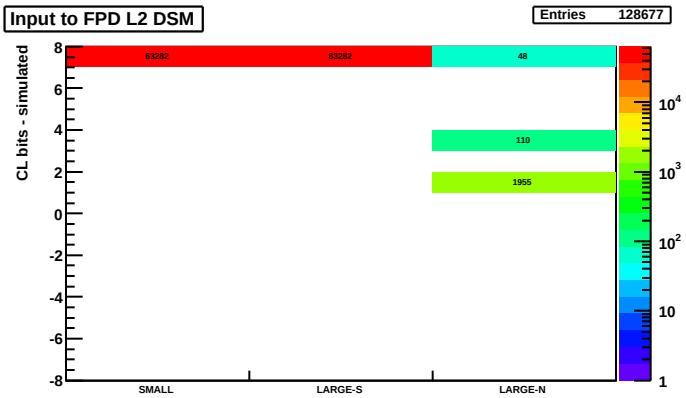

Input to FMS L1 DSM
Entries 759384
FMS L1 HT bits
DSM board

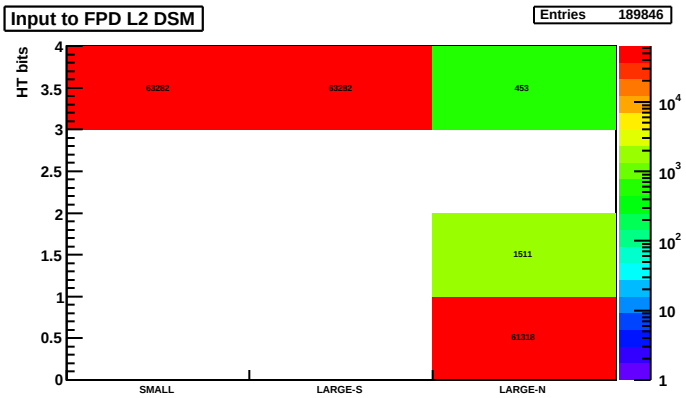

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

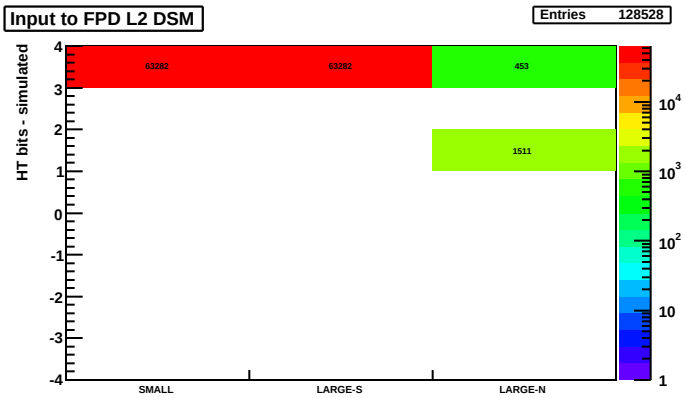

Input to FPD L2 DSM
Entries 506256
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

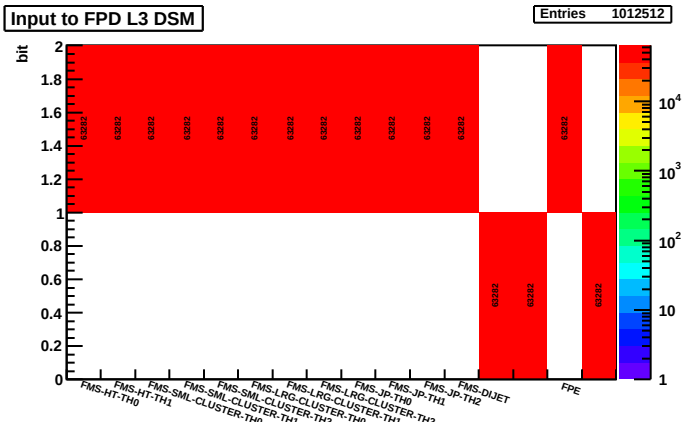

Input to FPD L2 DSM
Entries 250721
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

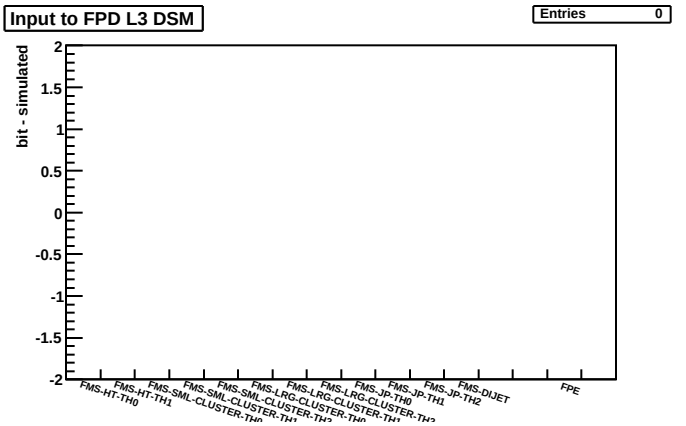

Input to FPD L2 DSM
Entries 189846
CL bits
63282
63282
48
110
1955
61169
SMALL
LARGE-S
LARGE-N

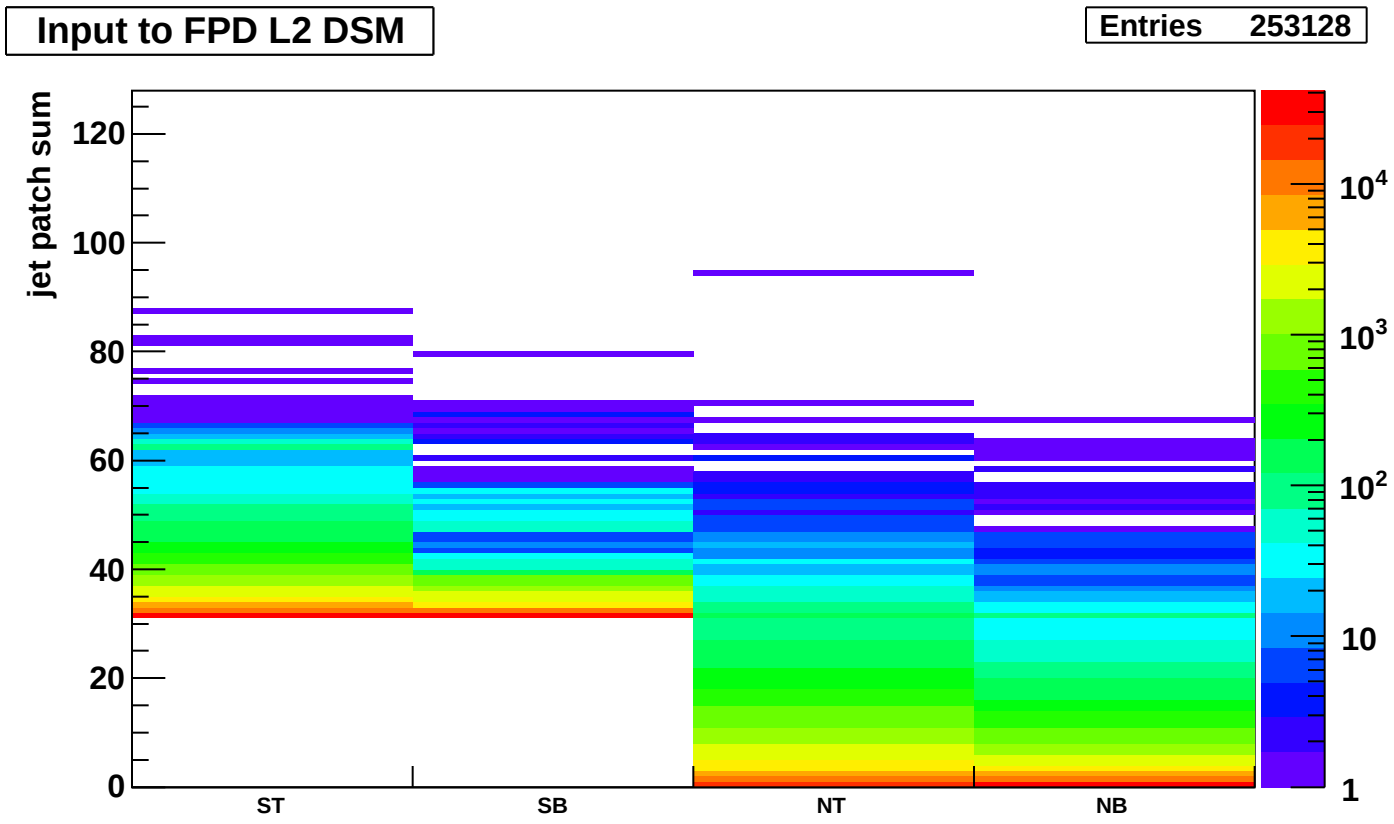

Input to FPD L2 DSM
Entries 128677
CL bits - simulated
63282
63282
48
110
1955
SMALL
LARGE-S
LARGE-N

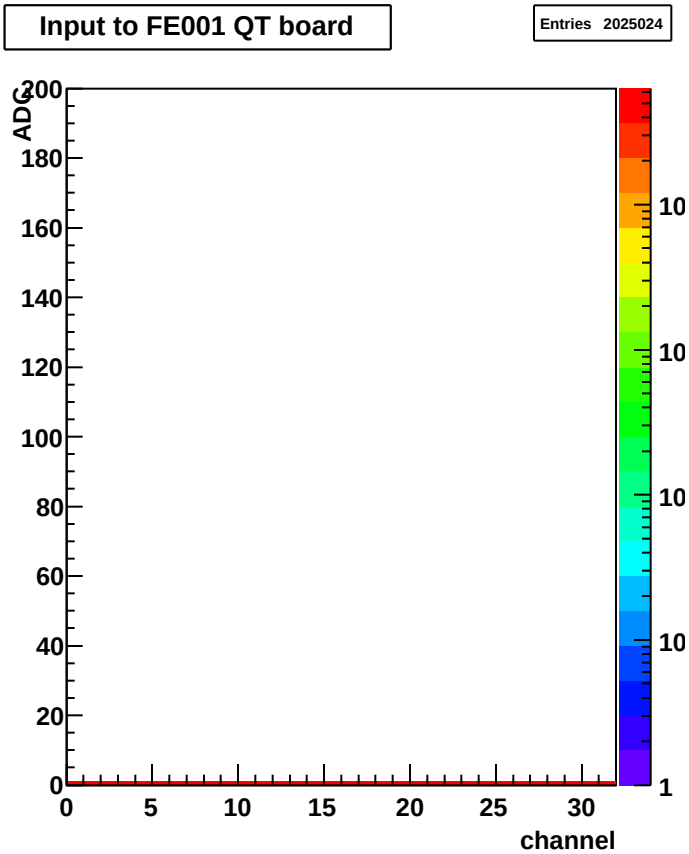

Input to FPD L2 DSM
Entries 189846
HT bits
63282
63282
453
1511
61318
SMALL
LARGE-S
LARGE-N

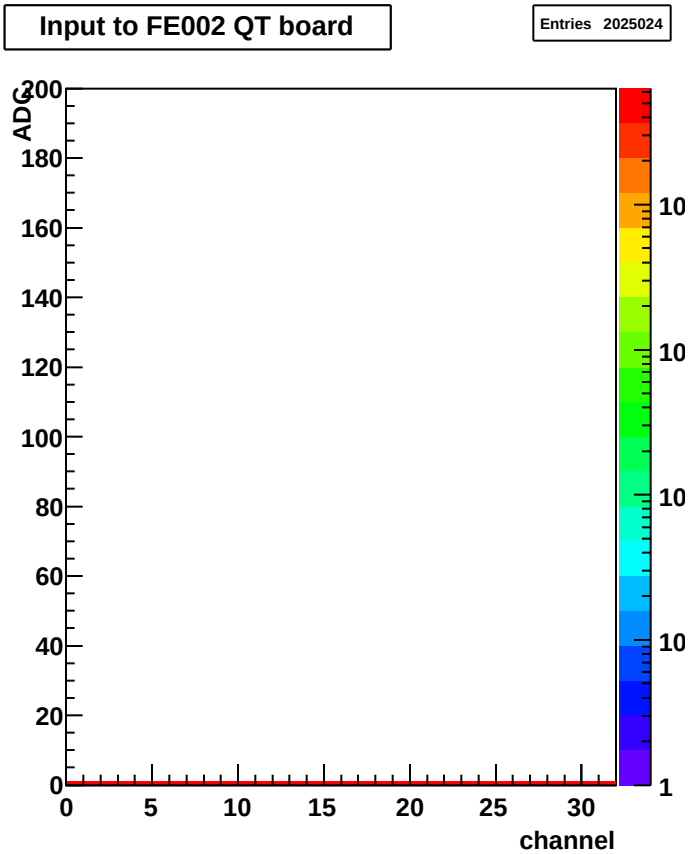

Input to FPD L2 DSM
Entries 128528
HT bits - simulated
63282
63282
453
1511
SMALL
LARGE-S
LARGE-N

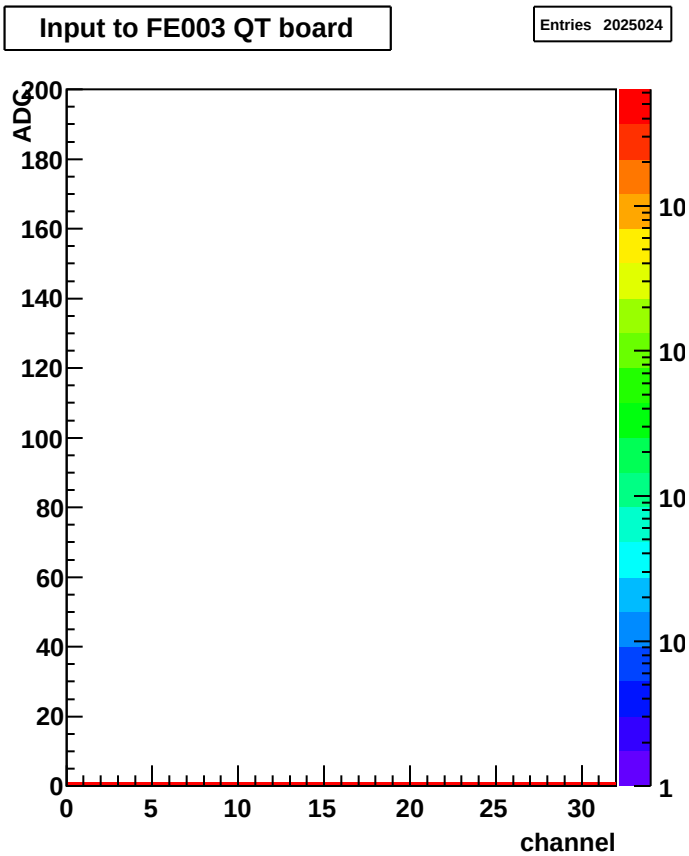

Input to FPD L3 DSM
Entries 1012512
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

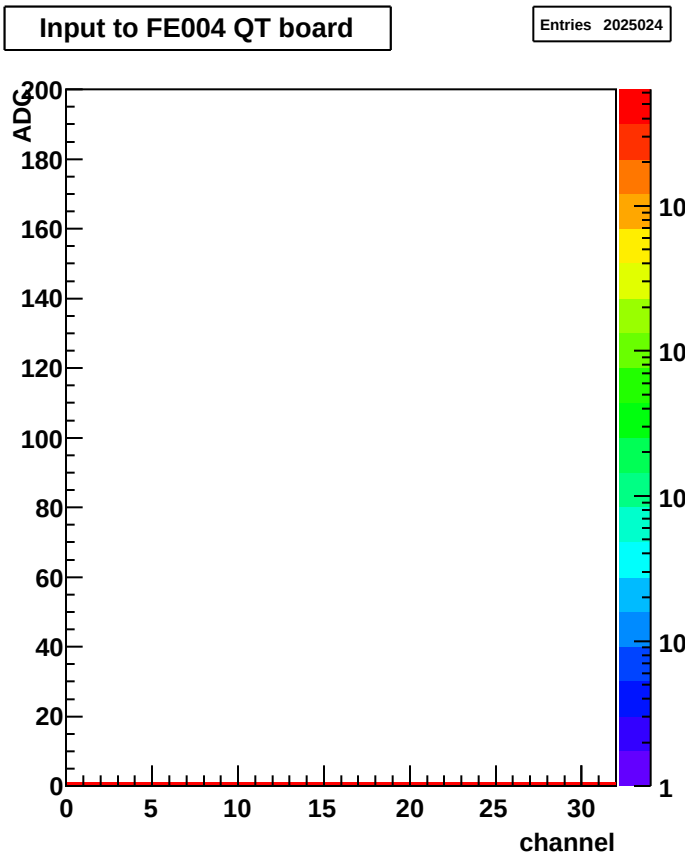

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

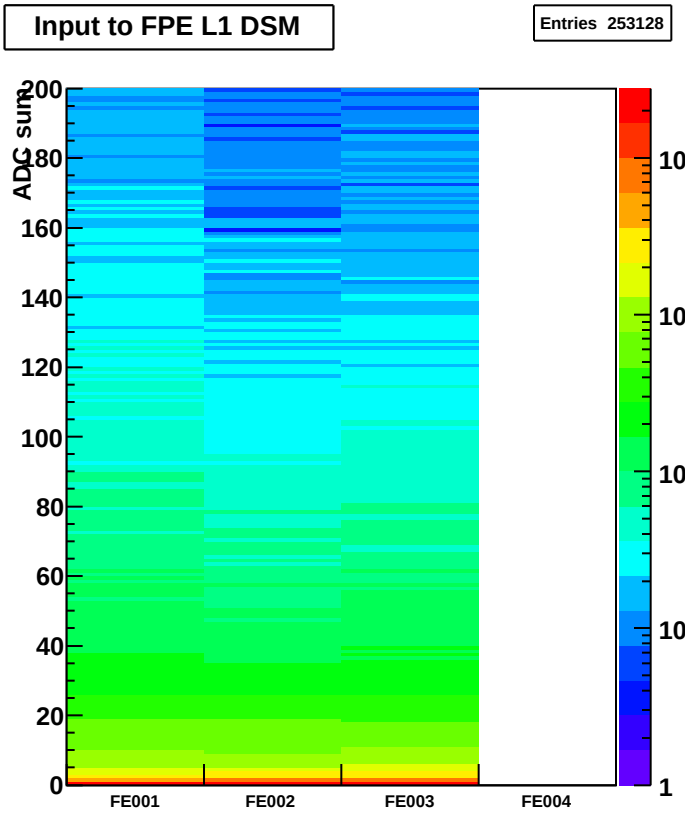

Input to FPD L2 DSM
Entries 253128
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

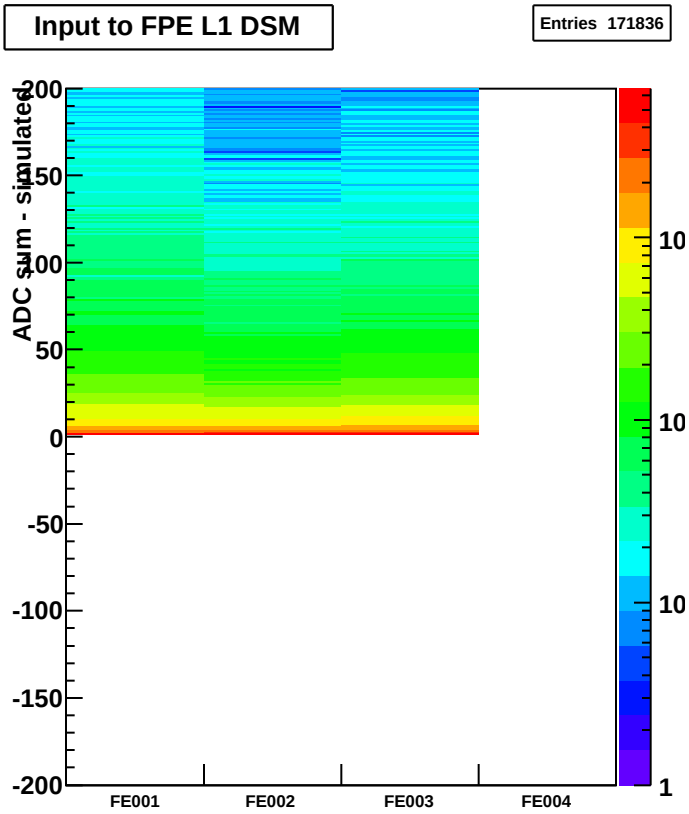

Input to FE001 QT board
Entries 2025024
ADC
channel

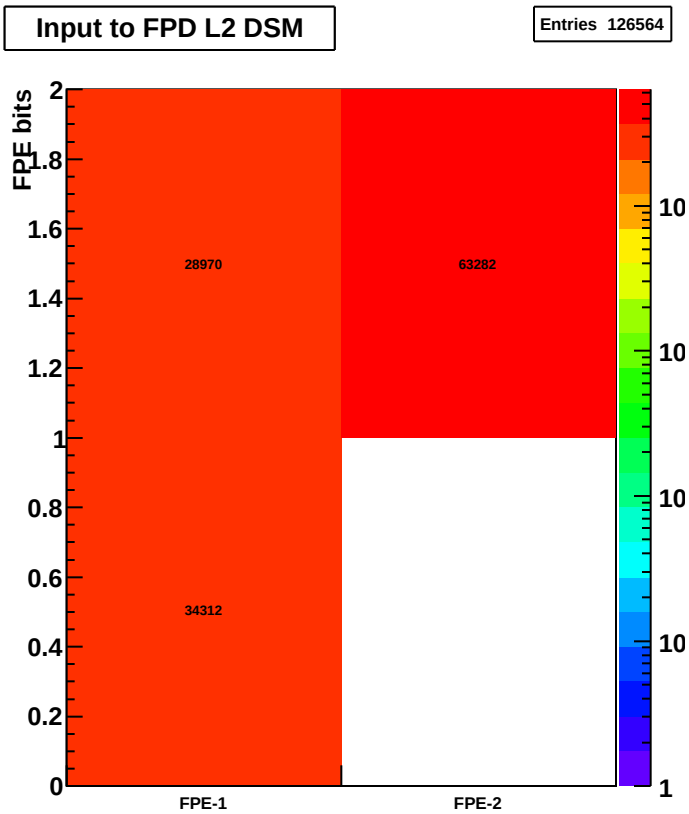

Input to FE002 QT board
Entries 2025024
ADC
channel

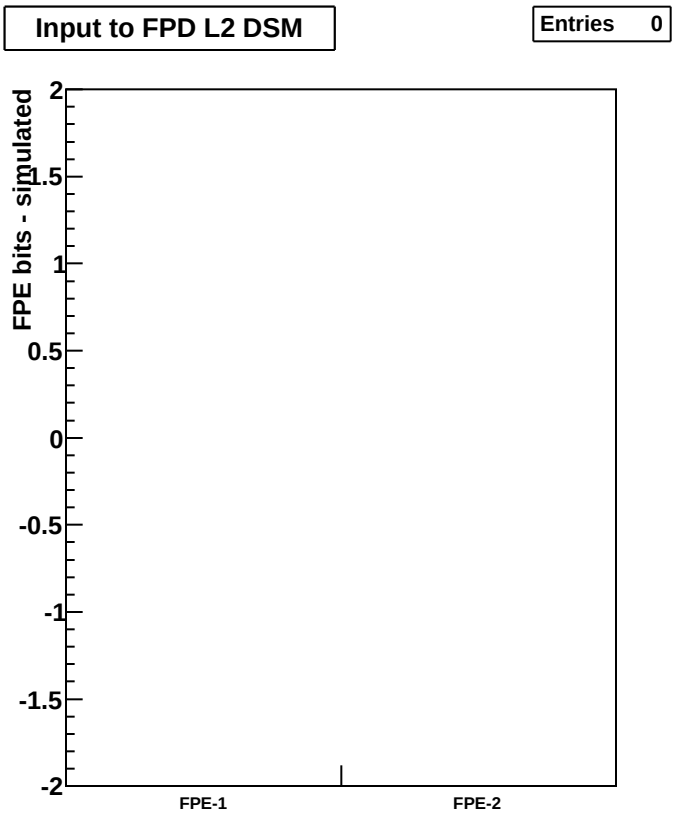

Input to FE003 QT board
Entries 2025024
ADC
channel


Input to FE004 QT board
Entries 2025024
ADC
channel


Input to FPE L1 DSM
Entries 253128
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 171836
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 126564
FPE bits
28970
63282
34312
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2