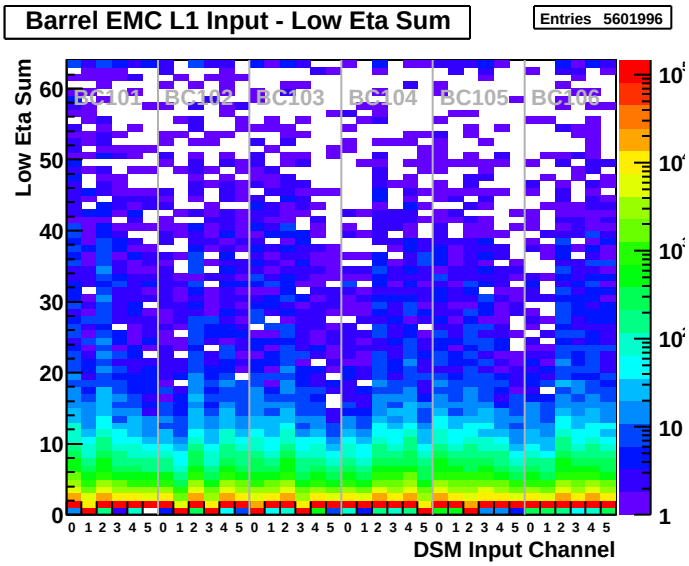
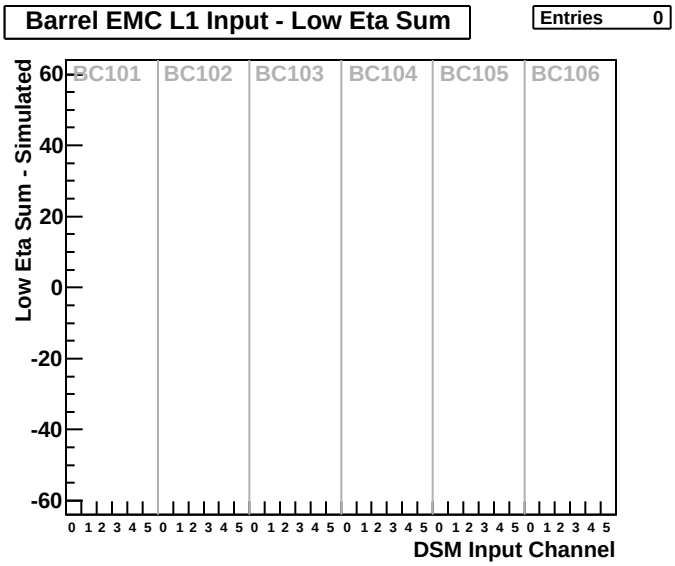
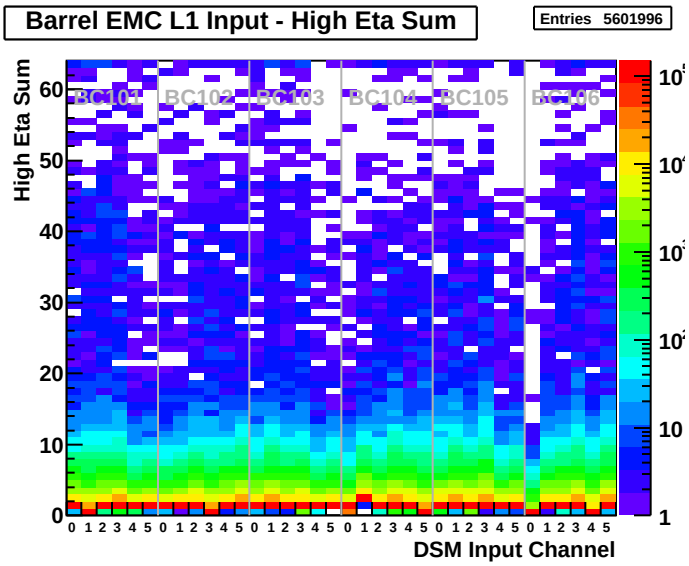
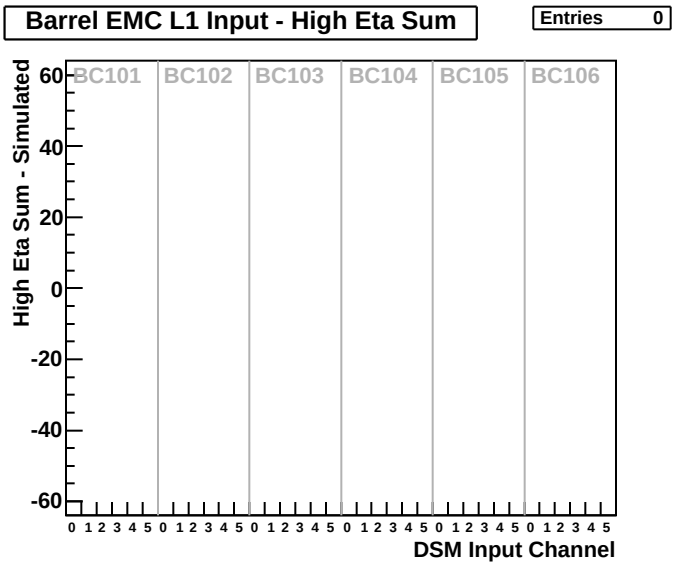
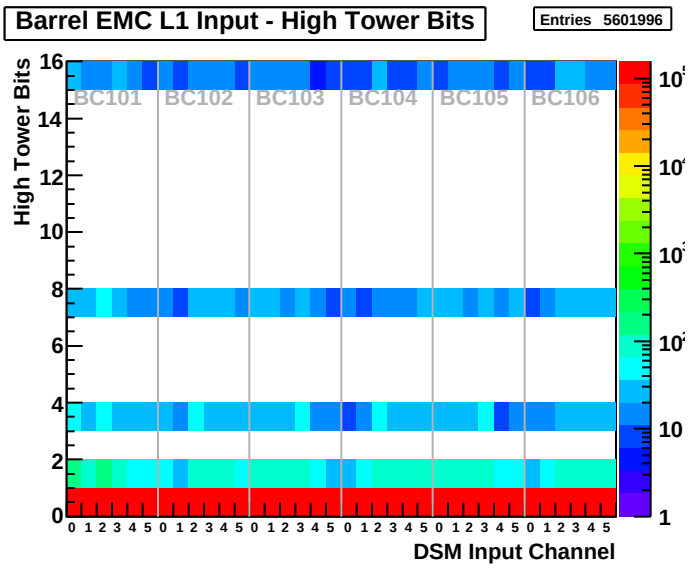
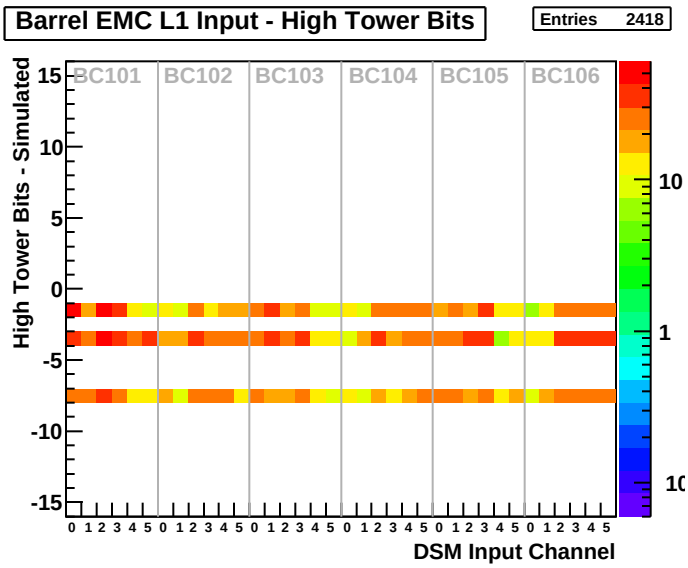

Barrel EMC L1 Input - Low Eta Sum
Entries 5601996
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

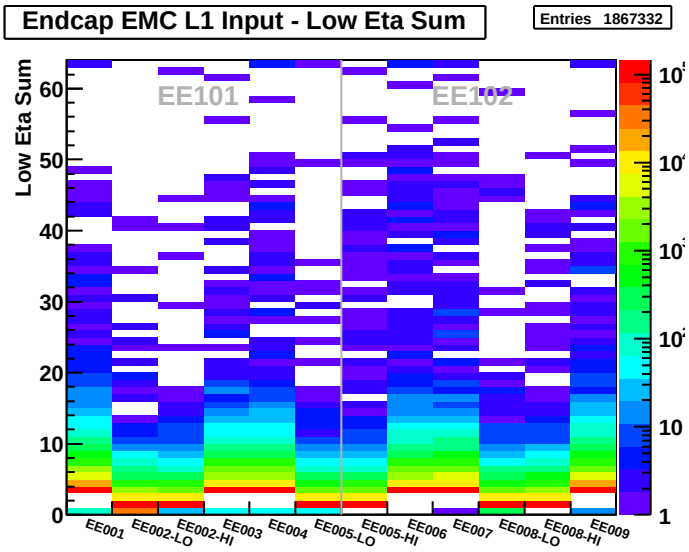

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

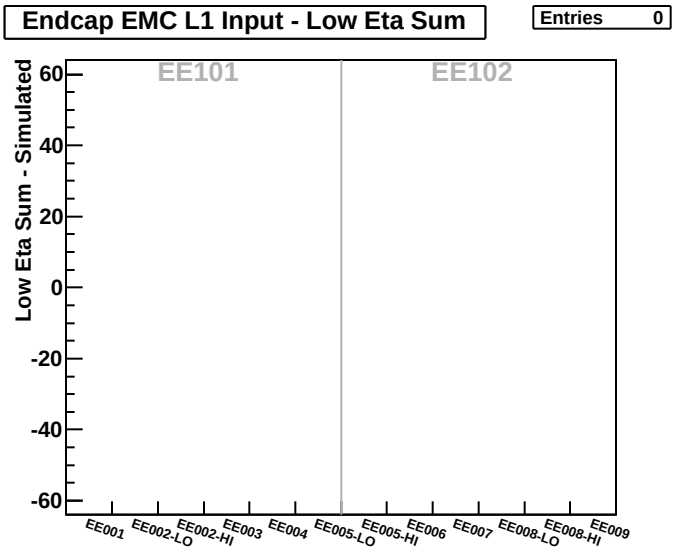

Barrel EMC L1 Input - High Eta Sum
Entries 5601996
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

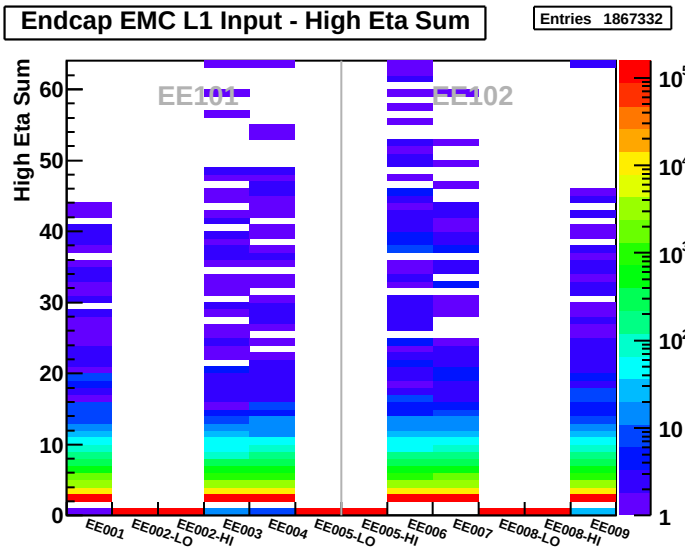

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

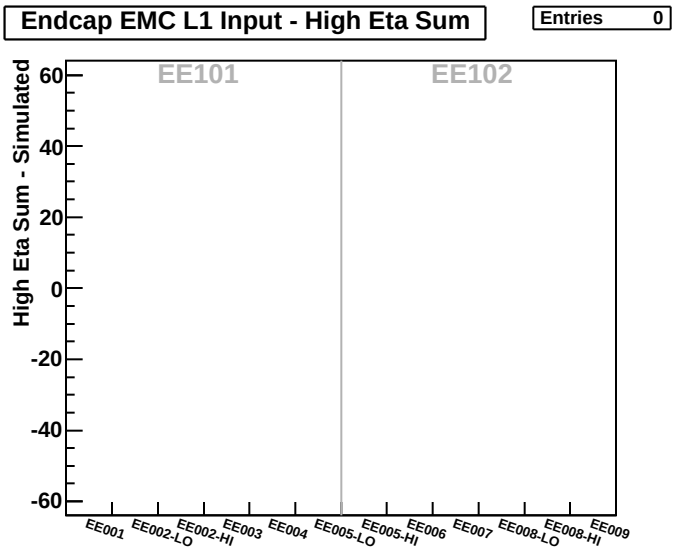

Barrel EMC L1 Input - High Tower Bits
Entries 5601996
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

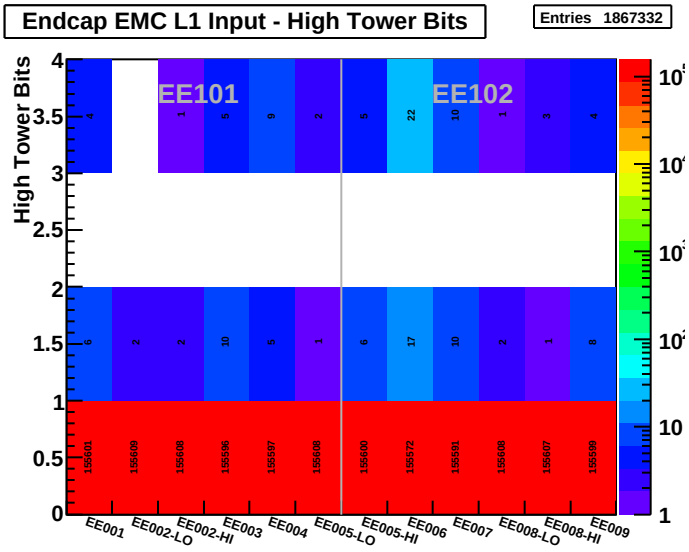

Barrel EMC L1 Input - High Tower Bits
Entries 2418
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

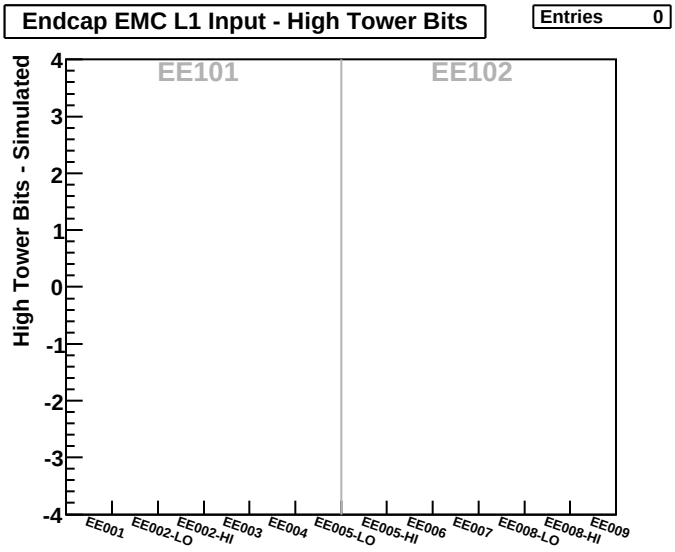

Endcap EMC L1 Input - Low Eta Sum
Entries 1867332
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

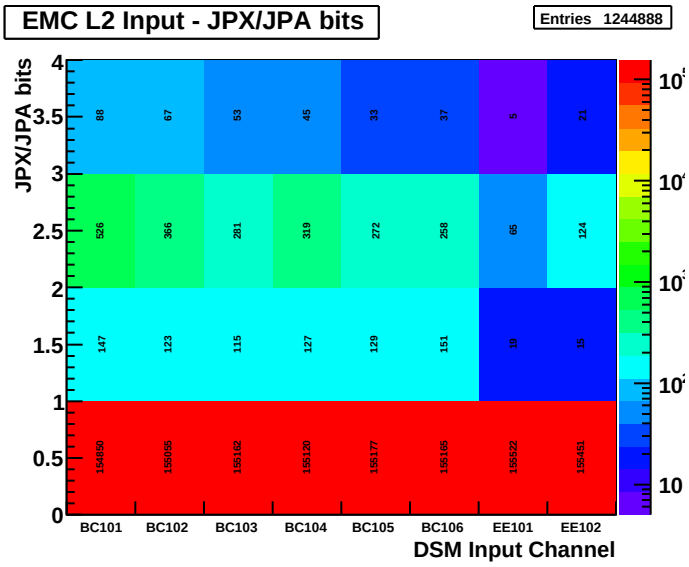

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

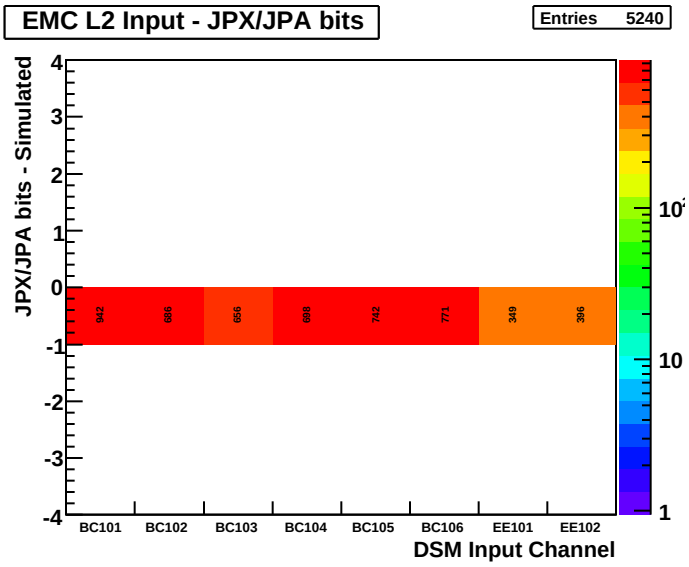

Endcap EMC L1 Input - High Eta Sum
Entries 1867332
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

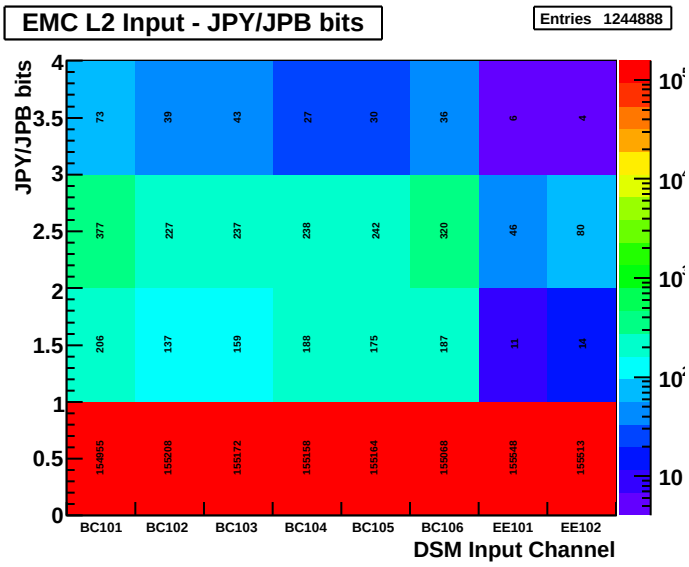

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

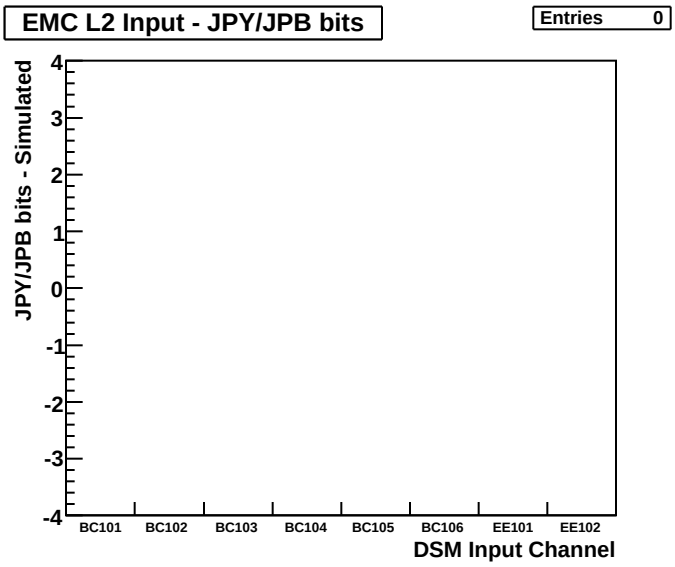

Endcap EMC L1 Input - High Tower Bits
Entries 1867332
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

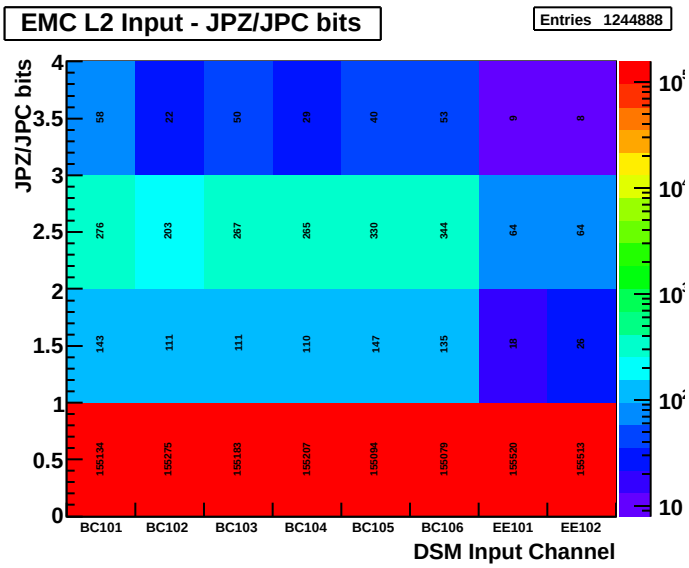

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

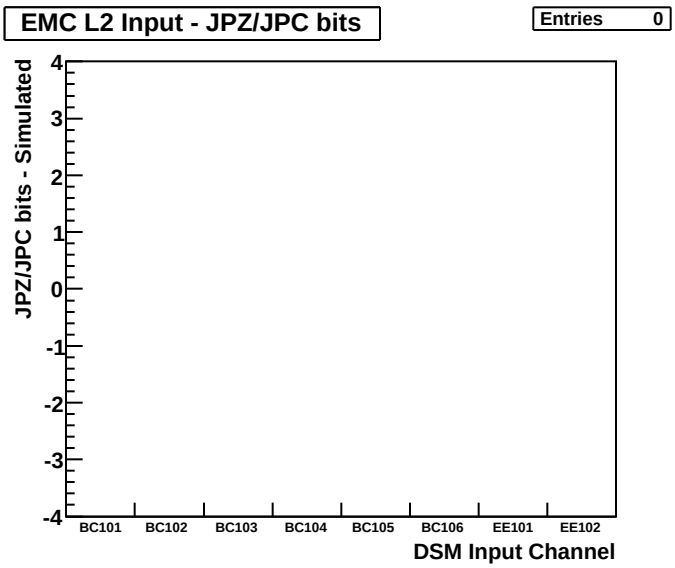

EMC L2 Input - JPX/JPA bits
Entries 1244888
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
88 67 53 45 33 37 5 21
526 366 281 319 272 258 65 124
147 123 115 127 129 151 19 15
154850 155055 155162 155120 155177 155165 155522 155451

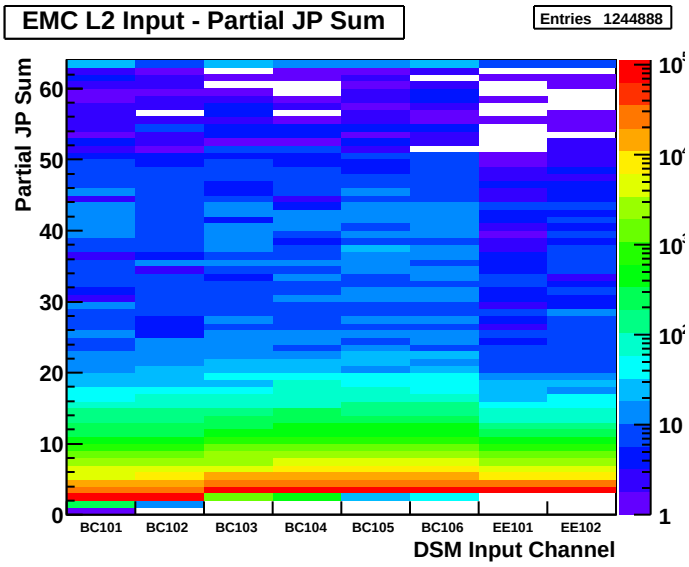

EMC L2 Input - JPX/JPA bits
Entries 5240
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
942 686 656 698 742 771 349 386

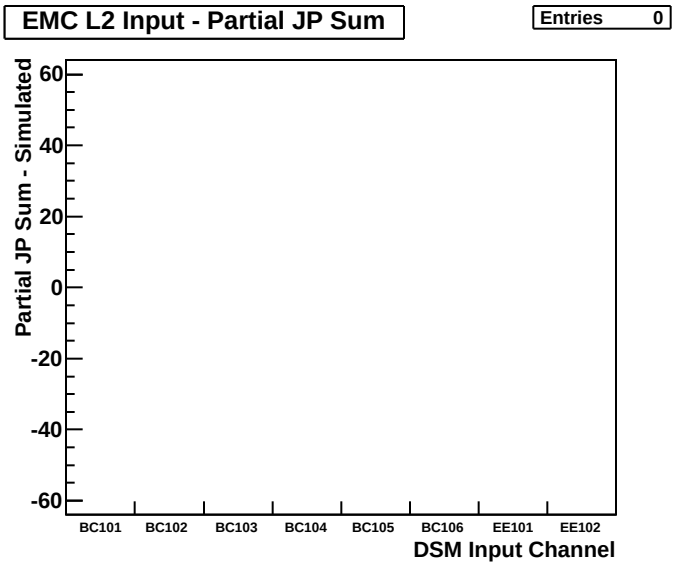

EMC L2 Input - JPY/JPB bits
Entries 1244888
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
73 39 43 27 30 36 6 4
377 227 237 238 242 320 46 80
206 137 159 188 175 187 11 14
154955 155208 155172 155158 155164 155068 155548 155513

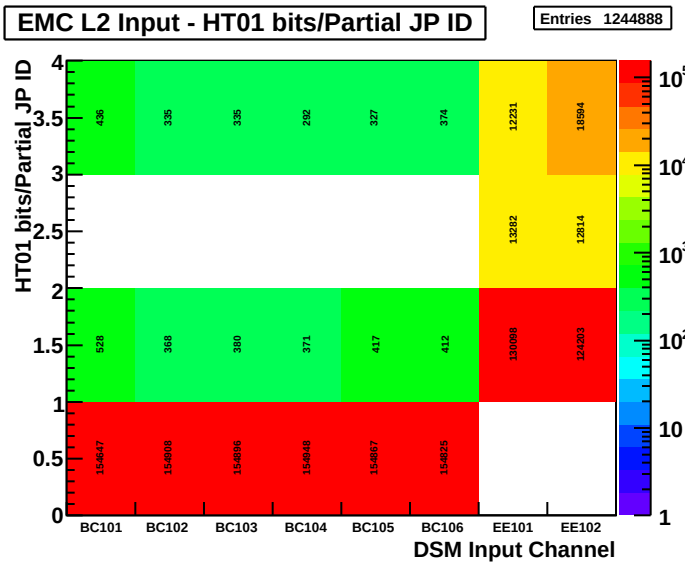

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

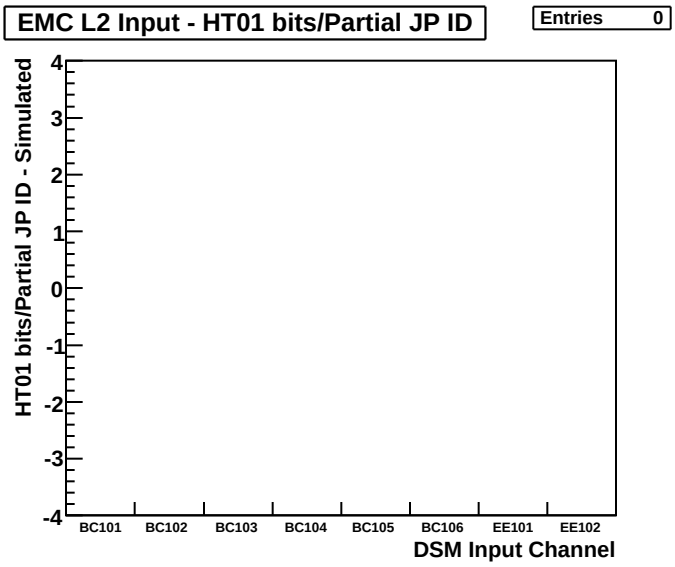

EMC L2 Input - JPZ/JPC bits
Entries 1244888
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
58 22 50 29 40 53 9 8
276 203 267 265 330 344 64 64
143 111 111 110 147 135 18 26
155134 155275 155183 155207 155094 155079 155520 155513

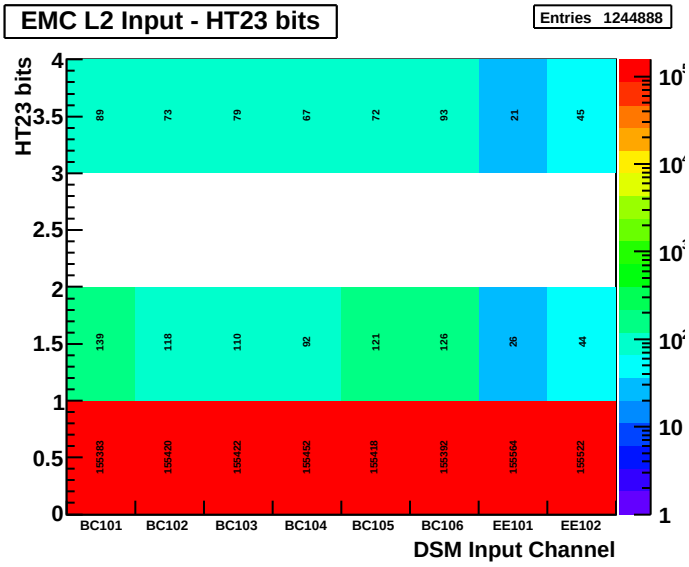

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

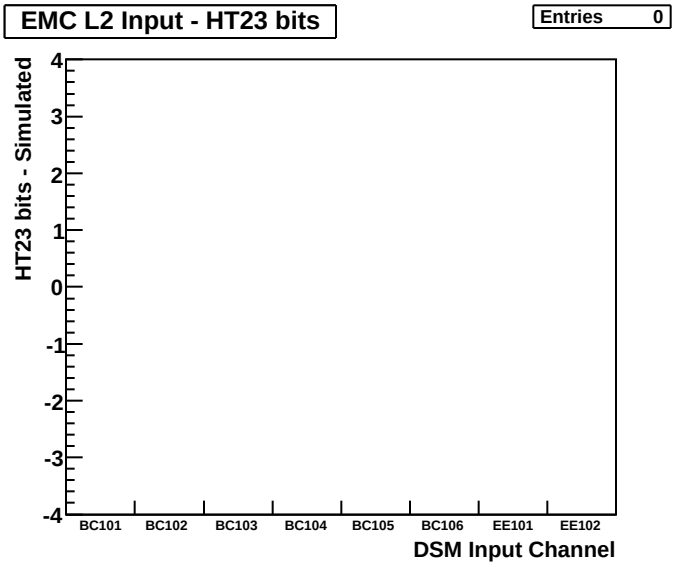

EMC L2 Input - Partial JP Sum
Entries 1244888
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

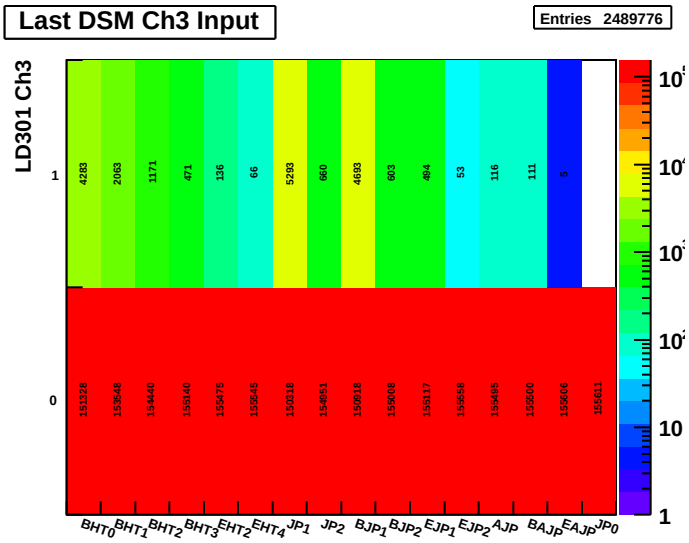

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

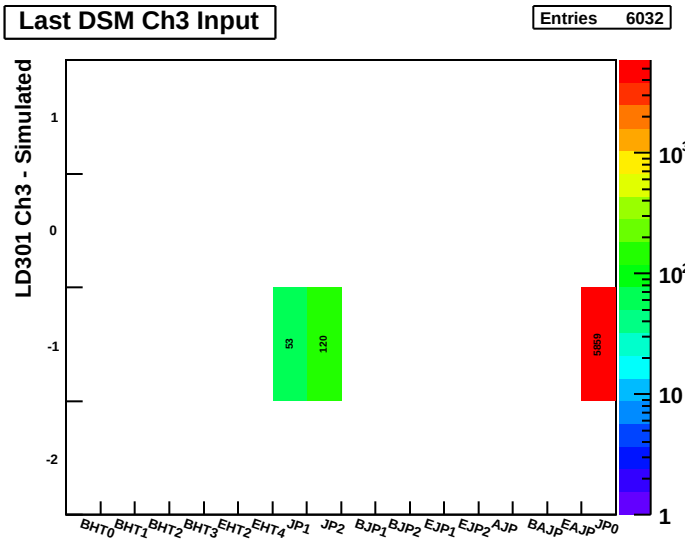

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1244888
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

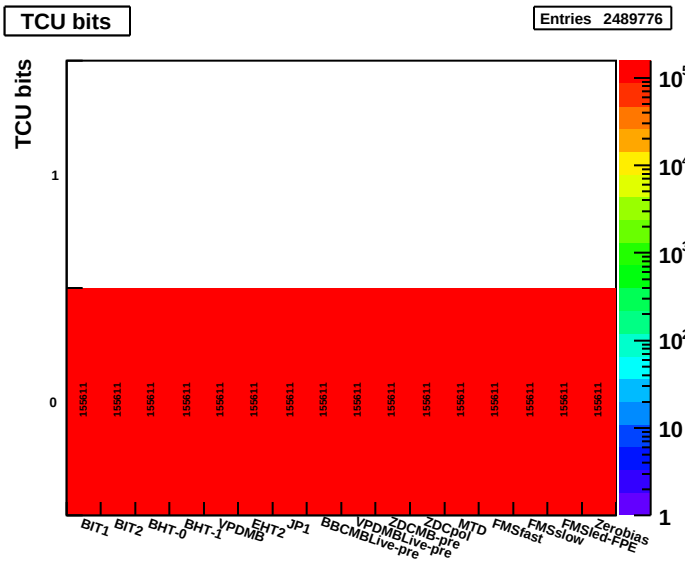

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

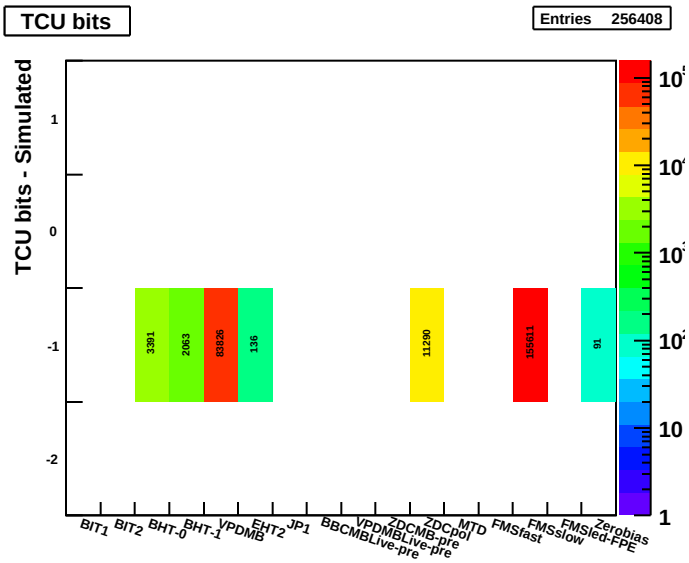

EMC L2 Input - HT23 bits
Entries 1244888
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

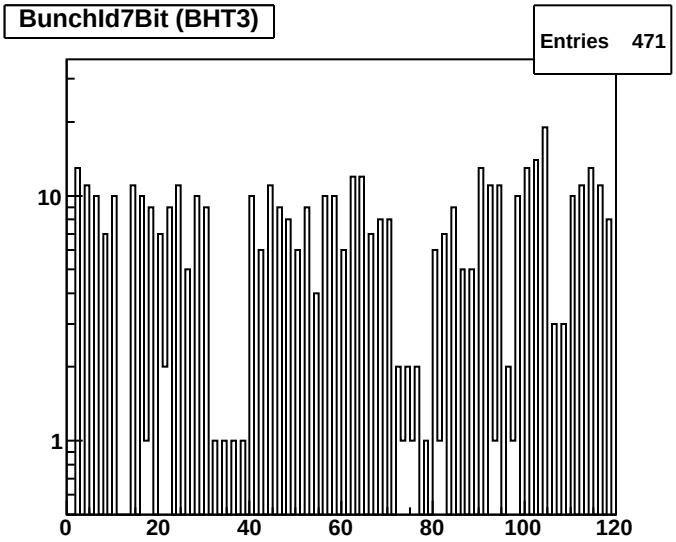

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

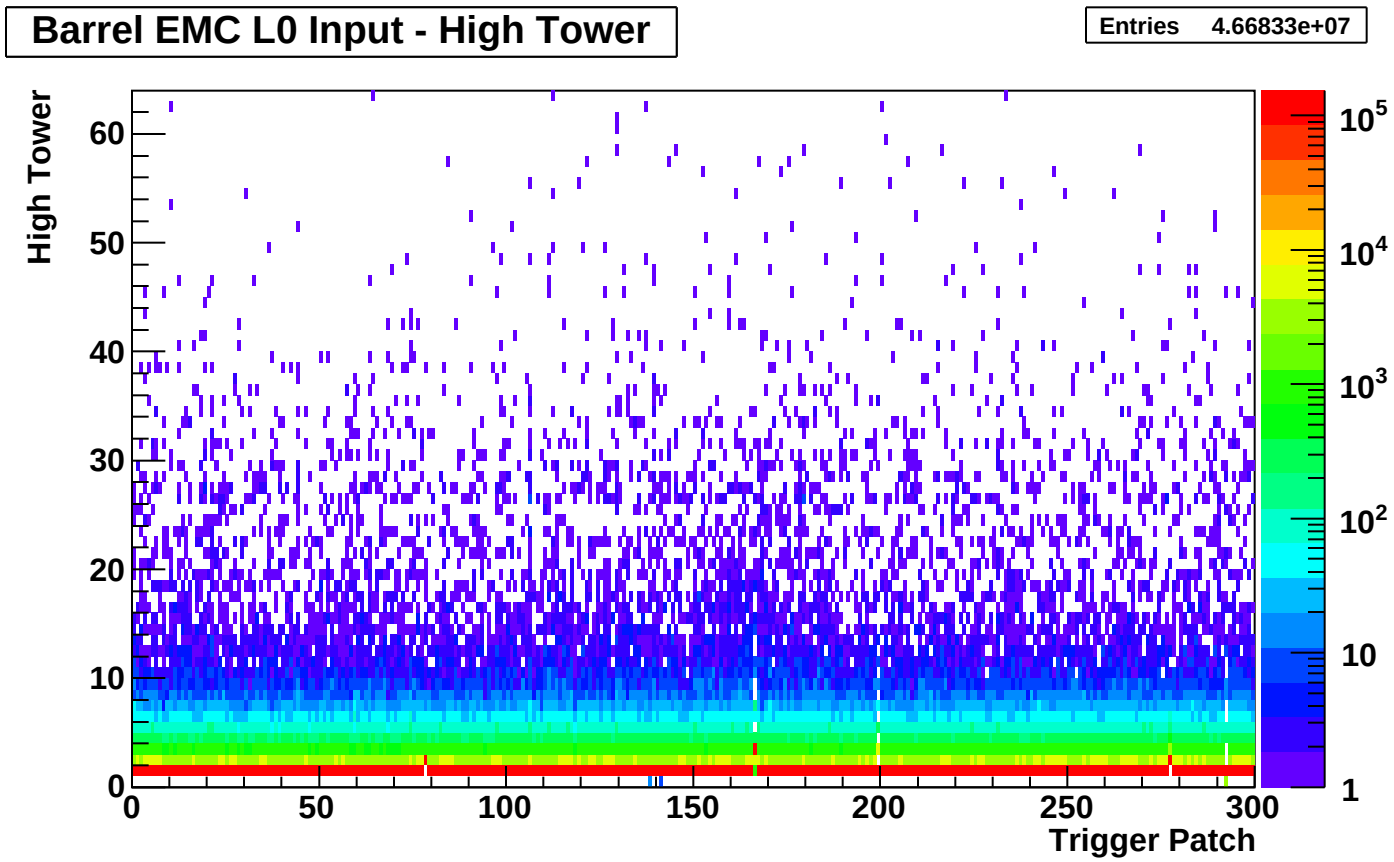

Last DSM Ch3 Input
Entries 2489776
LD301 Ch3
4283 2063 1171 471 136 66 5293 660 4693 603 494 53 116 111 5
151328 153848 154440 155140 155475 155545 150318 154951 150918 155008 155117 155568 155495 155500 155606 155611
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

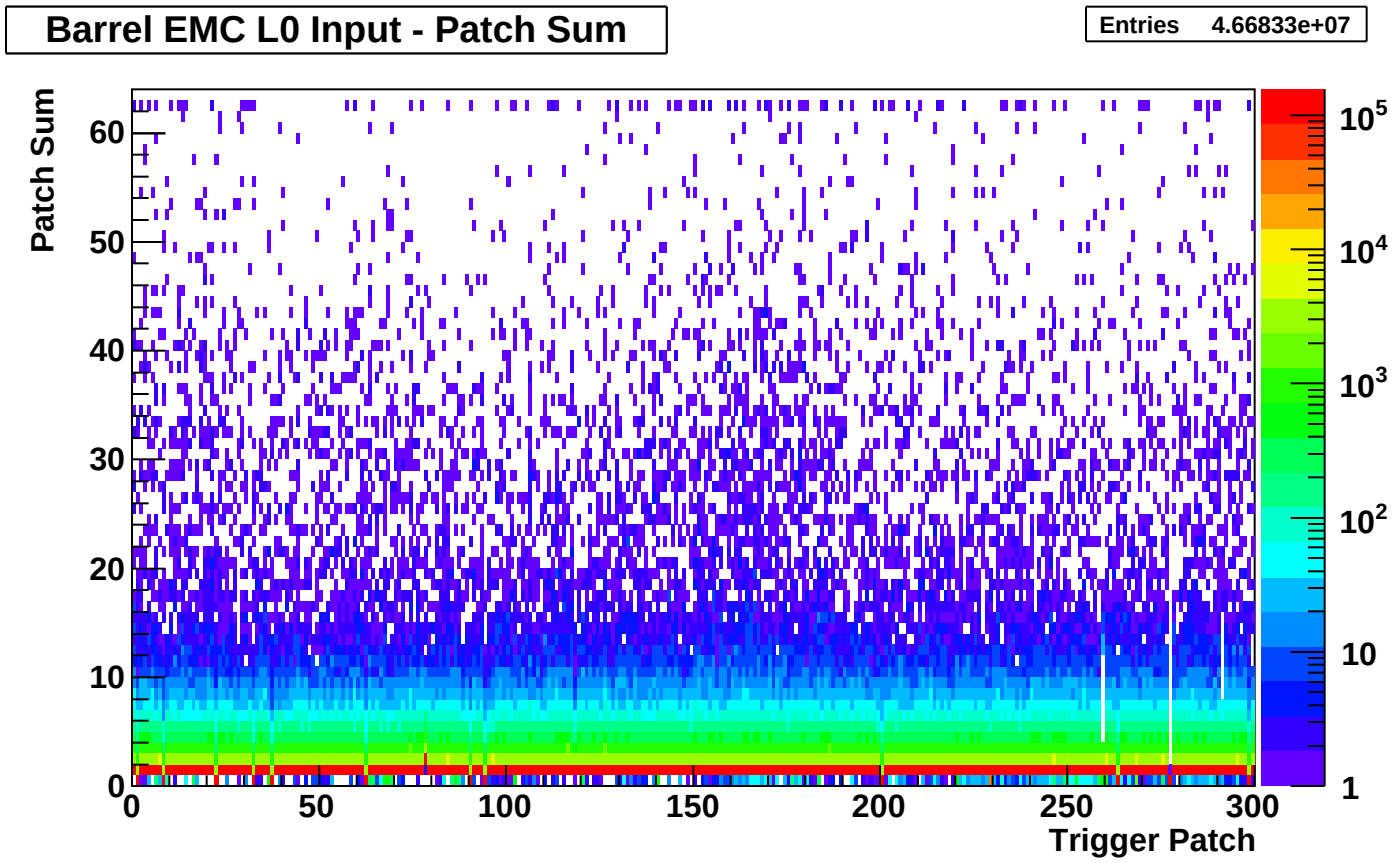

Last DSM Ch3 Input
Entries 6032
LD301 Ch3 - Simulated
53 120 5859
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

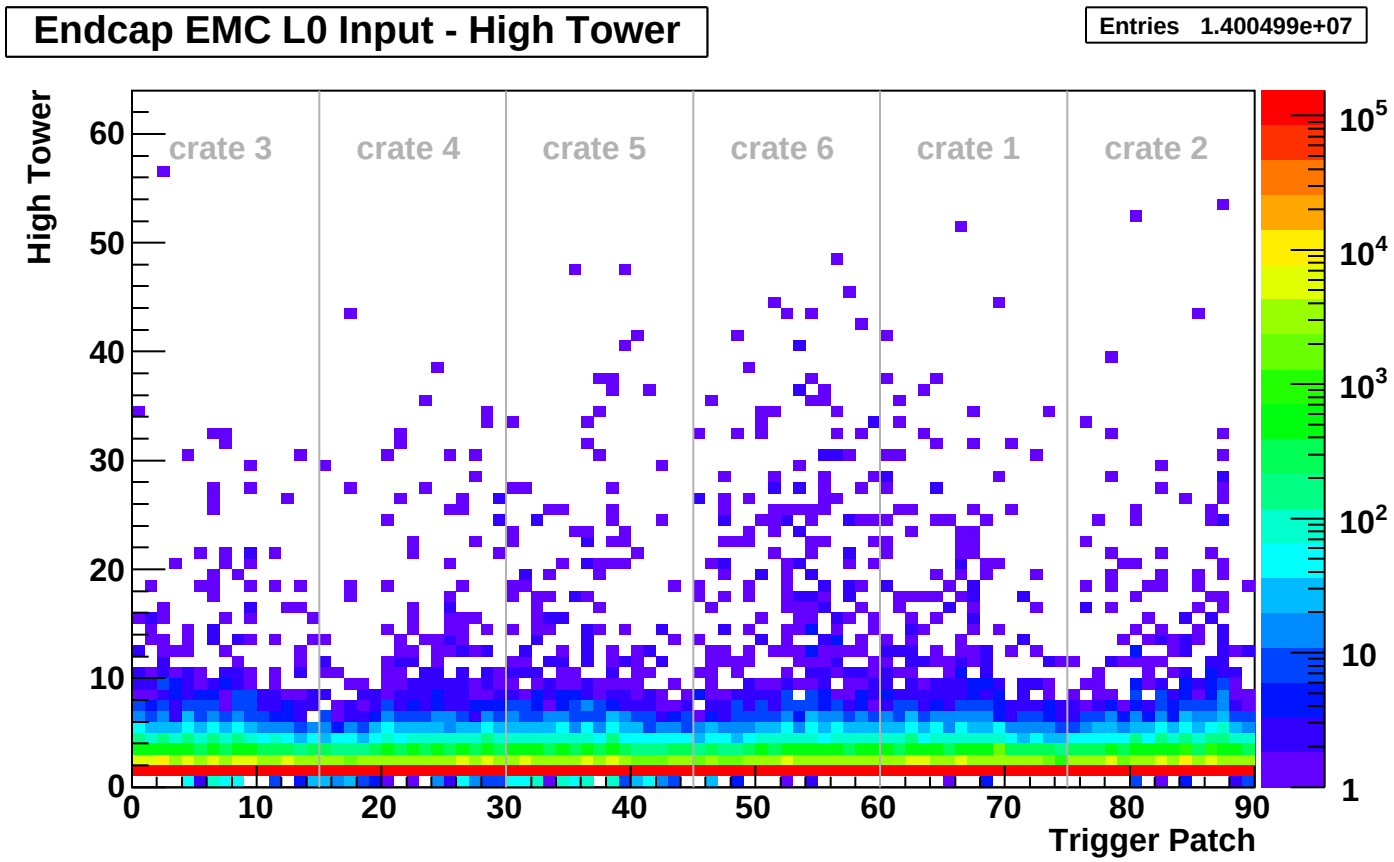

TCU bits
Entries 2489776
TCU bits
155611 155611 155611 155611 155611 155611 155611 155611 155611 155611 155611 155611 155611 155611 155611 155611
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

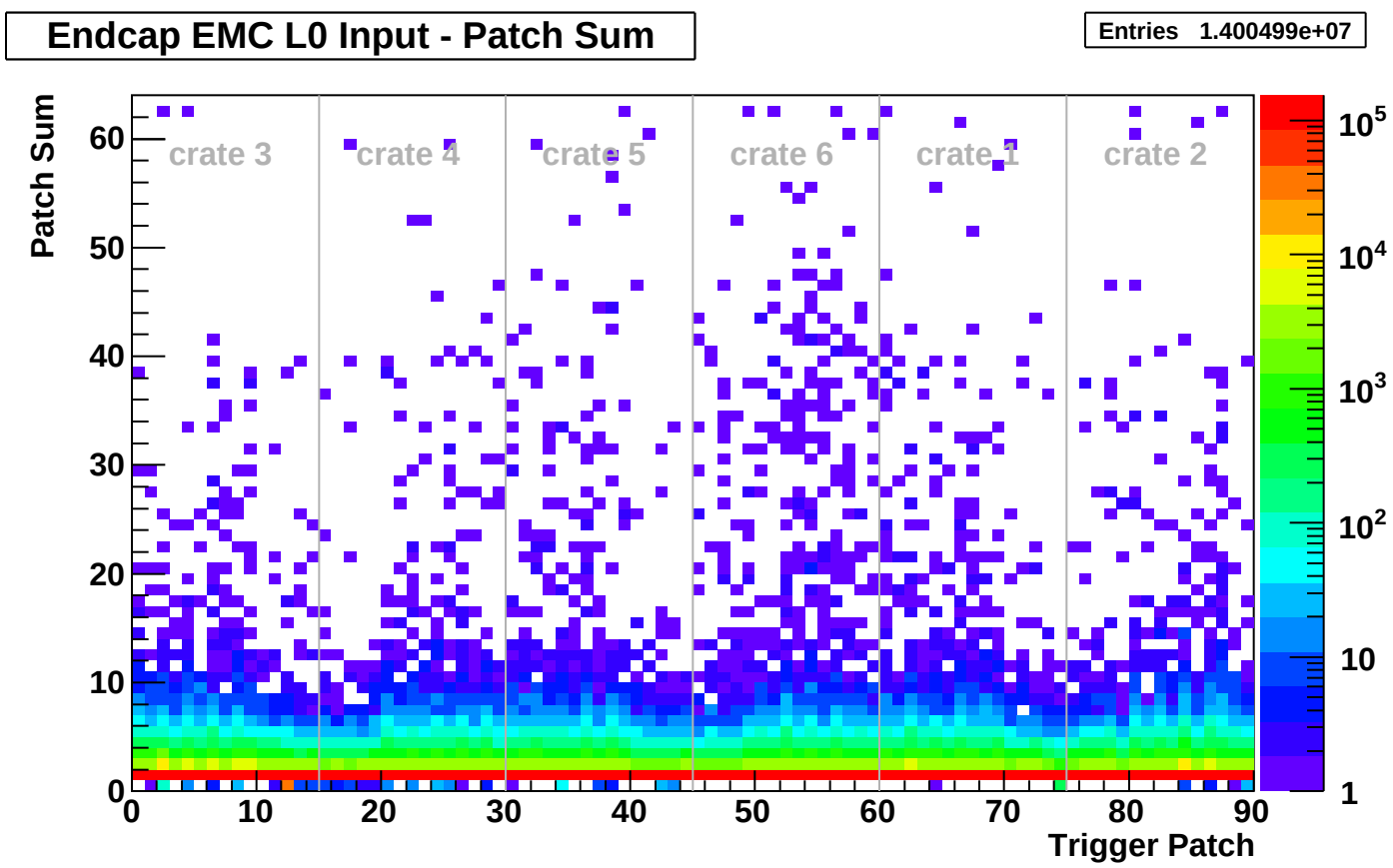

TCU bits
Entries 256408
TCU bits - Simulated
3391 2063 83826 136 11290 155611 91
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

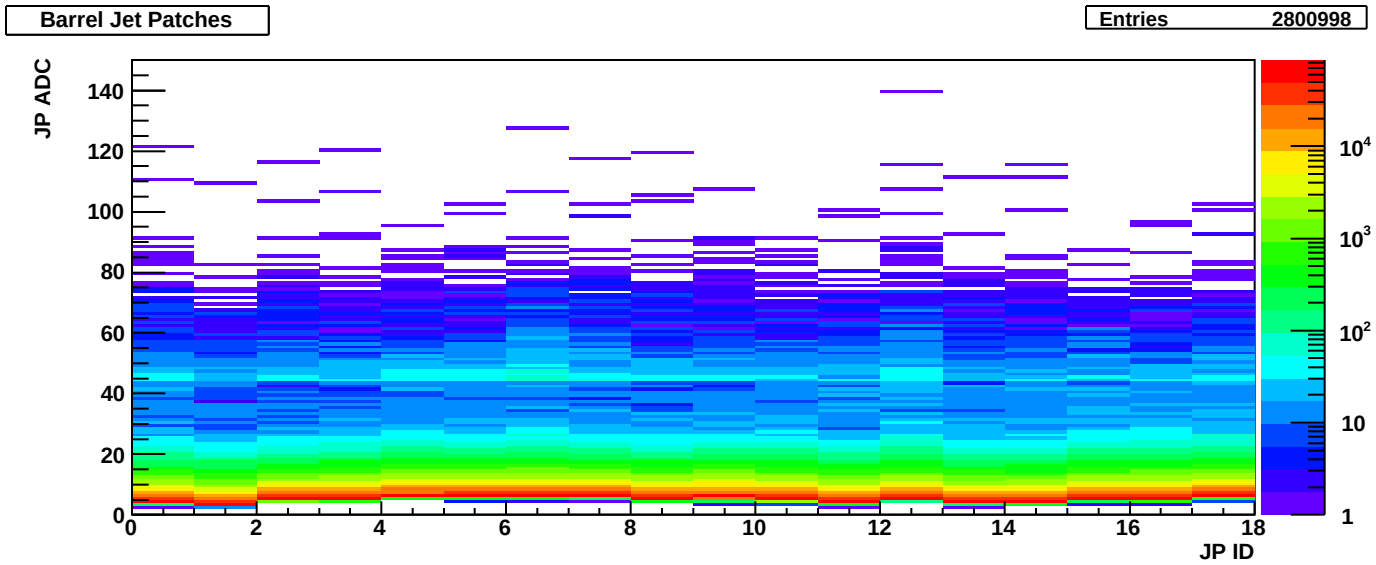

BunchId7Bit (BHT3)
Entries 471

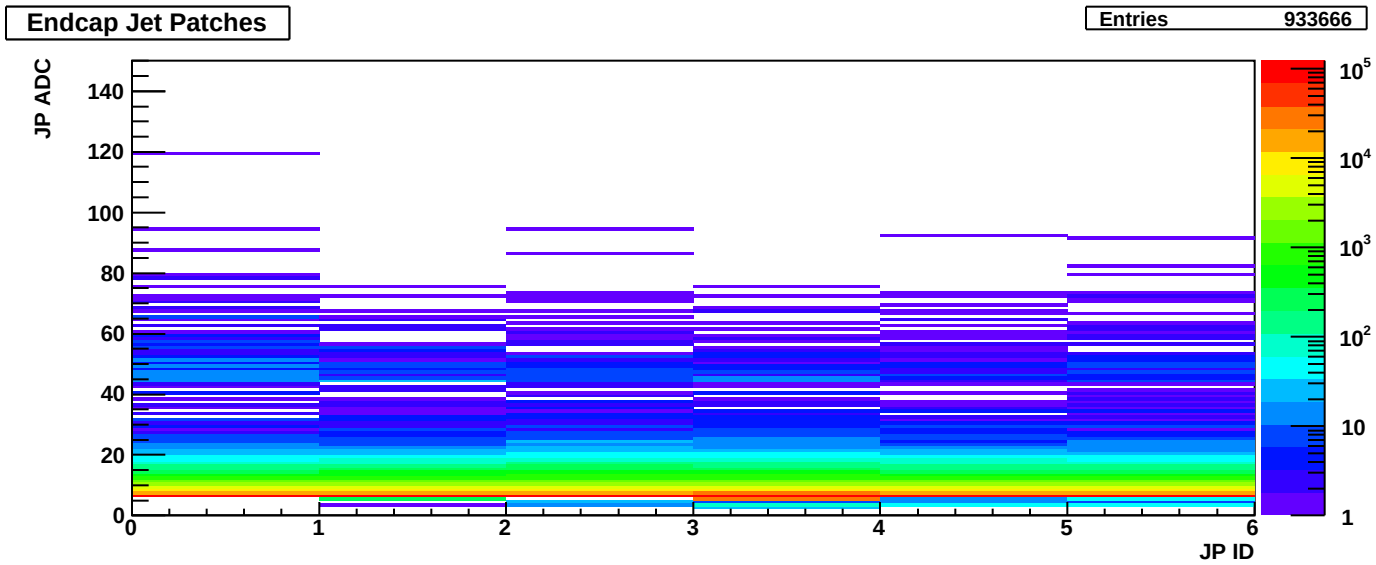

Barrel EMC L0 Input - High Tower
Entries 4.66833e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

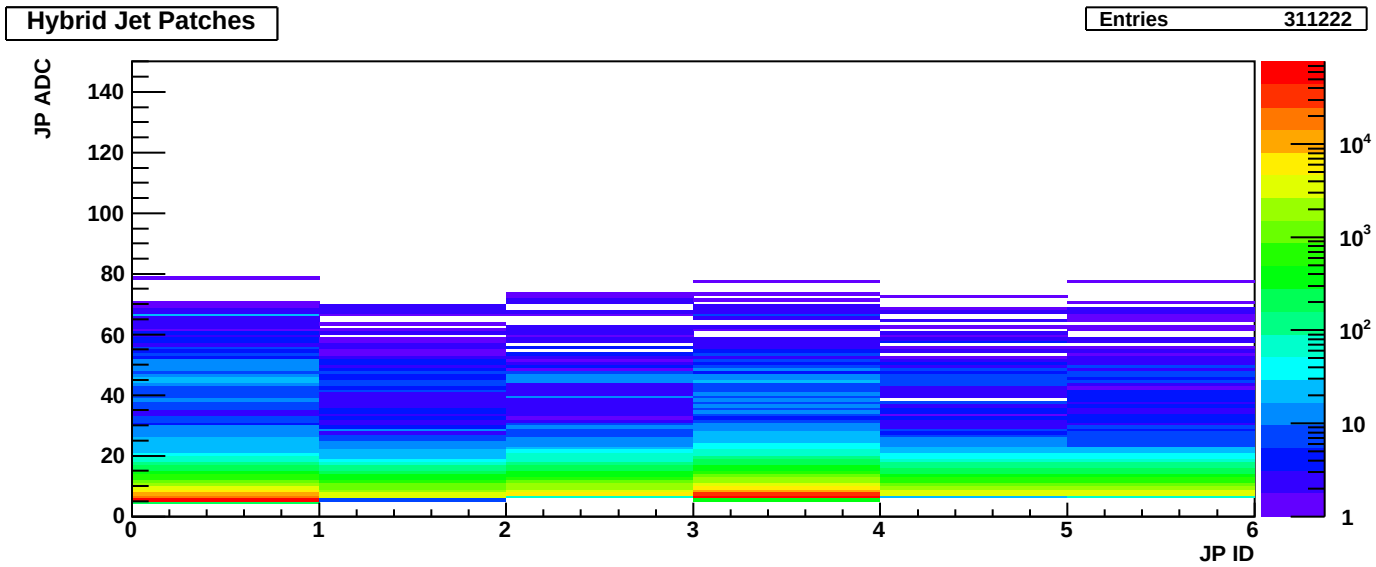

Barrel EMC L0 Input - Patch Sum
Entries 4.66833e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

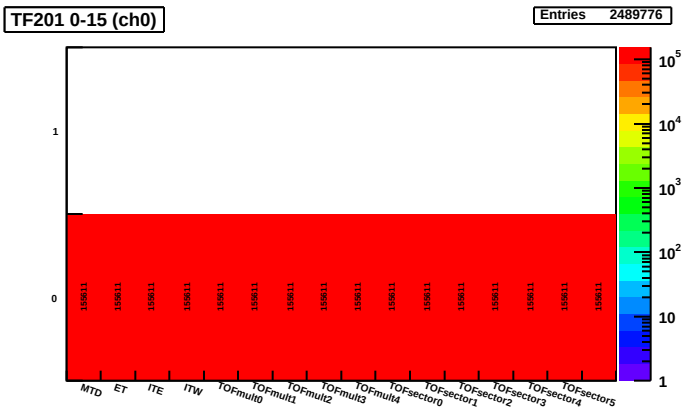

Endcap EMC L0 Input - High Tower
Entries 1.400499e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

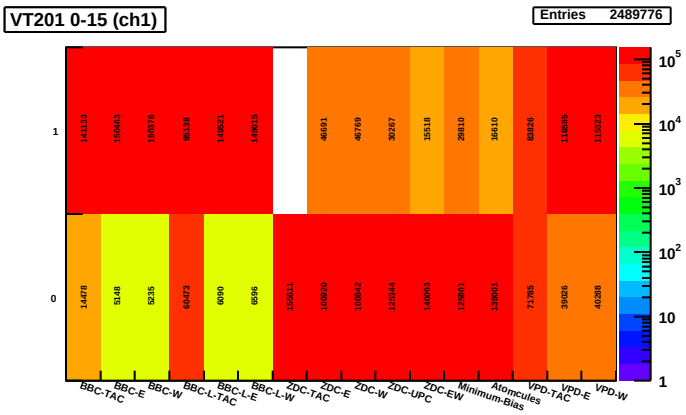

Endcap EMC L0 Input - Patch Sum
Entries 1.400499e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

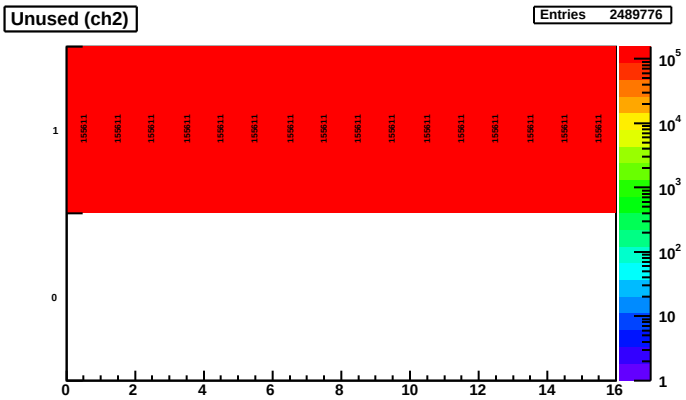

Barrel Jet Patches
Entries 2800998
JP ADC
JP ID

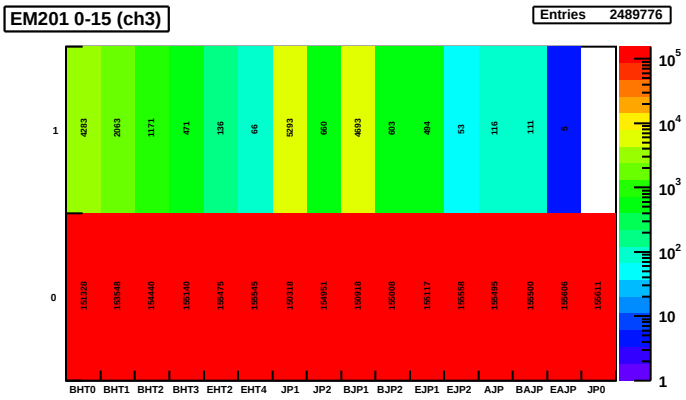

Endcap Jet Patches
Entries 933666
JP ADC
JP ID

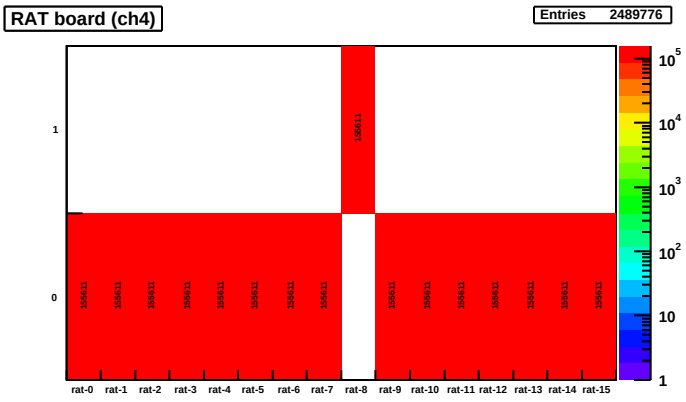

Hybrid Jet Patches
Entries 311222
JP ADC
JP ID

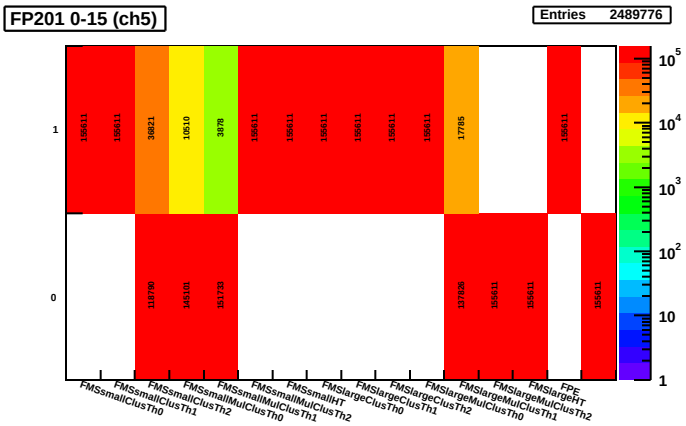

TF201 0-15 (ch0)
Entries 2489776
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

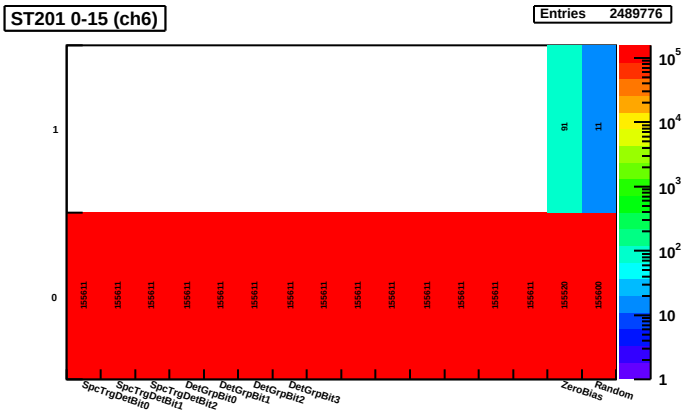

VT201 0-15 (ch1)
Entries 2489776
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

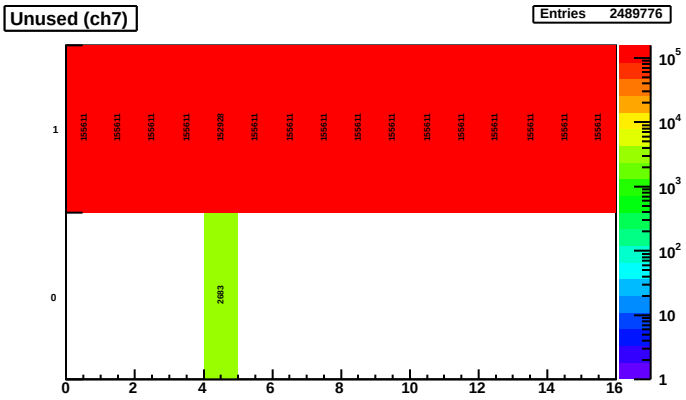

Unused (ch2)
Entries 2489776

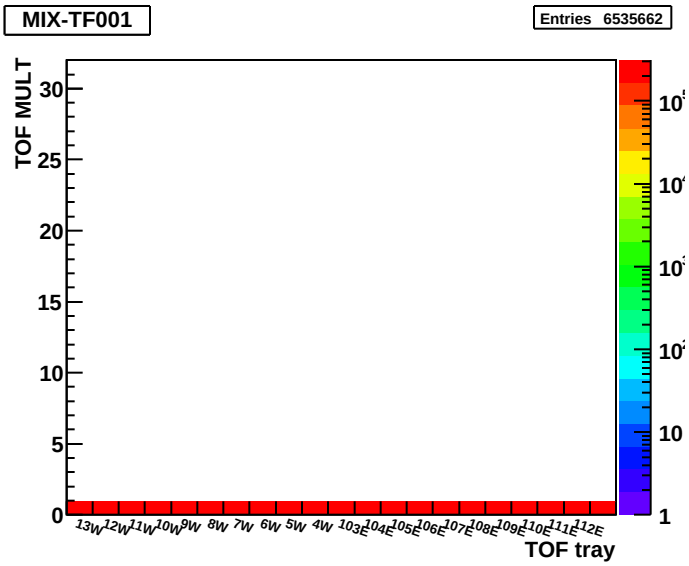

EM201 0-15 (ch3)
Entries 2489776
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

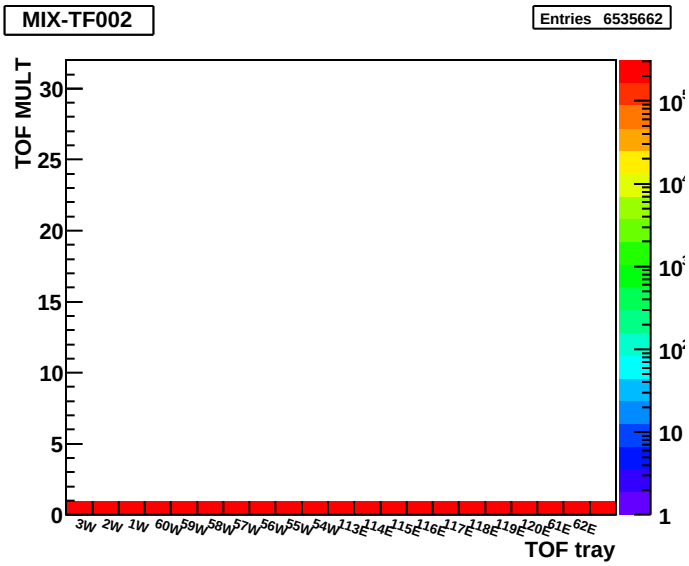

RAT board (ch4)
Entries 2489776
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

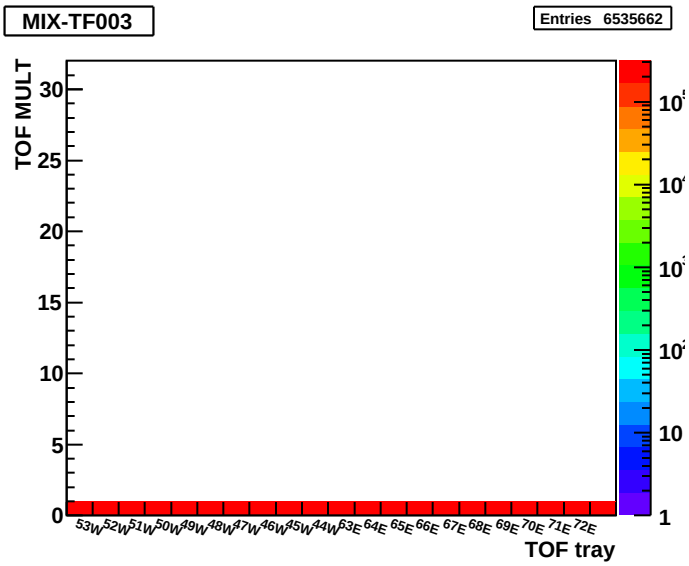

FP201 0-15 (ch5)
Entries 2489776
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

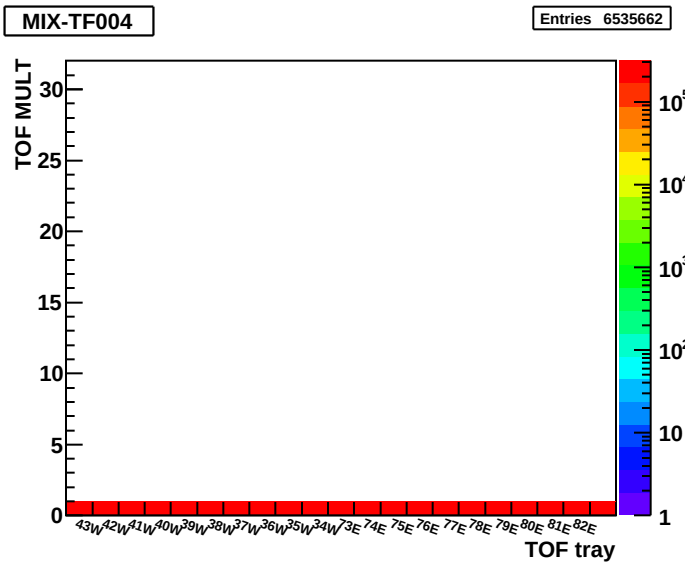

ST201 0-15 (ch6)
Entries 2489776
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

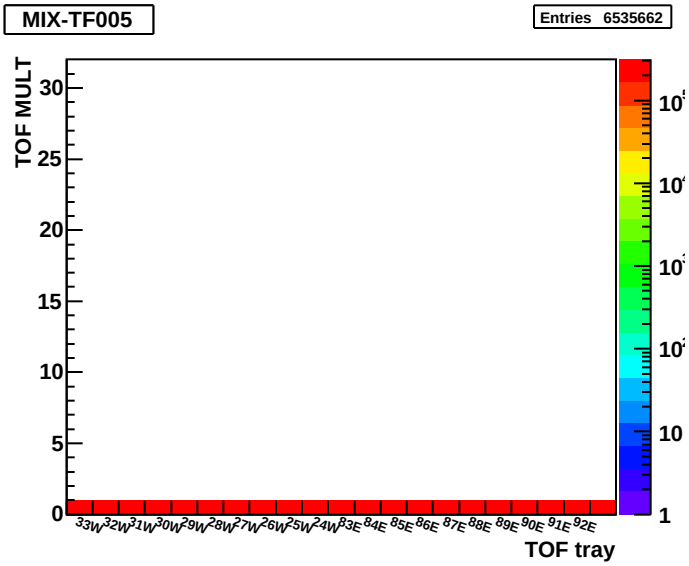

Unused (ch7)
Entries 2489776

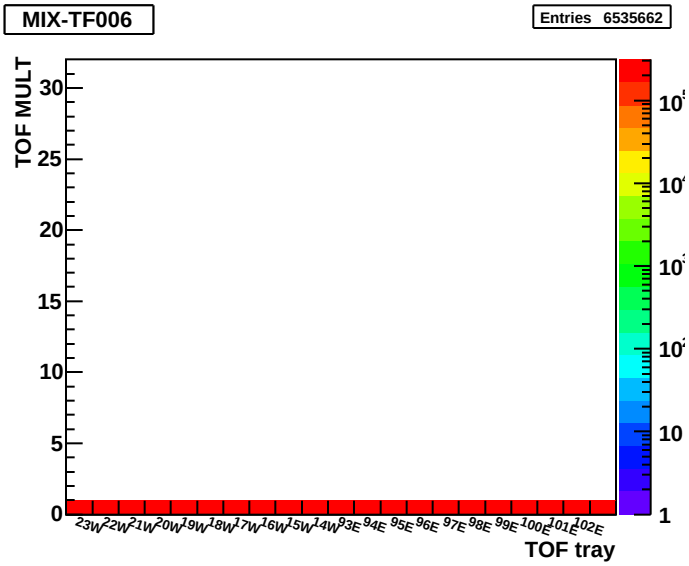

MIX-TF001
Entries 6535662
TOF MULT
TOF tray

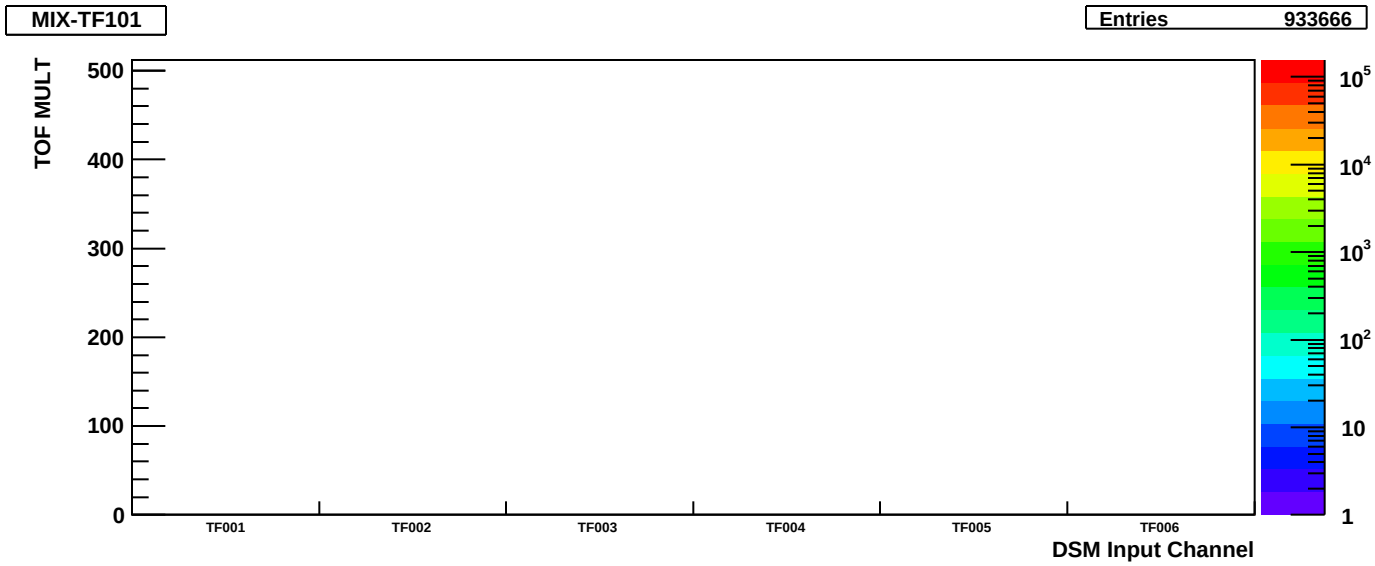

MIX-TF002
Entries 6535662
TOF MULT
TOF tray

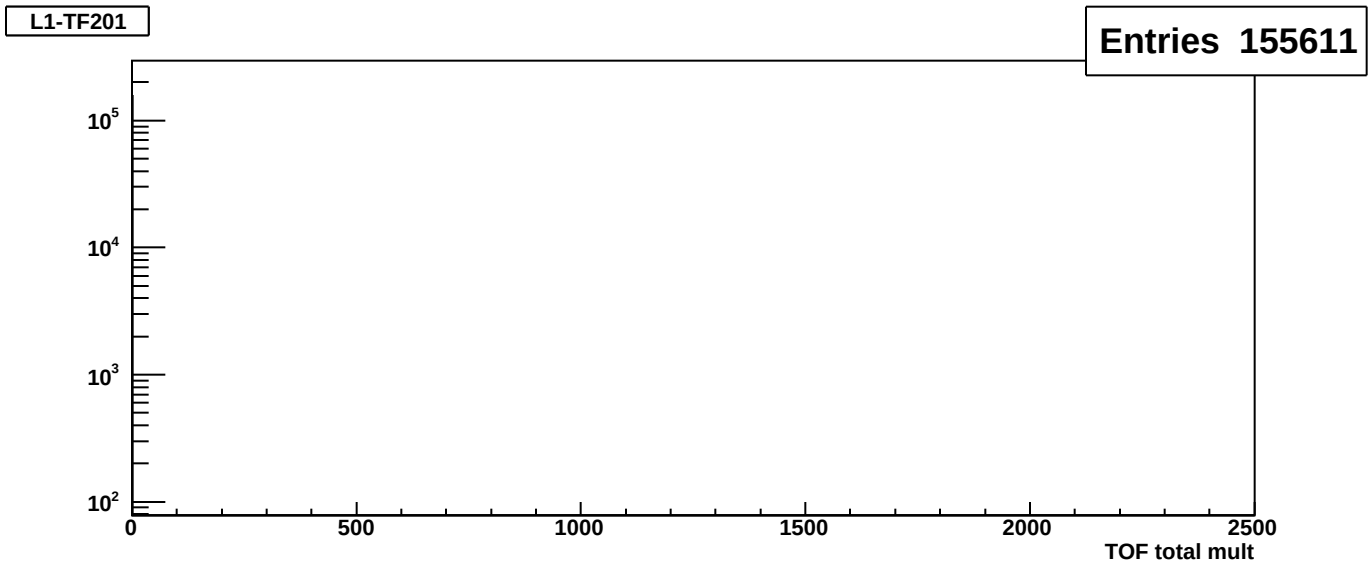

MIX-TF003
Entries 6535662
TOF MULT
TOF tray

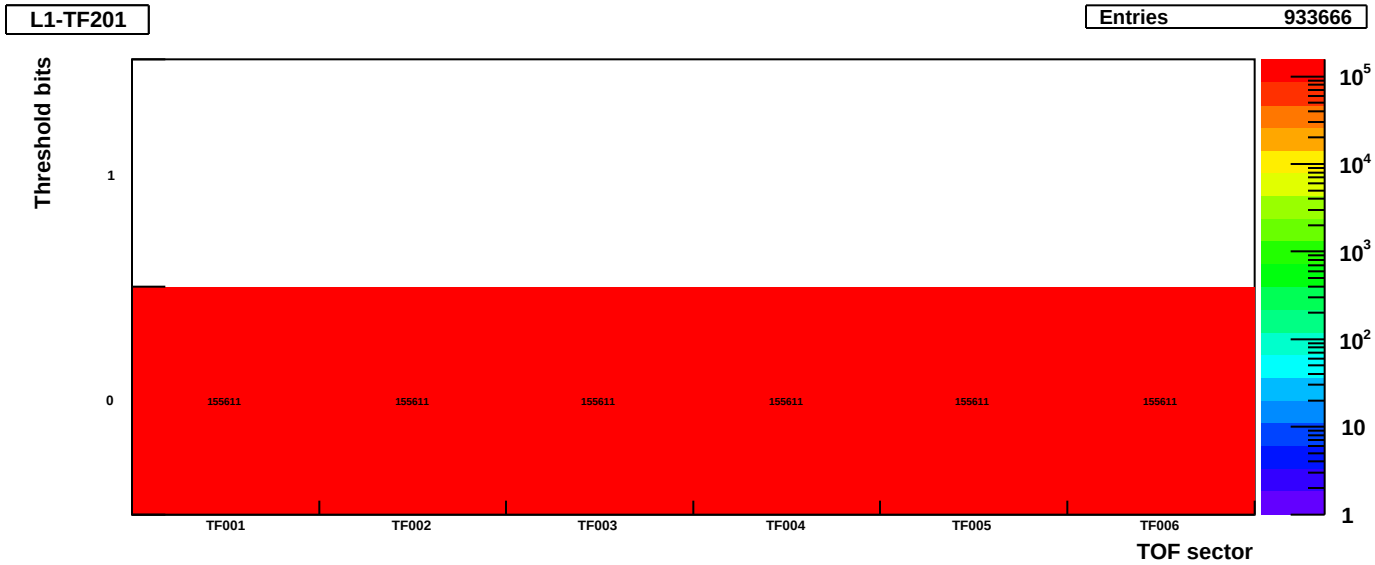

MIX-TF004
Entries 6535662
TOF MULT
TOF tray

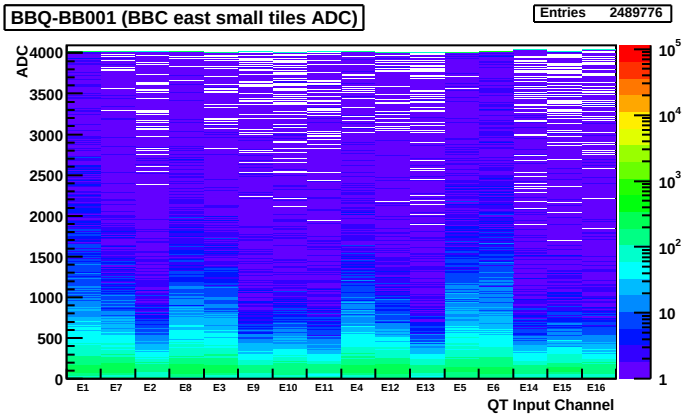

MIX-TF005
Entries 6535662
TOF MULT
TOF tray

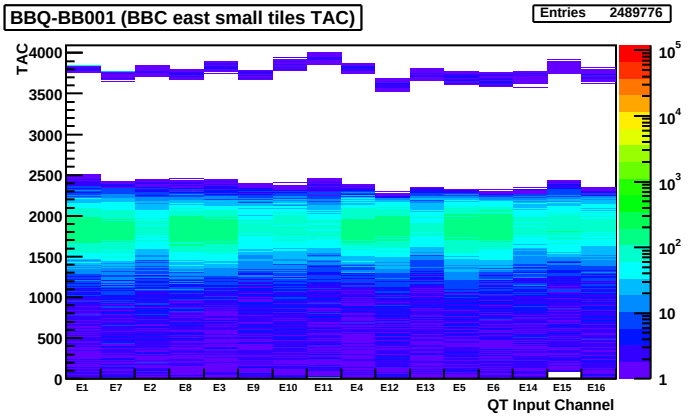

MIX-TF006
Entries 6535662
TOF MULT
TOF tray

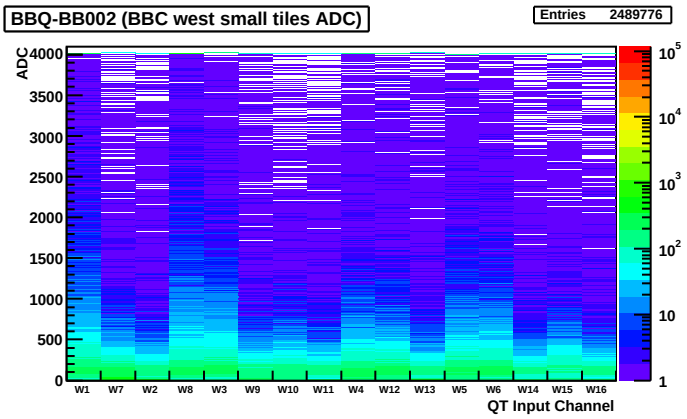

MIX-TF101
Entries 933666
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

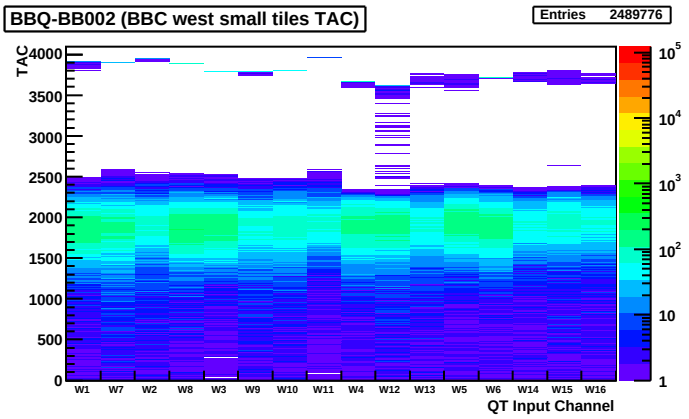

L1-TF201
Entries 155611
10^5
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

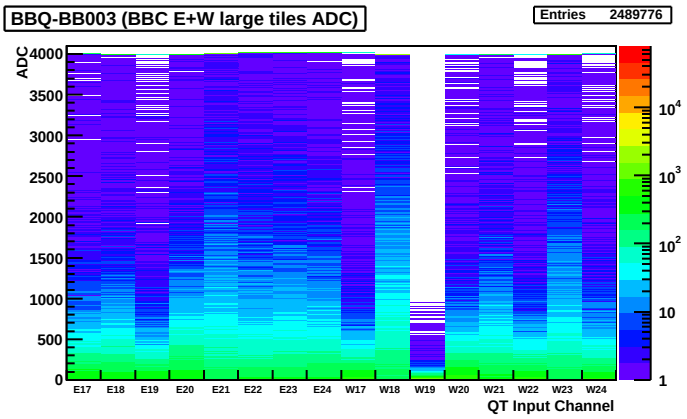

L1-TF201
Entries 933666
Threshold bits
1
0
155611
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3
10^2
10
1

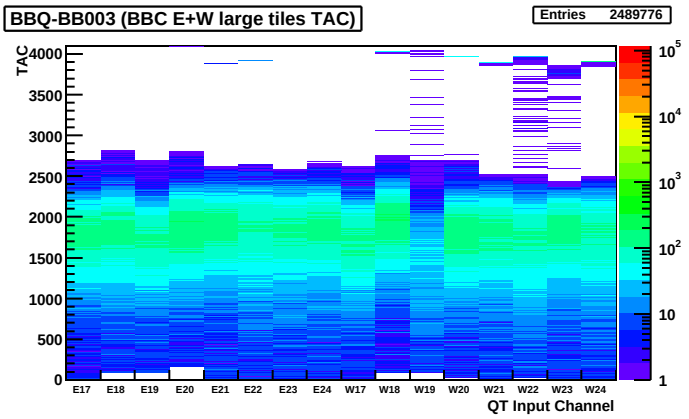

BBQ-BB001 (BBC east small tiles ADC)
Entries 2489776
ADC
QT Input Channel

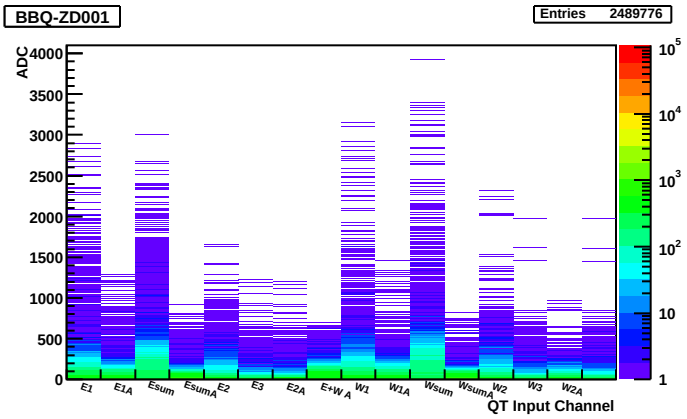

BBQ-BB001 (BBC east small tiles TAC)
Entries 2489776
TAC
QT Input Channel

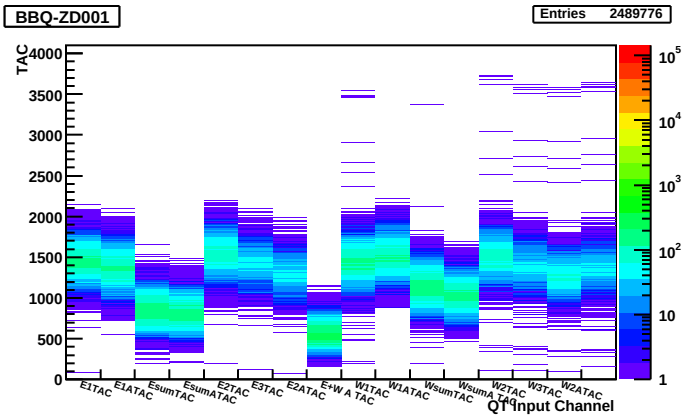

BBQ-BB002 (BBC west small tiles ADC)
Entries 2489776
ADC
QT Input Channel

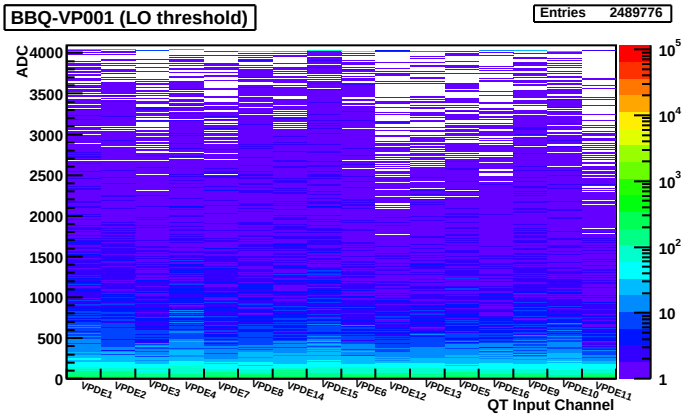

BBQ-BB002 (BBC west small tiles TAC)
Entries 2489776
TAC
QT Input Channel

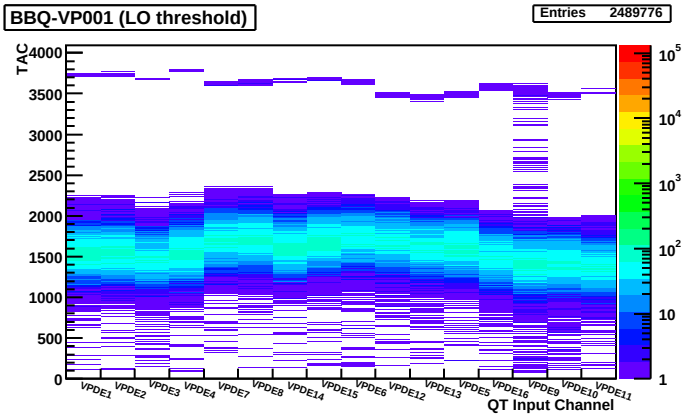

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2489776
ADC
QT Input Channel

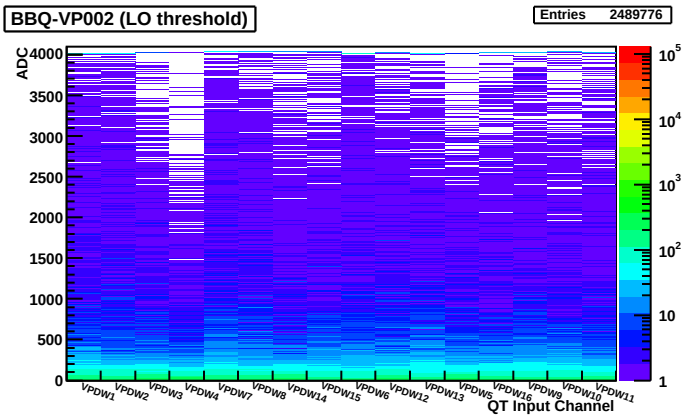

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2489776
TAC
QT Input Channel

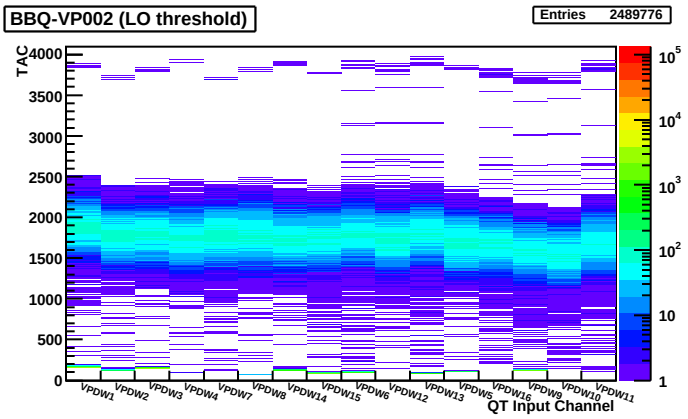

BBQ-ZD001
Entries 2489776
ADC
QT Input Channel

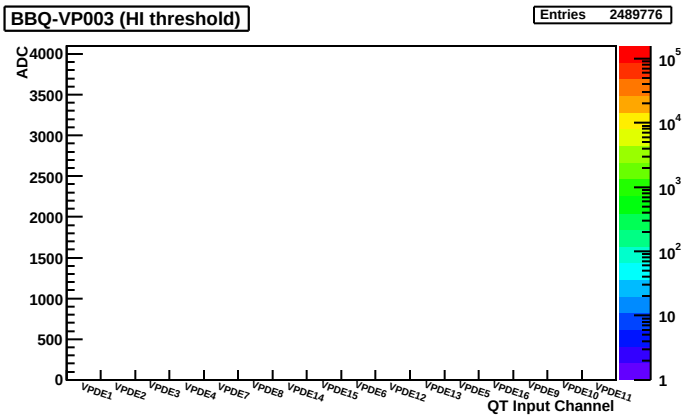

BBQ-ZD001
Entries 2489776
TAC
QT Input Channel

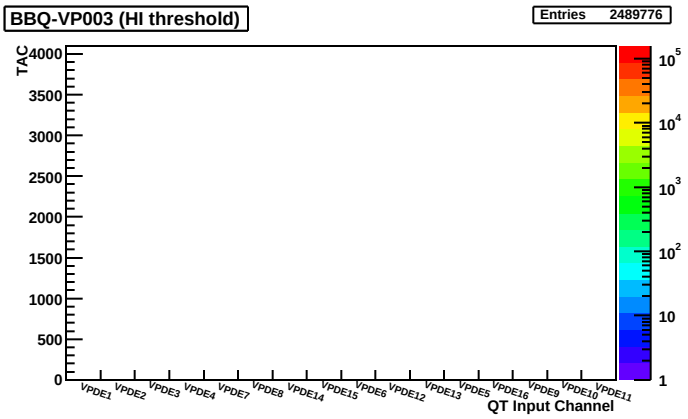

BBQ-VP001 (LO threshold)
Entries 2489776
ADC
QT Input Channel

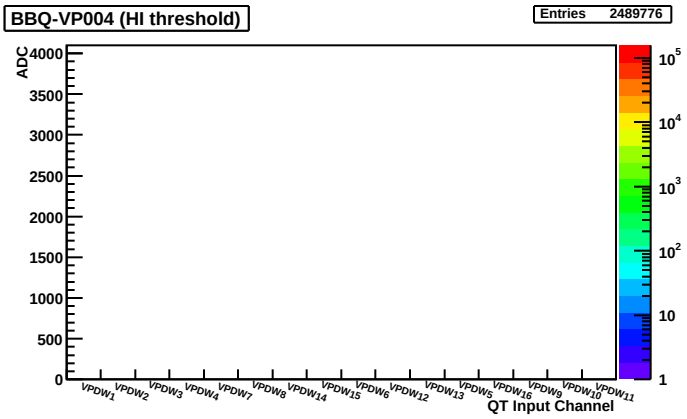

BBQ-VP001 (LO threshold)
Entries 2489776
TAC
QT Input Channel

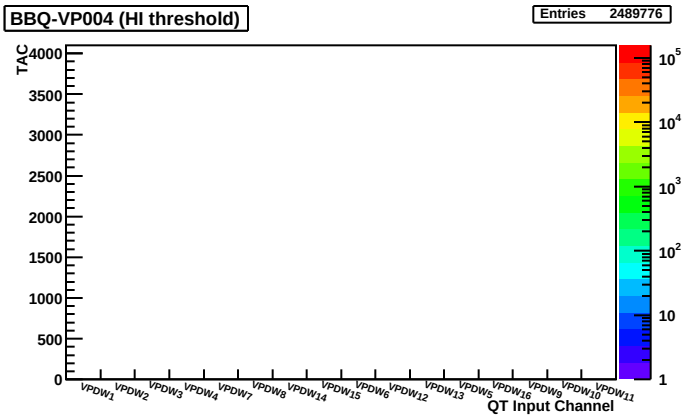

BBQ-VP002 (LO threshold)
Entries 2489776
ADC
QT Input Channel

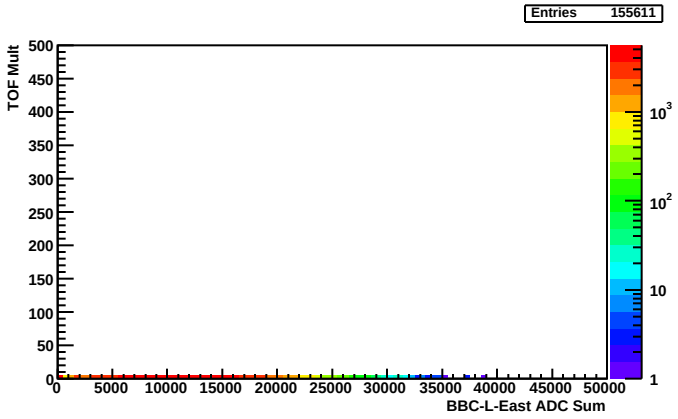

BBQ-VP002 (LO threshold)
Entries 2489776
TAC
QT Input Channel

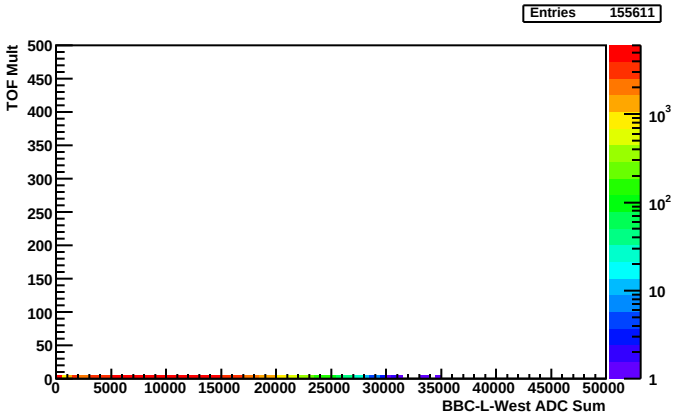

BBQ-VP003 (HI threshold)
Entries 2489776
ADC
QT Input Channel

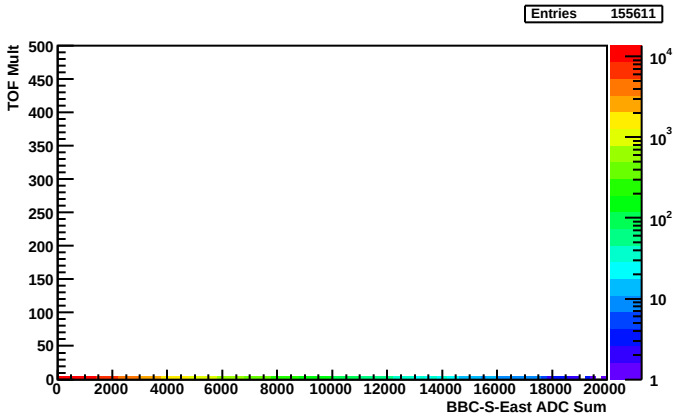

BBQ-VP003 (HI threshold)
Entries 2489776
TAC
QT Input Channel

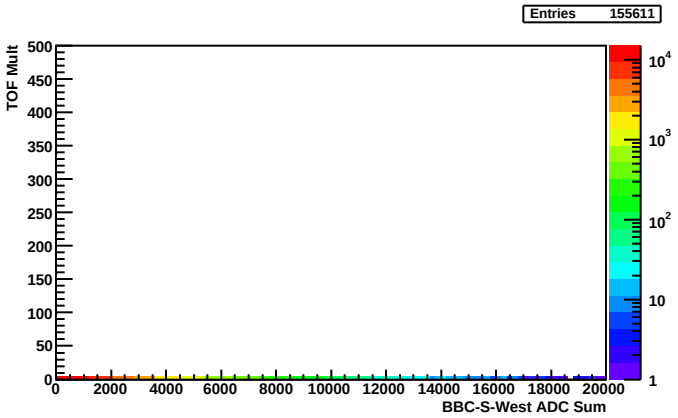

BBQ-VP004 (HI threshold)
Entries 2489776
ADC
QT Input Channel

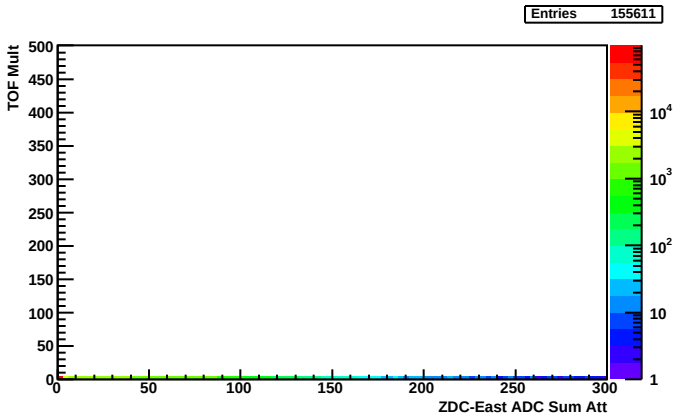

BBQ-VP004 (HI threshold)
Entries 2489776
TAC
QT Input Channel

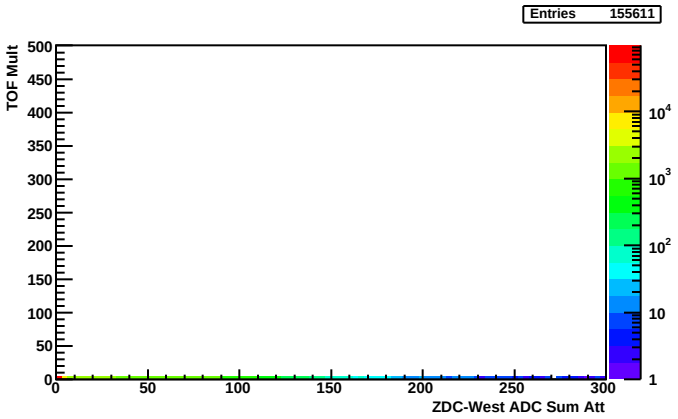

Entries 155611
TOF Mult
BBC-L-East ADC Sum

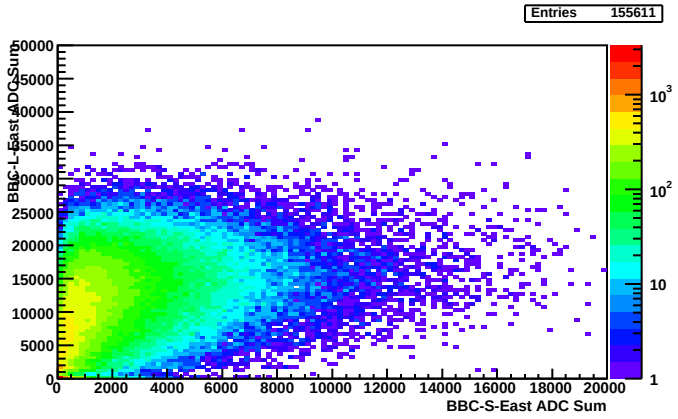

Entries 155611
TOF Mult
BBC-L-West ADC Sum

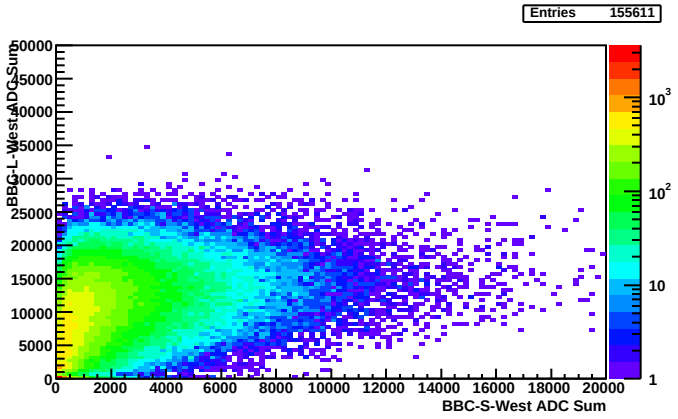

Entries 155611
TOF Mult
BBC-S-East ADC Sum

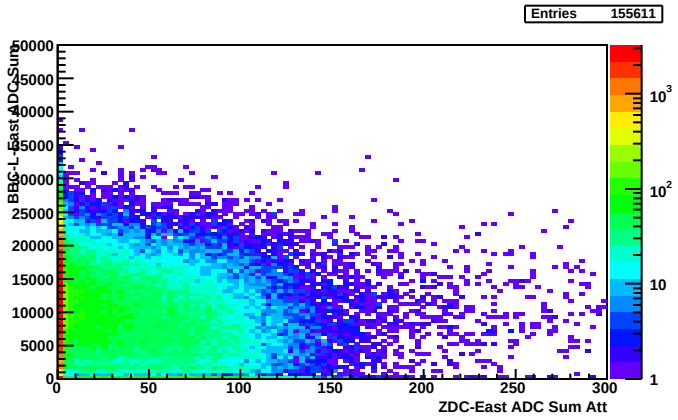

Entries 155611
TOF Mult
BBC-S-West ADC Sum

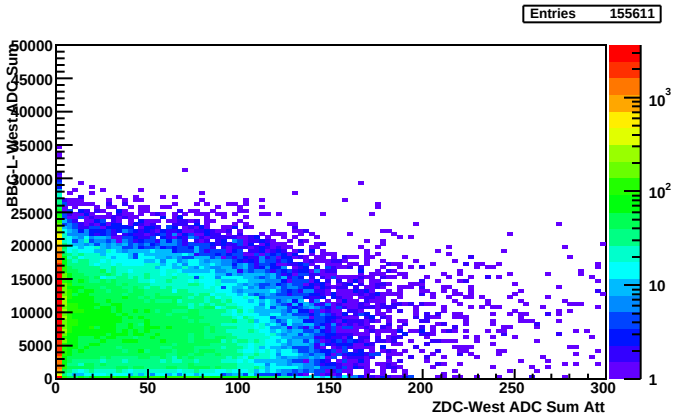

Entries 155611
TOF Mult
ZDC-East ADC Sum Att

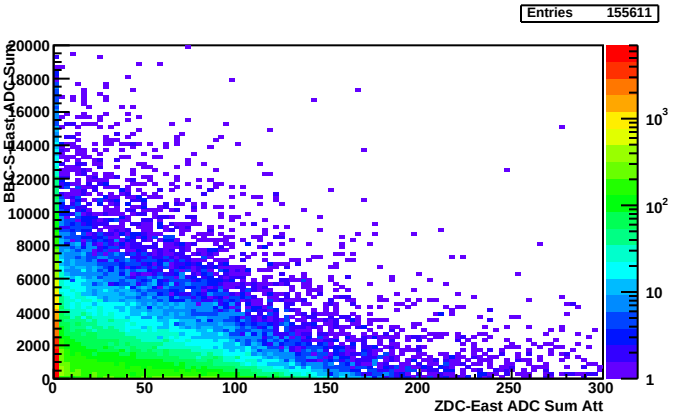

Entries 155611
TOF Mult
ZDC-West ADC Sum Att

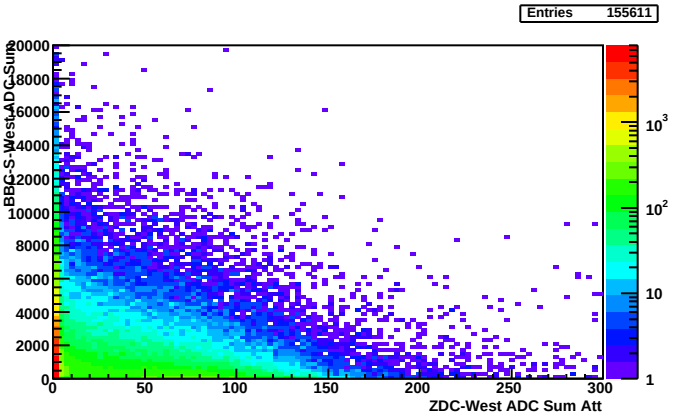

Entries 155611
BBC-L-East ADC Sum
BBC-S-East ADC Sum

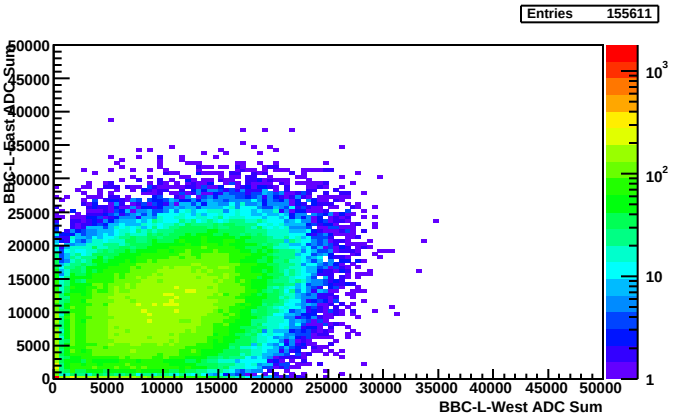

Entries 155611
BBC-L-West ADC Sum
BBC-S-West ADC Sum

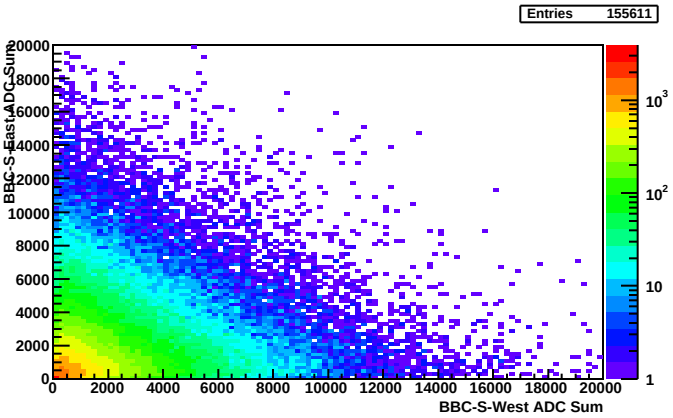

Entries 155611
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

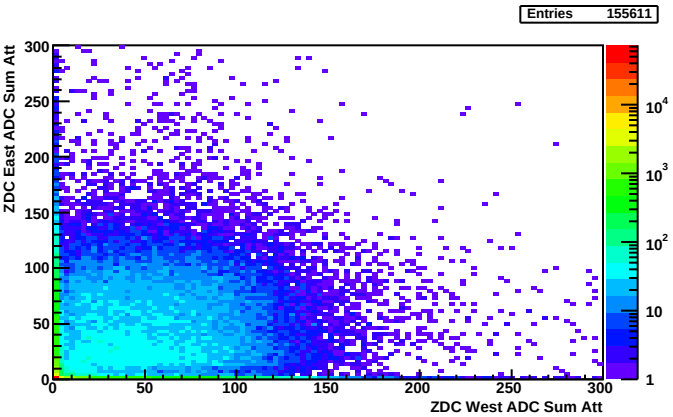

Entries 155611
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

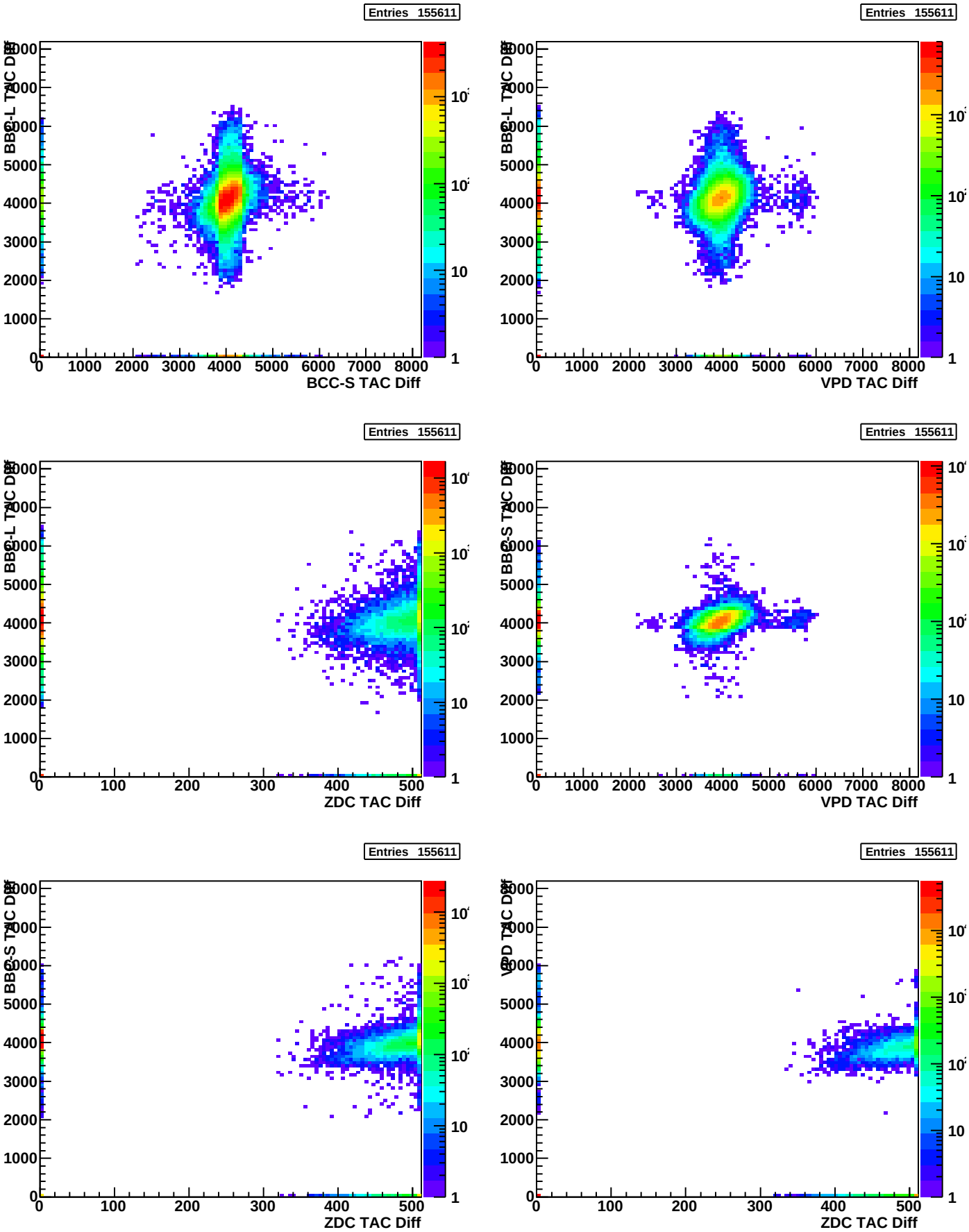

Entries 155611
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

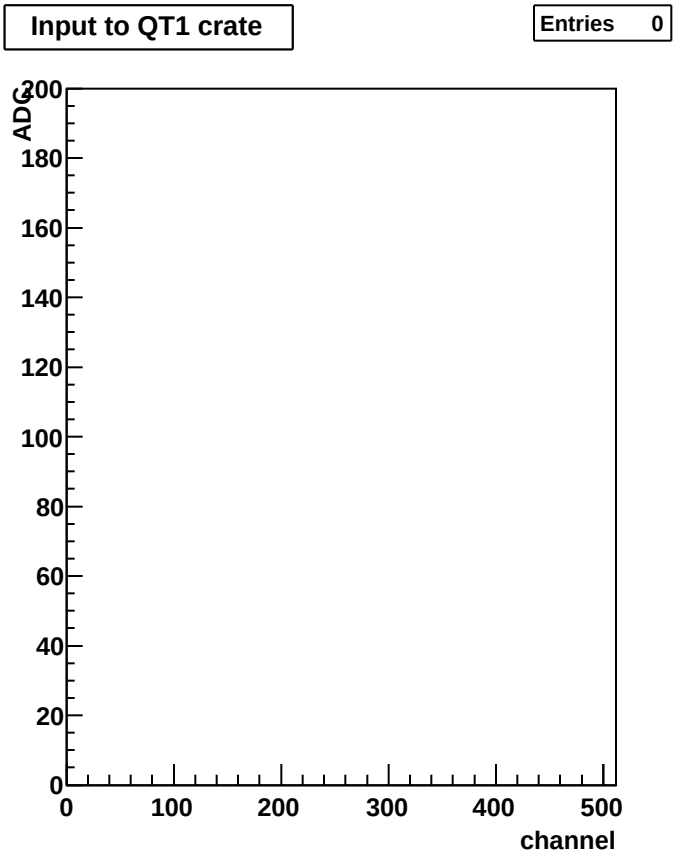

Entries 155611
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

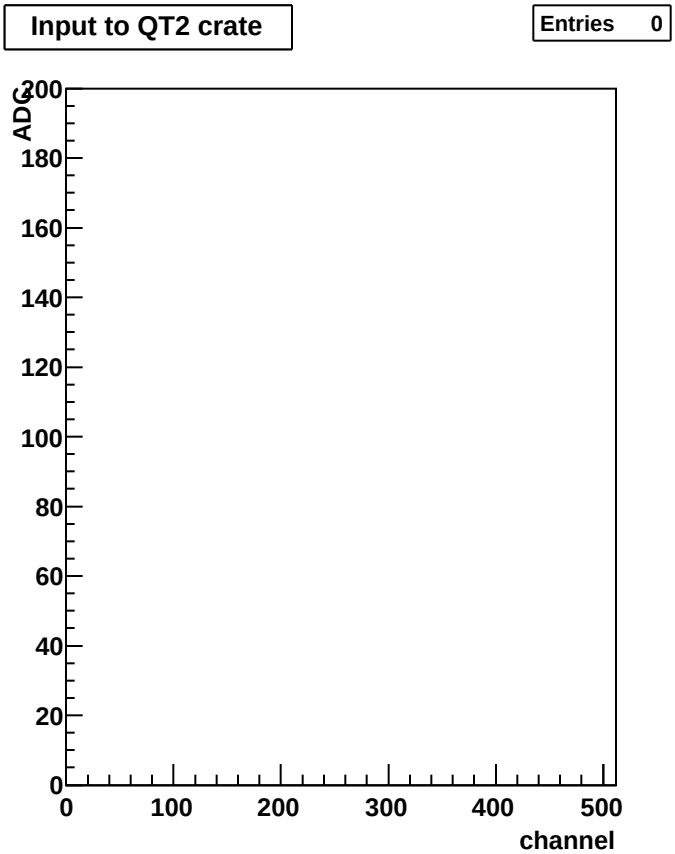

Entries 155611
BBC-L-East ADC Sum
BBC-L-West ADC Sum

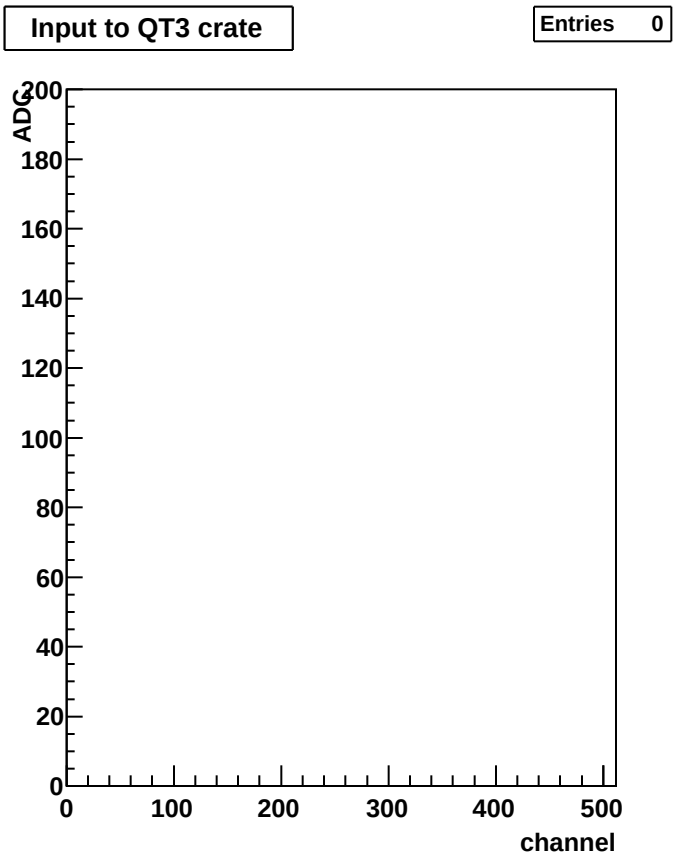

Entries 155611
BBC-S-East ADC Sum
BBC-S-West ADC Sum

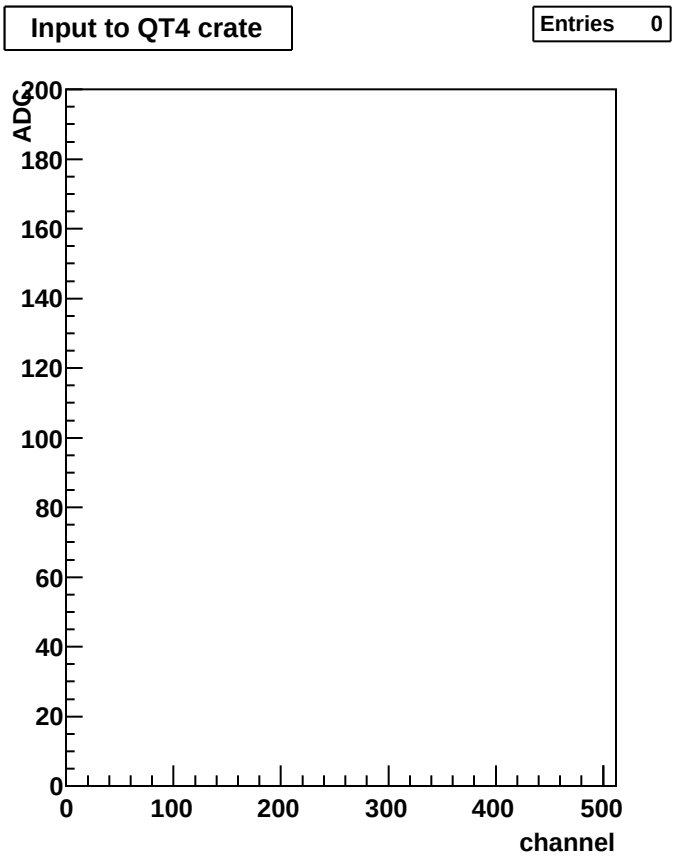

Entries 155611
ZDC East ADC Sum Att
ZDC West ADC Sum Att

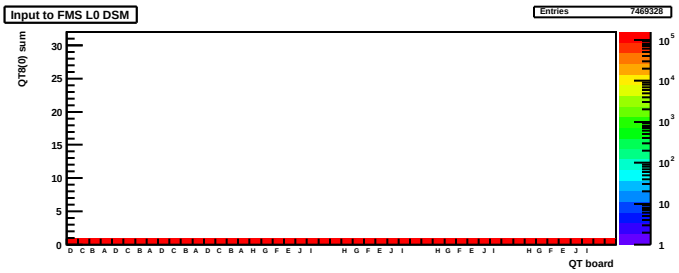

Entries 155611
BBC-L TAC Diff
BCC-S TAC Diff
Entries 155611
BBC-L TAC Diff
VPD TAC Diff
Entries 155611
BBC-L TAC Diff
ZDC TAC Diff
Entries 155611
BBC-S TAC Diff
VPD TAC Diff
Entries 155611
BBC-S TAC Diff
ZDC TAC Diff
Entries 155611
VPD TAC Diff
ZDC TAC Diff

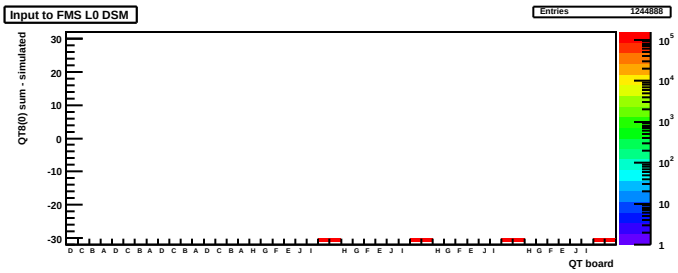

Input to QT1 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

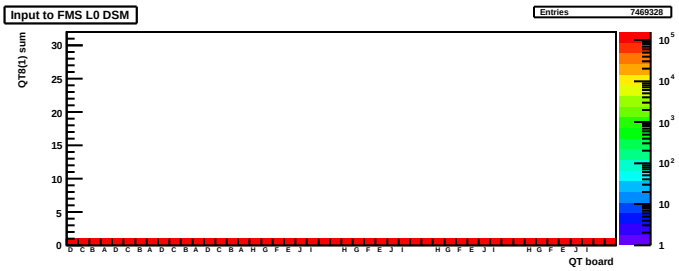

Input to QT2 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

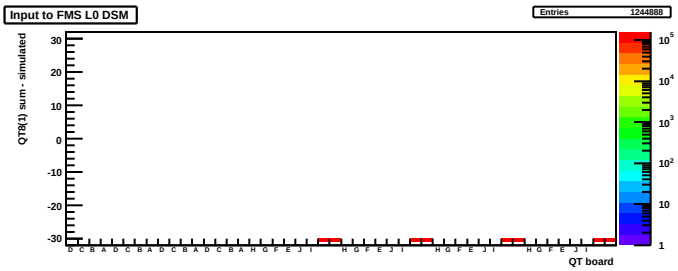

Input to QT3 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

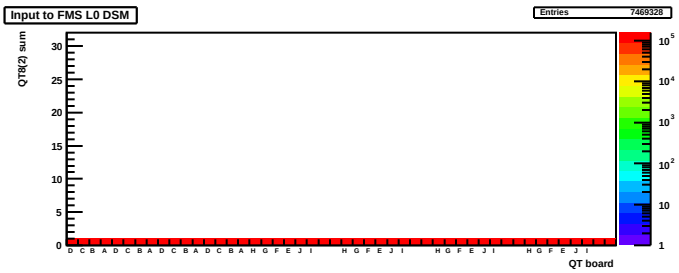

Input to QT4 crate
Entries 0
ADC
200
180
160
140
120
100
80
60
40
20
0
0
100
200
300
400
500
channel

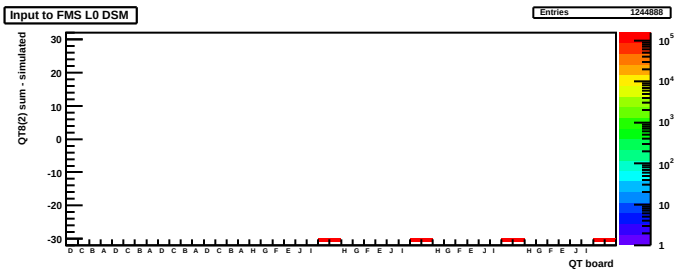

Input to FMS L0 DSM
Entries 7469328
QT8(0) sum
QT board

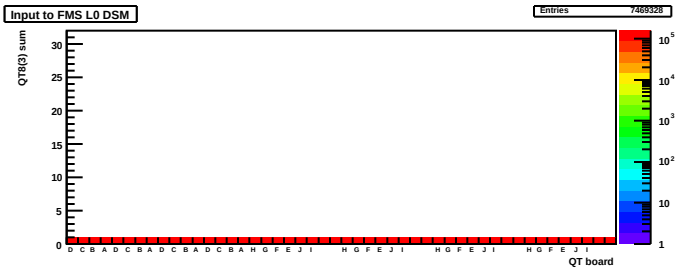

Input to FMS L0 DSM
Entries 1244888
QT8(0) sum - simulated
QT board

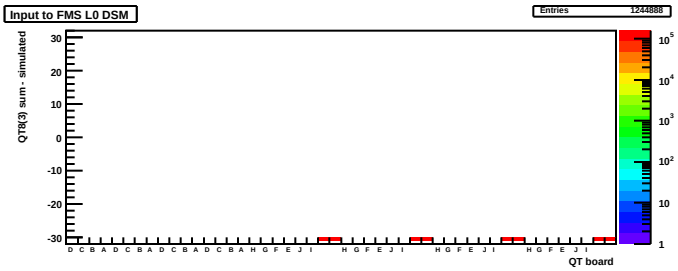

Input to FMS L0 DSM
Entries 7469328
QT8(1) sum
QT board

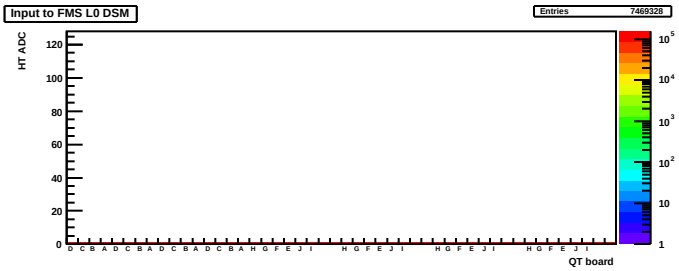

Input to FMS L0 DSM
Entries 1244888
QT8(1) sum - simulated
QT board

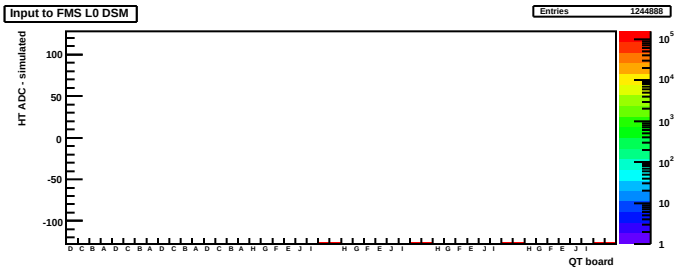

Input to FMS L0 DSM
Entries 7469328
QT8(2) sum
QT board

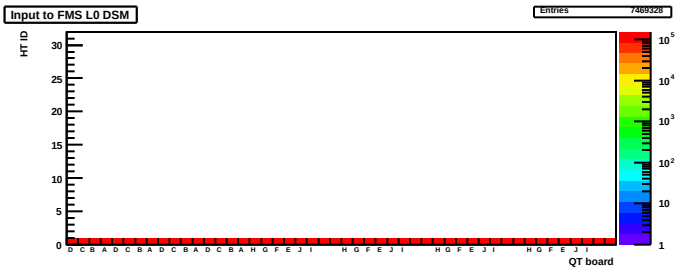

Input to FMS L0 DSM
Entries 1244888
QT8(2) sum - simulated
QT board

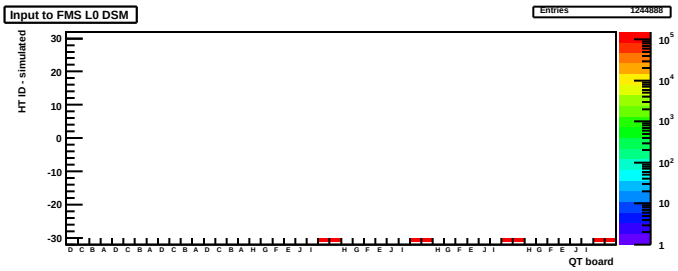

Input to FMS L0 DSM
Entries 7469328
QT8(3) sum
QT board

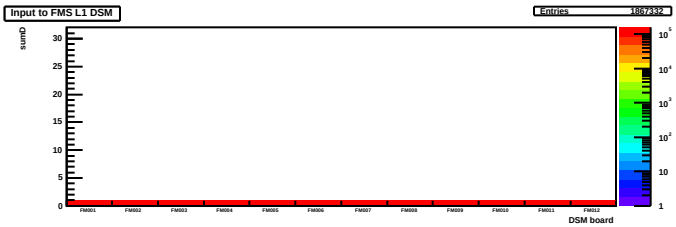

Input to FMS L0 DSM
Entries 1244888
QT8(3) sum - simulated
QT board

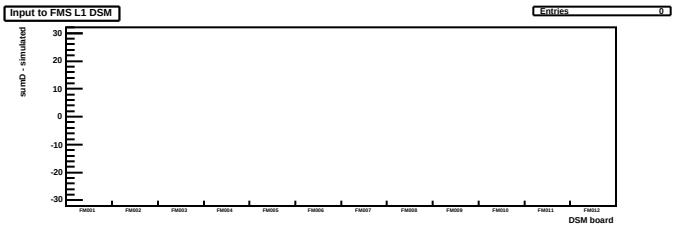

Input to FMS L0 DSM
Entries 7469328
HT ADC
QT board

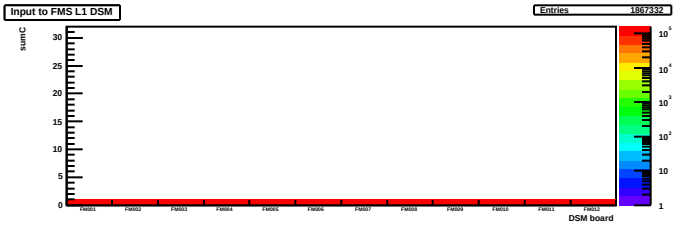

Input to FMS L0 DSM
Entries 1244888
HT ADC - simulated
QT board

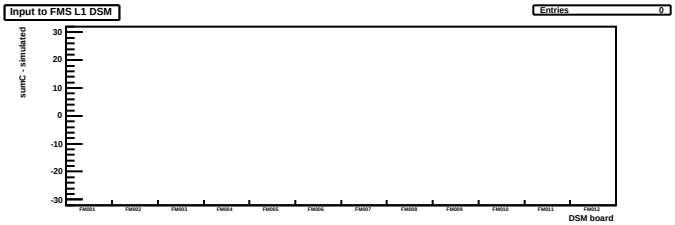

Input to FMS L0 DSM
Entries 7469328
HT ID
QT board

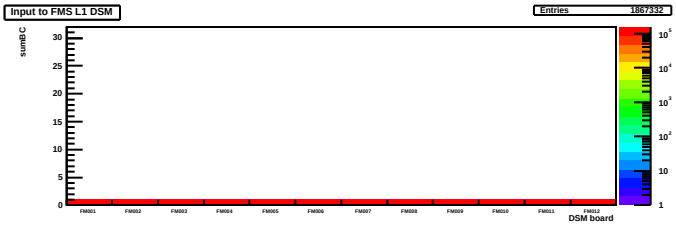

Input to FMS L0 DSM
Entries 1244888
HT ID - simulated
QT board

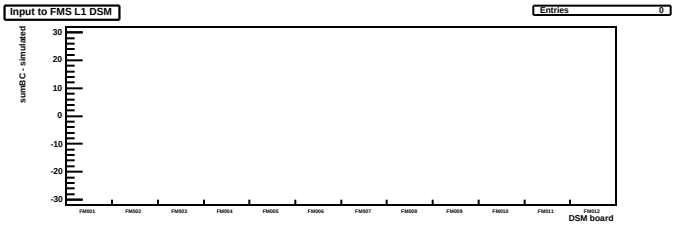

Input to FMS L1 DSM
Entries 1867332
sumD
DSM board

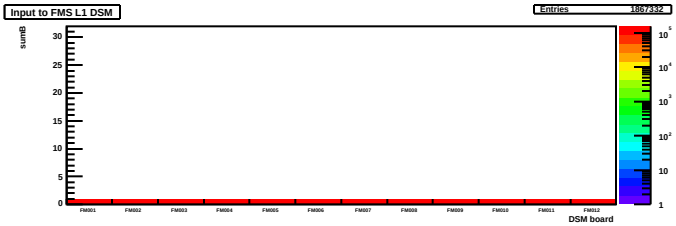

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

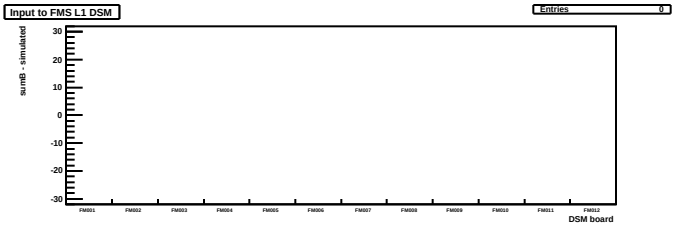

Input to FMS L1 DSM
Entries 1867332
sumC
DSM board

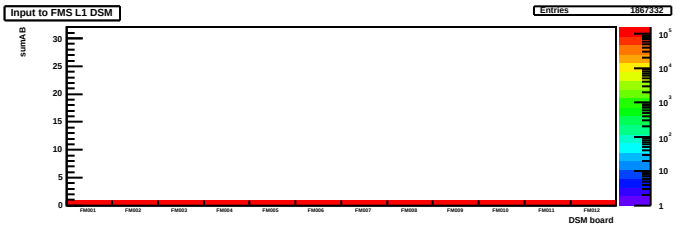

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

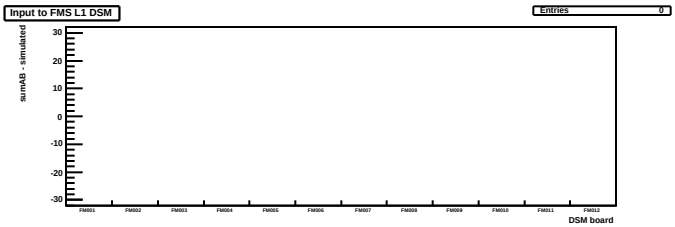

Input to FMS L1 DSM
Entries 1867332
sumBC
DSM board

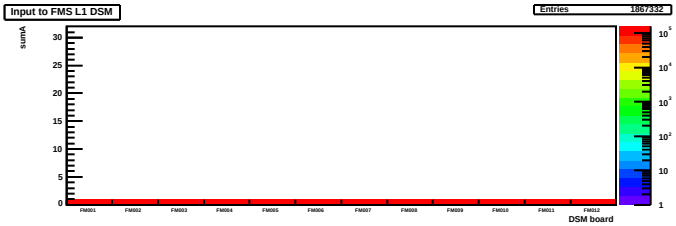

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

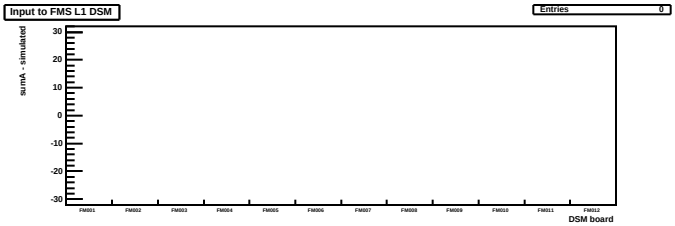

Input to FMS L1 DSM
Entries 1867332
sumB
DSM board

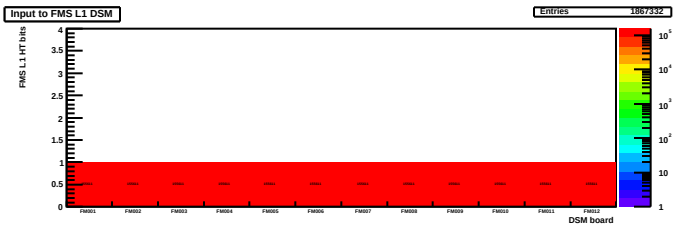

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

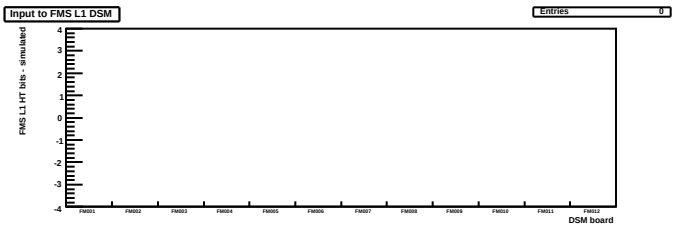

Input to FMS L1 DSM
Entries 1867332
sumAB
DSM board

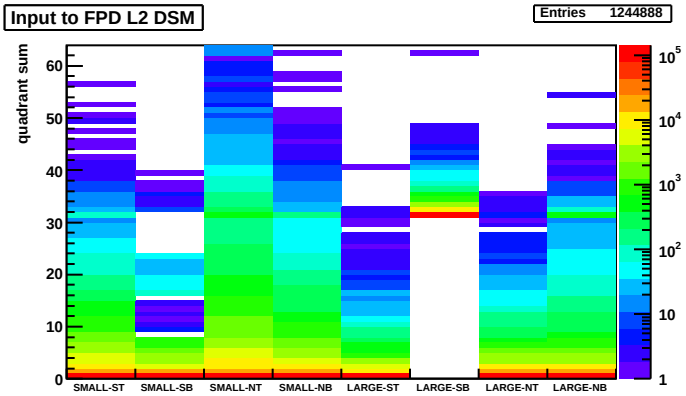

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

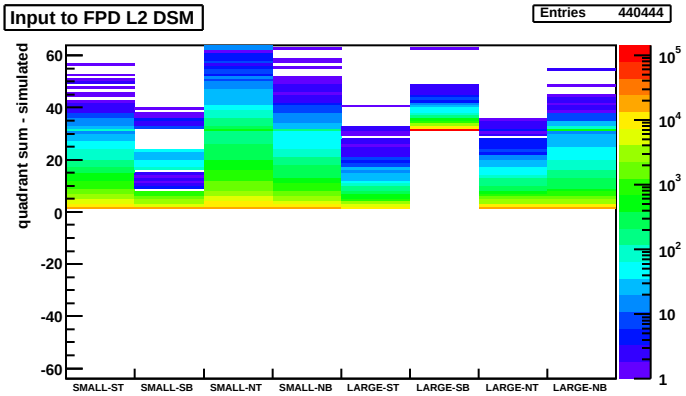

Input to FMS L1 DSM
Entries 1867332
sumA
DSM board

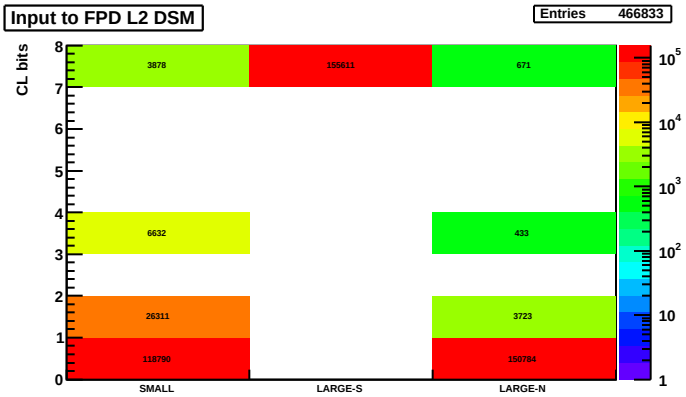

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

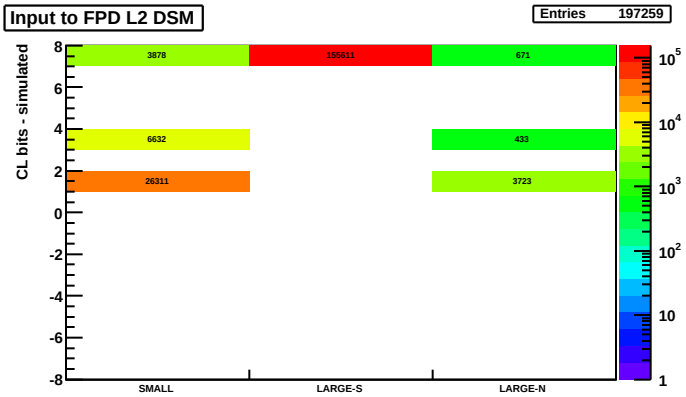

Input to FMS L1 DSM
Entries 1867332
FMS L1 HT bits
DSM board

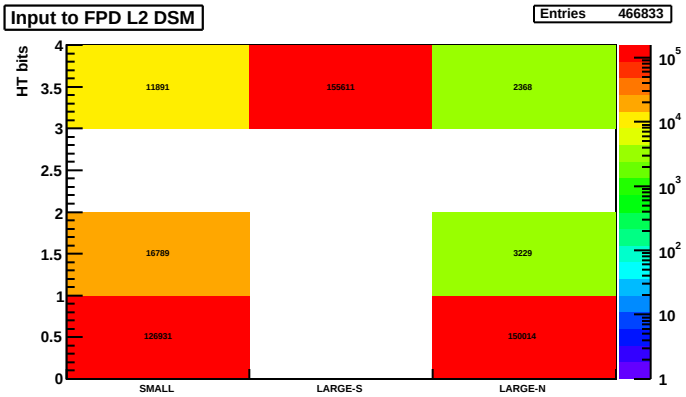

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

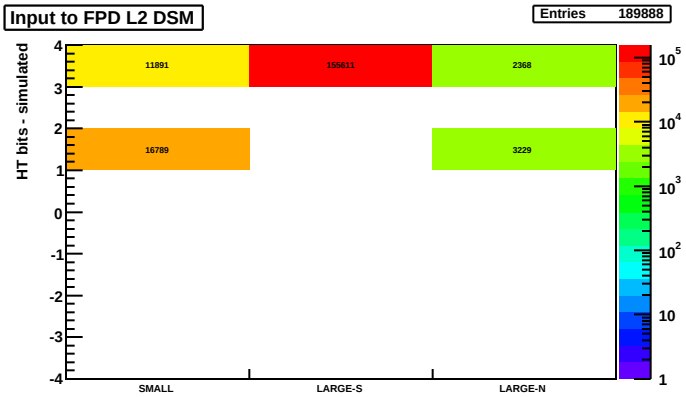

Input to FPD L2 DSM
Entries 1244888
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

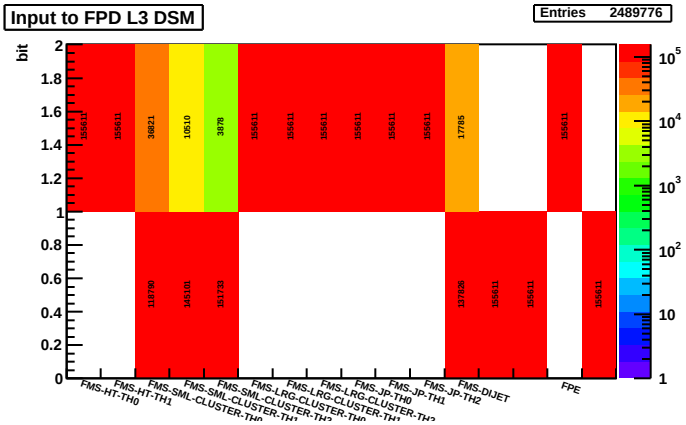

Input to FPD L2 DSM
Entries 440444
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

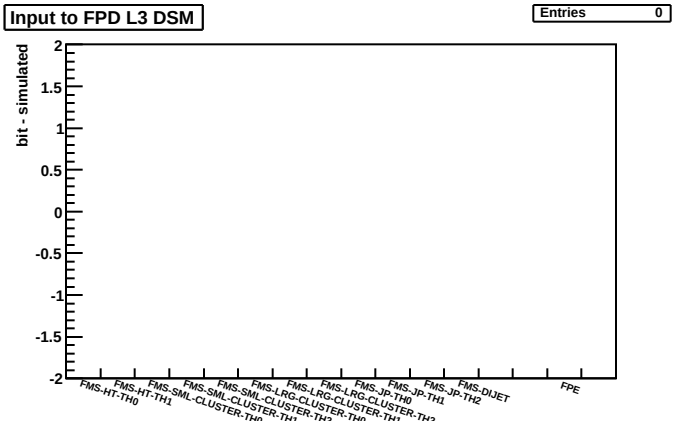

Input to FPD L2 DSM
Entries 466833
CL bits
3878
155611
671
6632
433
26311
3723
118790
150784
SMALL
LARGE-S
LARGE-N

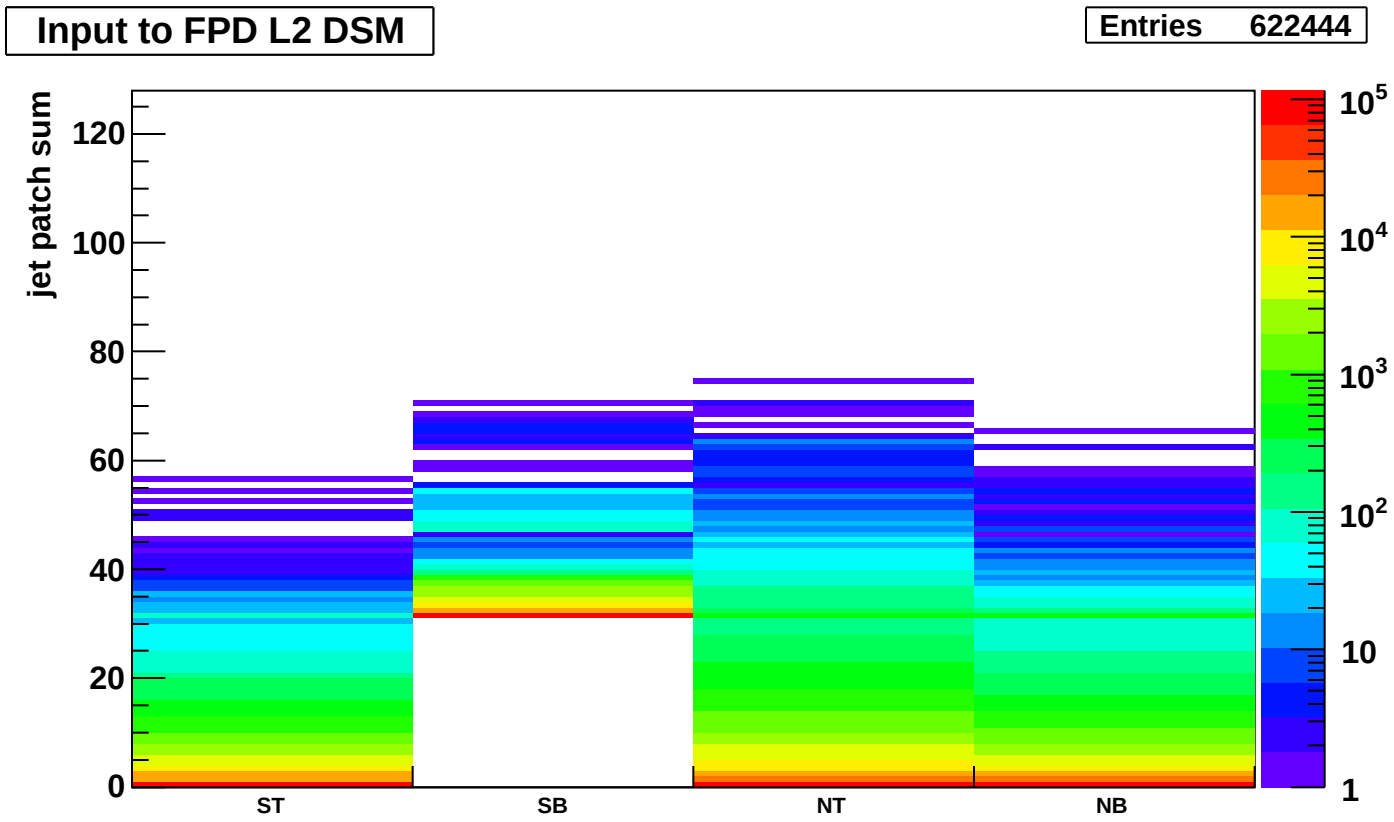

Input to FPD L2 DSM
Entries 197259
CL bits - simulated
3878
155611
671
6632
433
26311
3723
SMALL
LARGE-S
LARGE-N

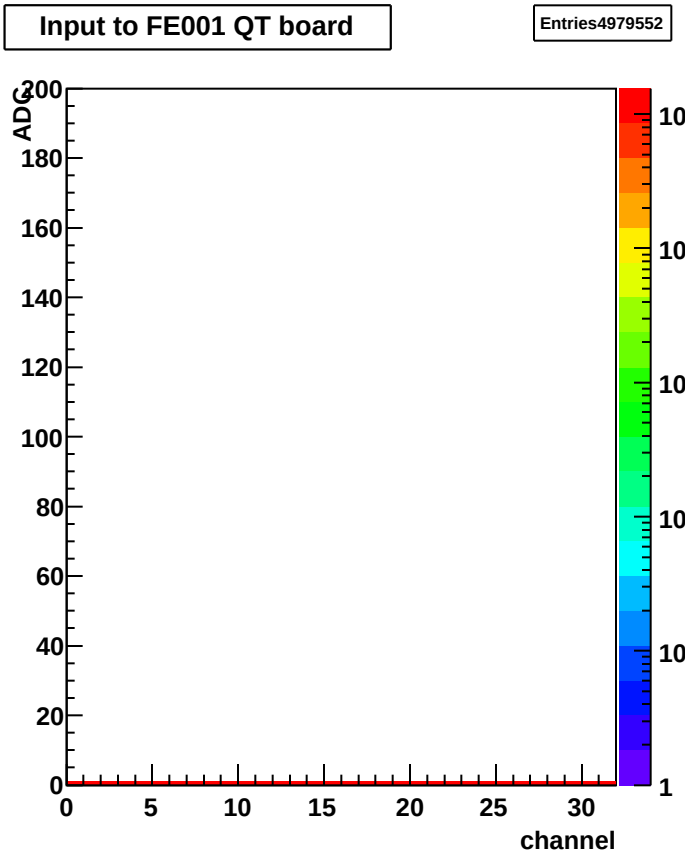

Input to FPD L2 DSM
Entries 466833
HT bits
11891
155611
2368
16789
3229
126931
150014
SMALL
LARGE-S
LARGE-N

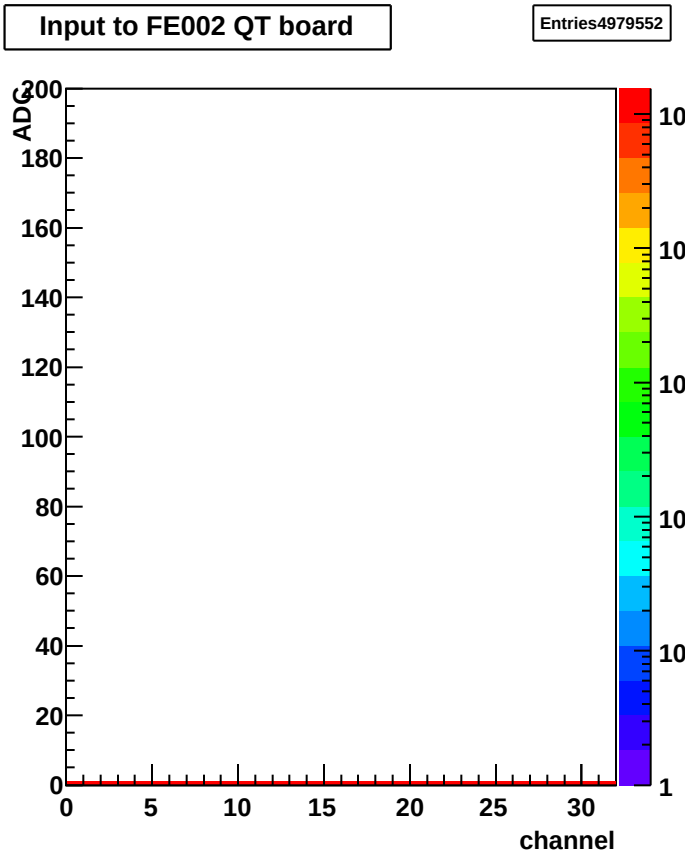

Input to FPD L2 DSM
Entries 189888
HT bits - simulated
11891
155611
2368
16789
3229
SMALL
LARGE-S
LARGE-N

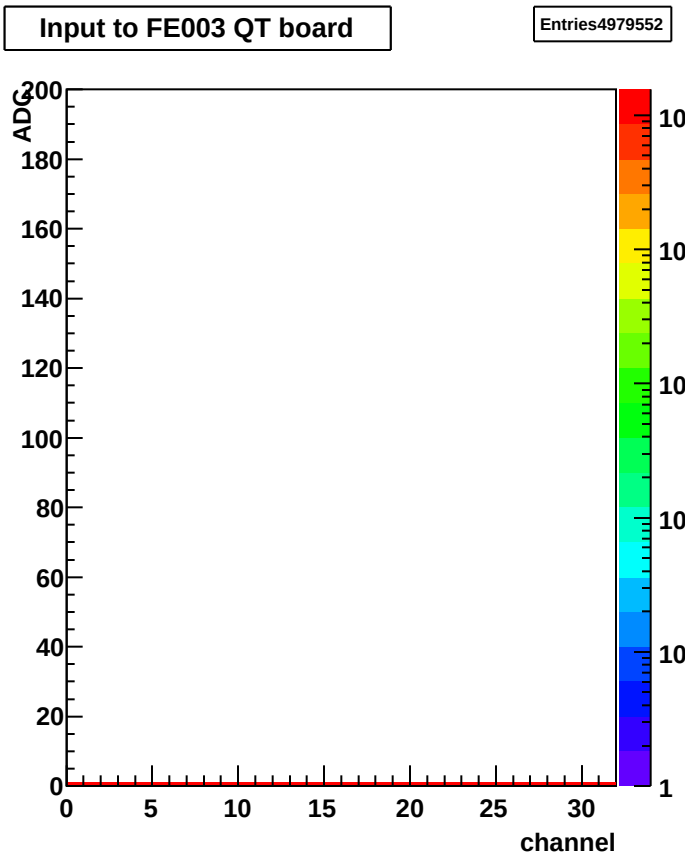

Input to FPD L3 DSM
Entries 2489776
bit
155611
30821
10510
3878
17785
118790
145101
151733
137826
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

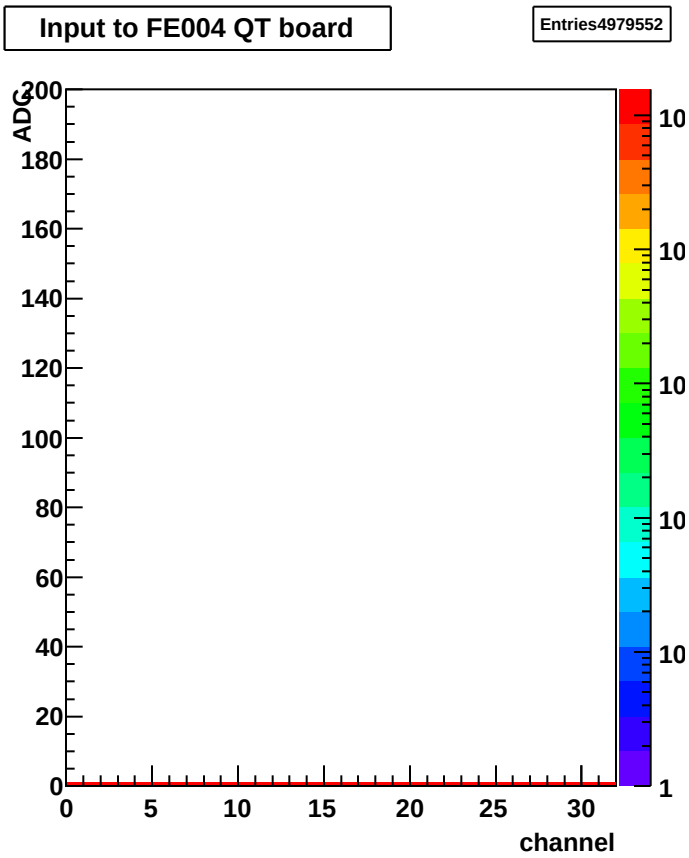

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

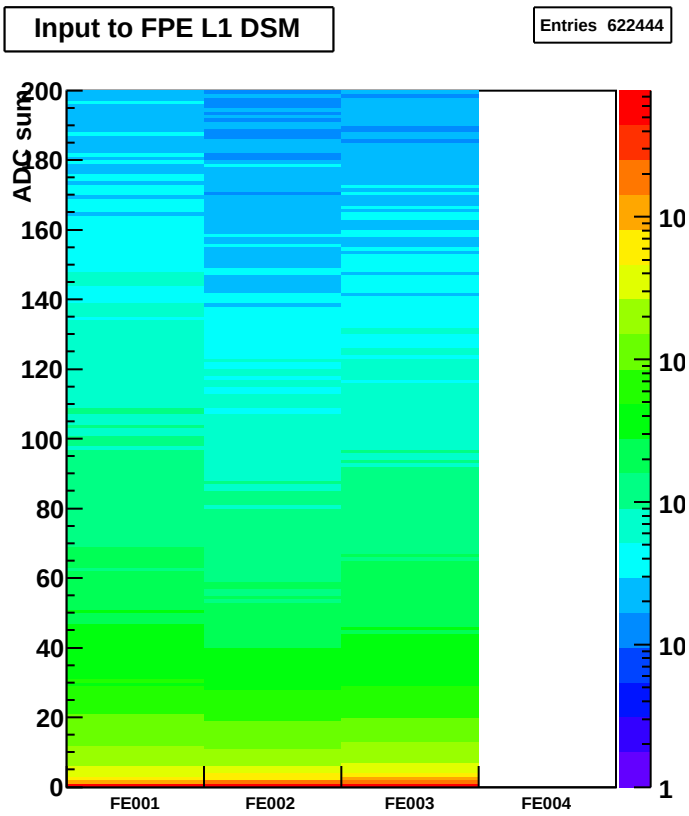

Input to FPD L2 DSM
Entries 622444
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

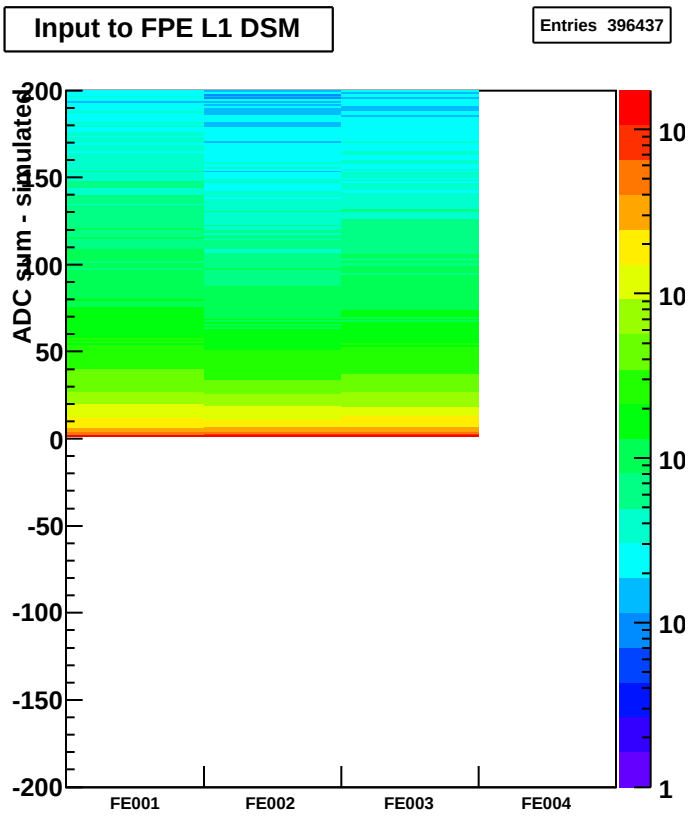

Input to FE001 QT board
Entries4979552
ADC
channel

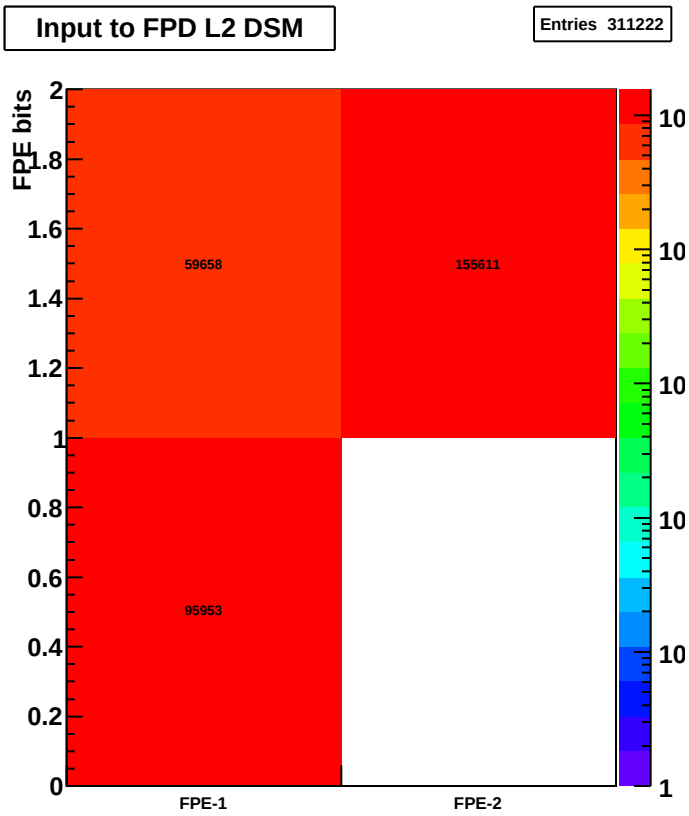

Input to FE002 QT board
Entries4979552
ADC
channel

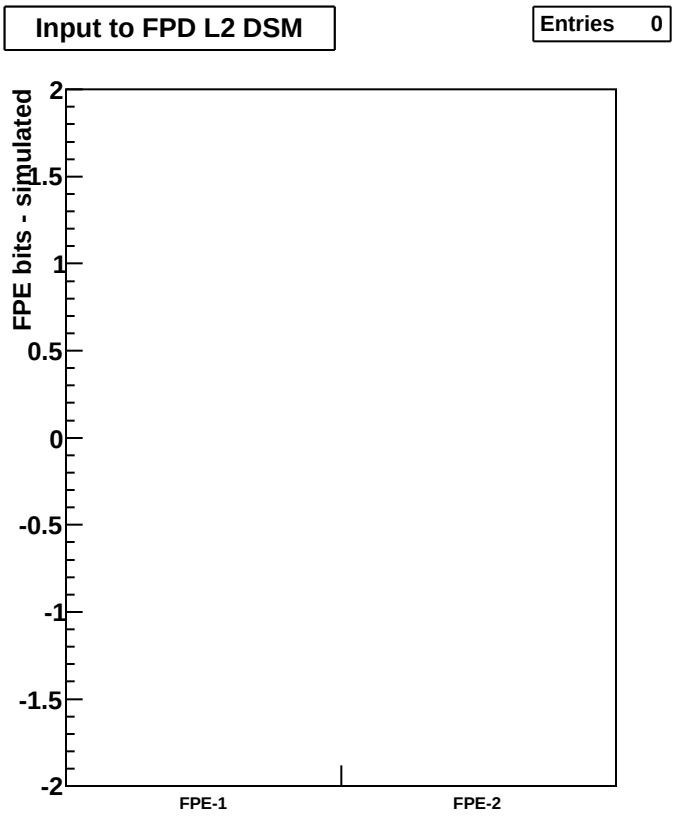

Input to FE003 QT board
Entries4979552
ADC
channel


Input to FE004 QT board
Entries4979552
ADC
channel


Input to FPE L1 DSM
Entries 622444
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 396437
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 311222
FPE bits
59658
155611
95953
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2