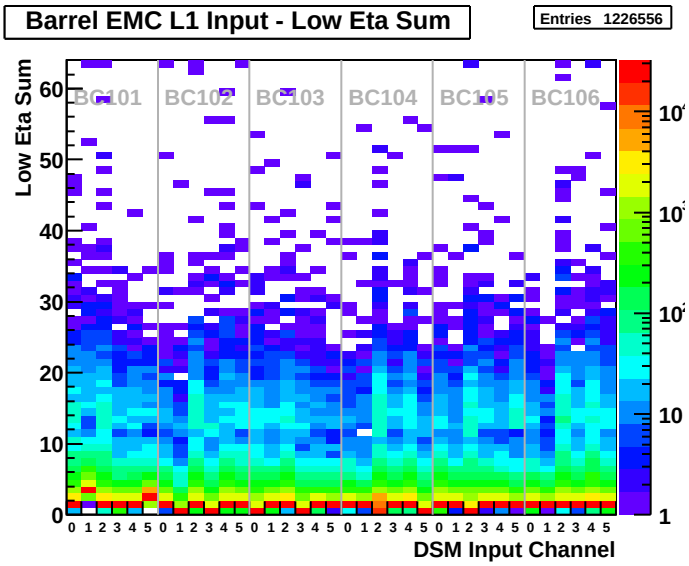
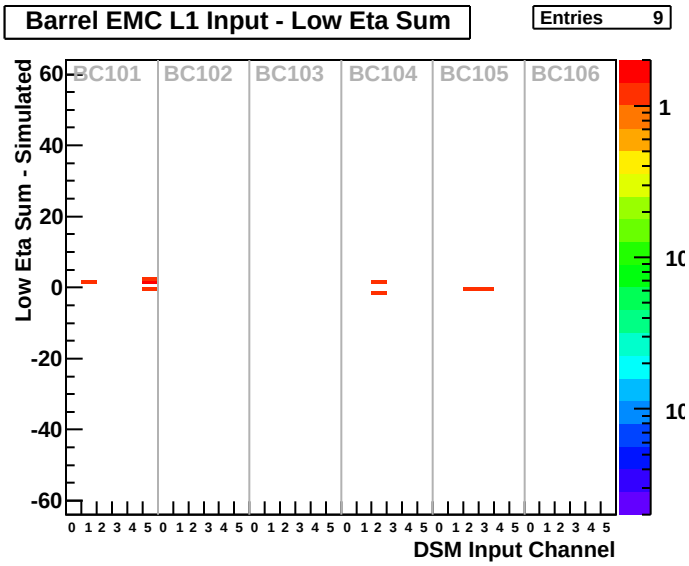
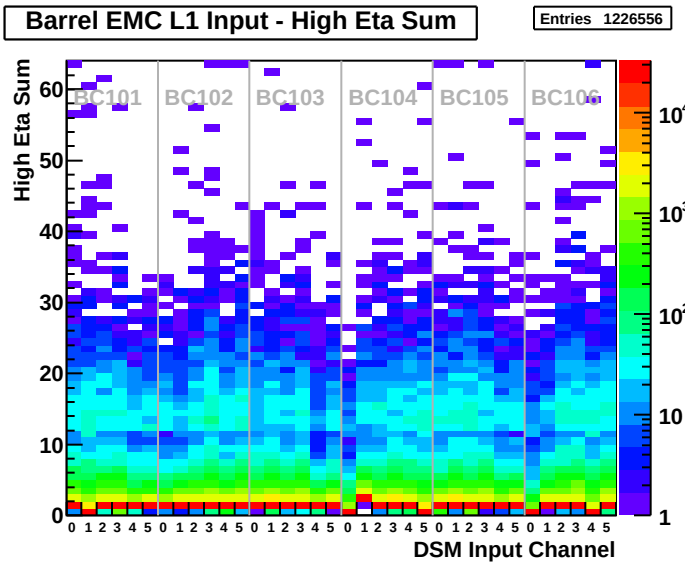
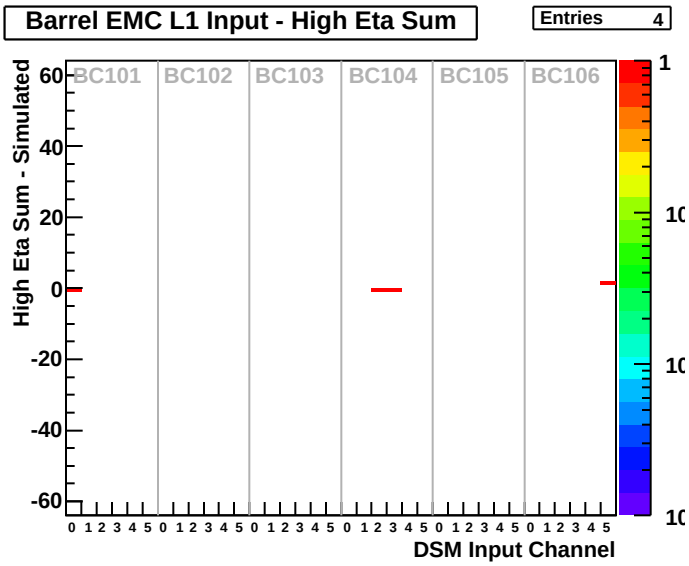
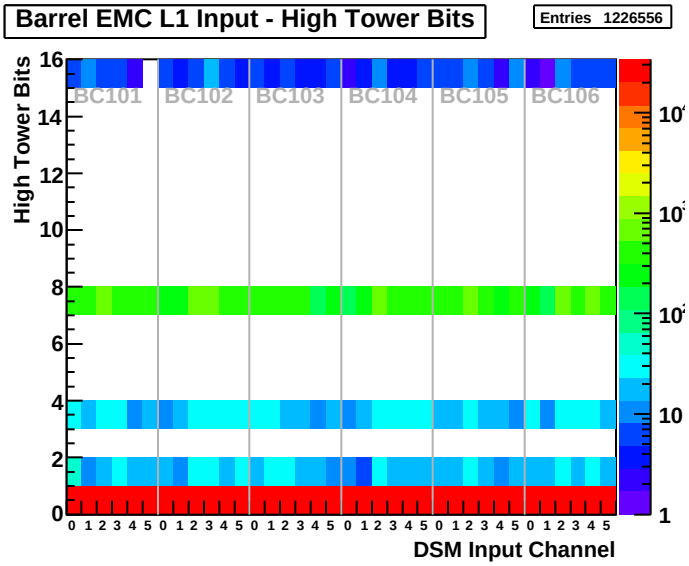
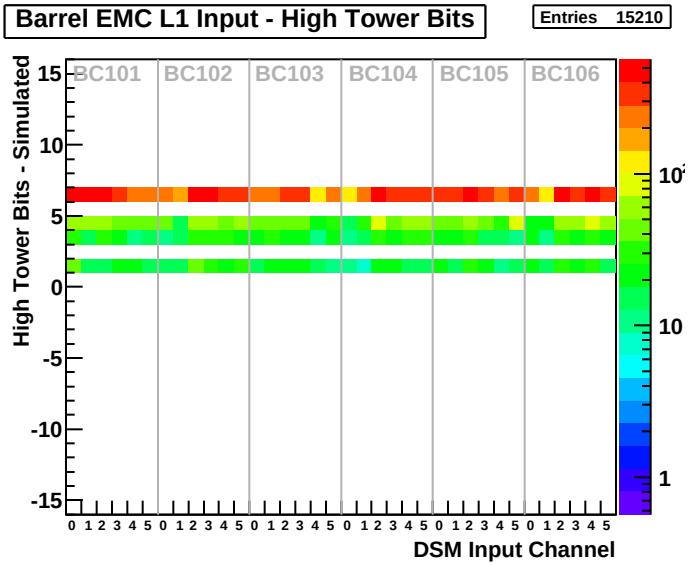

Barrel EMC L1 Input - Low Eta Sum
Entries 1226556
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

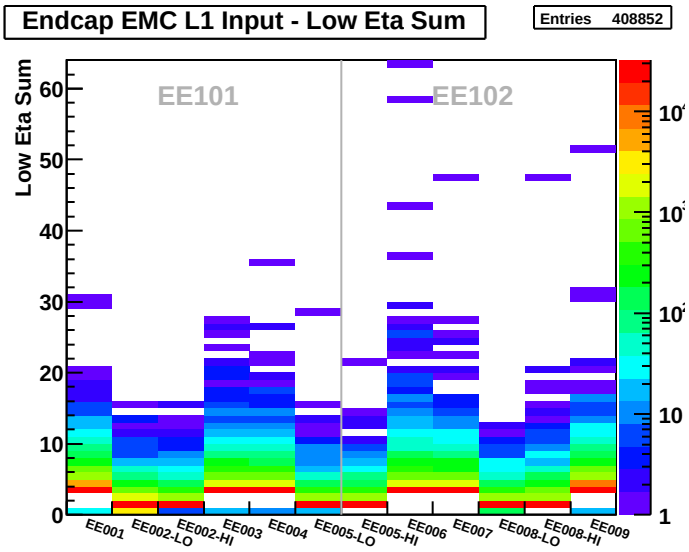

Barrel EMC L1 Input - Low Eta Sum
Entries 9
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

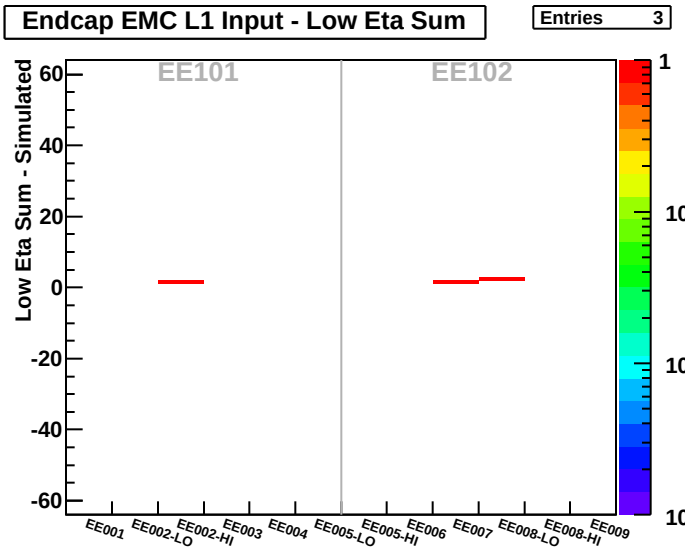

Barrel EMC L1 Input - High Eta Sum
Entries 1226556
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

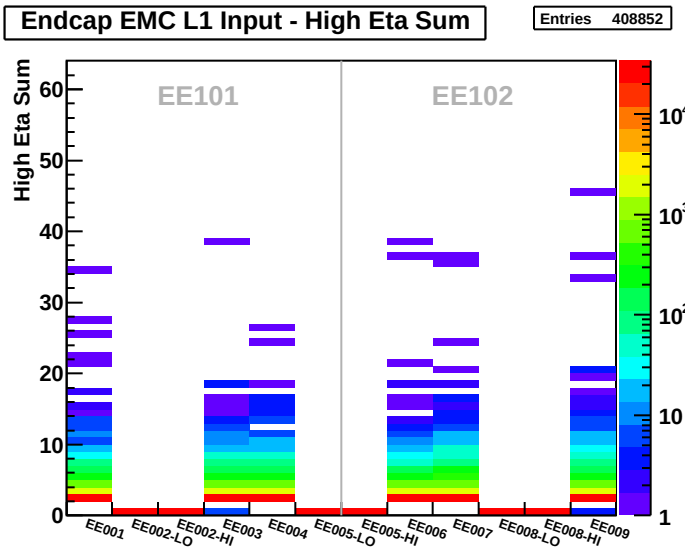

Barrel EMC L1 Input - High Eta Sum
Entries 4
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

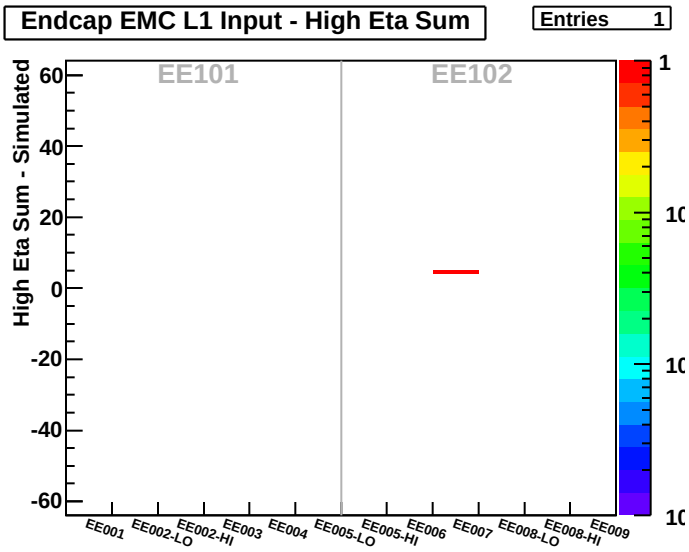

Barrel EMC L1 Input - High Tower Bits
Entries 1226556
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

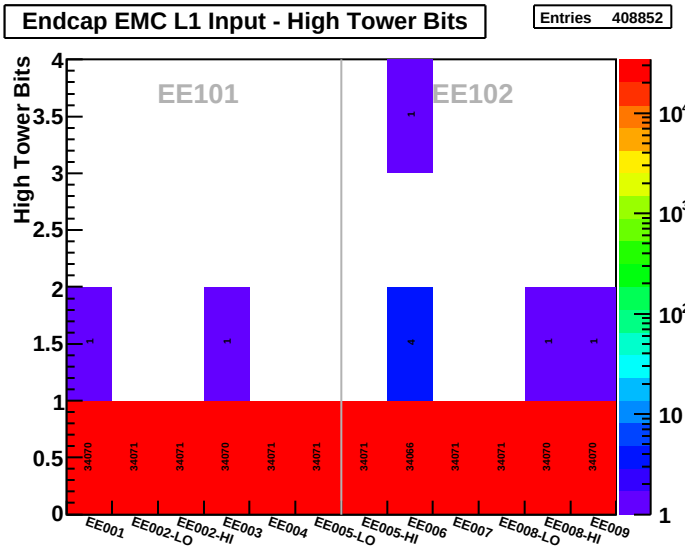

Barrel EMC L1 Input - High Tower Bits
Entries 15210
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

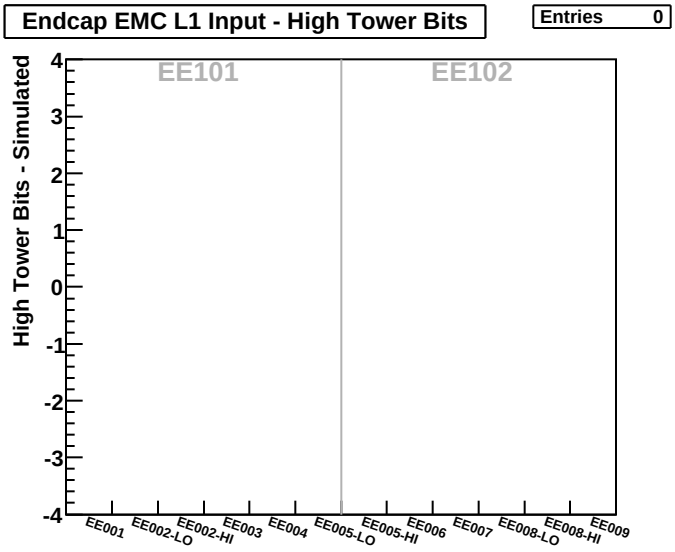

Endcap EMC L1 Input - Low Eta Sum
Entries 408852
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

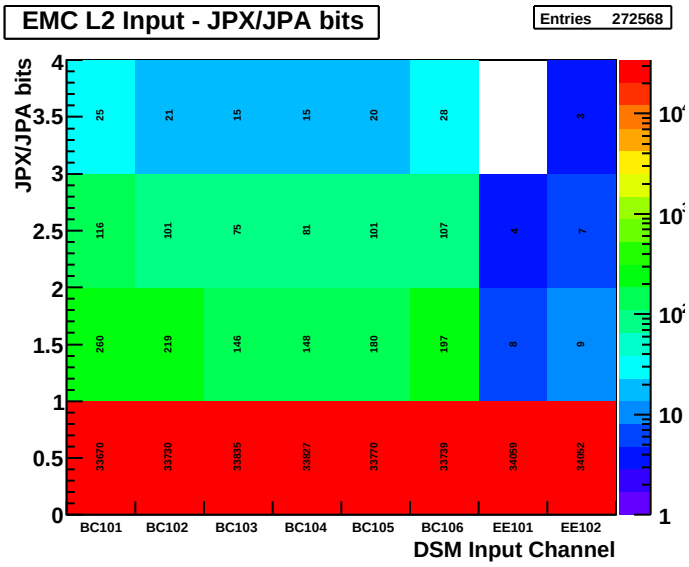

Endcap EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

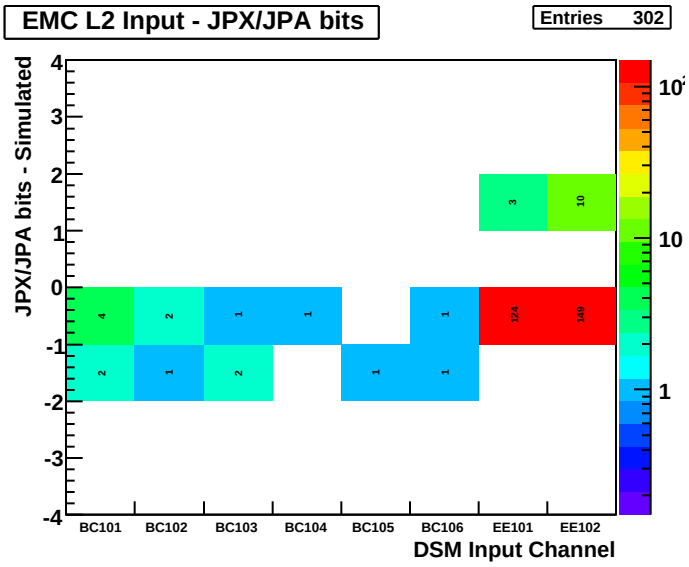

Endcap EMC L1 Input - High Eta Sum
Entries 408852
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

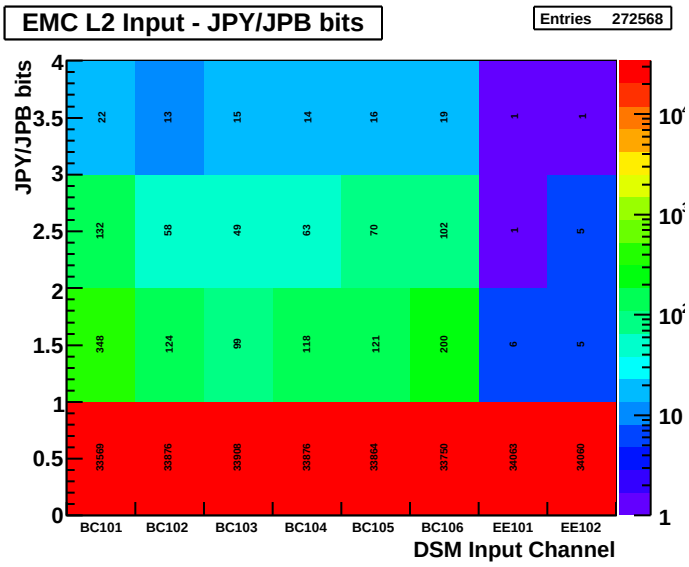

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

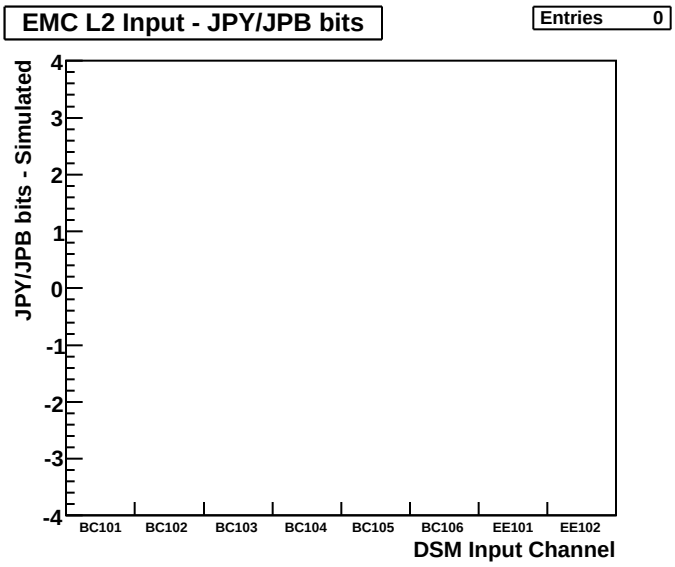

Endcap EMC L1 Input - High Tower Bits
Entries 408852
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

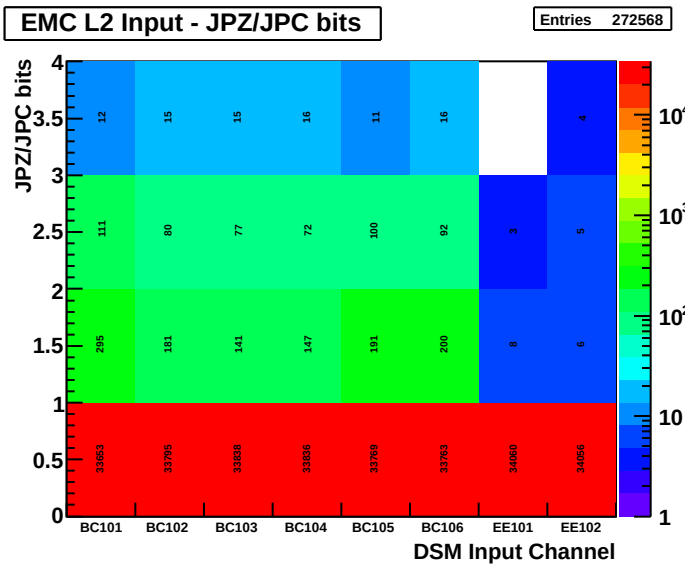

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

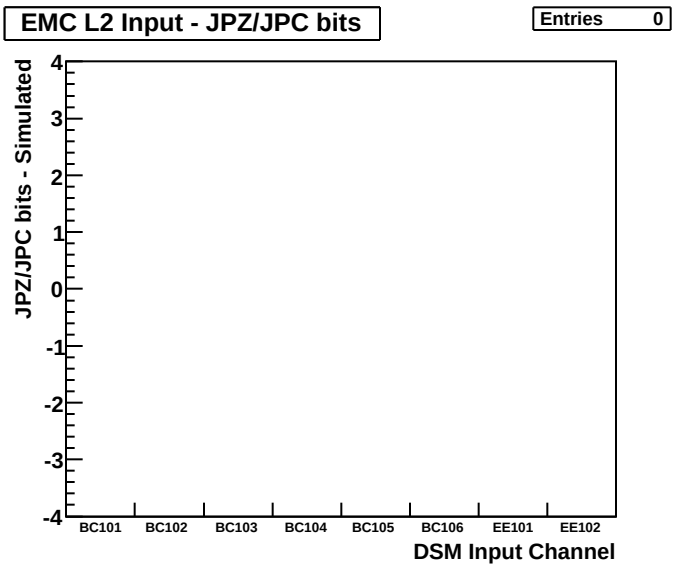

EMC L2 Input - JPX/JPA bits
Entries 272568
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
25 21 15 15 20 28 3
116 101 75 81 101 107 4 7
260 219 146 148 180 197 8 9
33670 33730 33835 33827 33770 33739 34059 34052

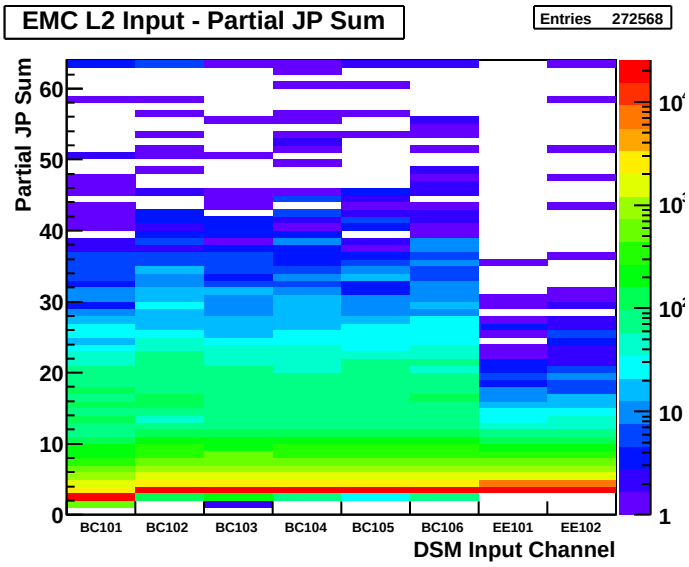

EMC L2 Input - JPX/JPA bits
Entries 302
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 10
4 2 1 1 1 124 149
2 1 2 1 1

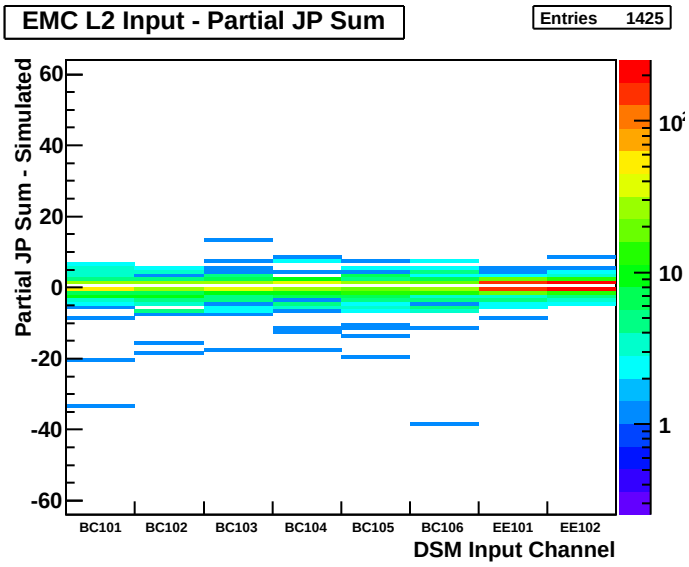

EMC L2 Input - JPY/JPB bits
Entries 272568
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
22 13 15 14 16 19 1 1
132 58 49 63 70 102 1 5
348 124 99 118 121 200 6 5
33569 33876 33908 33876 33864 33750 34063 34060

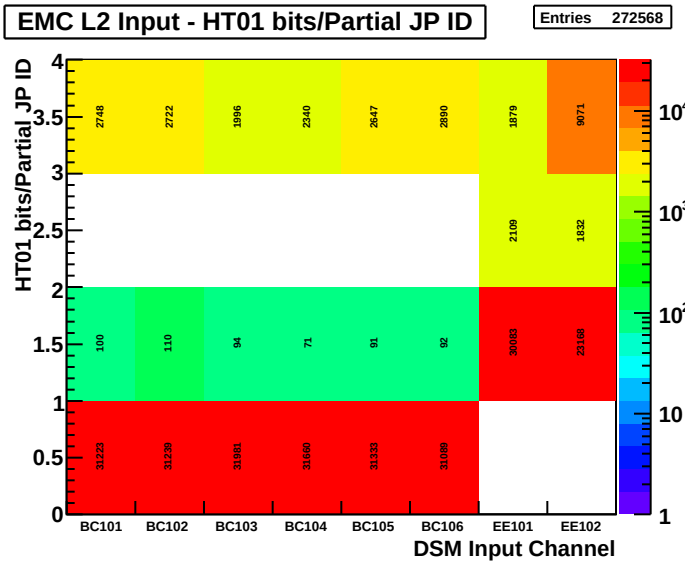

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

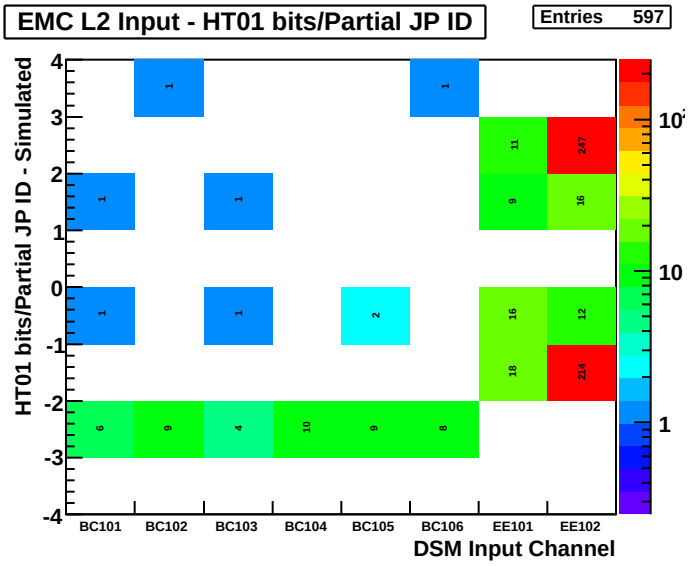

EMC L2 Input - JPZ/JPC bits
Entries 272568
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
12 15 15 16 11 16 4
111 80 77 72 100 92 3 5
295 181 141 147 191 200 8 6
33653 33795 33838 33836 33769 33763 34060 34056

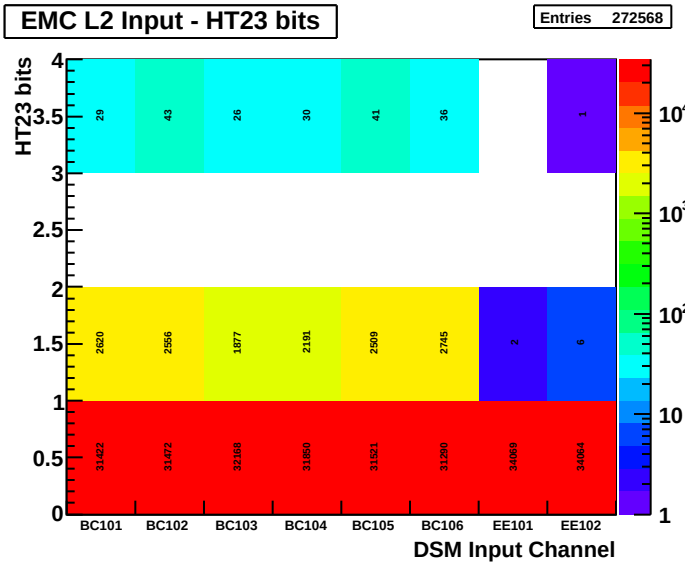

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

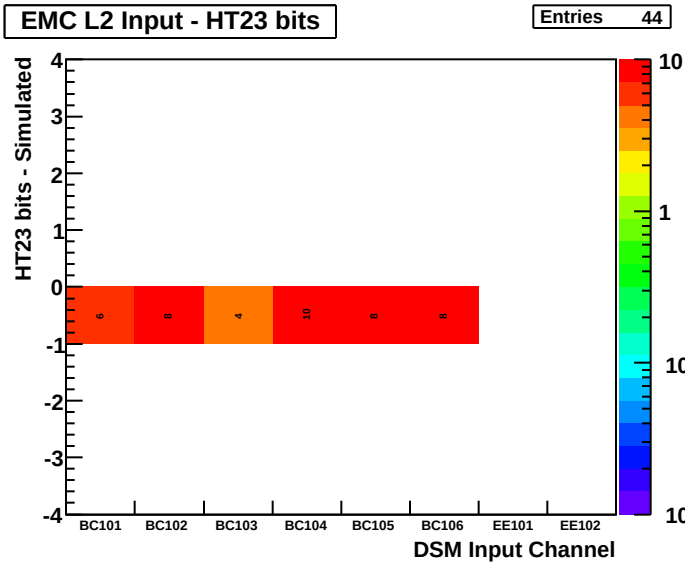

EMC L2 Input - Partial JP Sum
Entries 272568
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

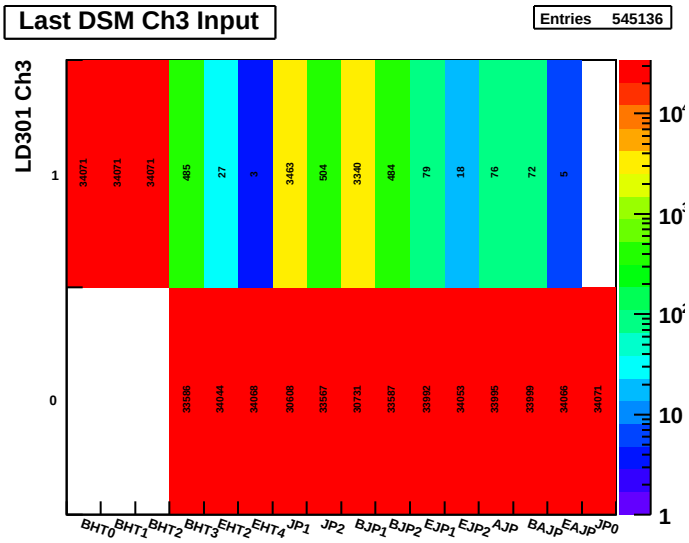

EMC L2 Input - Partial JP Sum
Entries 1425
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

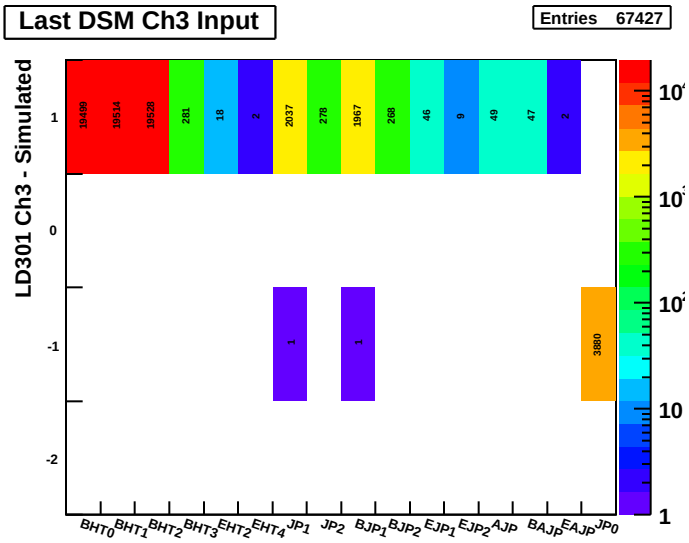

EMC L2 Input - HT01 bits/Partial JP ID
Entries 272568
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

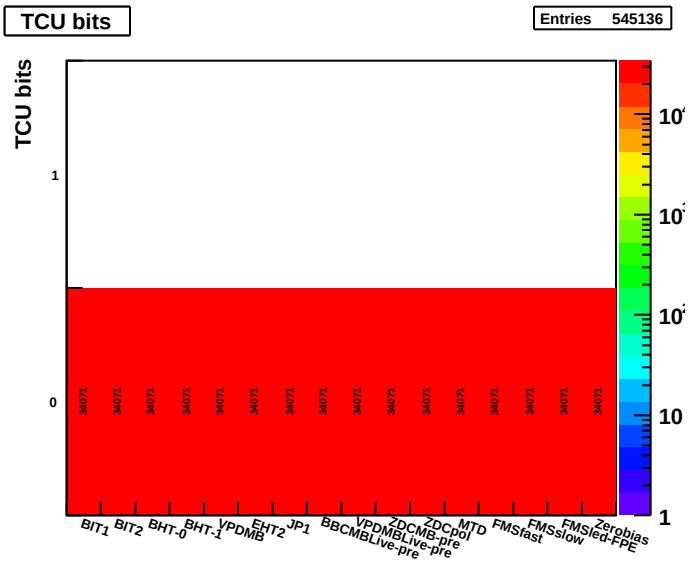

EMC L2 Input - HT01 bits/Partial JP ID
Entries 597
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

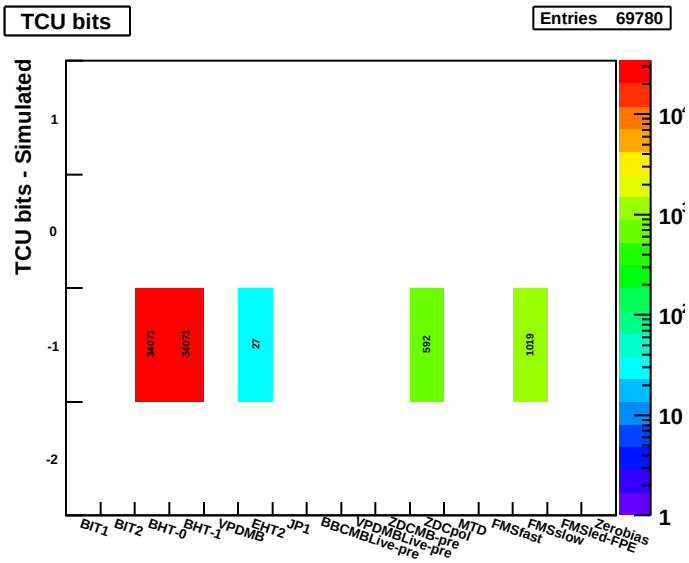

EMC L2 Input - HT23 bits
Entries 272568
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

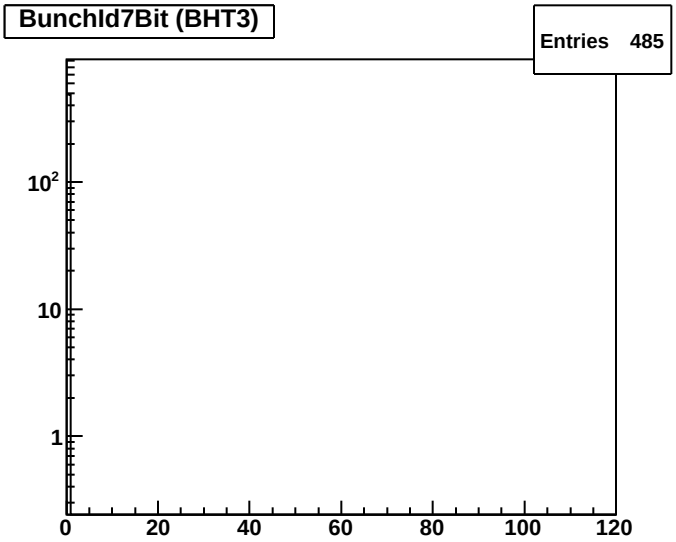

EMC L2 Input - HT23 bits
Entries 44
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

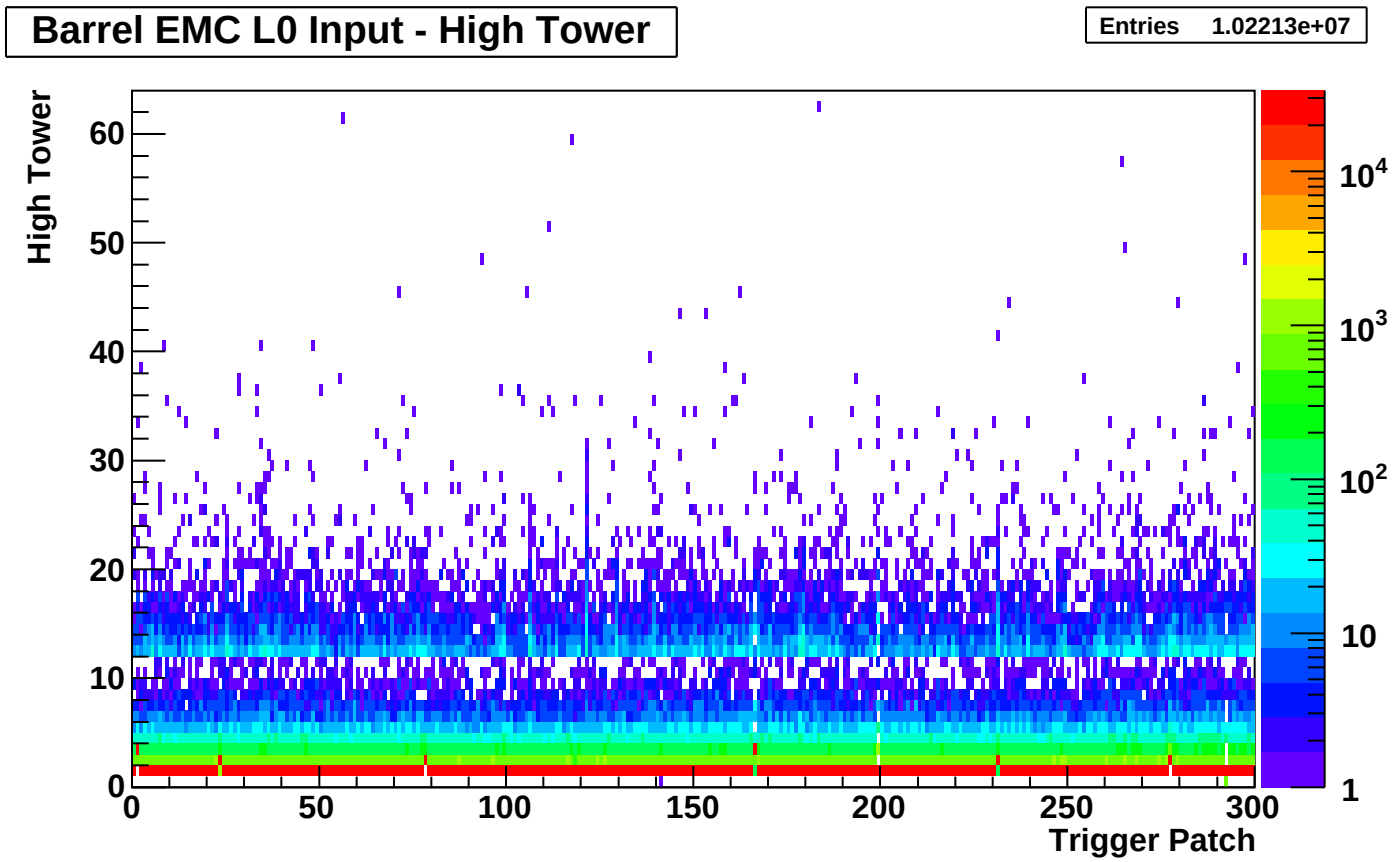

Last DSM Ch3 Input
Entries 545136
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

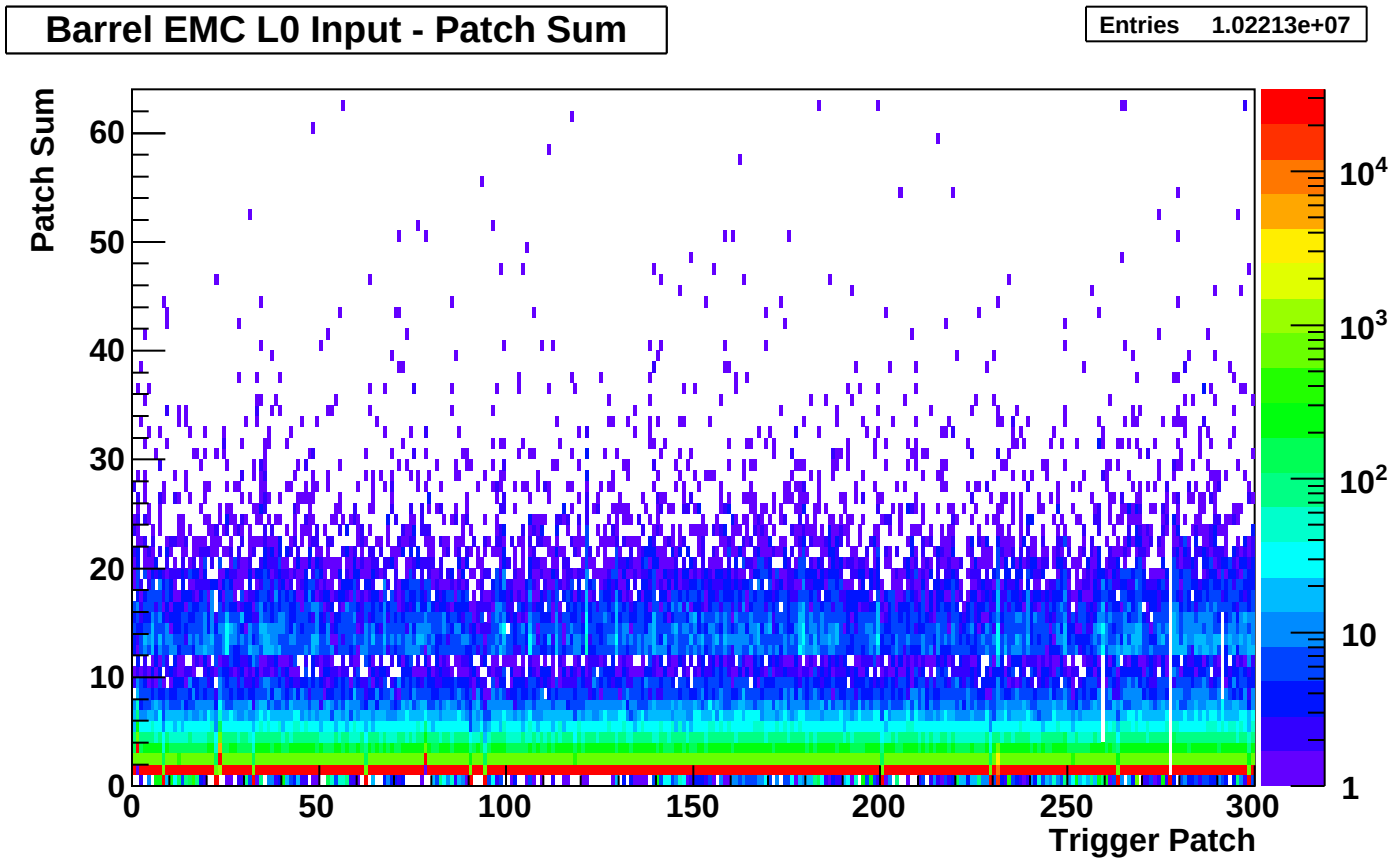

Last DSM Ch3 Input
Entries 67427
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

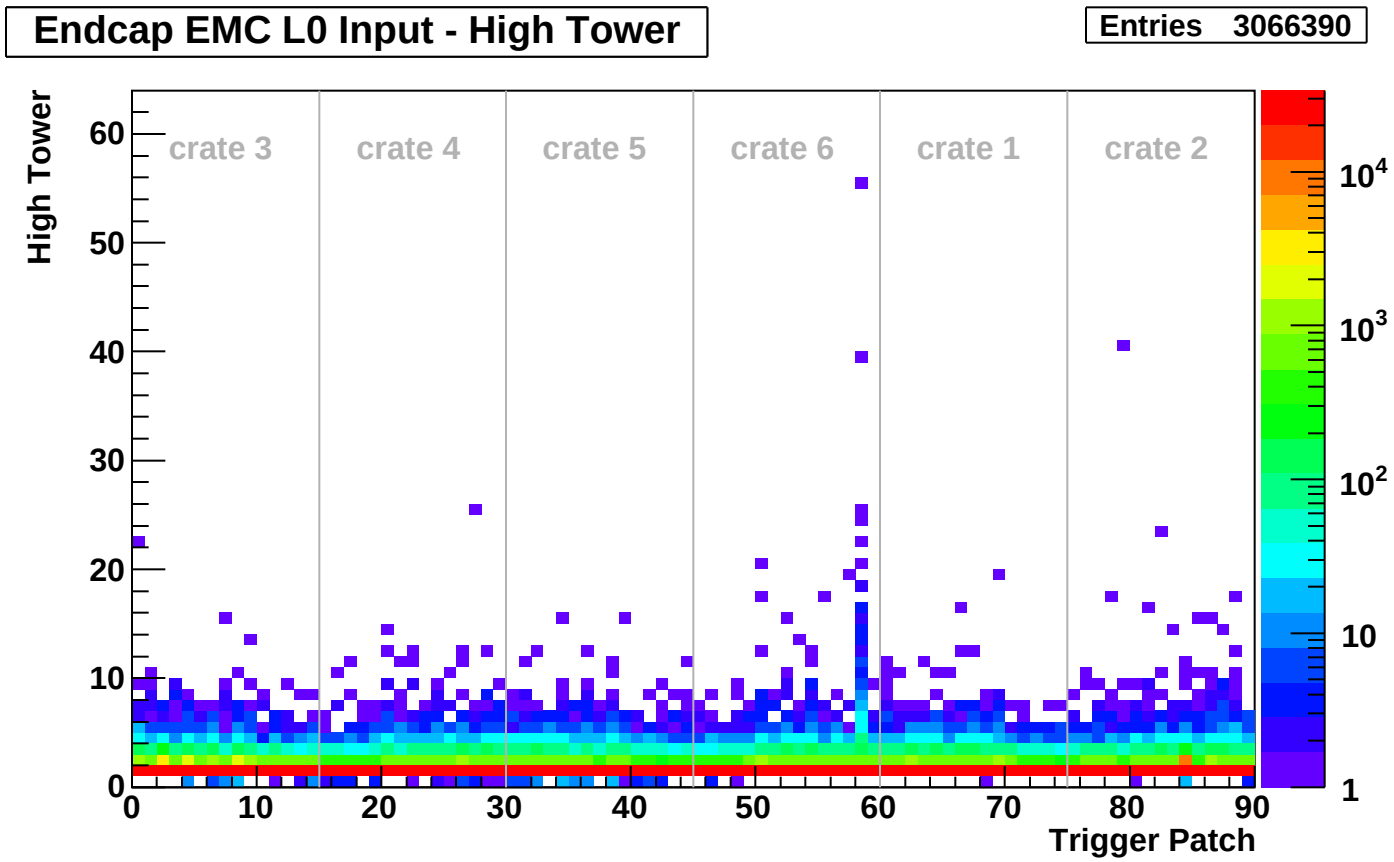

TCU bits
Entries 545136
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

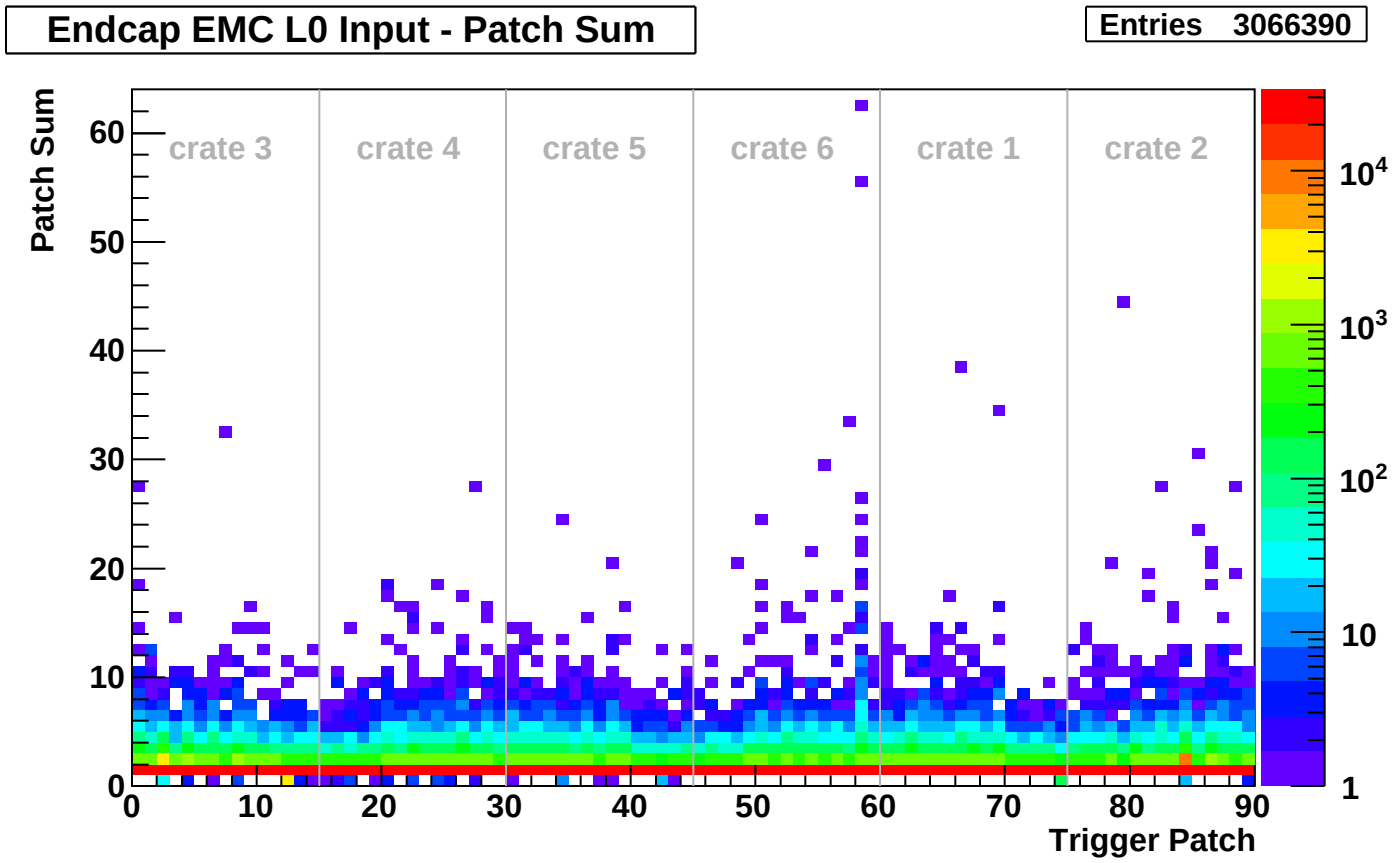

TCU bits
Entries 69780
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

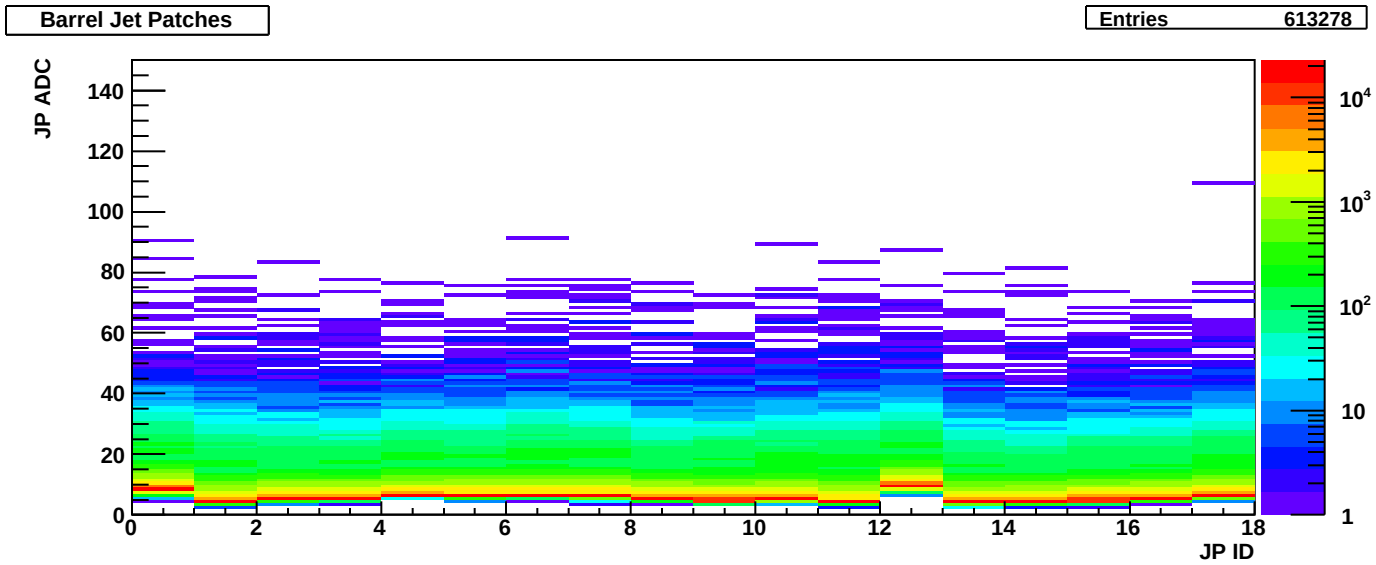

BunchId7Bit (BHT3)
Entries 485

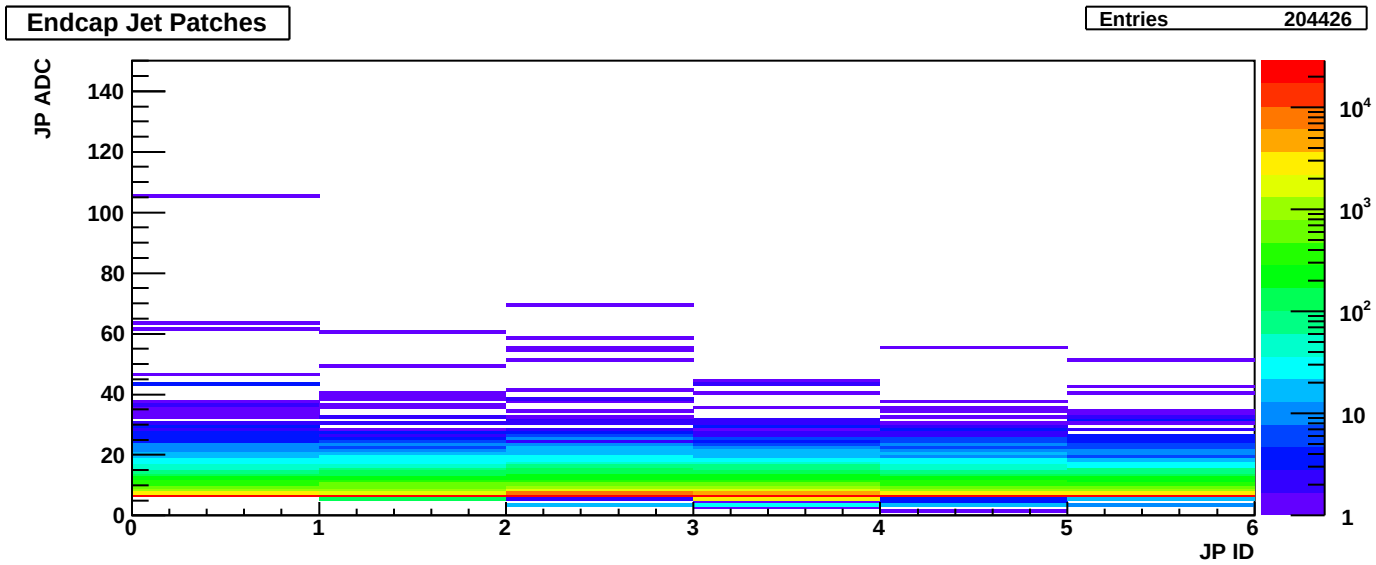

Barrel EMC L0 Input - High Tower
Entries 1.02213e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

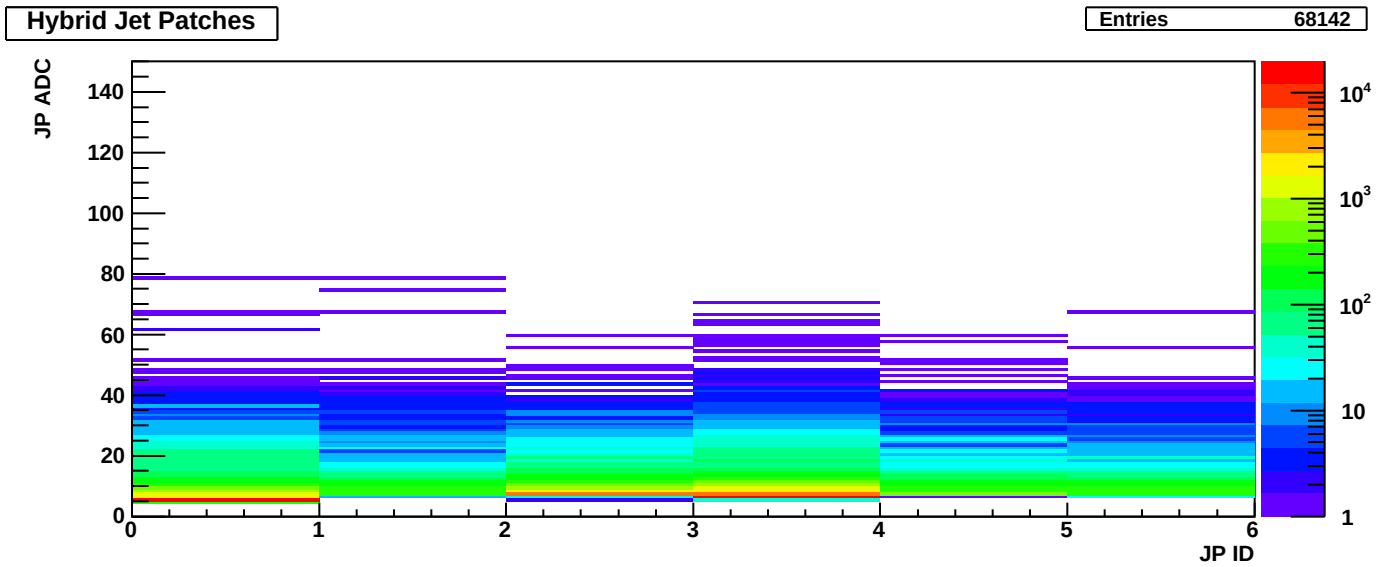

Barrel EMC L0 Input - Patch Sum
Entries 1.02213e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

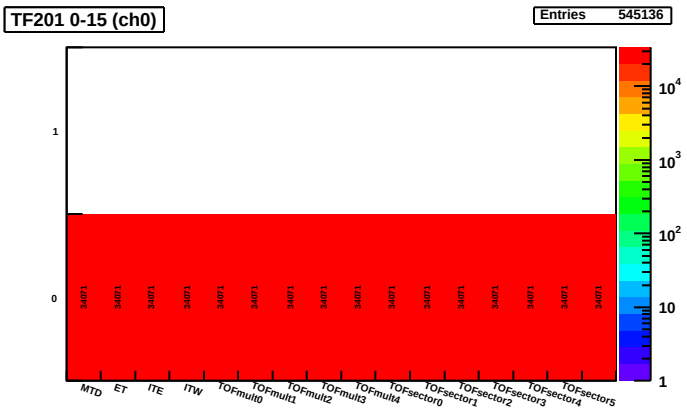

Endcap EMC L0 Input - High Tower
Entries 3066390
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

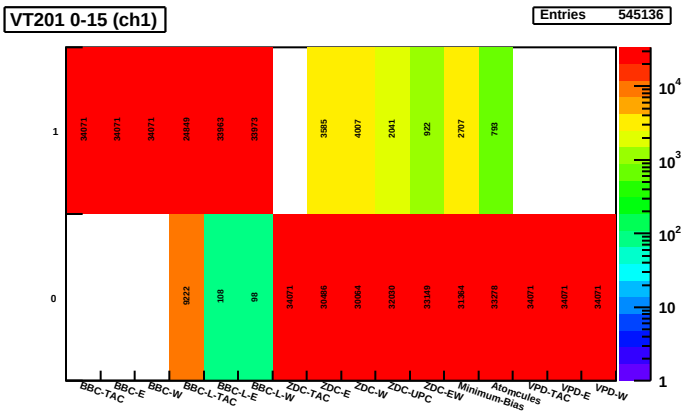

Endcap EMC L0 Input - Patch Sum
Entries 3066390
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

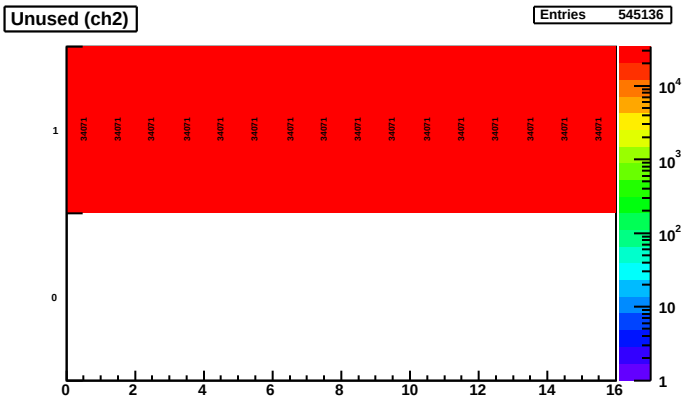

Barrel Jet Patches
Entries 613278
JP ADC
JP ID

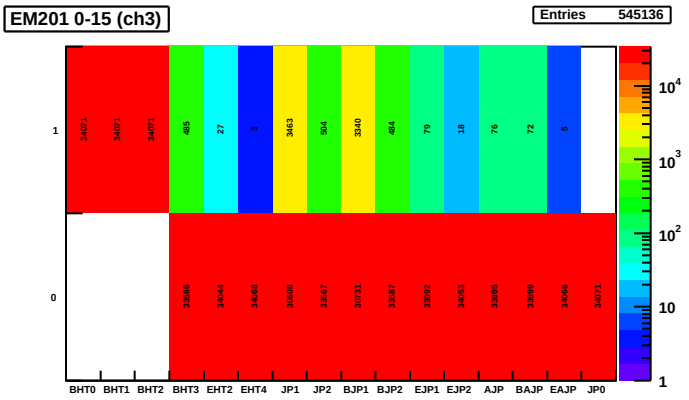

Endcap Jet Patches
Entries 204426
JP ADC
JP ID

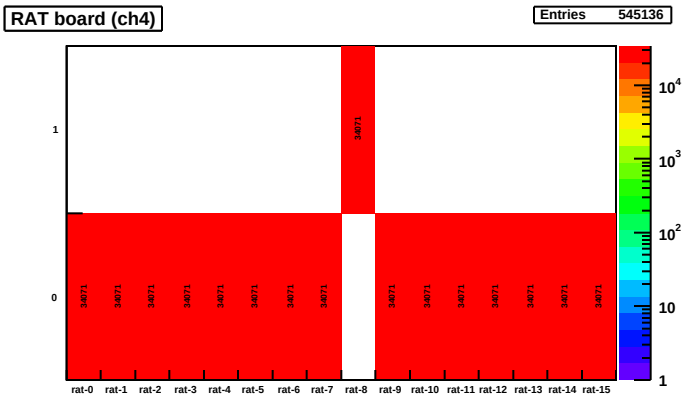

Hybrid Jet Patches
Entries 68142
JP ADC
JP ID

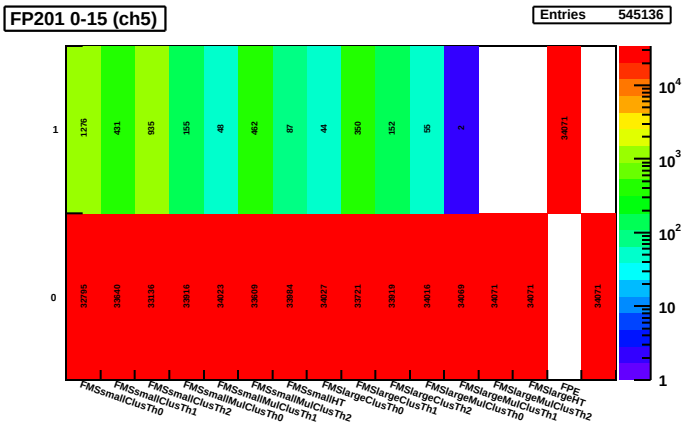

TF201 0-15 (ch0)
Entries 545136
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

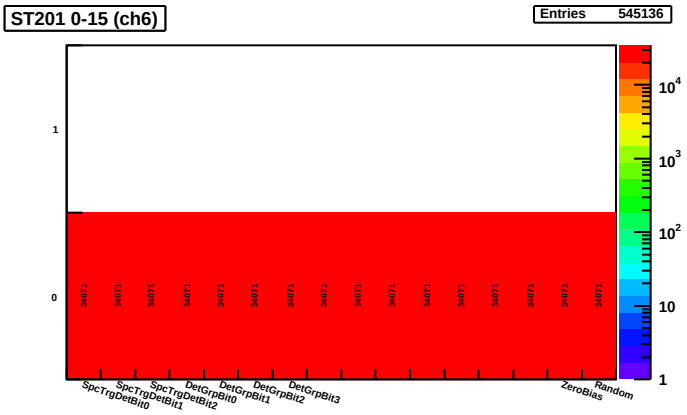

VT201 0-15 (ch1)
Entries 545136
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

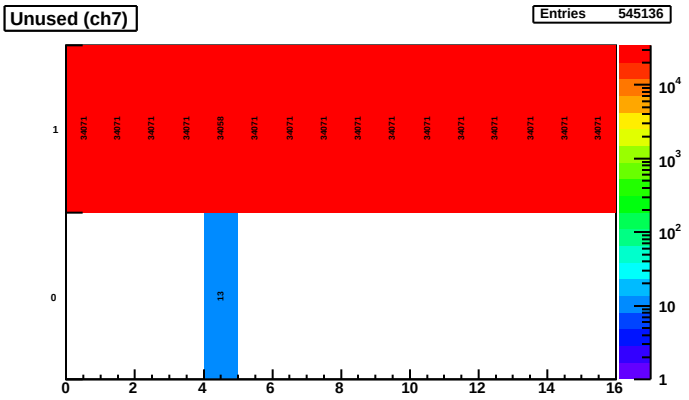

Unused (ch2)
Entries 545136

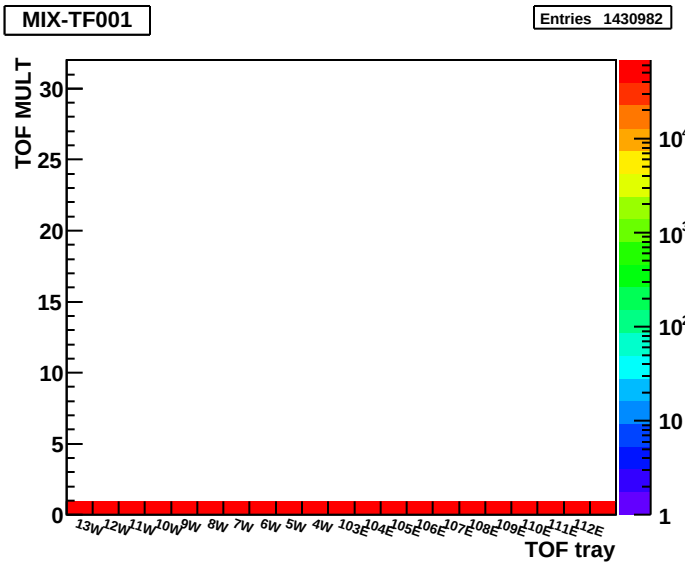

EM201 0-15 (ch3)
Entries 545136
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

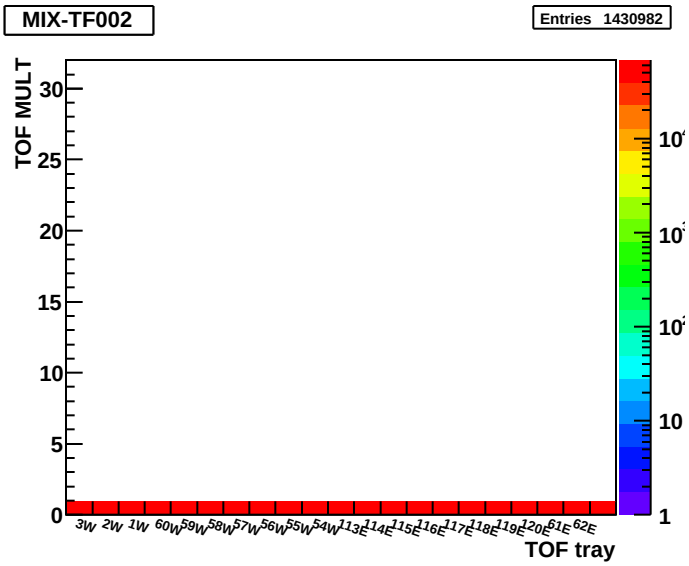

RAT board (ch4)
Entries 545136
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

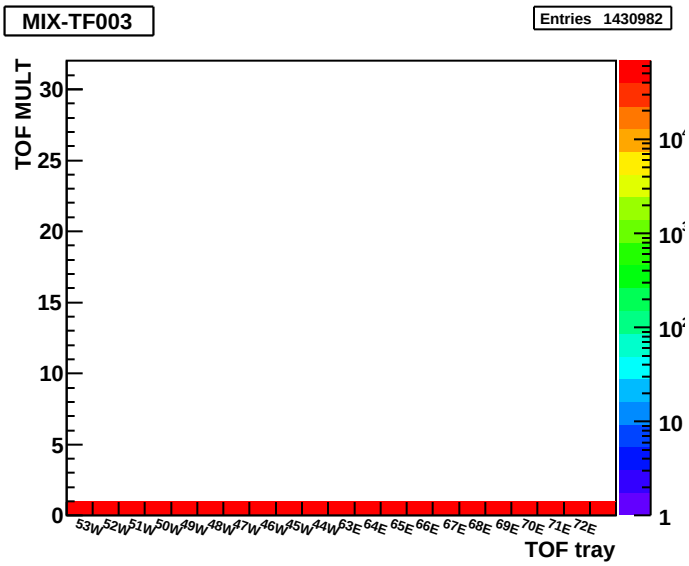

FP201 0-15 (ch5)
Entries 545136
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

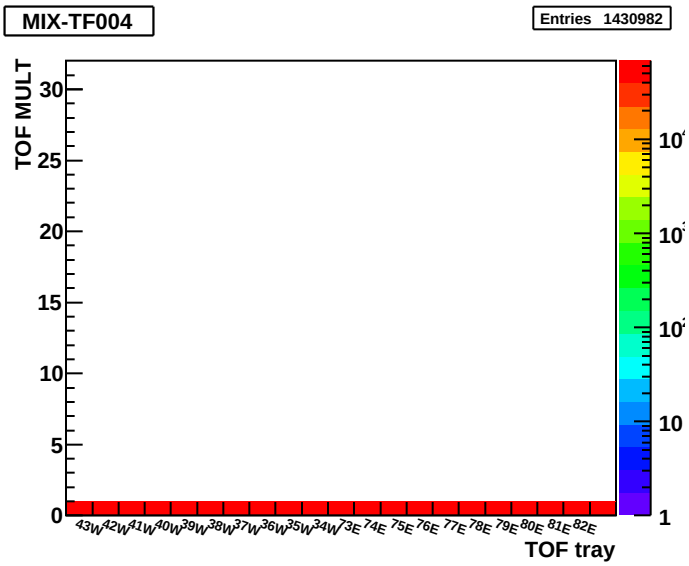

ST201 0-15 (ch6)
Entries 545136
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

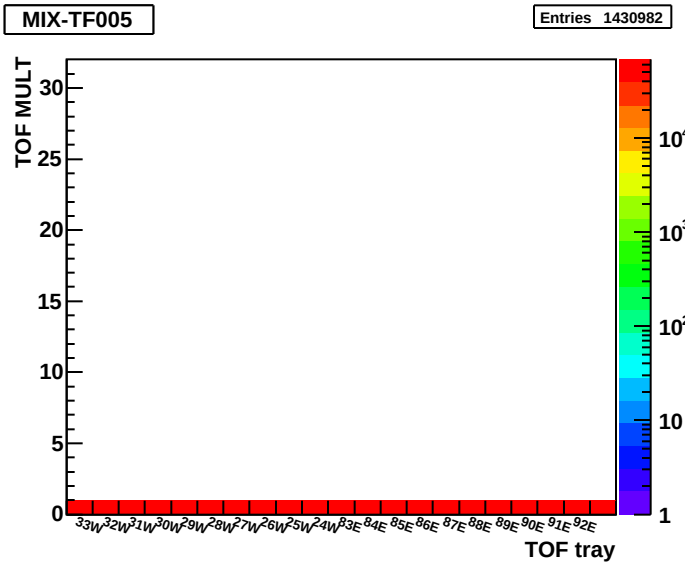

Unused (ch7)
Entries 545136

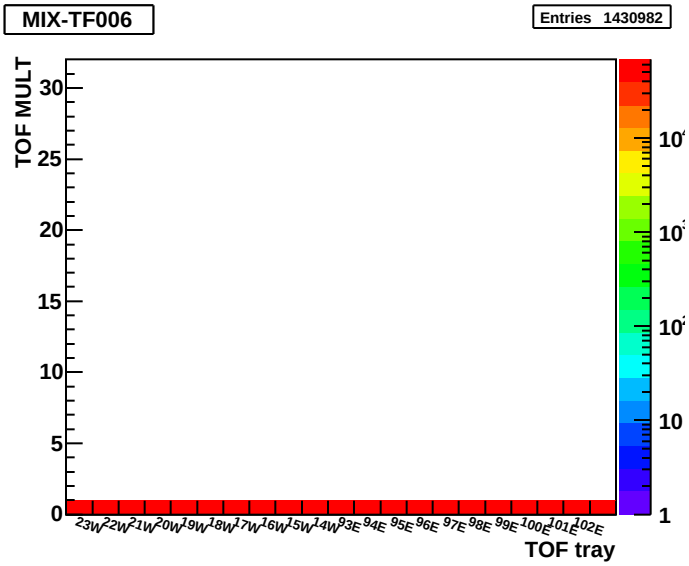

MIX-TF001
Entries 1430982
TOF MULT
TOF tray

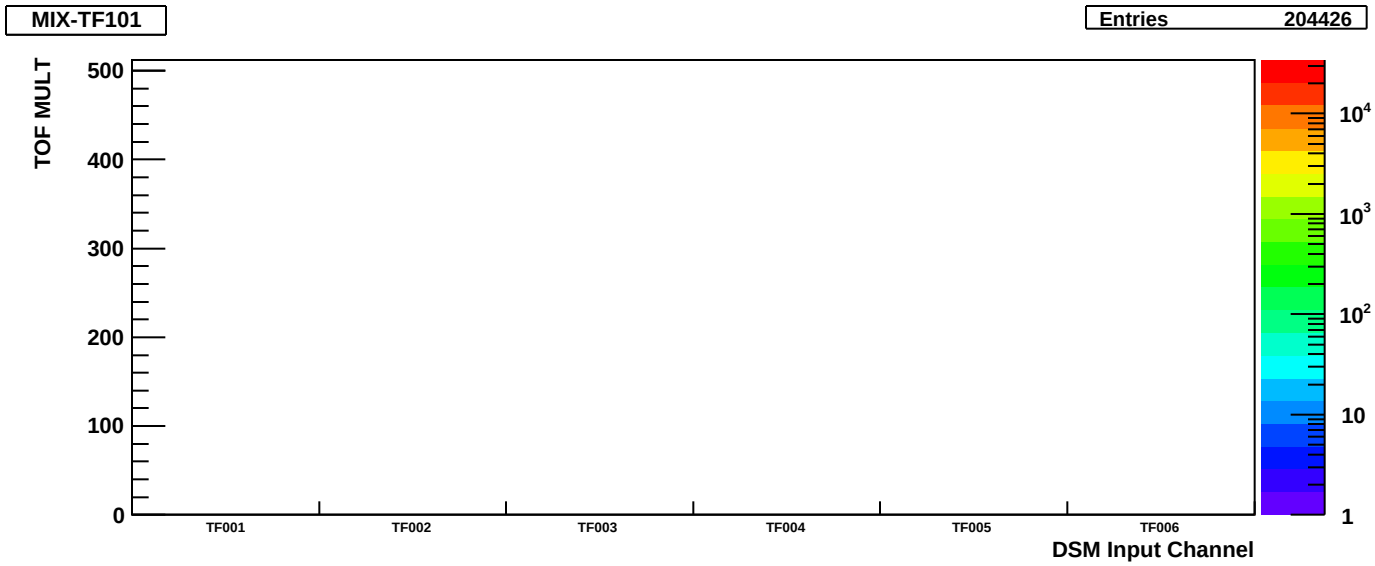

MIX-TF002
Entries 1430982
TOF MULT
TOF tray

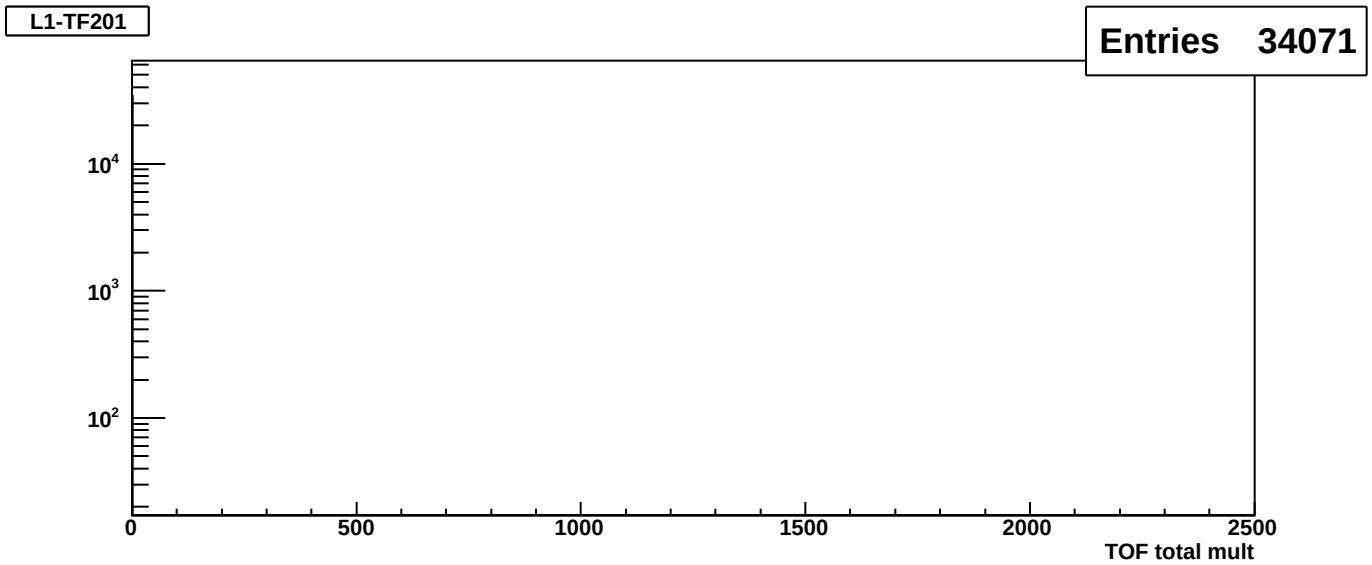

MIX-TF003
Entries 1430982
TOF MULT
TOF tray

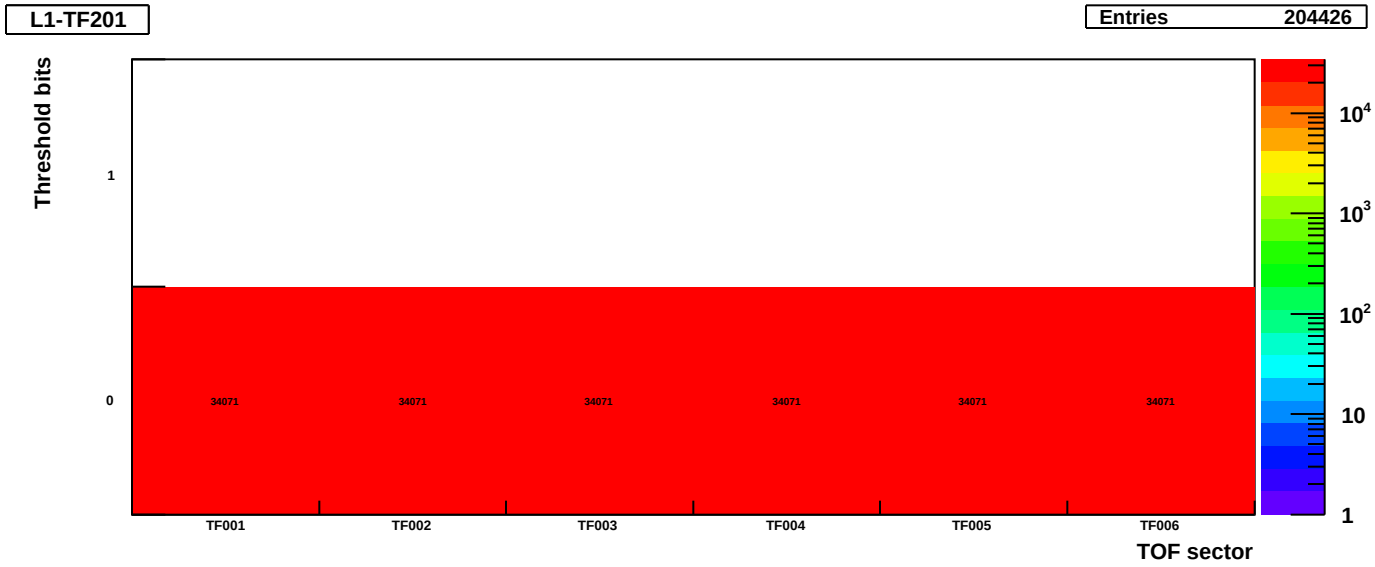

MIX-TF004
Entries 1430982
TOF MULT
TOF tray

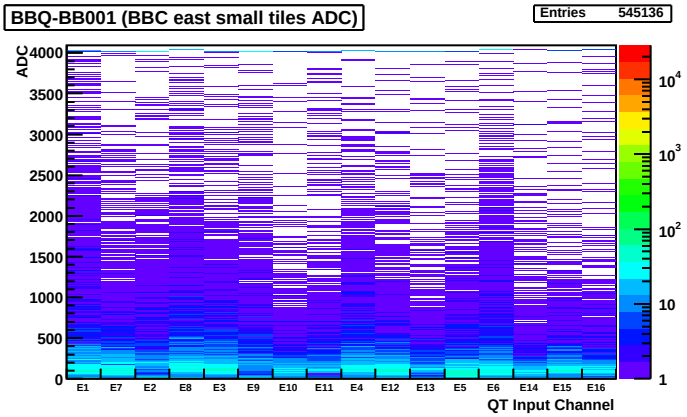

MIX-TF005
Entries 1430982
TOF MULT
TOF tray

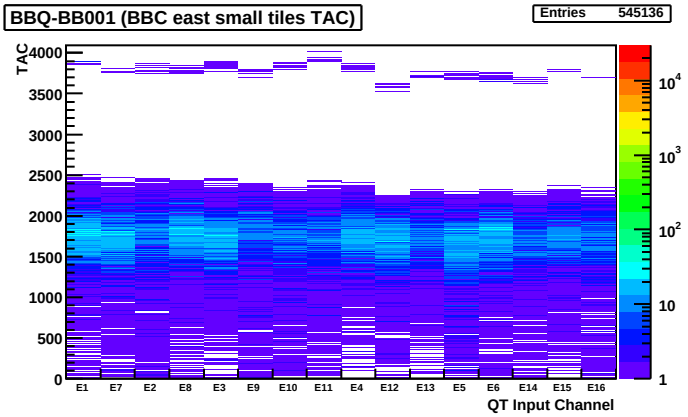

MIX-TF006
Entries 1430982
TOF MULT
TOF tray

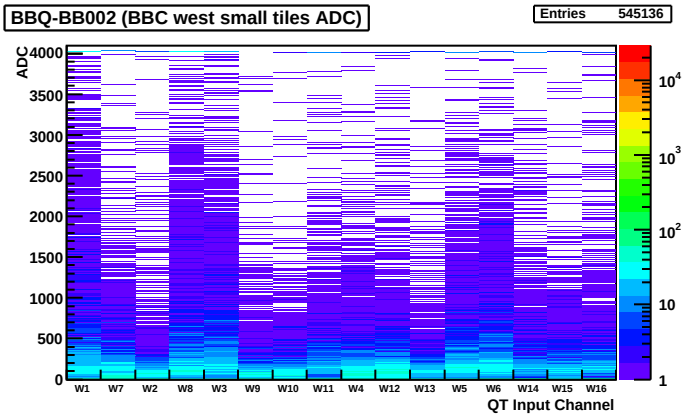

MIX-TF101
Entries 204426
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

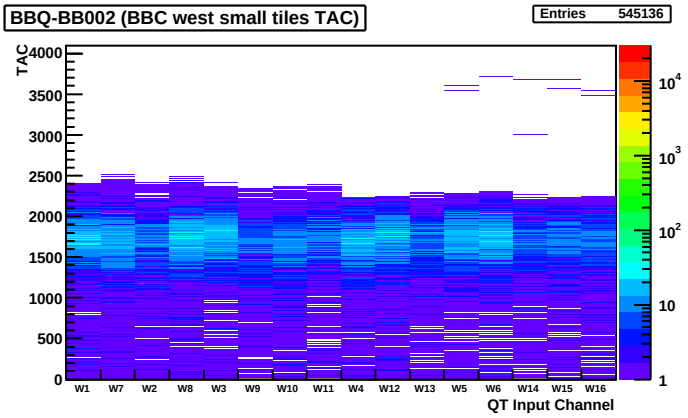

L1-TF201
Entries 34071
10^4
10^3
10^2
0
500
1000
1500
2000
2500
TOF total mult

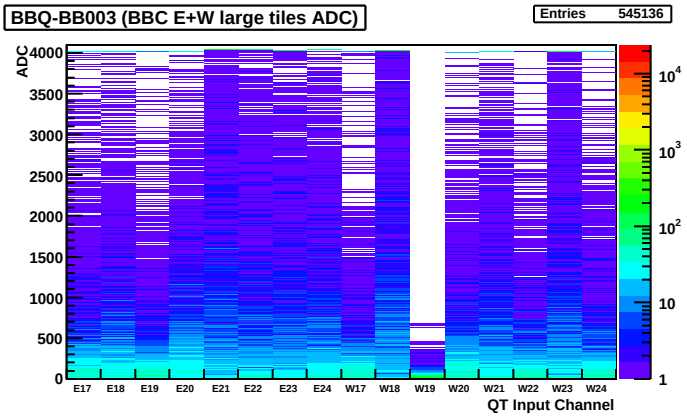

L1-TF201
Entries 204426
Threshold bits
1
0
34071
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

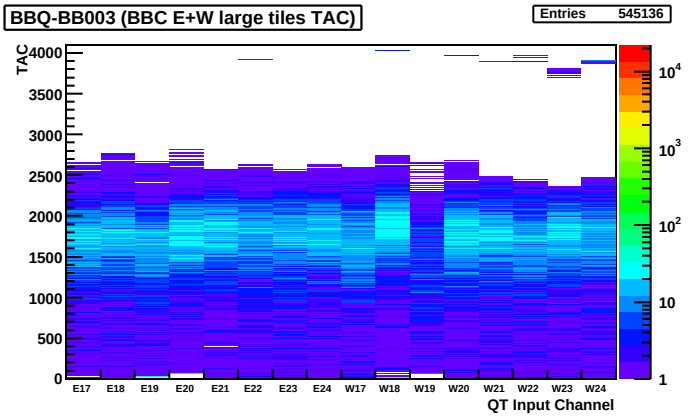

BBQ-BB001 (BBC east small tiles ADC)
Entries 545136
ADC
QT Input Channel

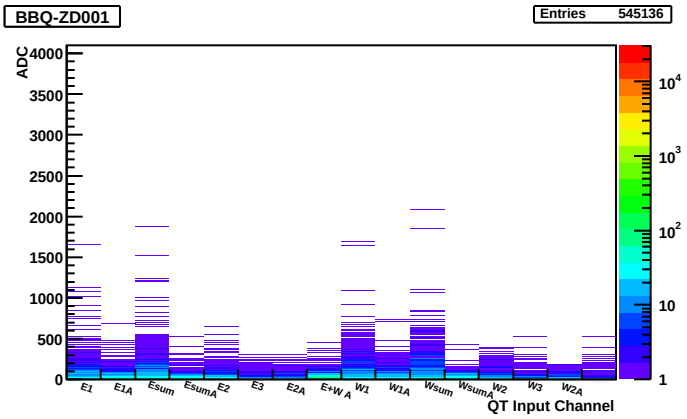

BBQ-BB001 (BBC east small tiles TAC)
Entries 545136
TAC
QT Input Channel

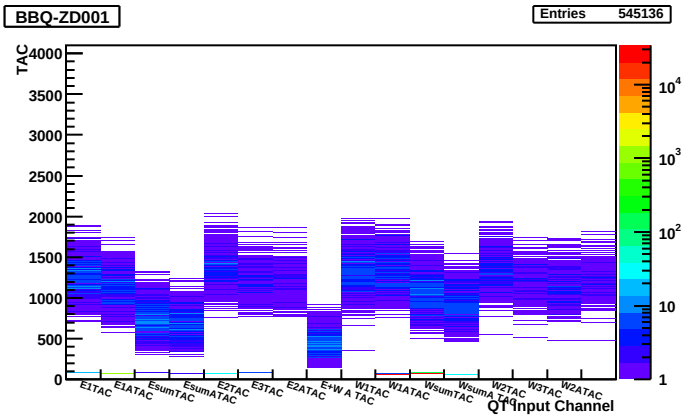

BBQ-BB002 (BBC west small tiles ADC)
Entries 545136
ADC
QT Input Channel

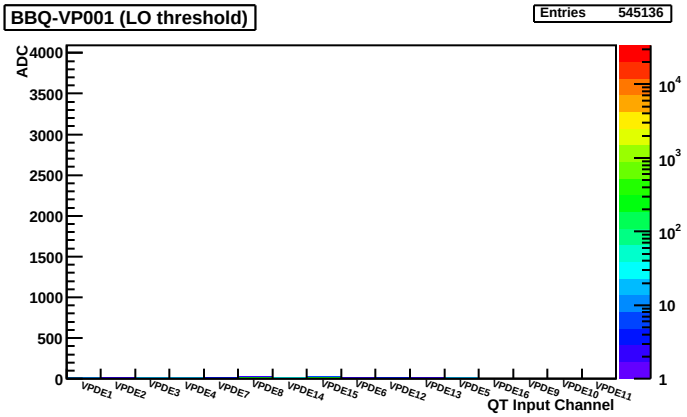

BBQ-BB002 (BBC west small tiles TAC)
Entries 545136
TAC
QT Input Channel

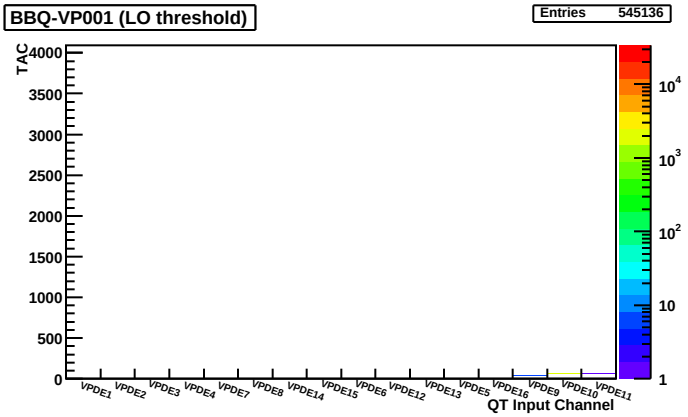

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 545136
ADC
QT Input Channel

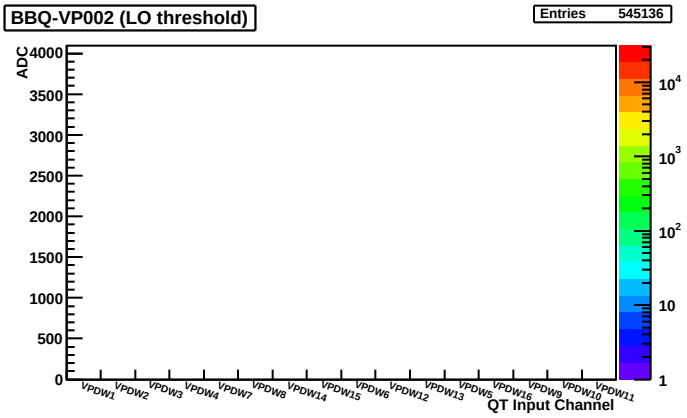

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 545136
TAC
QT Input Channel

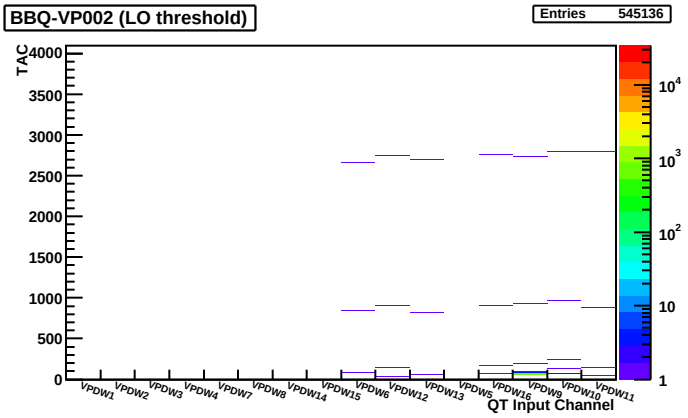

BBQ-ZD001
Entries 545136
ADC
QT Input Channel

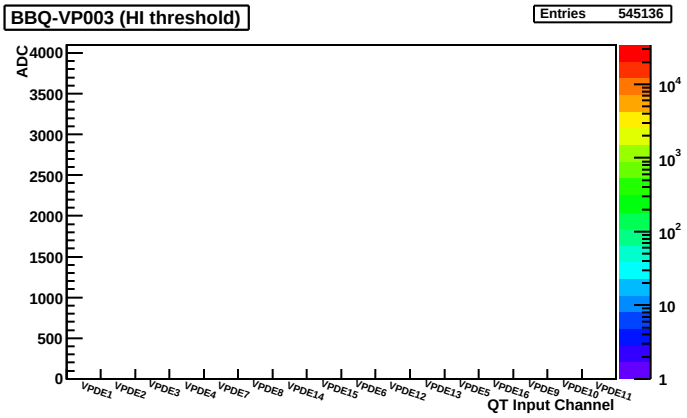

BBQ-ZD001
Entries 545136
TAC
QT Input Channel

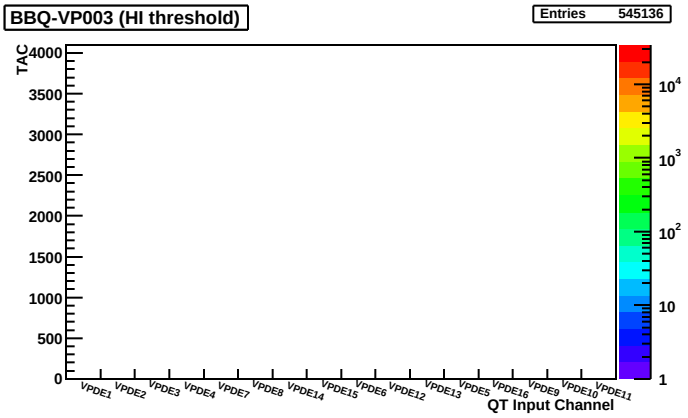

BBQ-VP001 (LO threshold)
Entries 545136
ADC
QT Input Channel

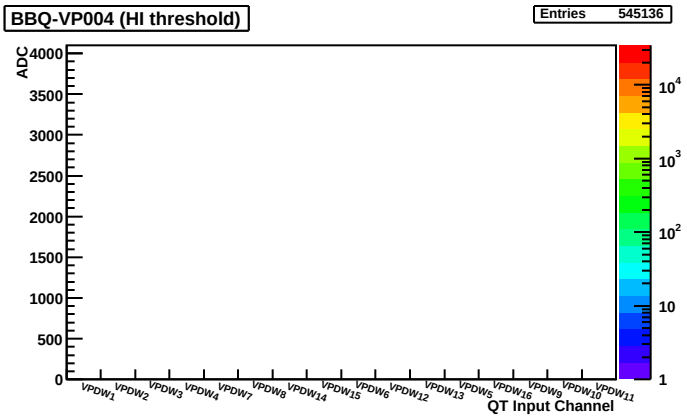

BBQ-VP001 (LO threshold)
Entries 545136
TAC
QT Input Channel

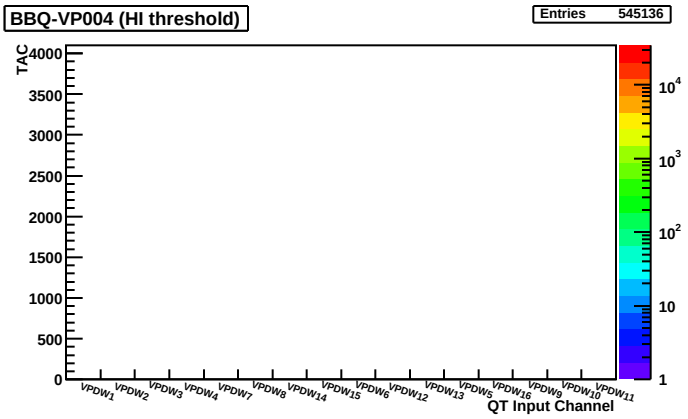

BBQ-VP002 (LO threshold)
Entries 545136
ADC
QT Input Channel

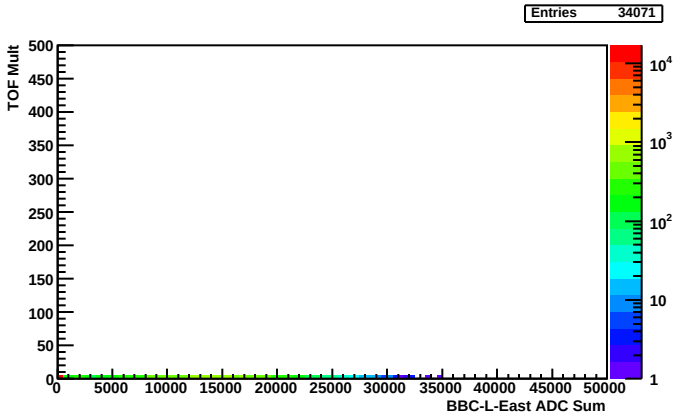

BBQ-VP002 (LO threshold)
Entries 545136
TAC
QT Input Channel

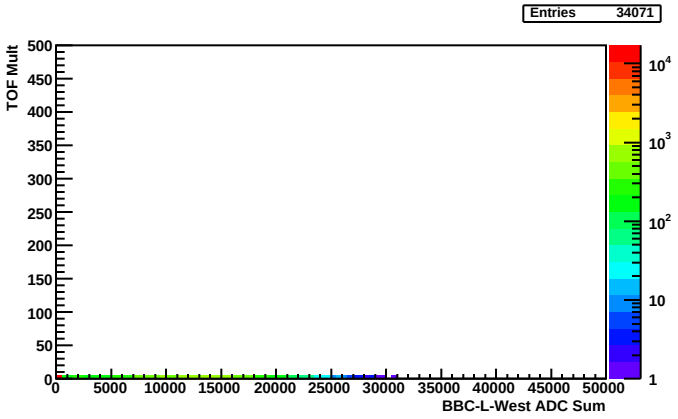

BBQ-VP003 (HI threshold)
Entries 545136
ADC
QT Input Channel

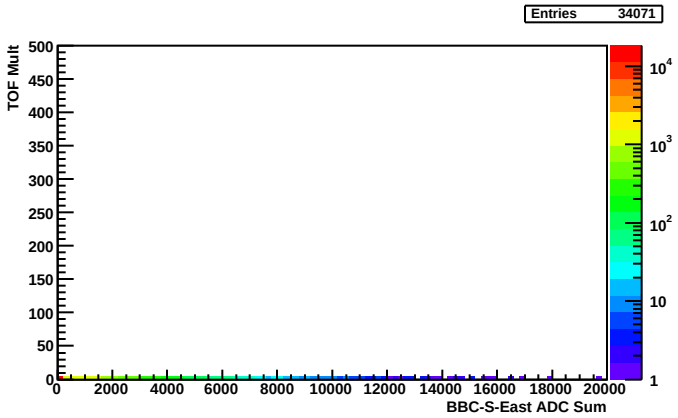

BBQ-VP003 (HI threshold)
Entries 545136
TAC
QT Input Channel

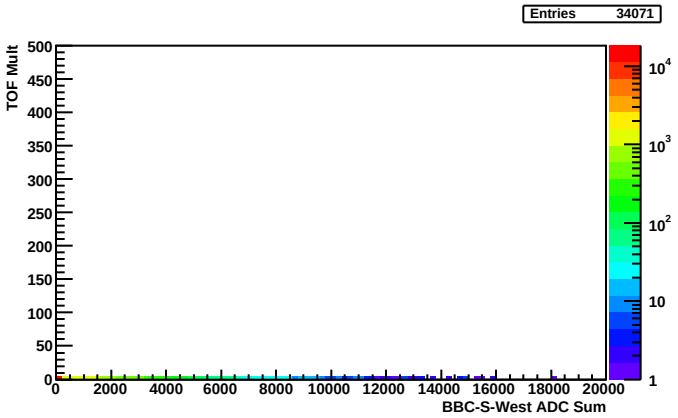

BBQ-VP004 (HI threshold)
Entries 545136
ADC
QT Input Channel

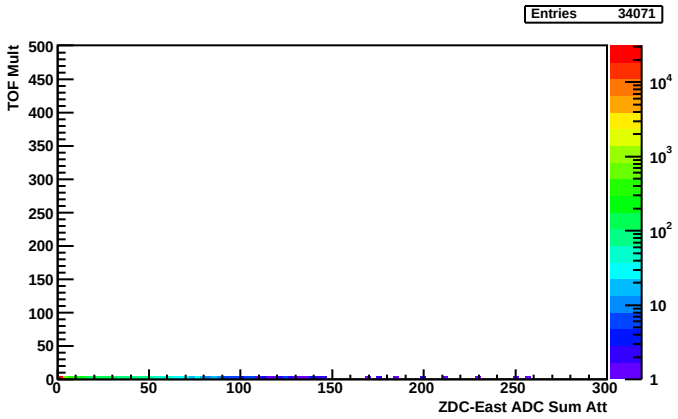

BBQ-VP004 (HI threshold)
Entries 545136
TAC
QT Input Channel

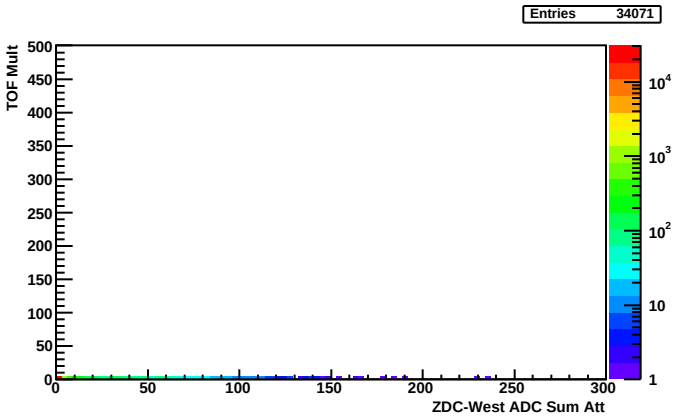

Entries 34071
TOF Mult
BBC-L-East ADC Sum

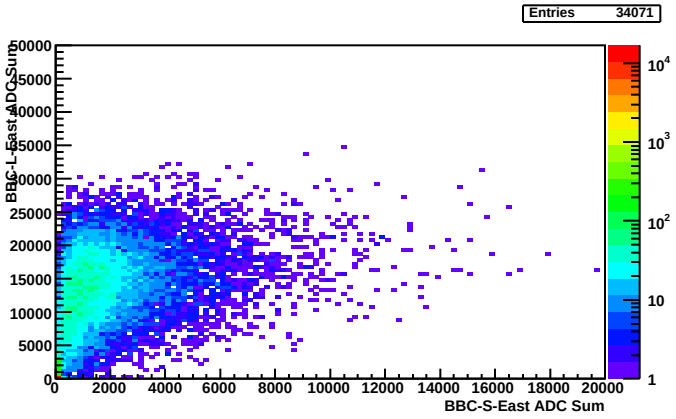

Entries 34071
TOF Mult
BBC-L-West ADC Sum

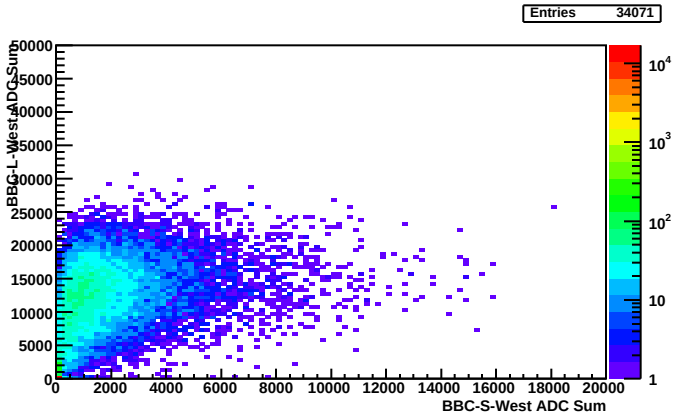

Entries 34071
TOF Mult
BBC-S-East ADC Sum

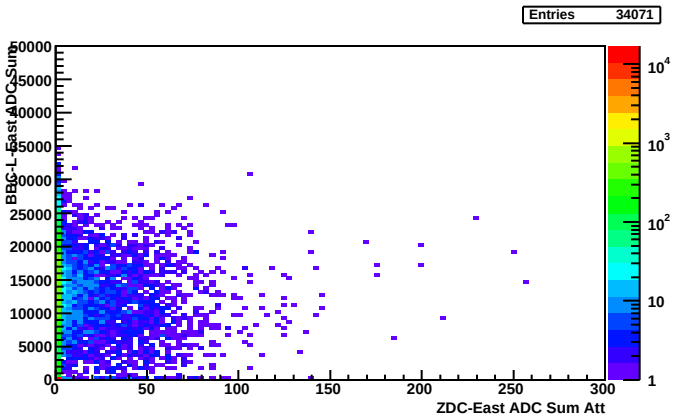

Entries 34071
TOF Mult
BBC-S-West ADC Sum

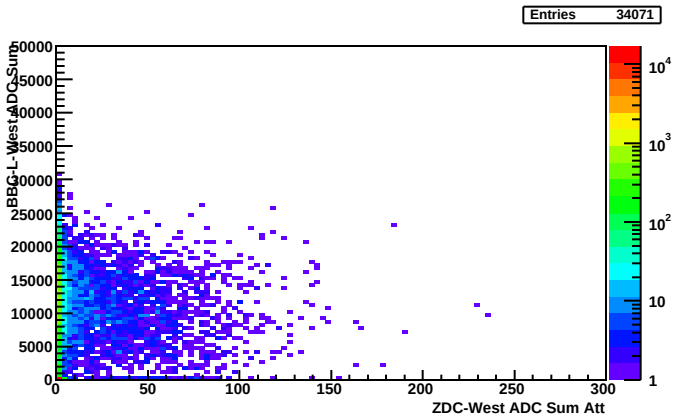

Entries 34071
TOF Mult
ZDC-East ADC Sum Att

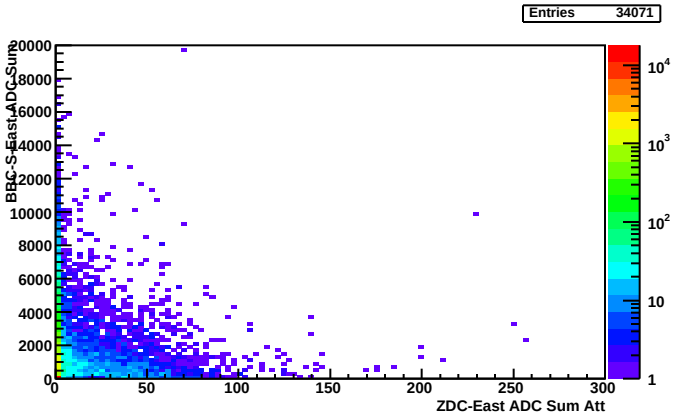

Entries 34071
TOF Mult
ZDC-West ADC Sum Att

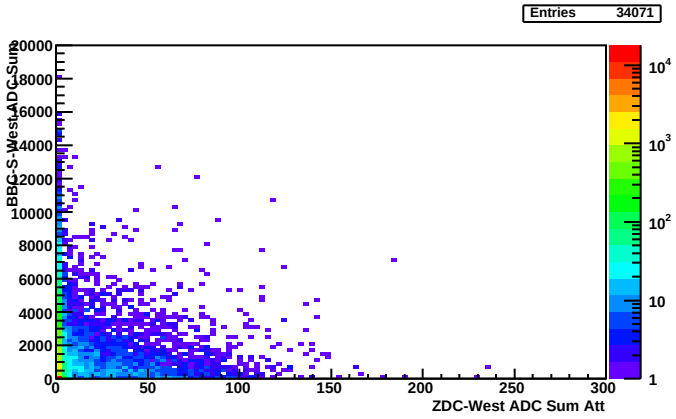

Entries 34071
BBC-L-East ADC Sum
BBC-S-East ADC Sum

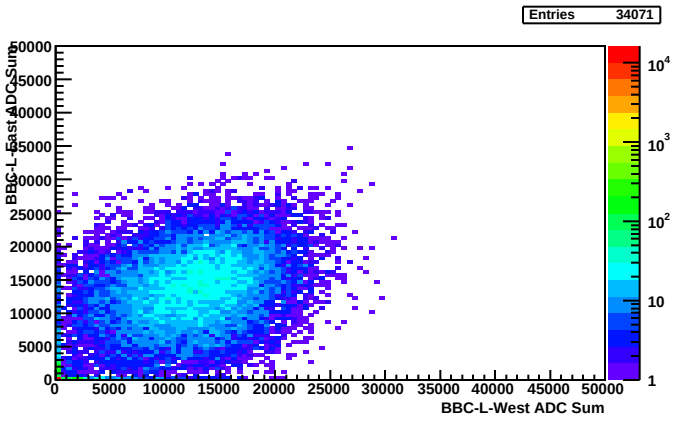

Entries 34071
BBC-L-West ADC Sum
BBC-S-West ADC Sum

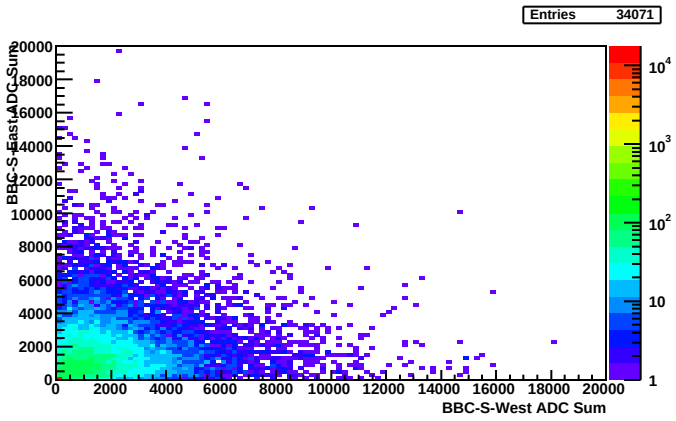

Entries 34071
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

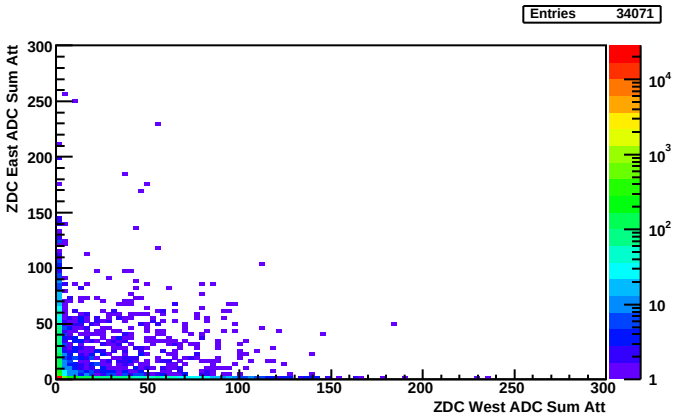

Entries 34071
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

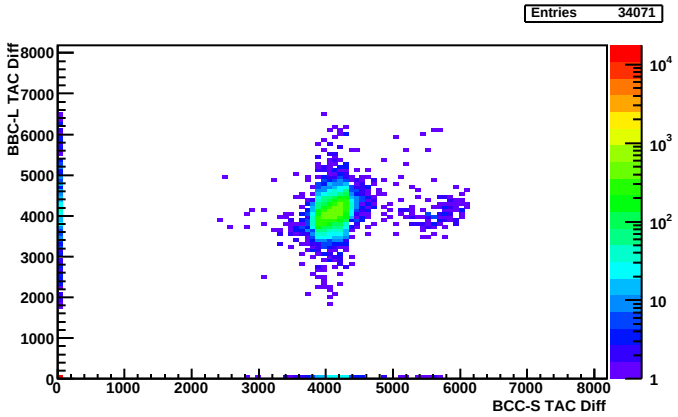

Entries 34071
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

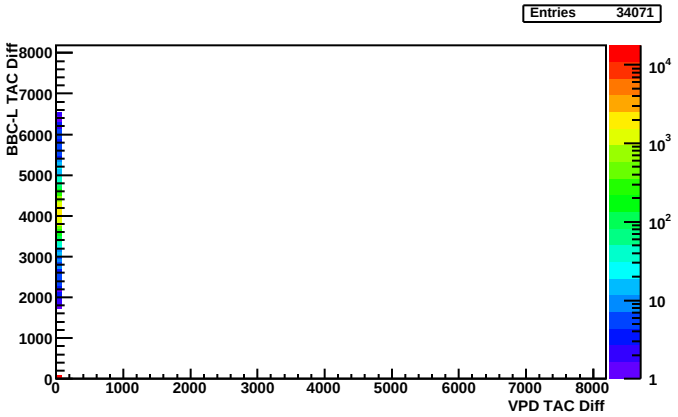

Entries 34071
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

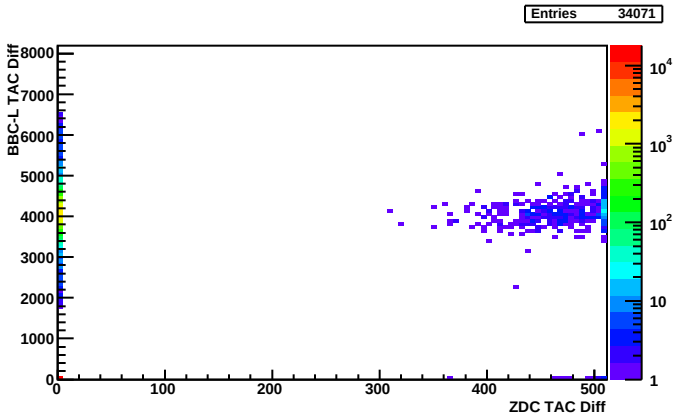

Entries 34071
BBC-L-East ADC Sum
BBC-L-West ADC Sum

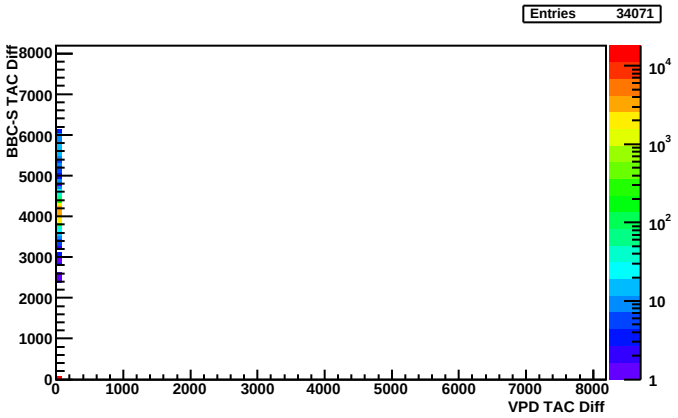

Entries 34071
BBC-S-East ADC Sum
BBC-S-West ADC Sum

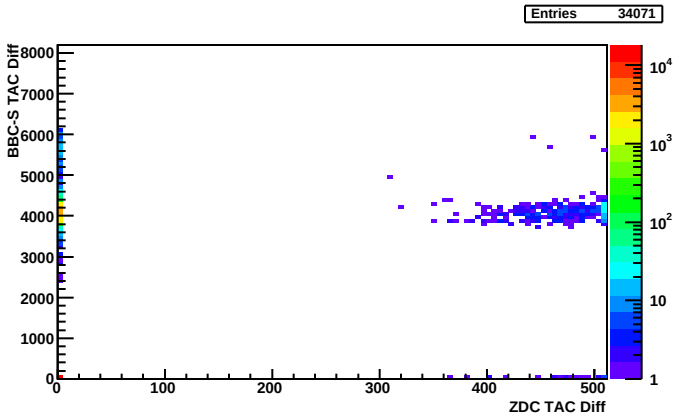

Entries 34071
ZDC East ADC Sum Att
ZDC West ADC Sum Att

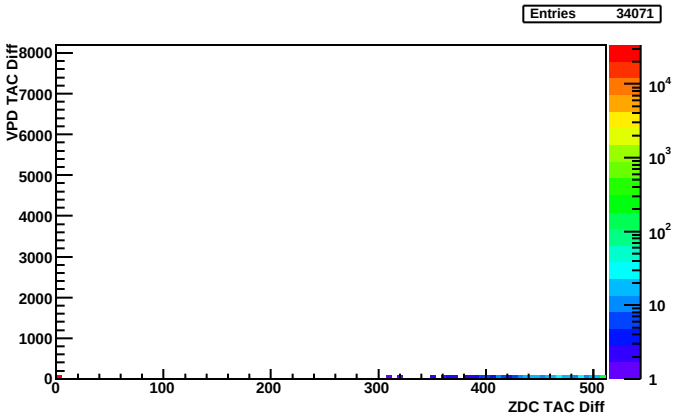

Entries 34071
BBC-L TAC Diff
BCC-S TAC Diff

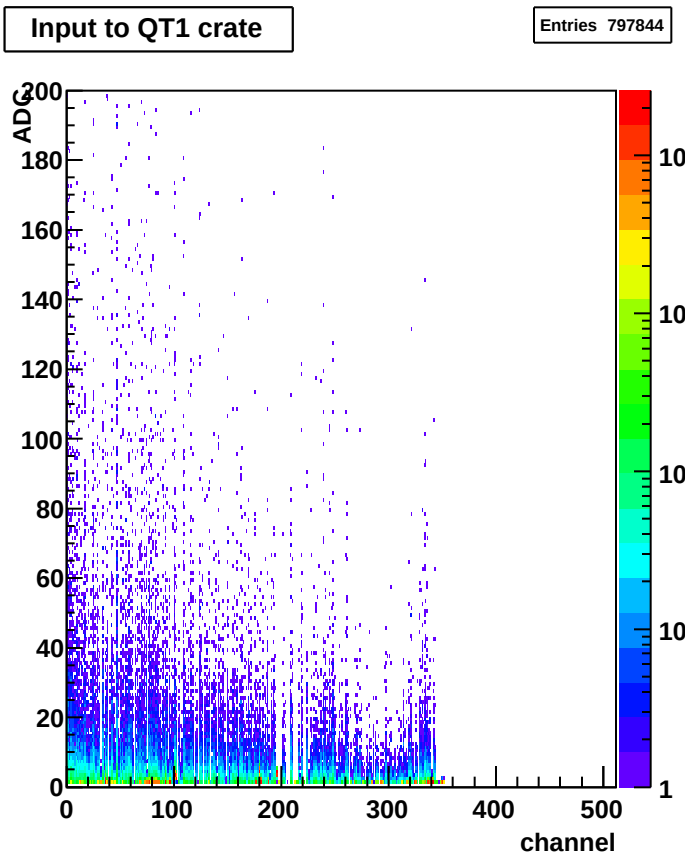

Entries 34071
BBC-L TAC Diff
VPD TAC Diff

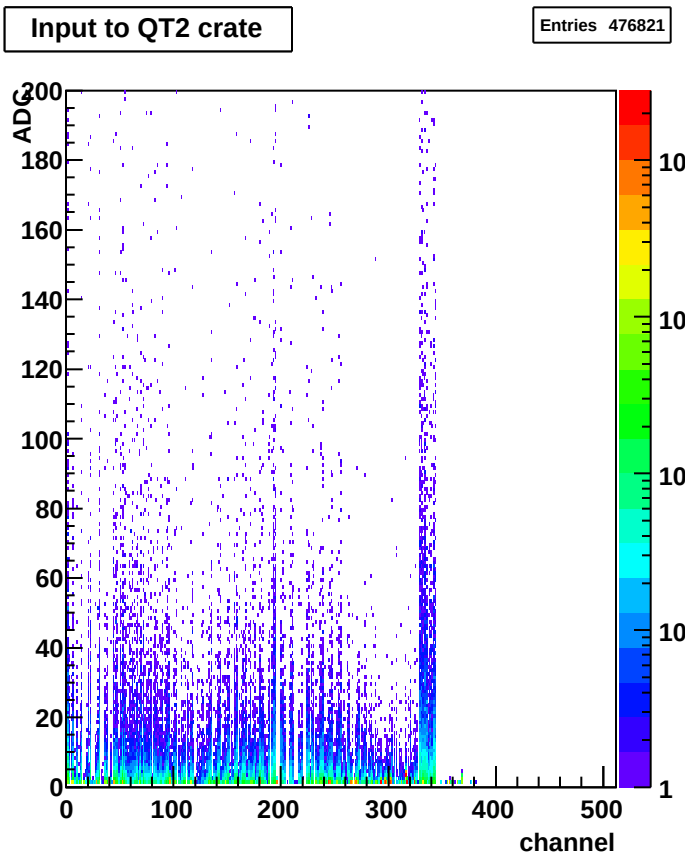

Entries 34071
BBC-L TAC Diff
ZDC TAC Diff

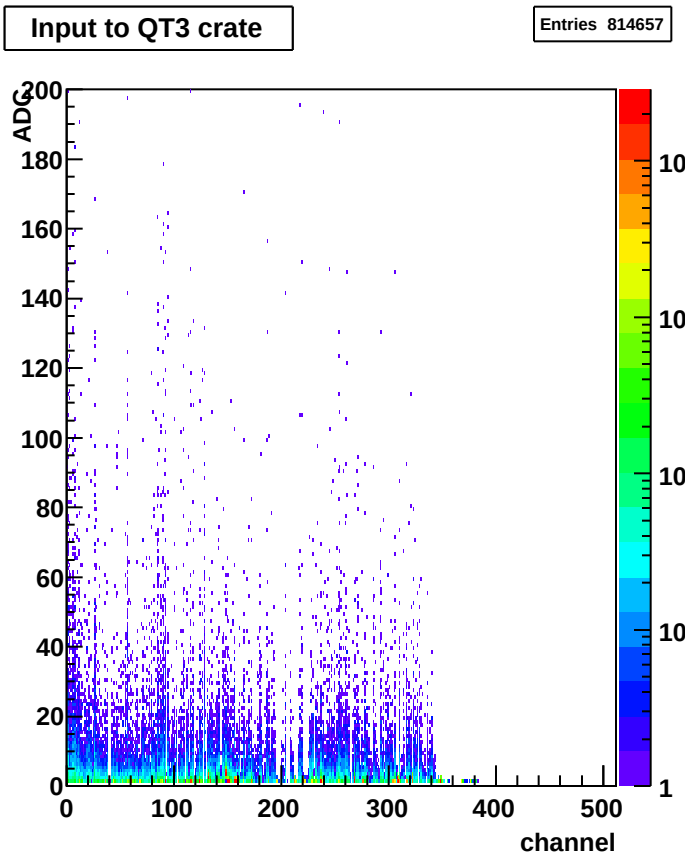

Entries 34071
BBC-S TAC Diff
VPD TAC Diff

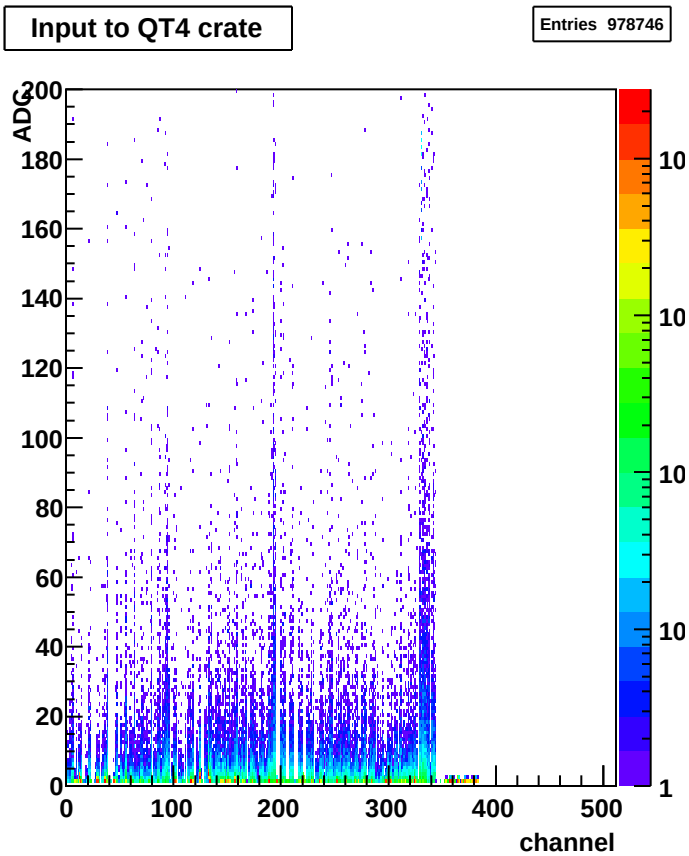

Entries 34071
BBC-S TAC Diff
ZDC TAC Diff

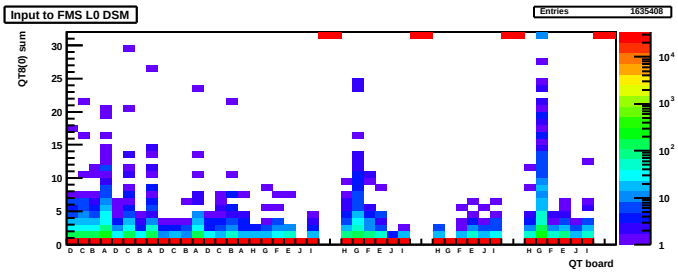

Entries 34071
VPD TAC Diff
ZDC TAC Diff

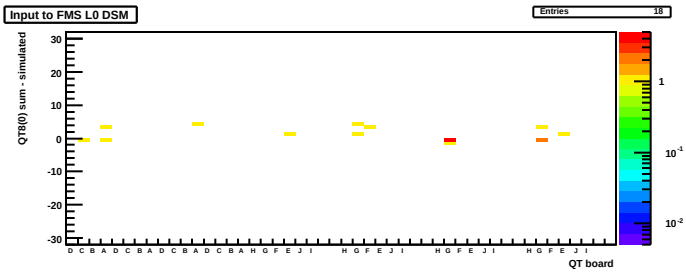

Input to QT1 crate
Entries 797844
ADC
channel

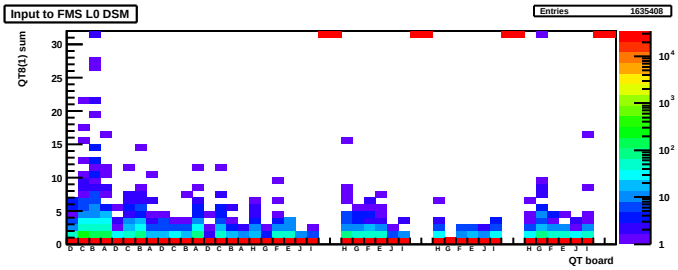

Input to QT2 crate
Entries 476821
ADC
channel

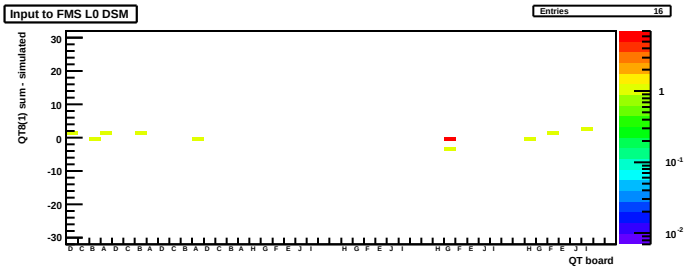

Input to QT3 crate
Entries 814657
ADC
channel

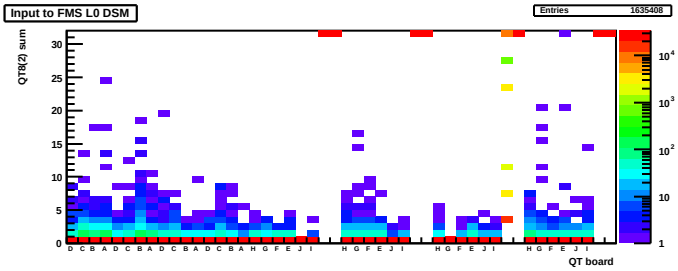

Input to QT4 crate
Entries 978746
ADC
channel

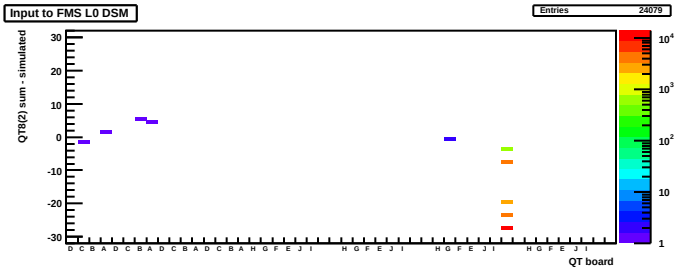

Input to FMS L0 DSM
Entries 1635408
QT8(0) sum
QT board

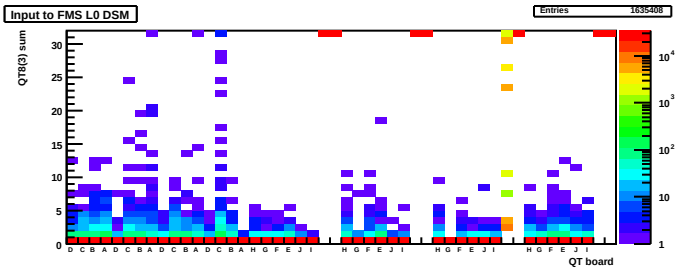

Input to FMS L0 DSM
Entries 18
QT8(0) sum - simulated
QT board

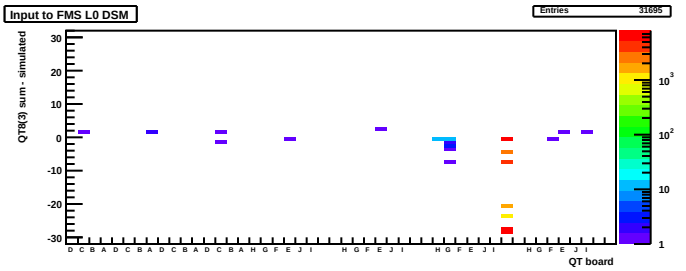

Input to FMS L0 DSM
Entries 1635408
QT8(1) sum
QT board

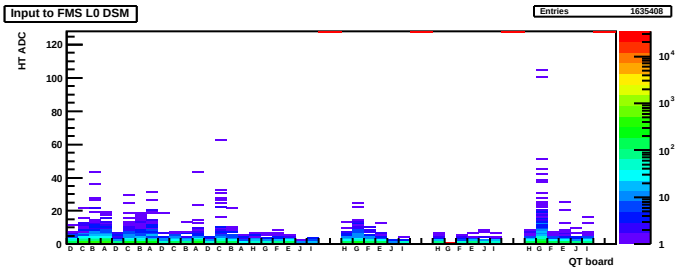

Input to FMS L0 DSM
Entries 16
QT8(1) sum - simulated
QT board

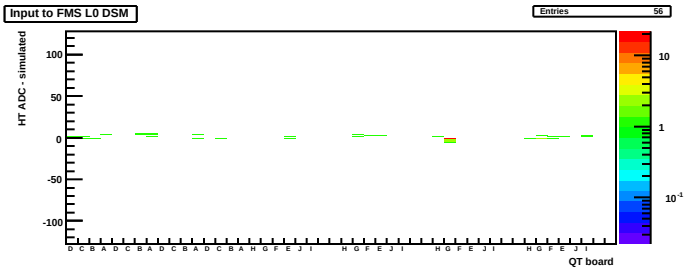

Input to FMS L0 DSM
Entries 1635408
QT8(2) sum
QT board

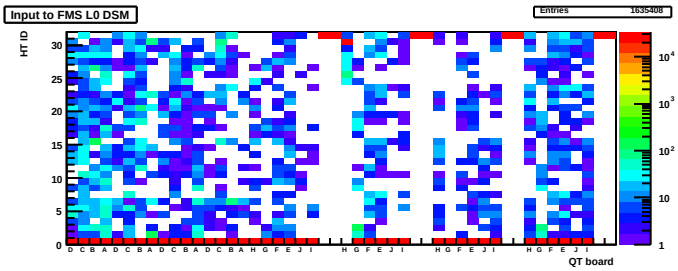

Input to FMS L0 DSM
Entries 24079
QT8(2) sum - simulated
QT board

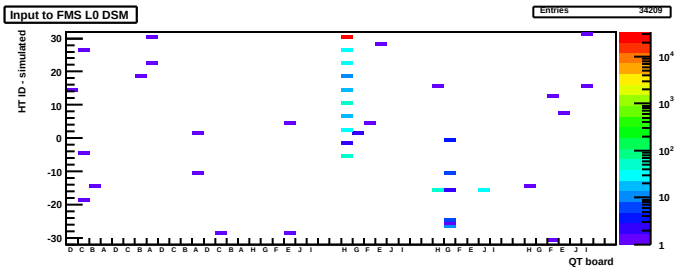

Input to FMS L0 DSM
Entries 1635408
QT8(3) sum
QT board

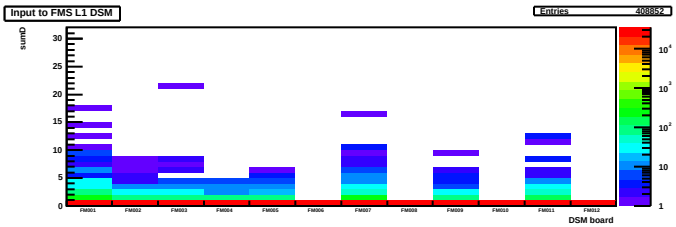

Input to FMS L0 DSM
Entries 31695
QT8(3) sum - simulated
QT board

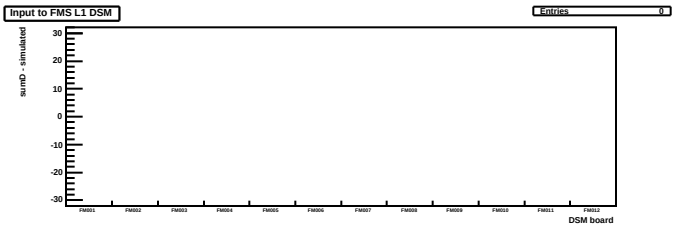

Input to FMS L0 DSM
Entries 1635408
HT ADC
QT board

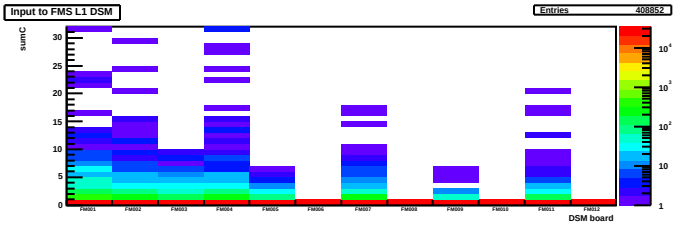

Input to FMS L0 DSM
Entries 56
HT ADC - simulated
QT board

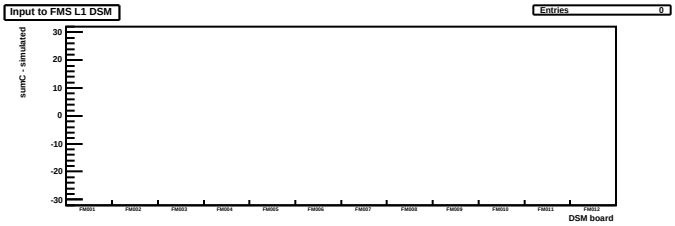

Input to FMS L0 DSM
Entries 1635408
HT ID
QT board

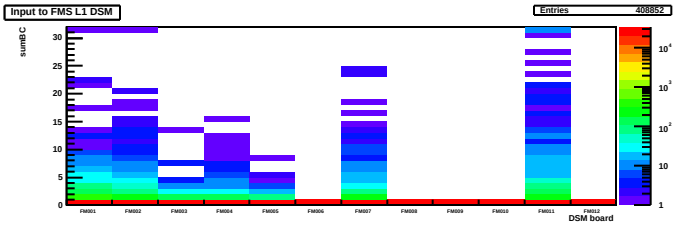

Input to FMS L0 DSM
Entries 34209
HT ID - simulated
QT board

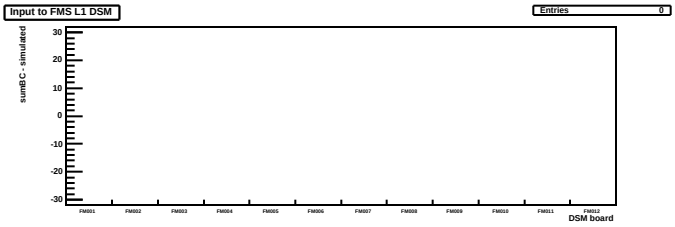

Input to FMS L1 DSM
Entries 408852
sumD
DSM board

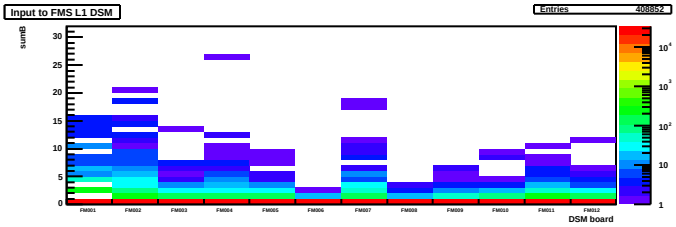

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

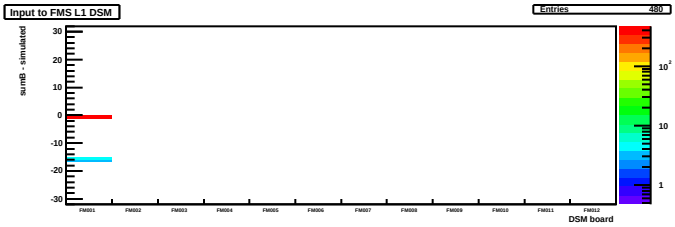

Input to FMS L1 DSM
Entries 408852
sumC
DSM board

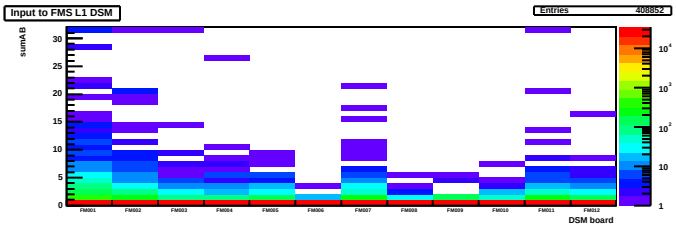

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

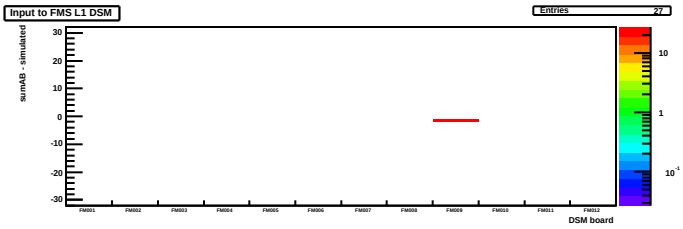

Input to FMS L1 DSM
Entries 408852
sumBC
DSM board

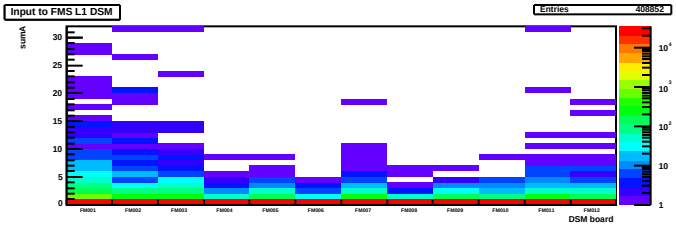

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

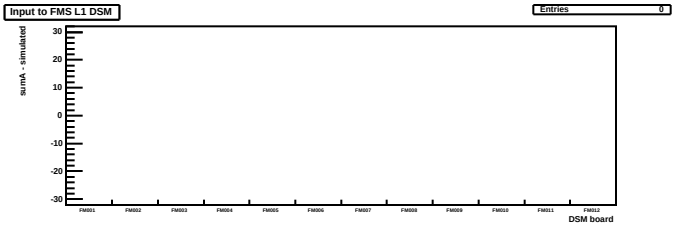

Input to FMS L1 DSM
Entries 408852
sumB
DSM board

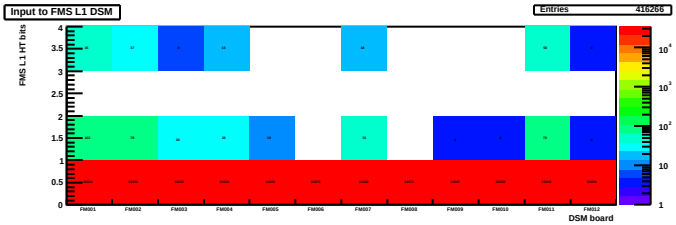

Input to FMS L1 DSM
Entries 480
sumB - simulated
DSM board

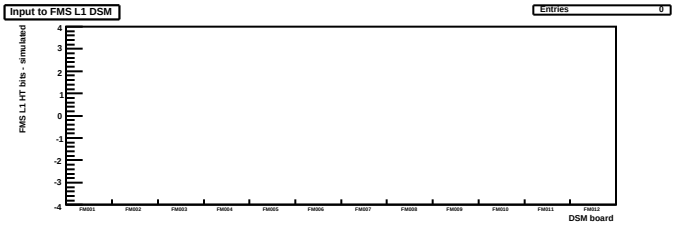

Input to FMS L1 DSM
Entries 408852
sumAB
DSM board

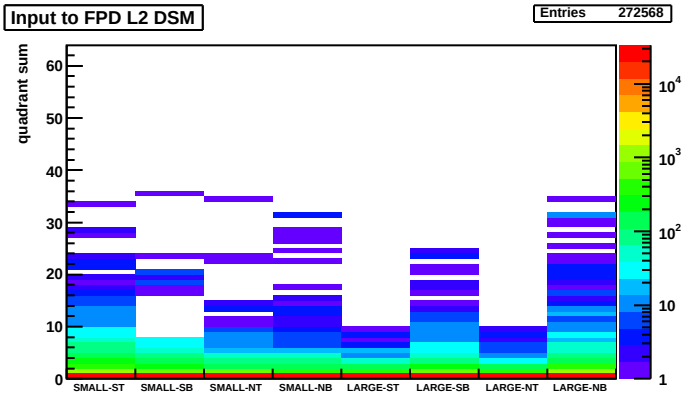

Input to FMS L1 DSM
Entries 27
sumAB - simulated
DSM board

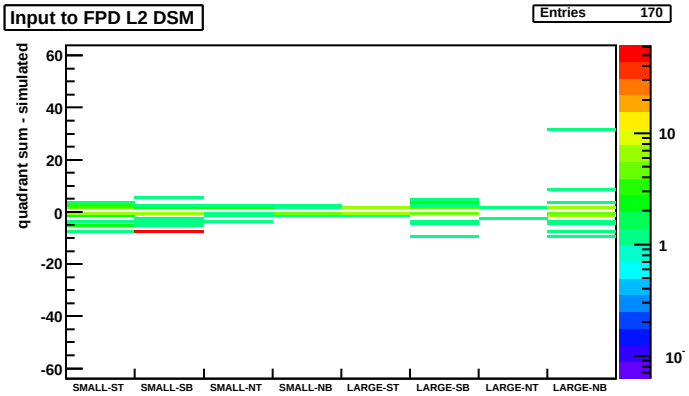

Input to FMS L1 DSM
Entries 408852
sumA
DSM board

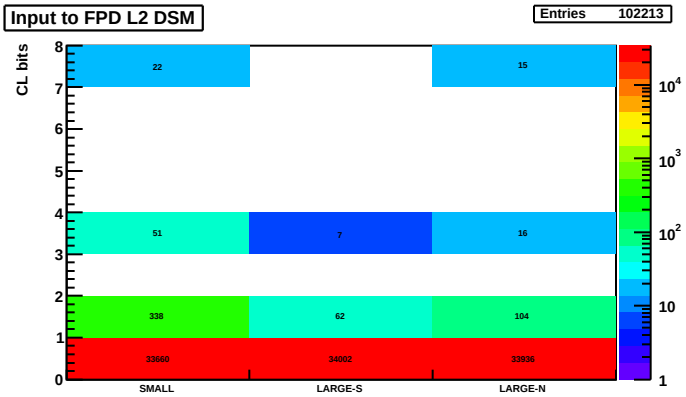

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

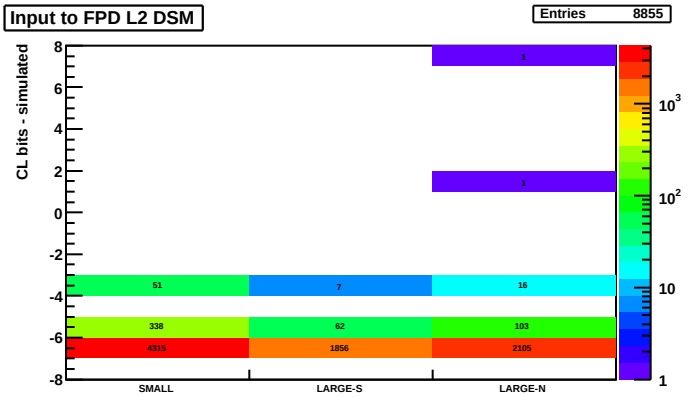

Input to FMS L1 DSM
Entries 416266
FMS L1 HT bits
DSM board

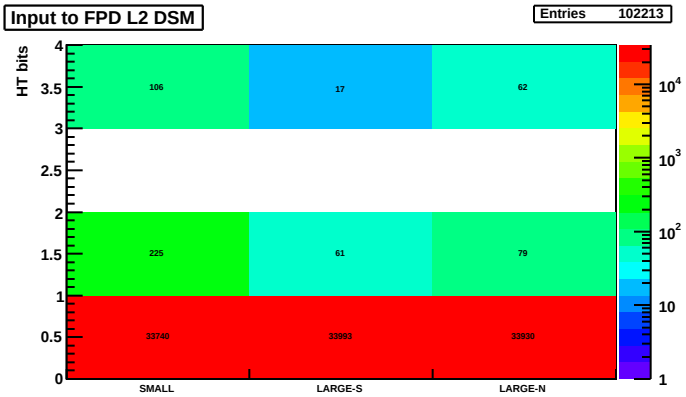

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

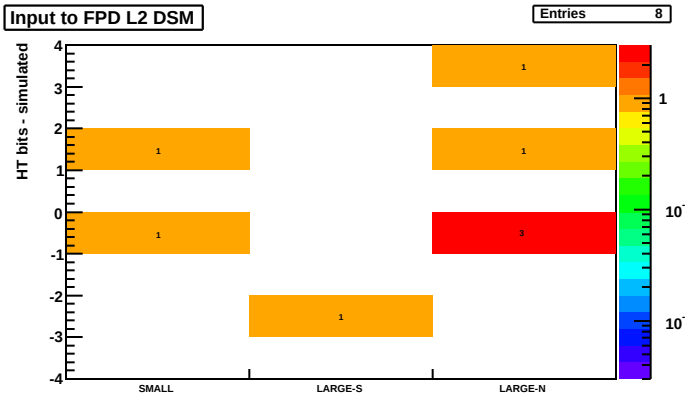

Input to FPD L2 DSM
Entries 272568
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

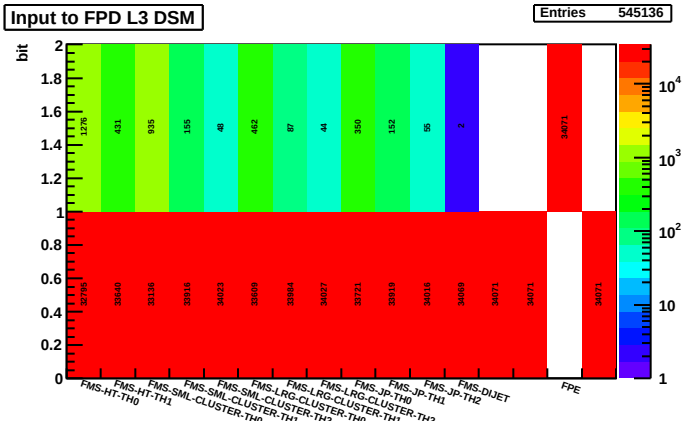

Input to FPD L2 DSM
Entries 170
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

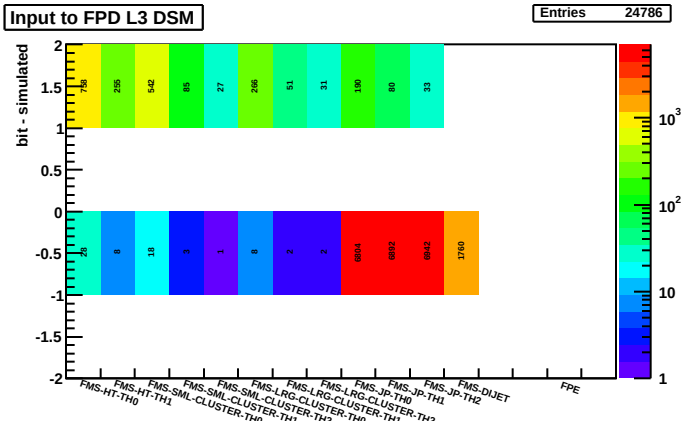

Input to FPD L2 DSM
Entries 102213
CL bits
22
15
51
7
16
338
62
104
33660
34002
33936
SMALL
LARGE-S
LARGE-N

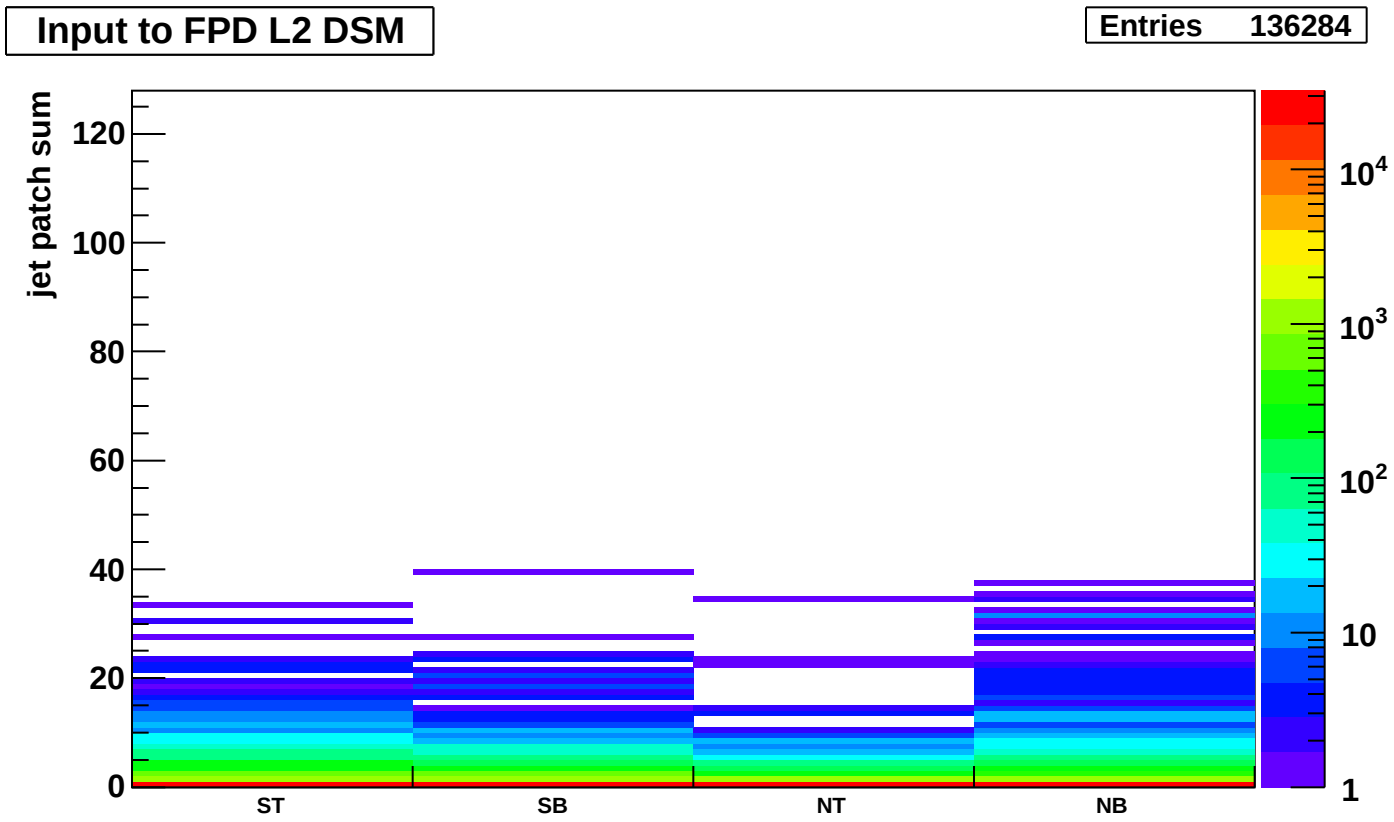

Input to FPD L2 DSM
Entries 8855
CL bits - simulated
1
1
51
7
16
338
62
103
4315
1856
2105
SMALL
LARGE-S
LARGE-N

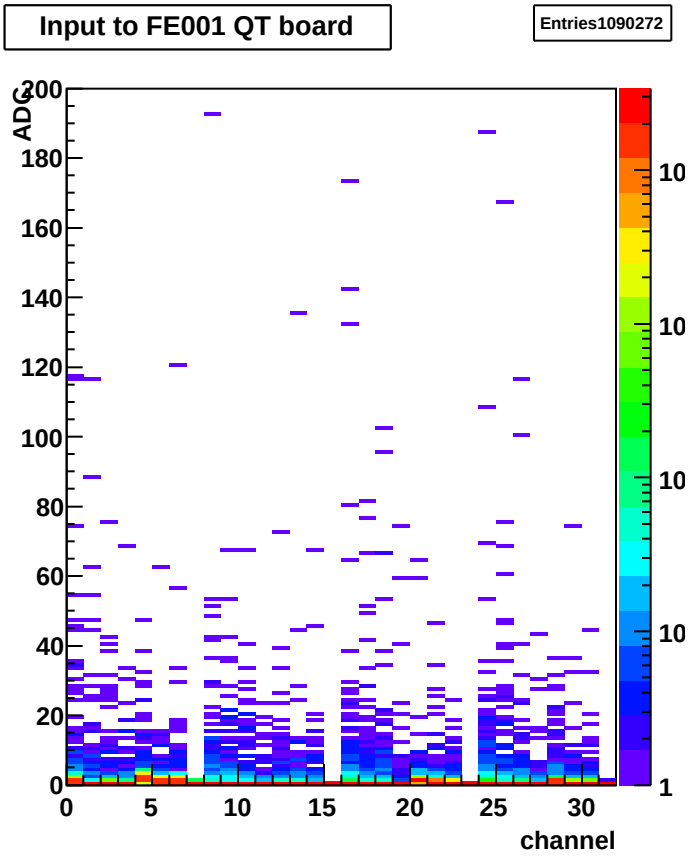

Input to FPD L2 DSM
Entries 102213
HT bits
106
17
62
225
61
79
33740
33993
33930
SMALL
LARGE-S
LARGE-N

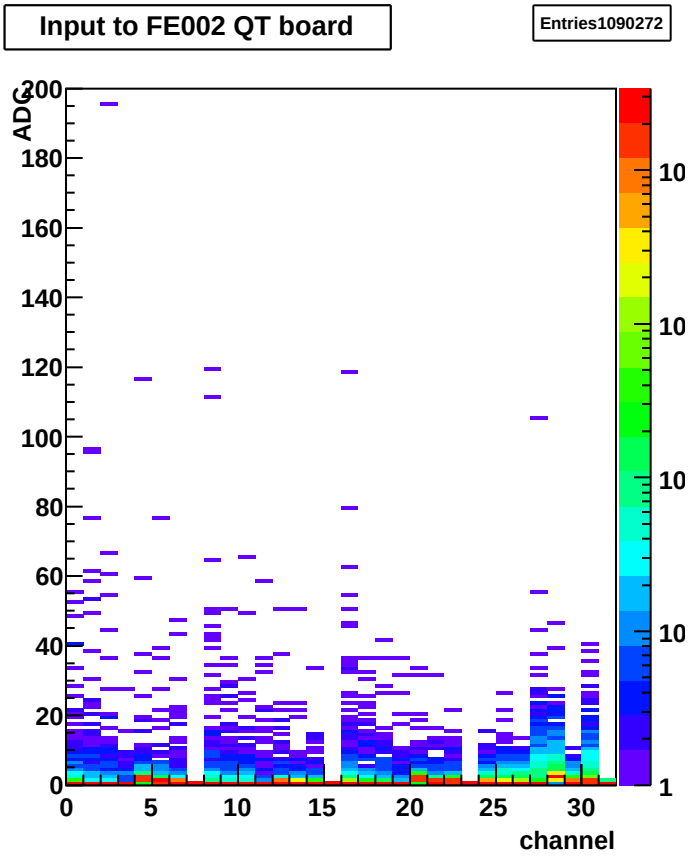

Input to FPD L2 DSM
Entries 8
HT bits - simulated
1
1
1
1
1
3
SMALL
LARGE-S
LARGE-N

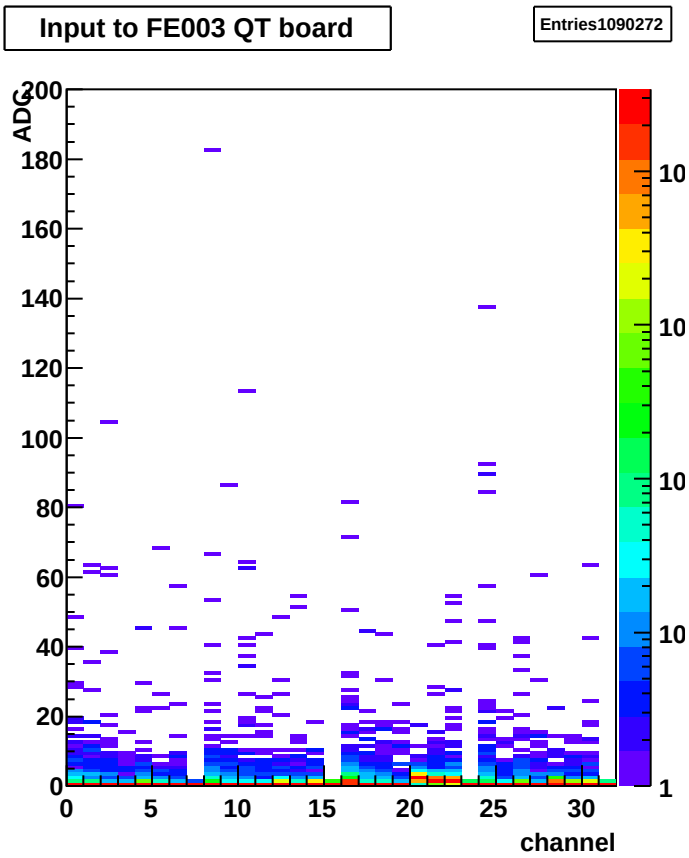

Input to FPD L3 DSM
Entries 545136
bit
1276
431
935
155
48
462
87
44
350
152
55
2
34071
32795
33640
33136
33916
34023
33609
33984
34027
33721
33919
34016
34069
34071
34071
34071
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

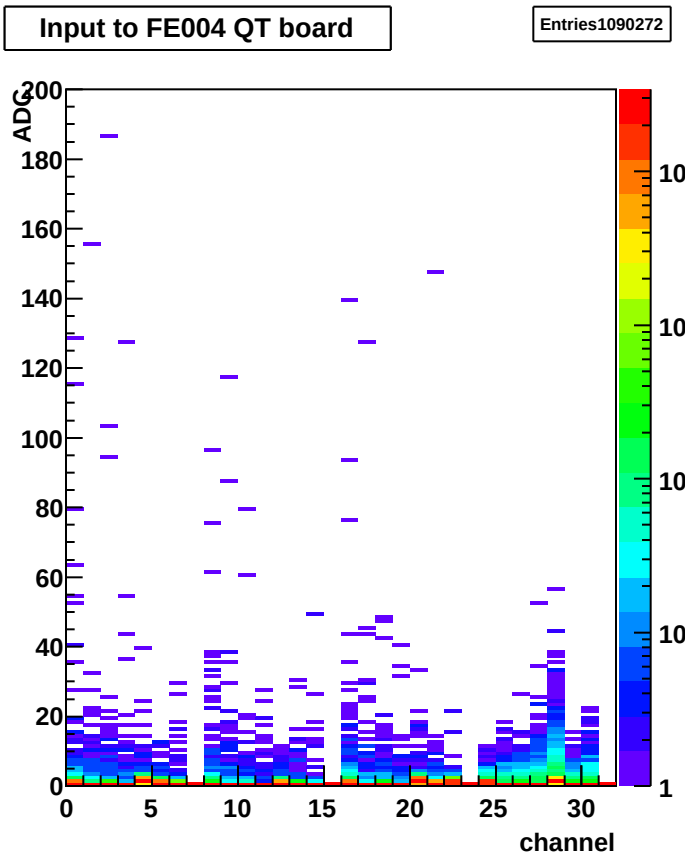

Input to FPD L3 DSM
Entries 24786
bit - simulated
758
265
542
85
27
266
51
31
190
80
33
26
8
18
3
1
8
2
2
6804
6692
6942
1760
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

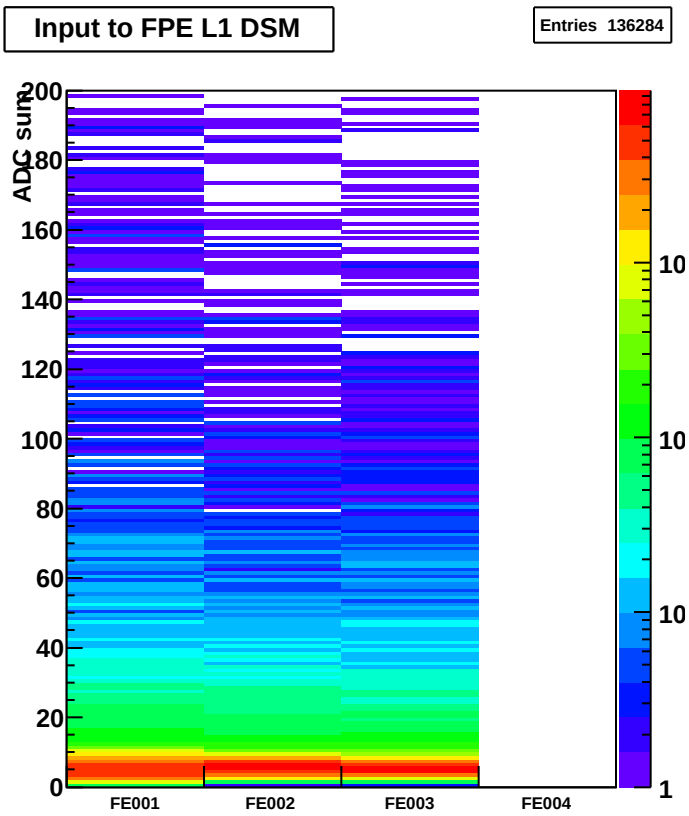

Input to FPD L2 DSM
Entries 136284
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

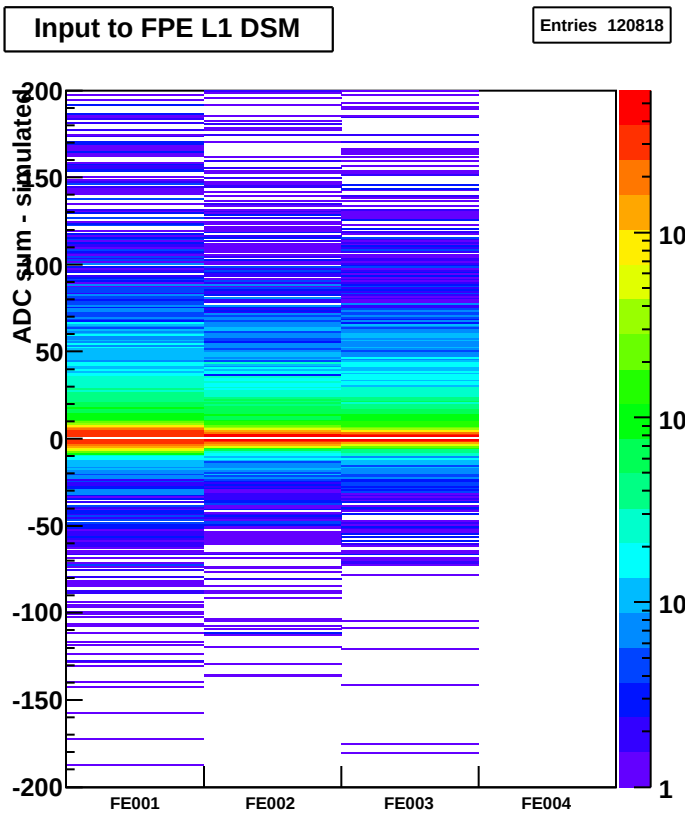

Input to FE001 QT board
Entries1090272
ADC
channel

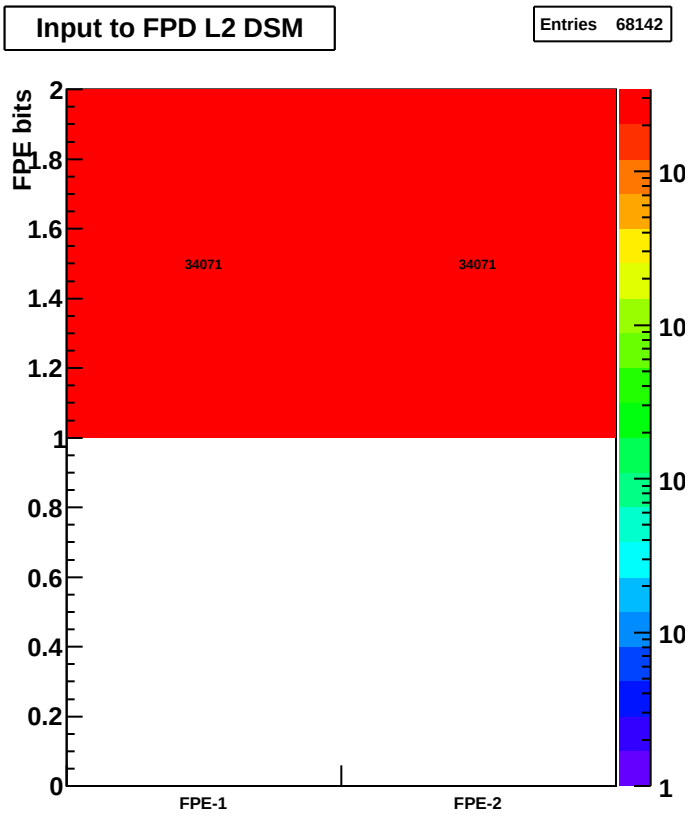

Input to FE002 QT board
Entries1090272
ADC
channel

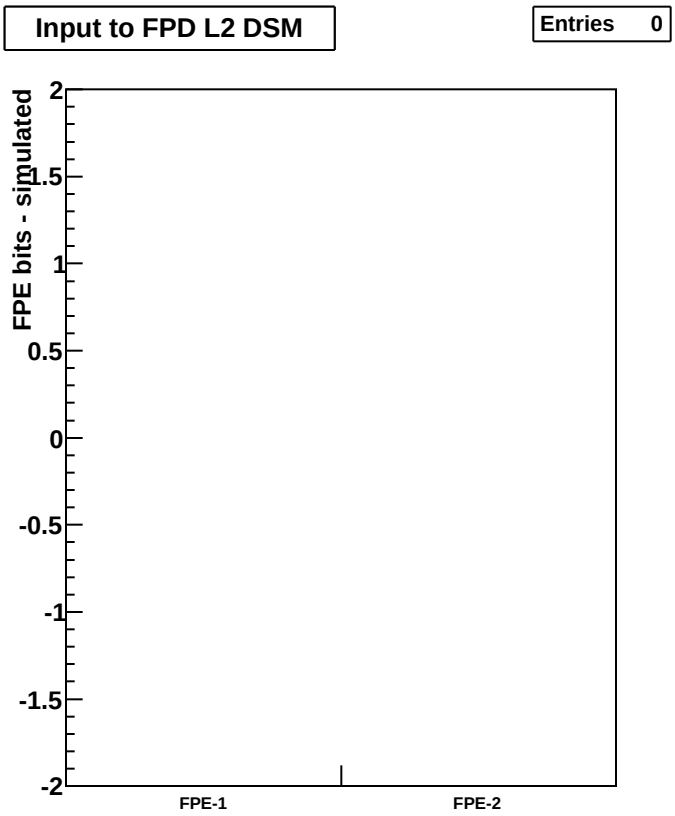

Input to FE003 QT board
Entries1090272
ADC
channel


Input to FE004 QT board
Entries1090272
ADC
channel


Input to FPE L1 DSM
Entries 136284
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 120818
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 68142
FPE bits
34071
34071
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2