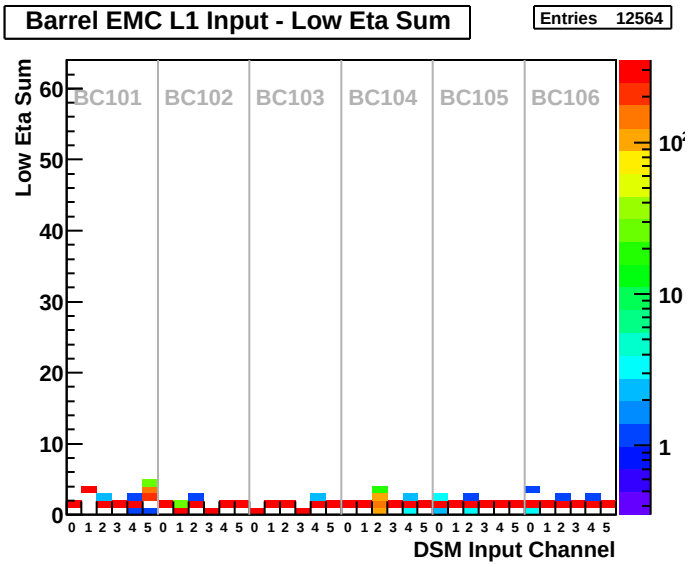
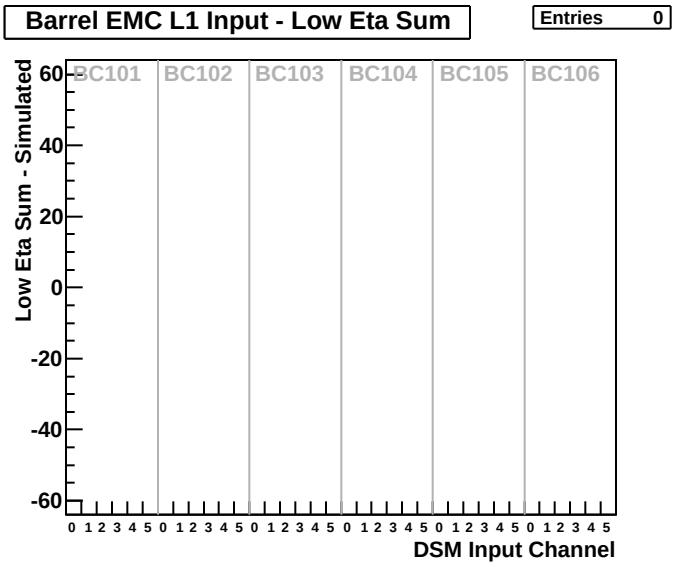
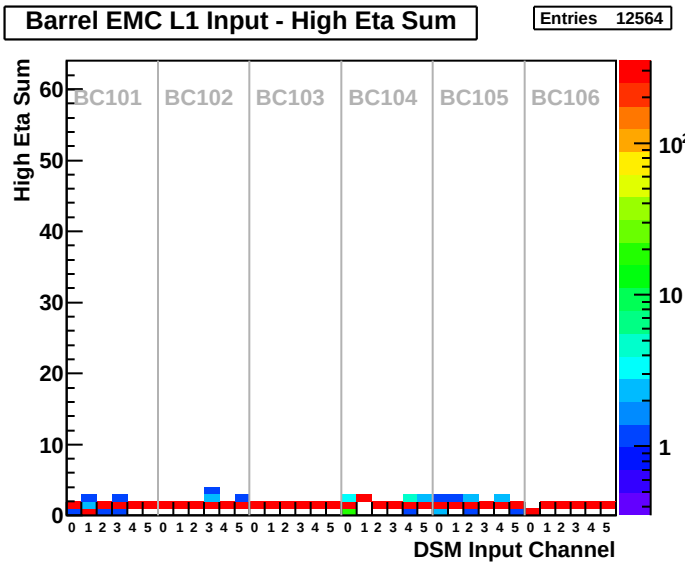
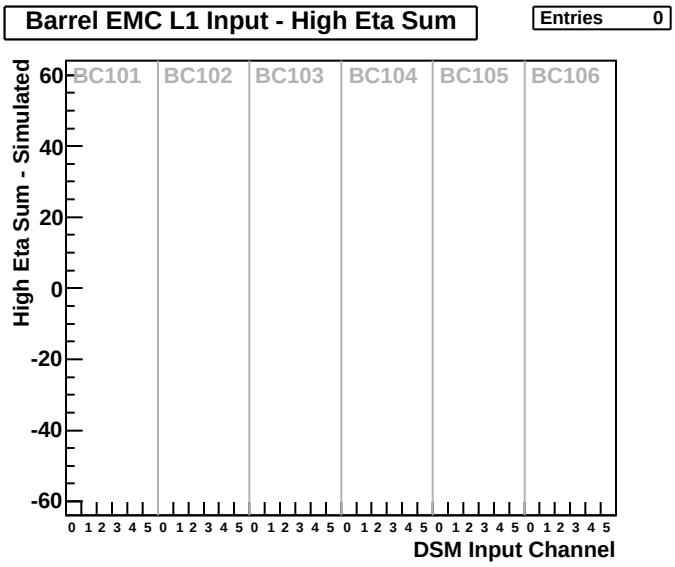
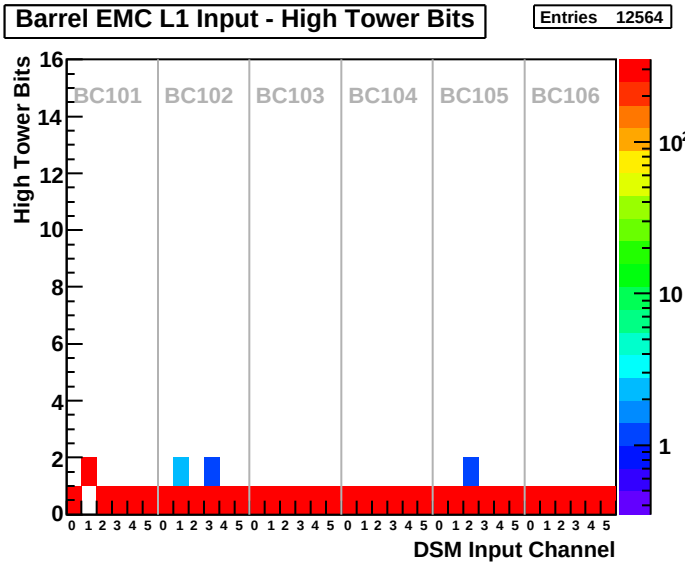
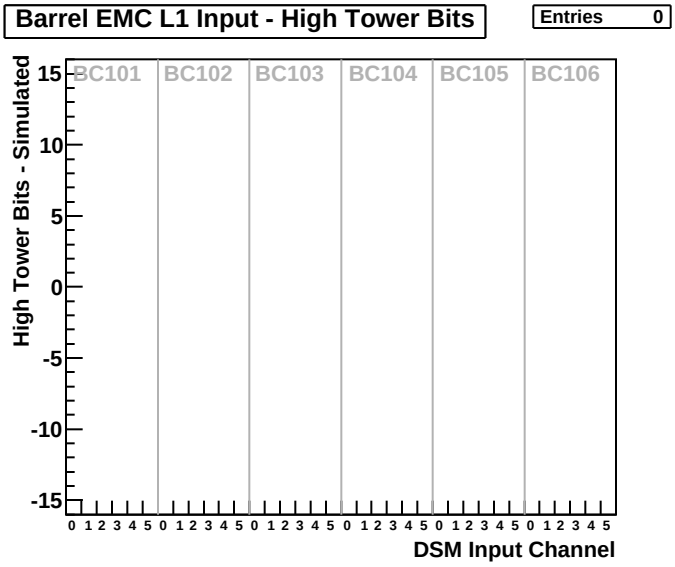

Barrel EMC L1 Input - Low Eta Sum
Entries 12564
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

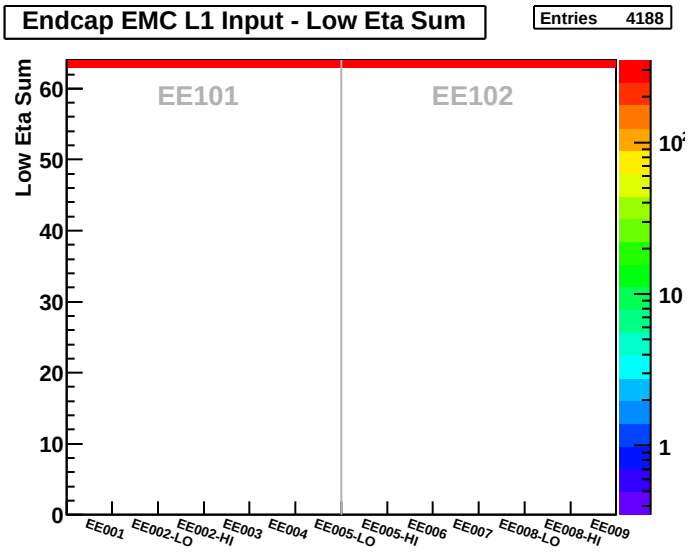

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

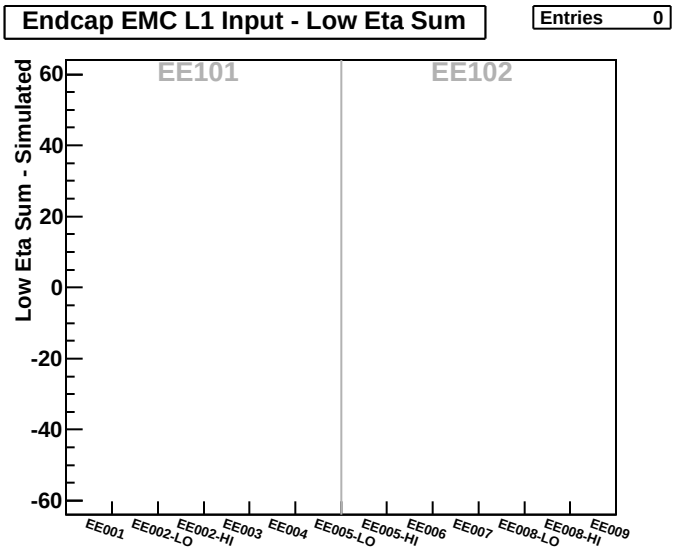

Barrel EMC L1 Input - High Eta Sum
Entries 12564
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

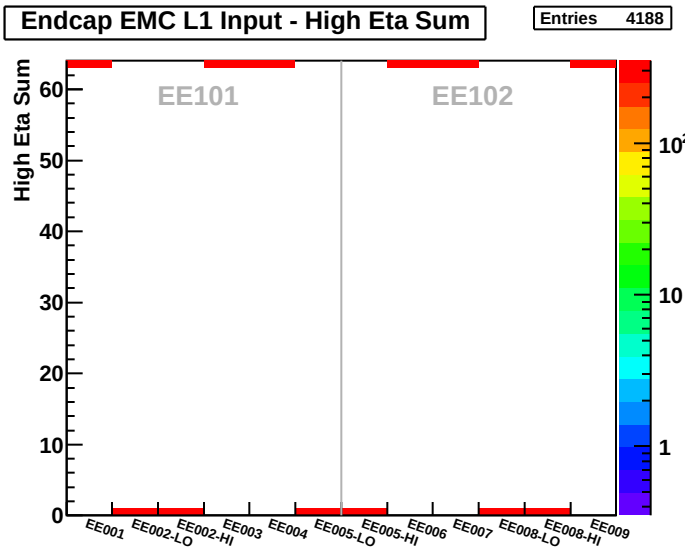

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

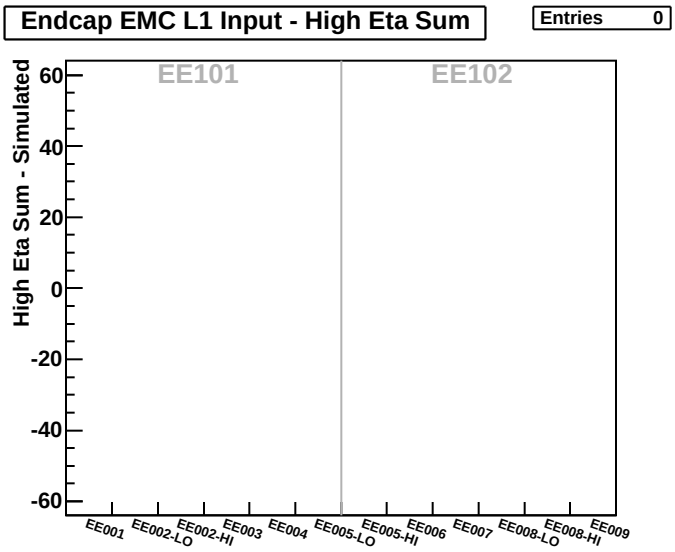

Barrel EMC L1 Input - High Tower Bits
Entries 12564
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

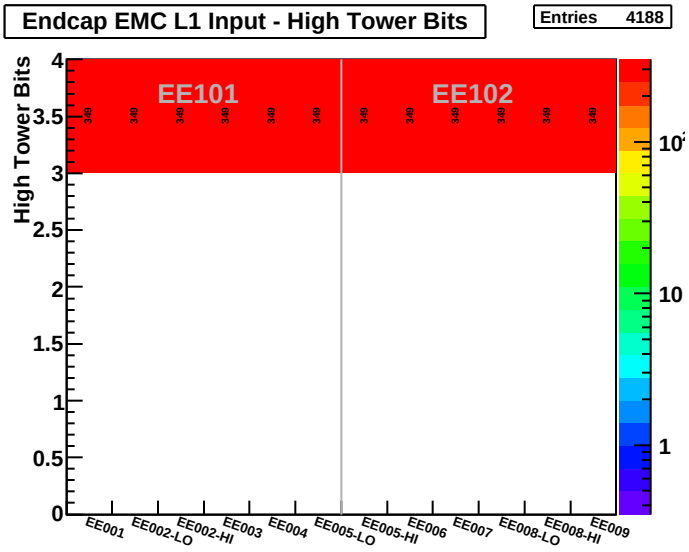

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

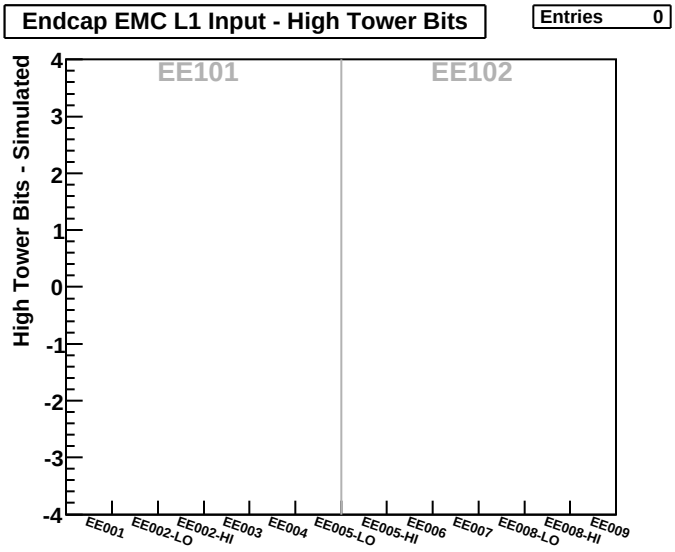

Endcap EMC L1 Input - Low Eta Sum
Entries 4188
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

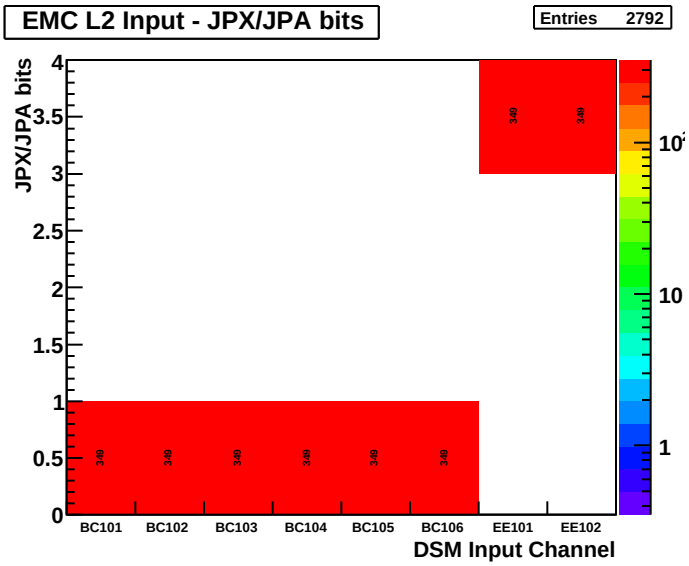

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

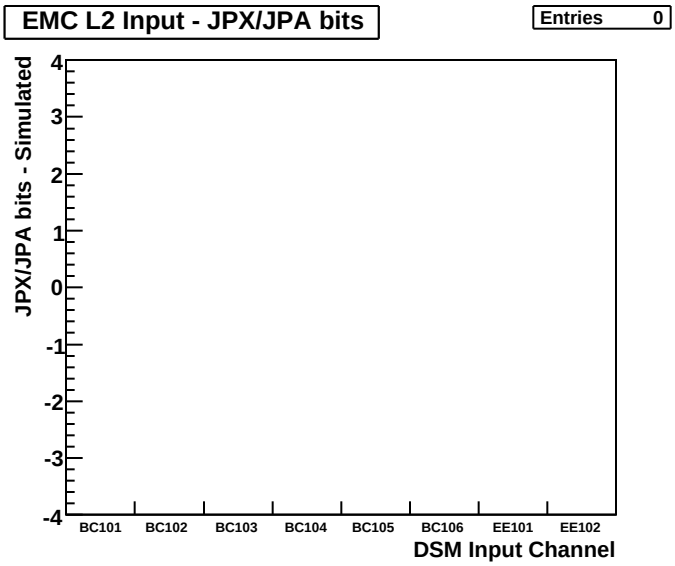

Endcap EMC L1 Input - High Eta Sum
Entries 4188
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

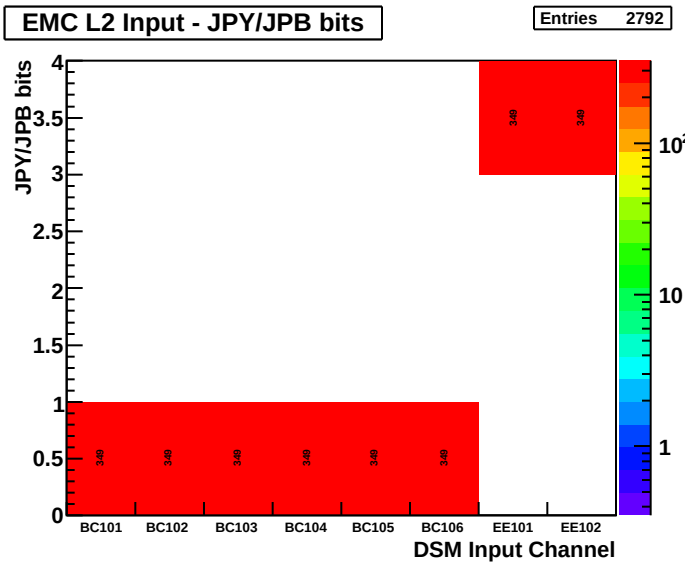

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

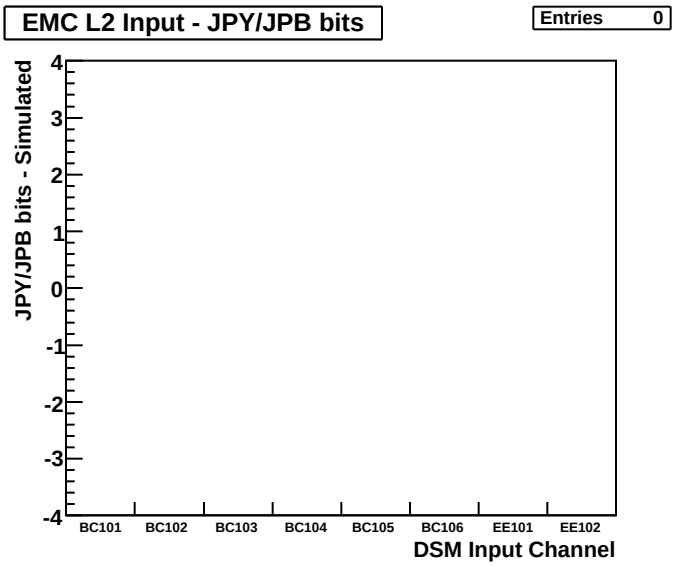

Endcap EMC L1 Input - High Tower Bits
Entries 4188
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

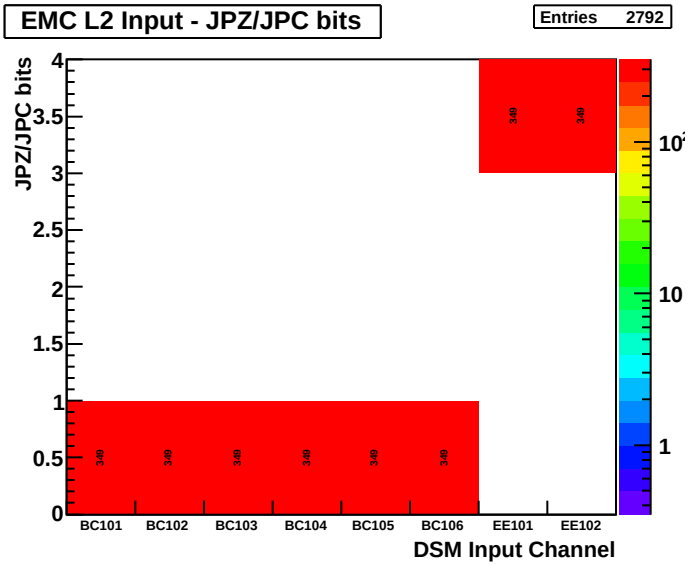

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

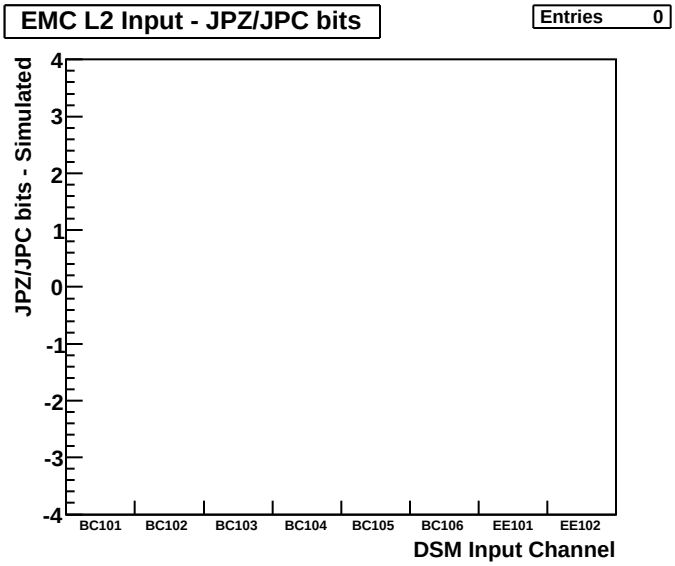

EMC L2 Input - JPX/JPA bits
Entries 2792
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

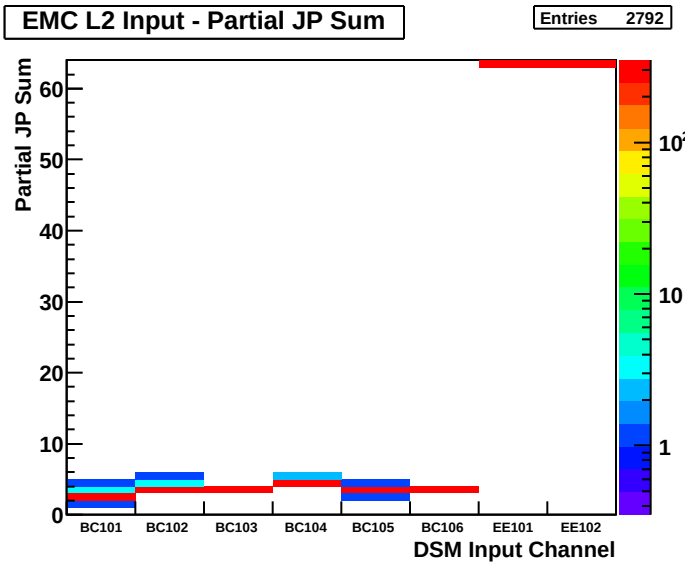

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

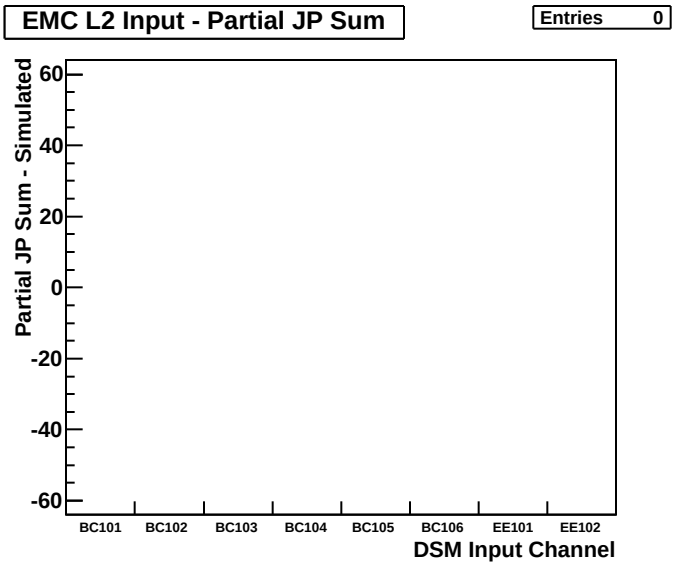

EMC L2 Input - JPY/JPB bits
Entries 2792
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

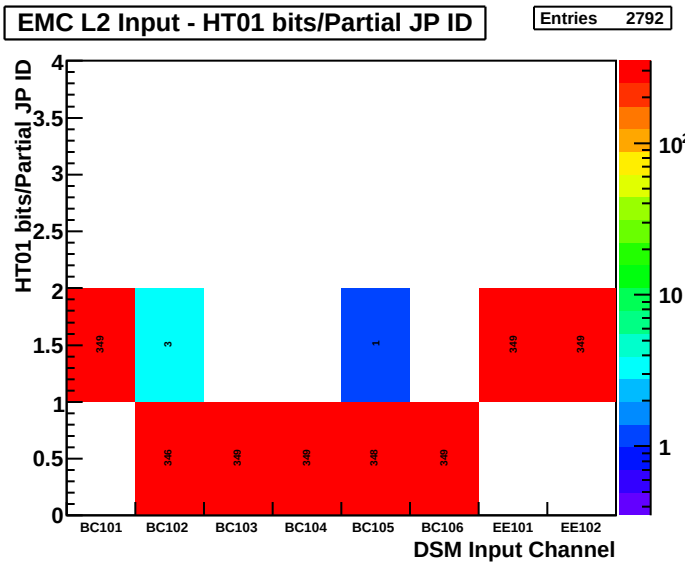

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

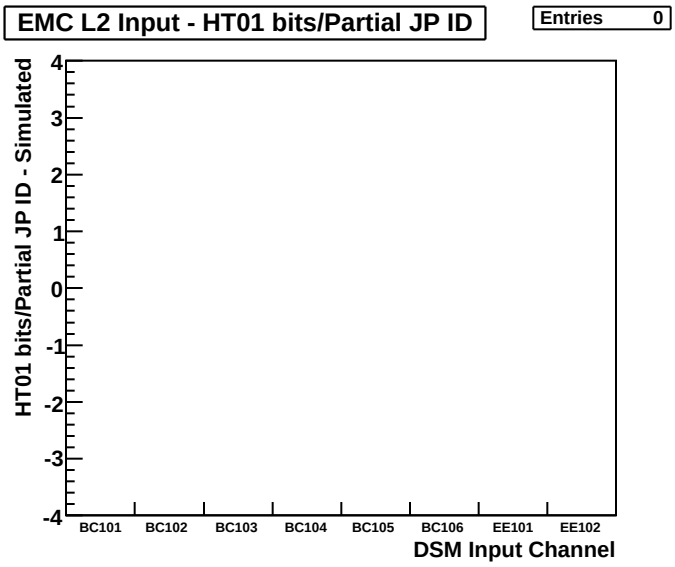

EMC L2 Input - JPZ/JPC bits
Entries 2792
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

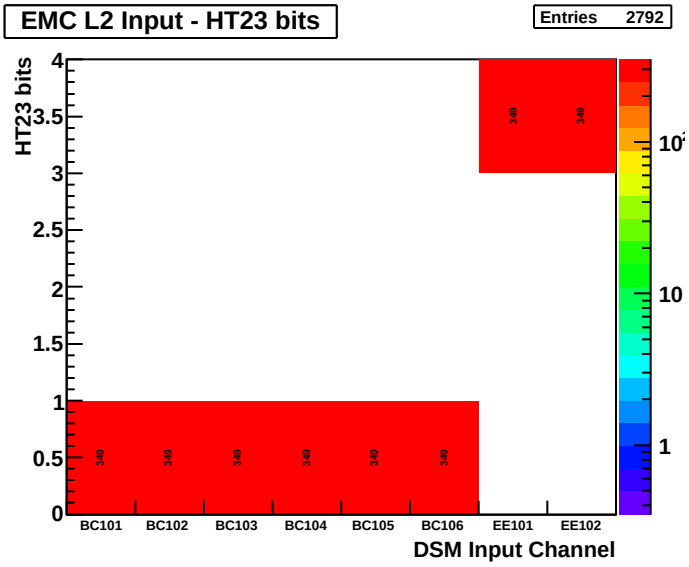

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

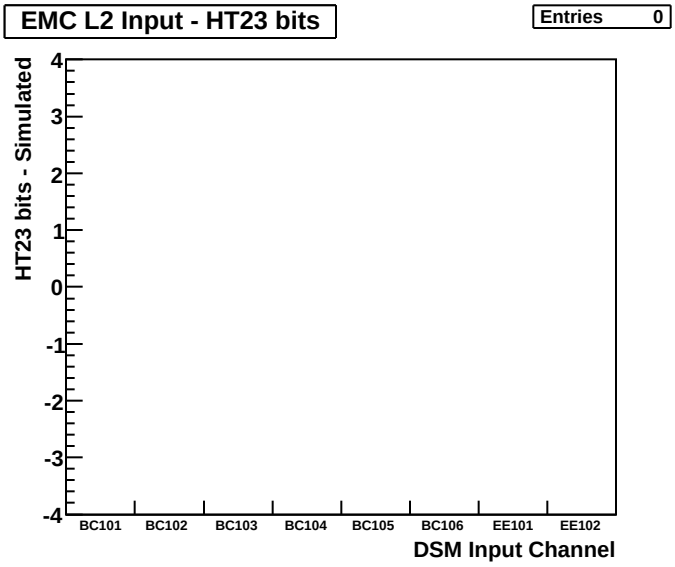

EMC L2 Input - Partial JP Sum
Entries 2792
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

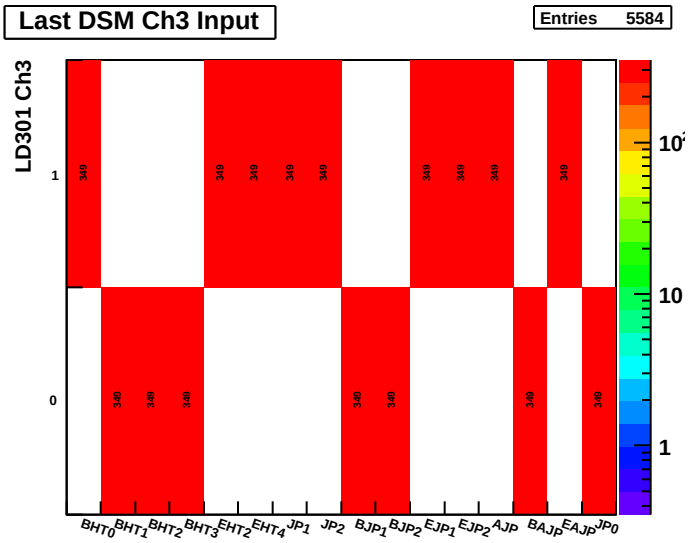

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

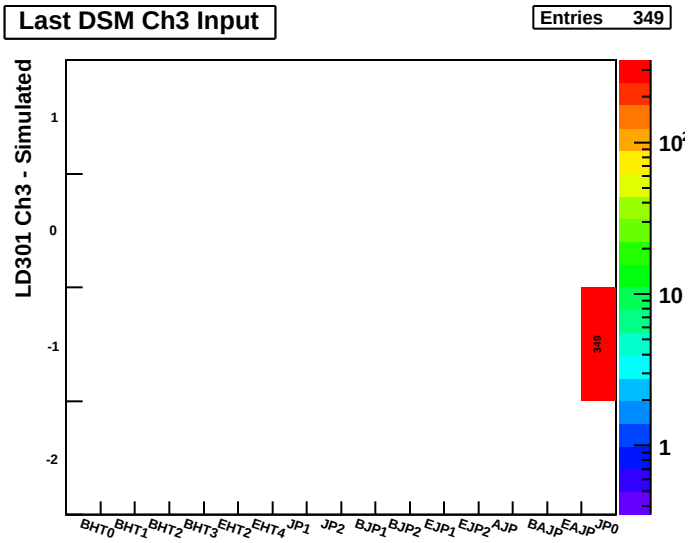

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2792
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

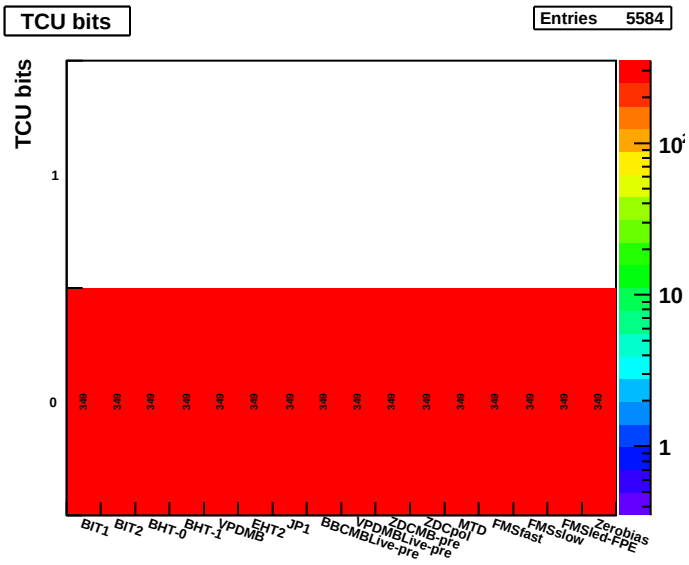

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

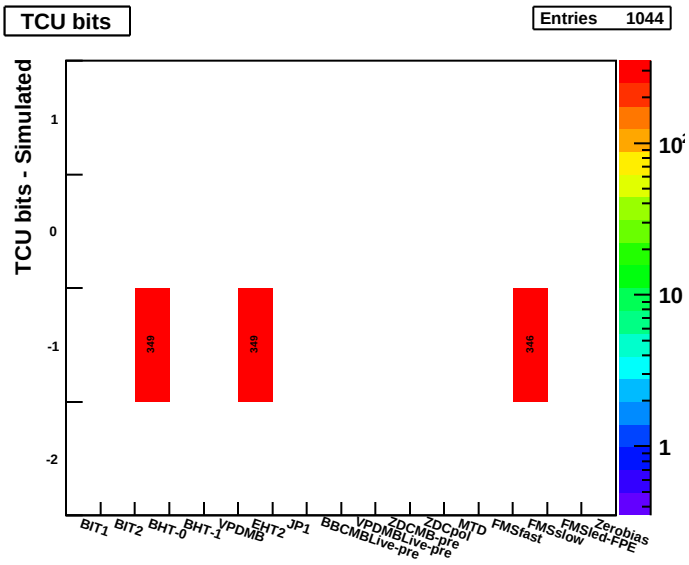

EMC L2 Input - HT23 bits
Entries 2792
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

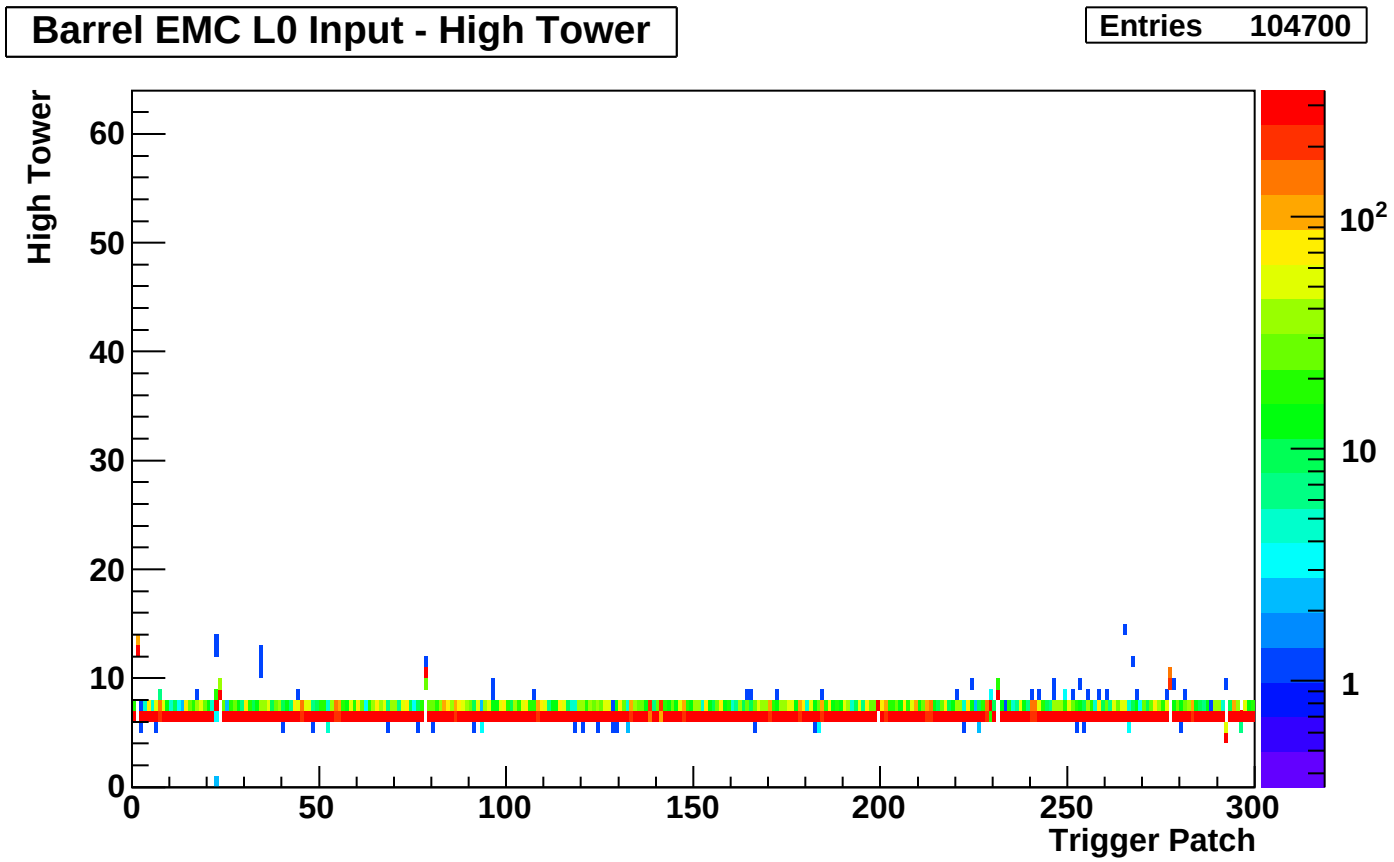

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

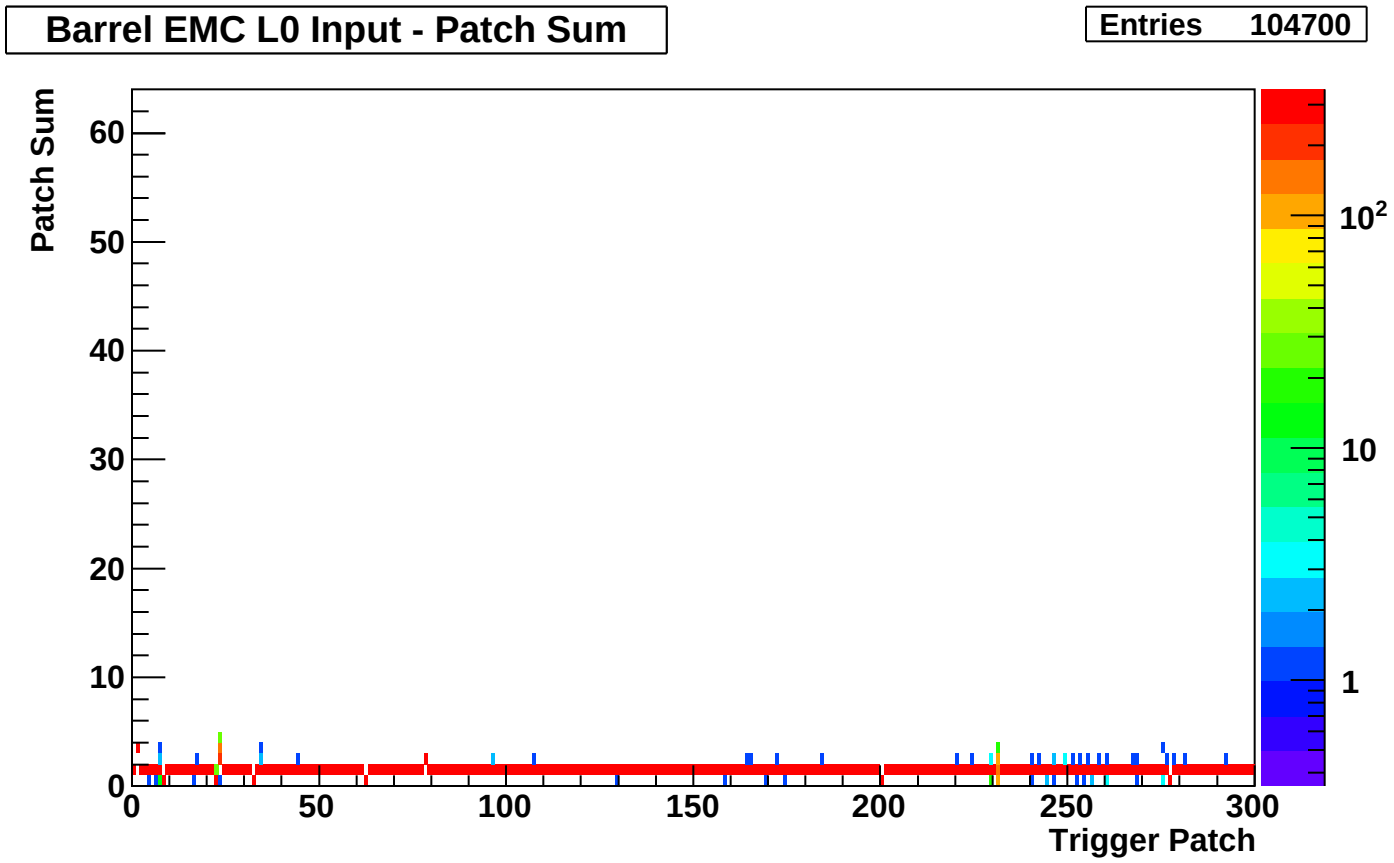

Last DSM Ch3 Input
Entries 5584
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

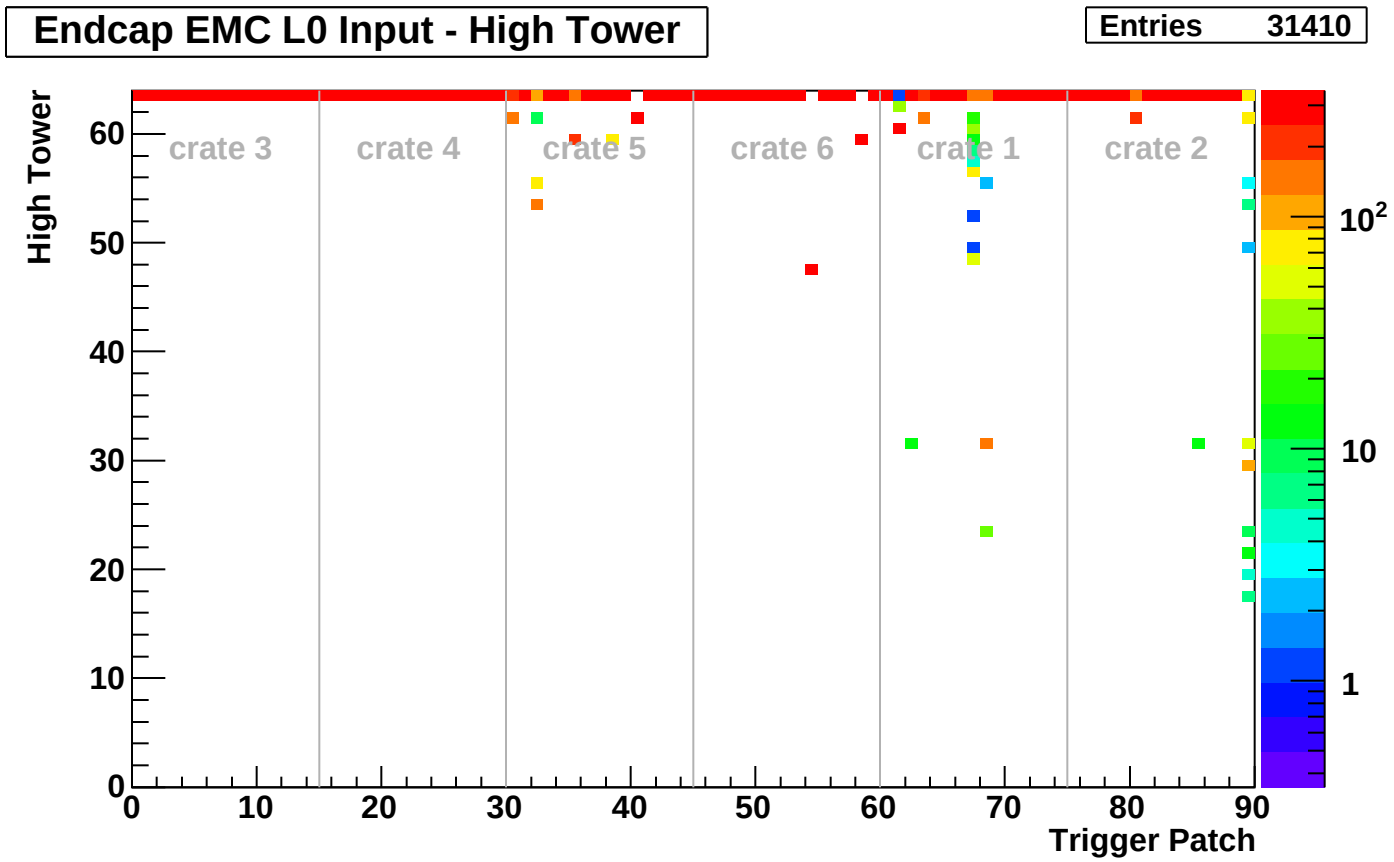

Last DSM Ch3 Input
Entries 349
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

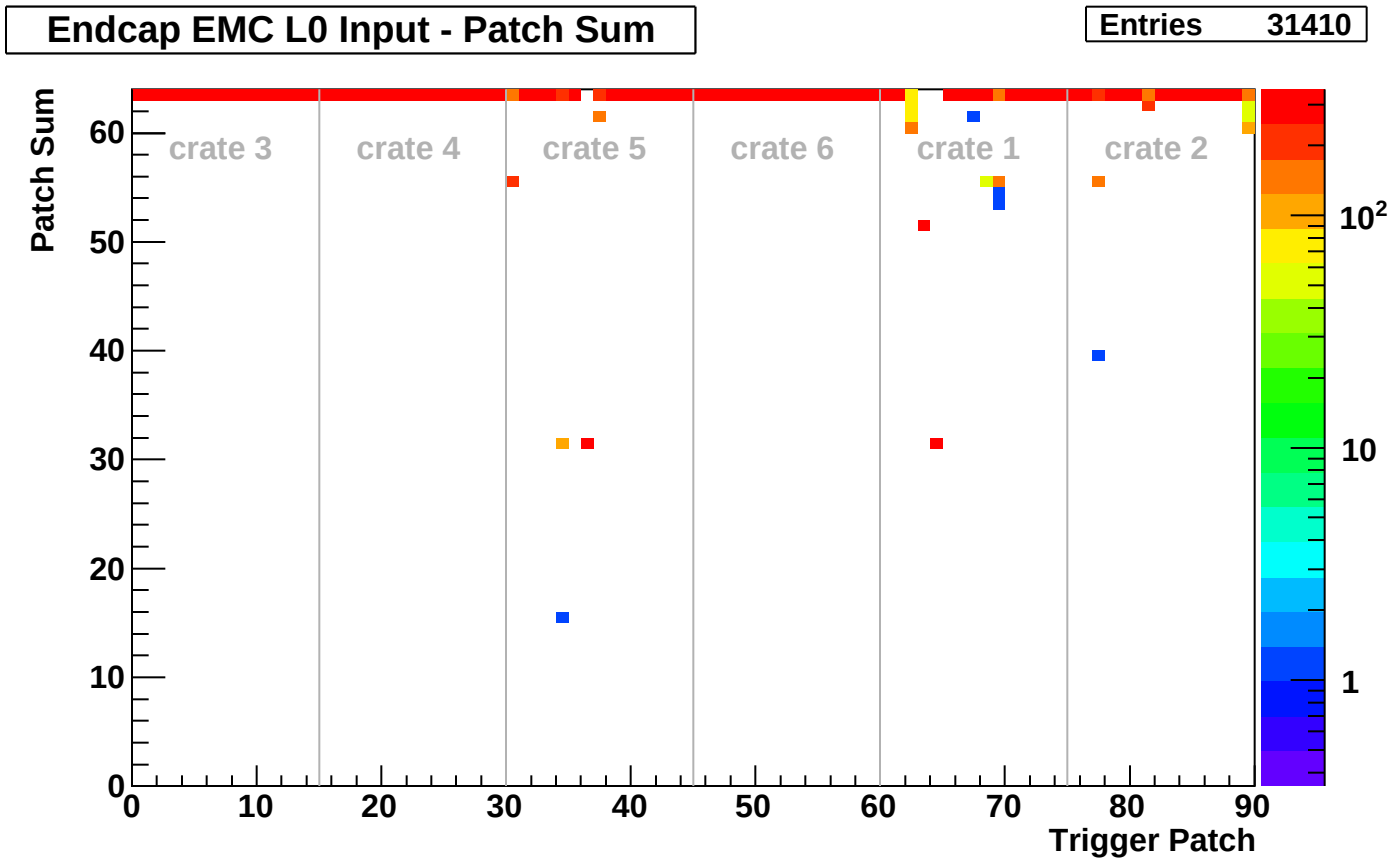

TCU bits
Entries 5584
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

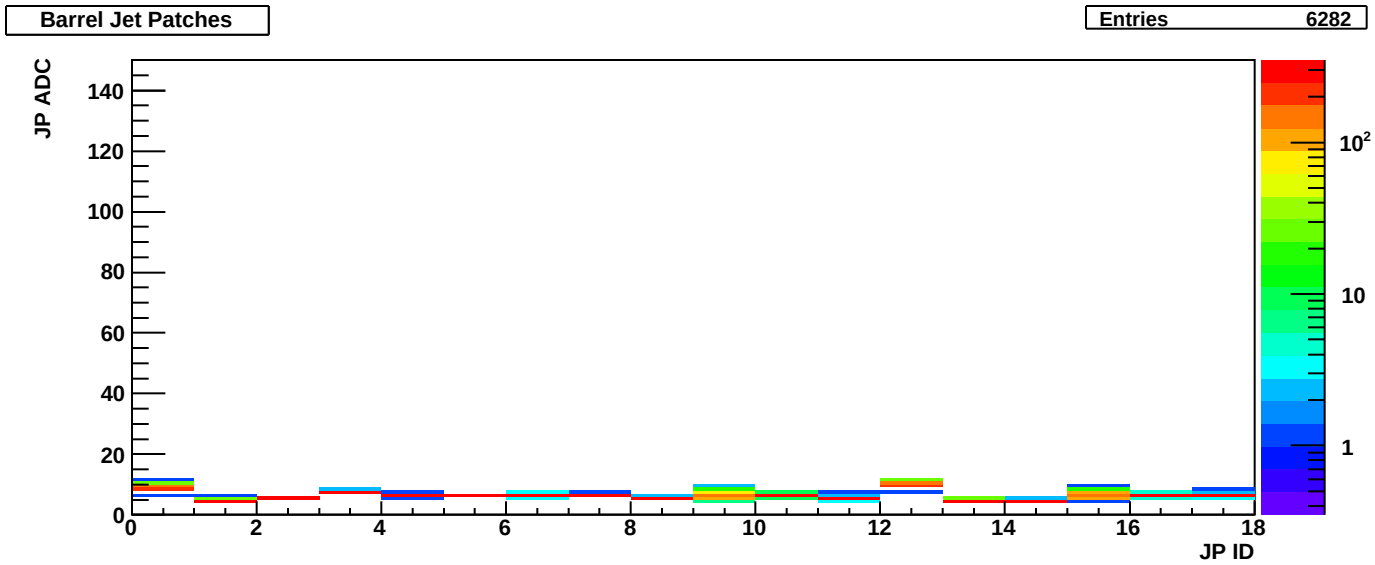

TCU bits
Entries 1044
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

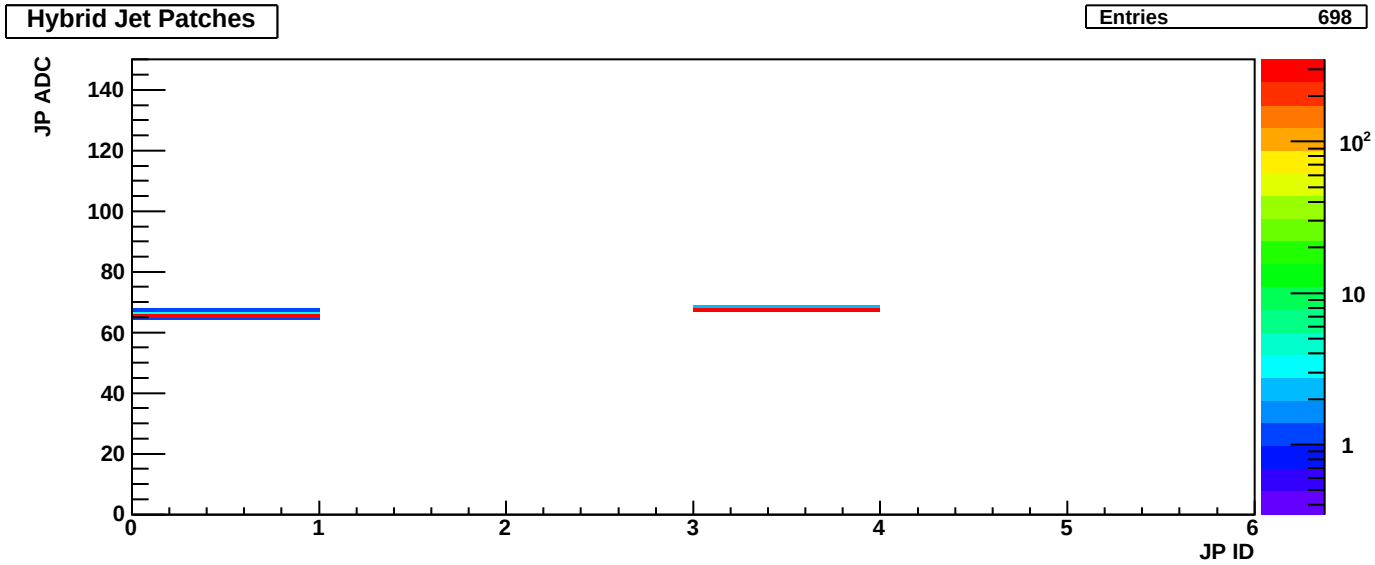

Barrel EMC L0 Input - High Tower
Entries 104700
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
10^2
10
1


Barrel EMC L0 Input - Patch Sum
Entries 104700
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
10^2
10
1

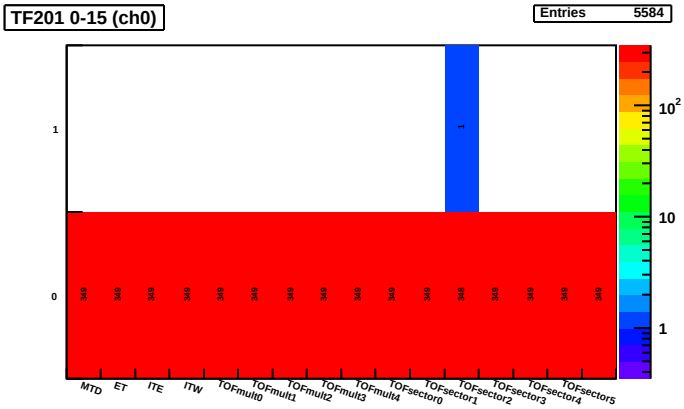

Endcap EMC L0 Input - High Tower
Entries 31410
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2


Endcap EMC L0 Input - Patch Sum
Entries 31410
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

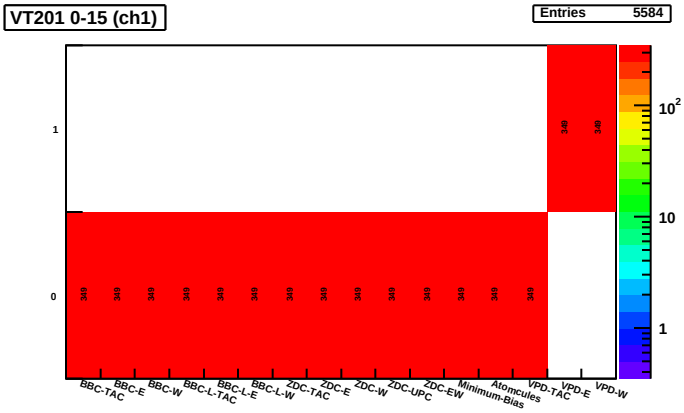

Barrel Jet Patches
Entries 6282
JP ADC
JP ID


Hybrid Jet Patches
Entries 698
JP ADC
JP ID

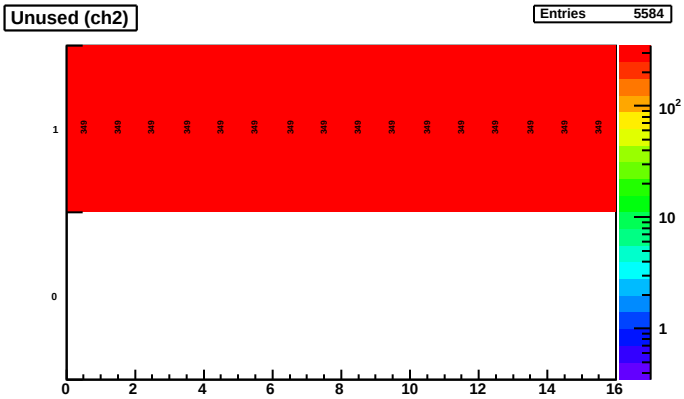
TF201 0-15 (ch0)



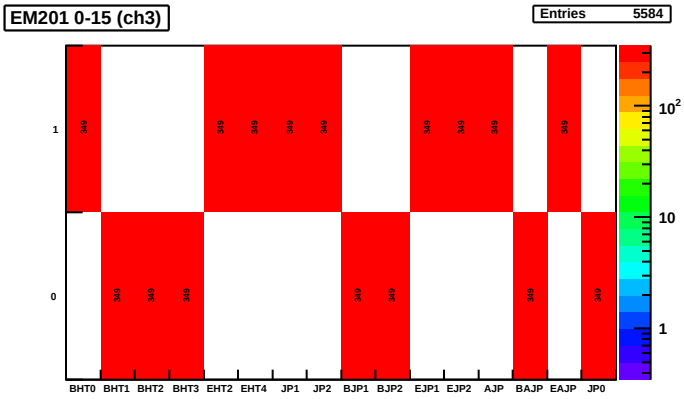
VT201 0-15 (ch1)



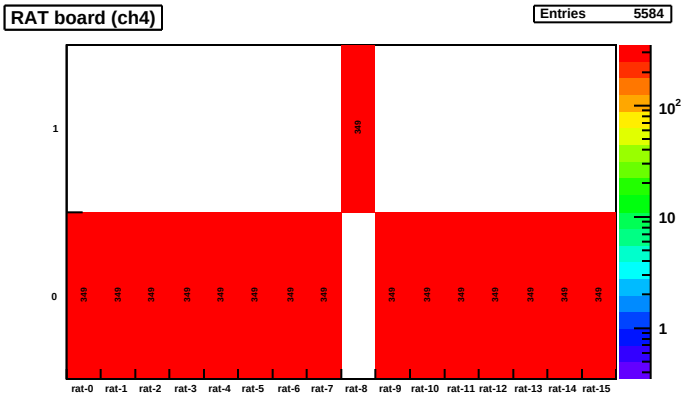
Unused (ch2)



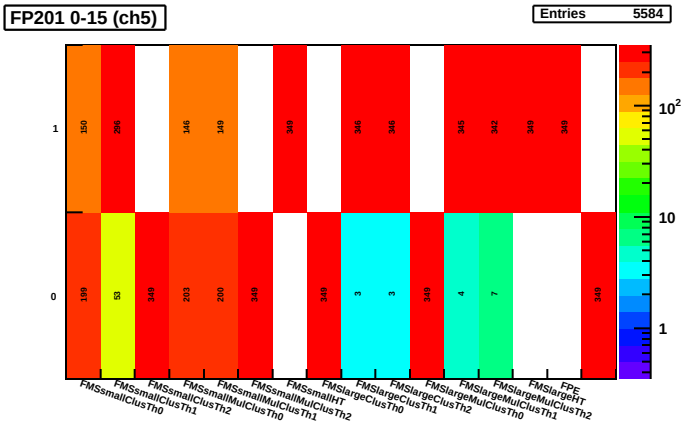
EM201 0-15 (ch3)



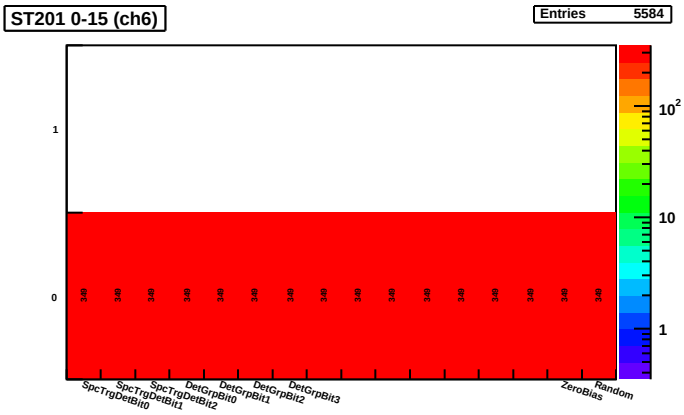
RAT board (ch4)



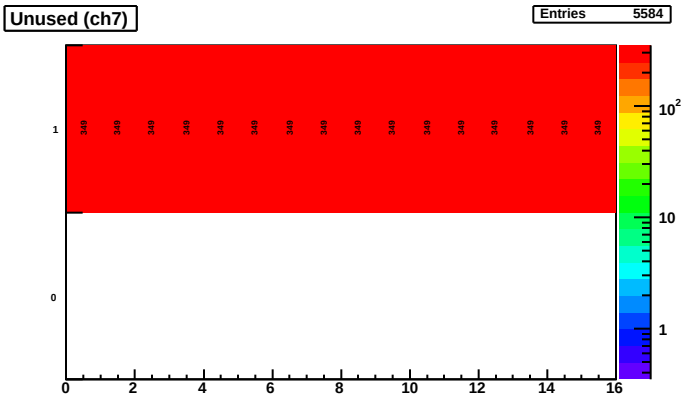
FP201 0-15 (ch5)

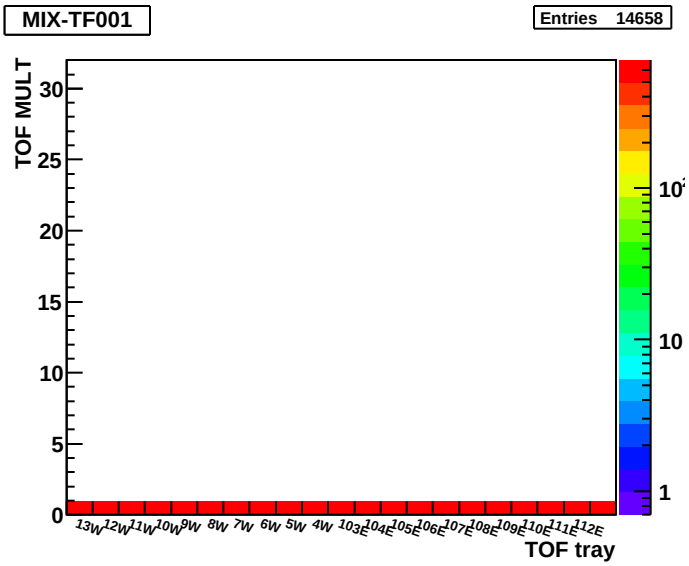


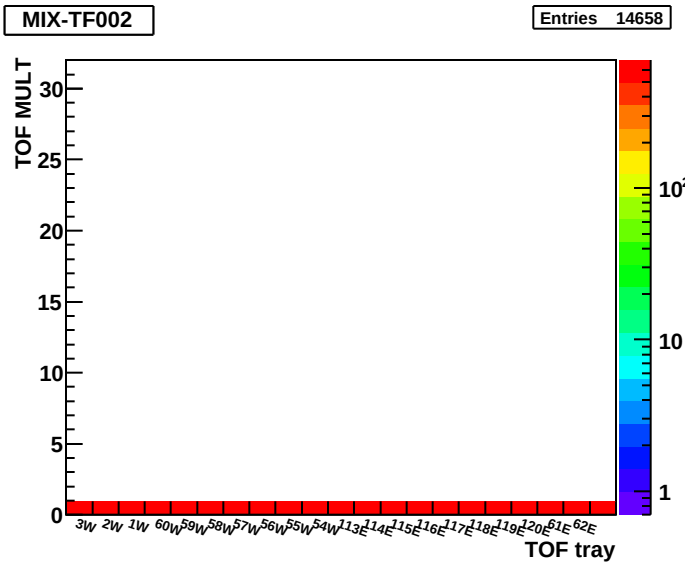
ST201 0-15 (ch6)

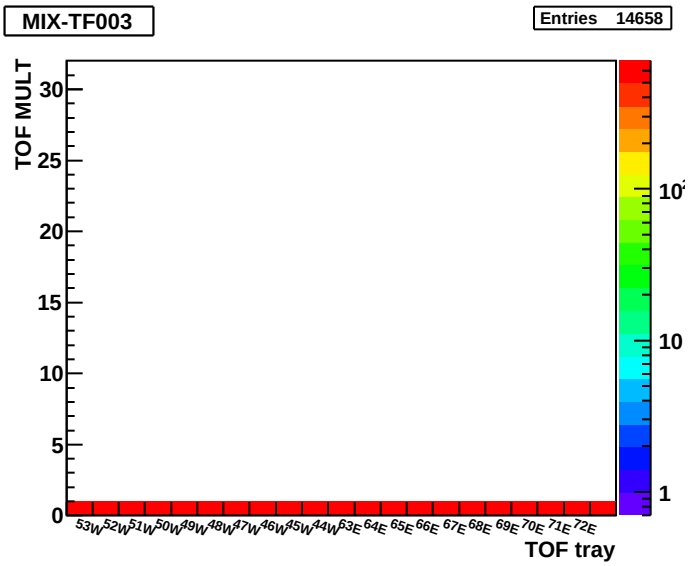


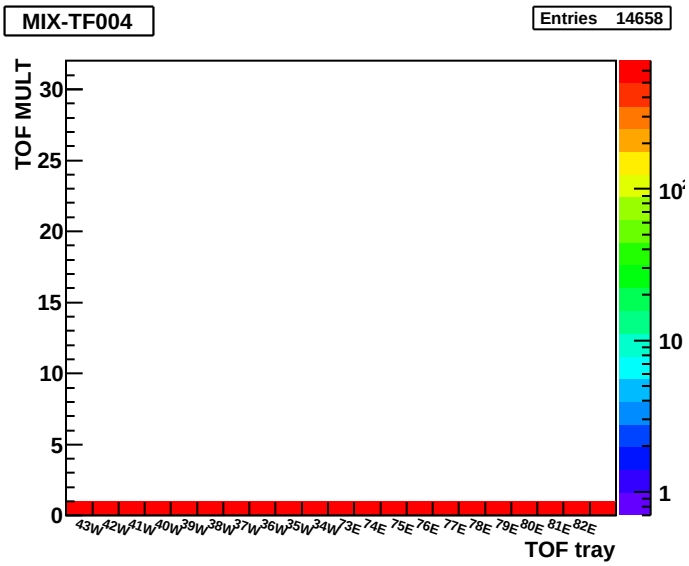
Unused (ch7)

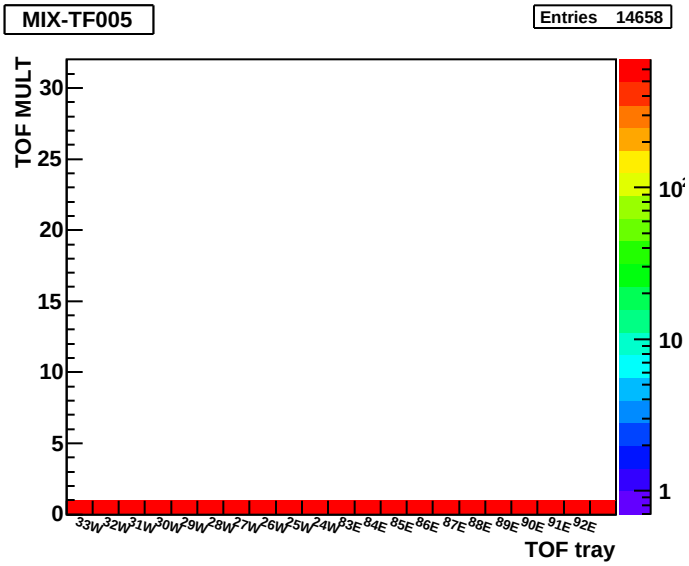


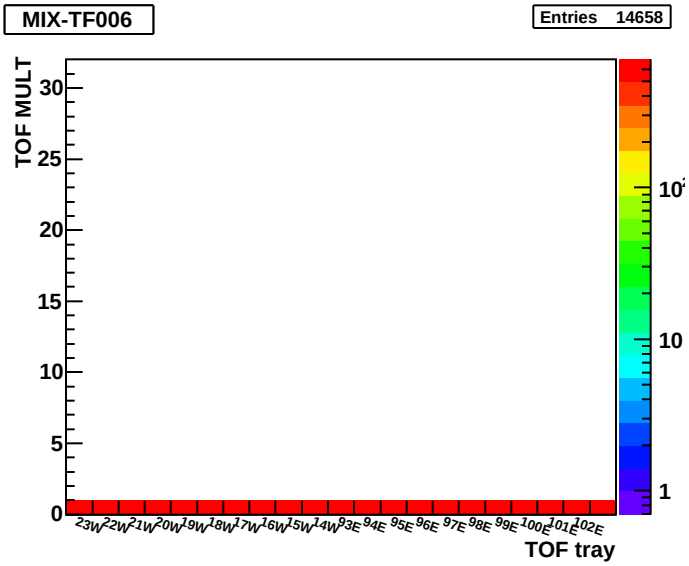

MIX-TF001
Entries 14658
TOF MULT
TOF tray

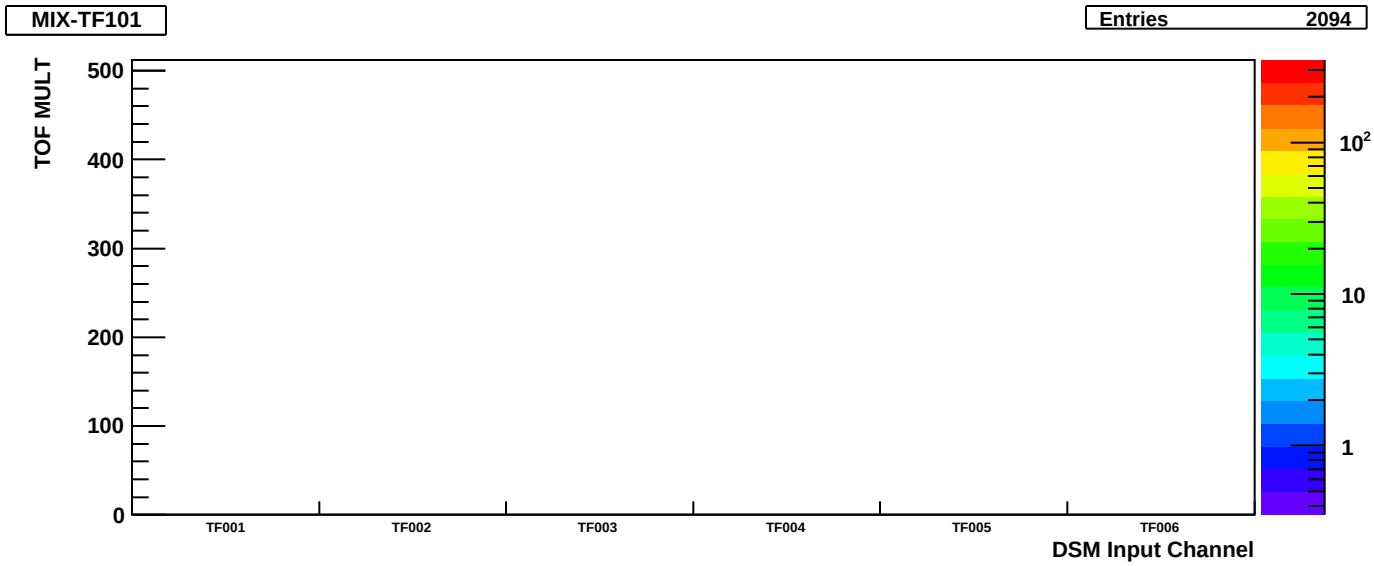

MIX-TF002
Entries 14658
TOF MULT
TOF tray

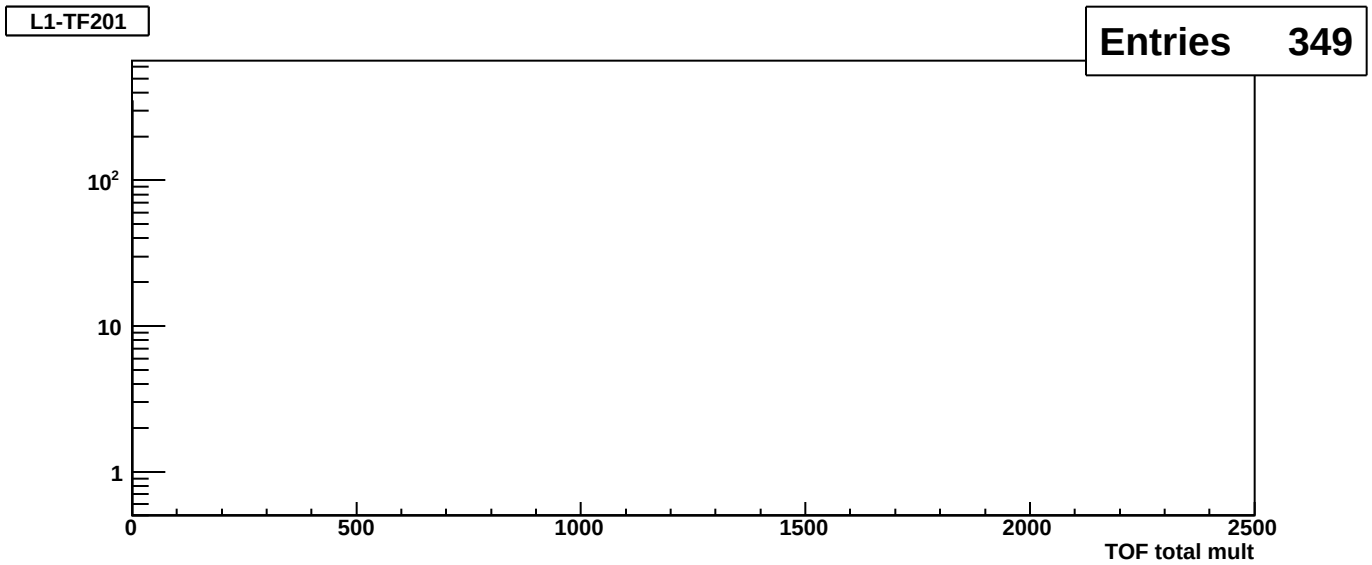

MIX-TF003
Entries 14658
TOF MULT
TOF tray

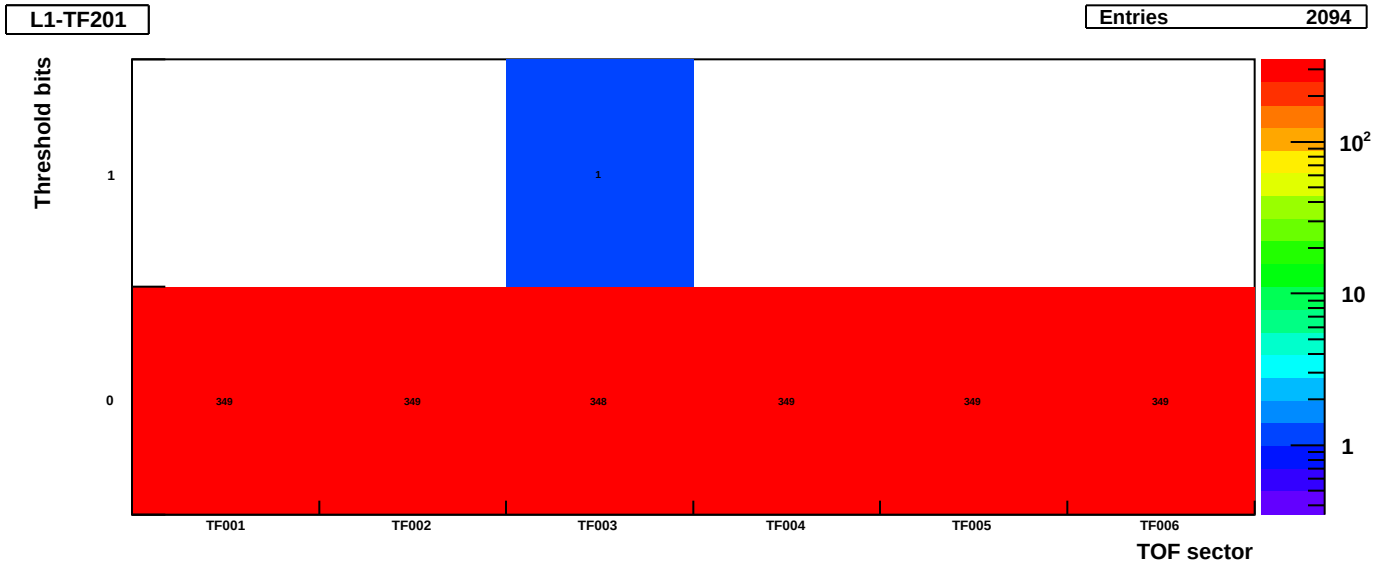

MIX-TF004
Entries 14658
TOF MULT
TOF tray

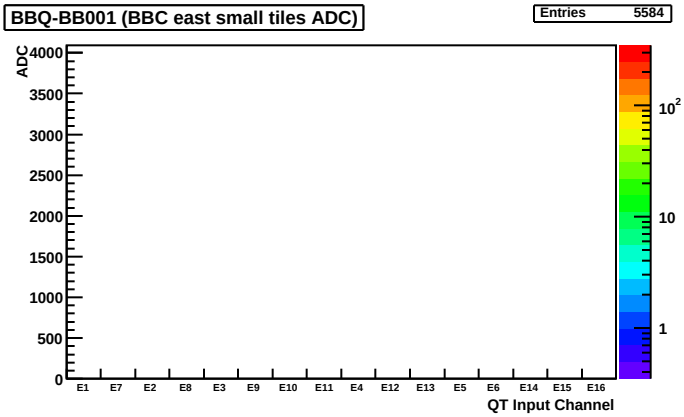

MIX-TF005
Entries 14658
TOF MULT
TOF tray

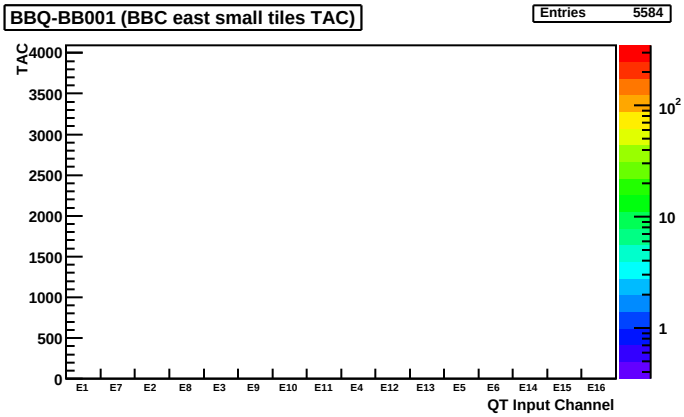

MIX-TF006
Entries 14658
TOF MULT
TOF tray

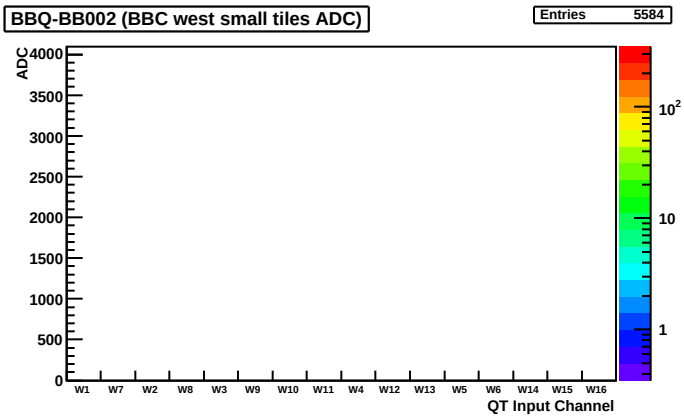

MIX-TF101
Entries 2094
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

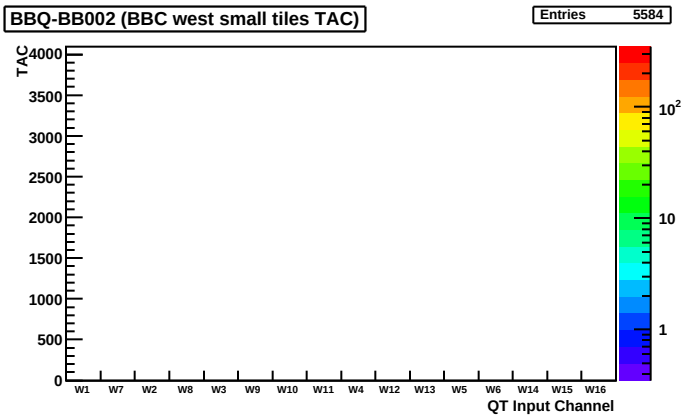

L1-TF201
Entries 349
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

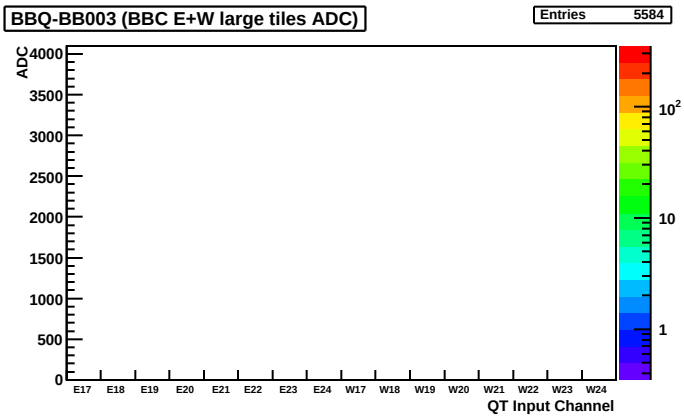

L1-TF201
Entries 2094
Threshold bits
1
0
1
349
349
348
349
349
349
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^2
10
1

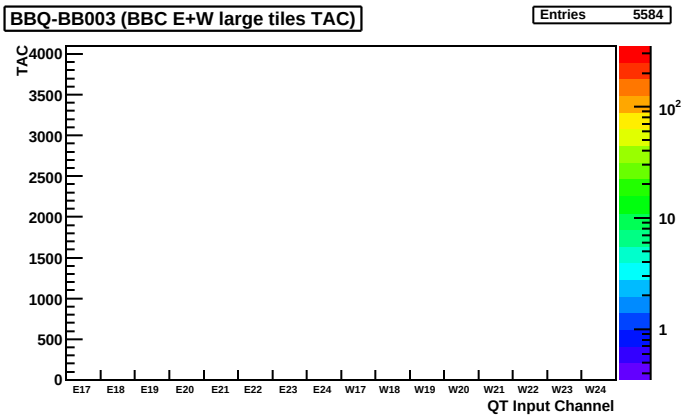

BBQ-BB001 (BBC east small tiles ADC)
Entries 5584
ADC
QT Input Channel

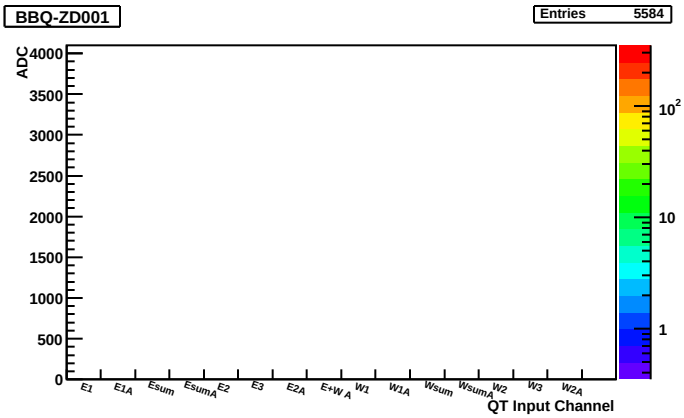

BBQ-BB001 (BBC east small tiles TAC)
Entries 5584
TAC
QT Input Channel

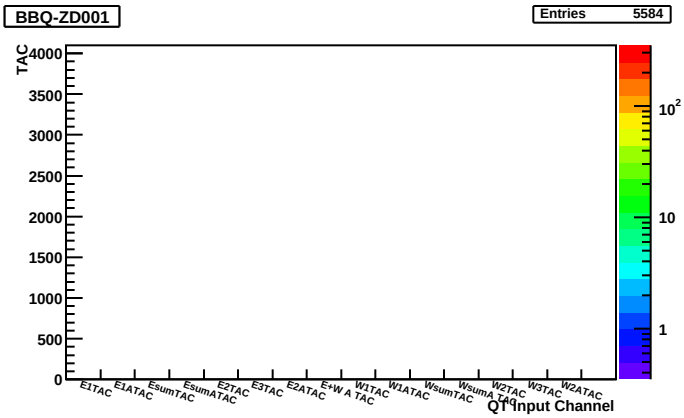

BBQ-BB002 (BBC west small tiles ADC)
Entries 5584
ADC
QT Input Channel

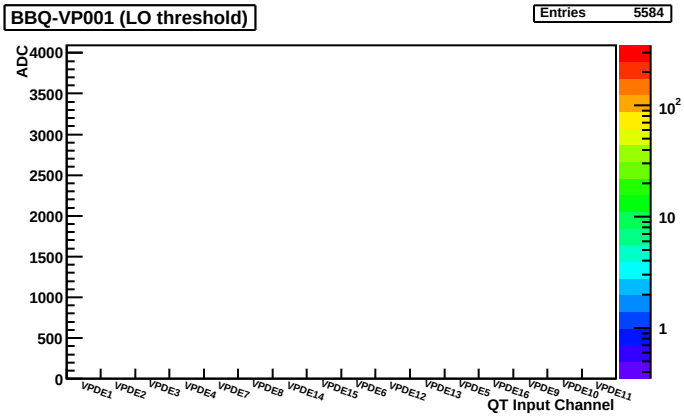

BBQ-BB002 (BBC west small tiles TAC)
Entries 5584
TAC
QT Input Channel

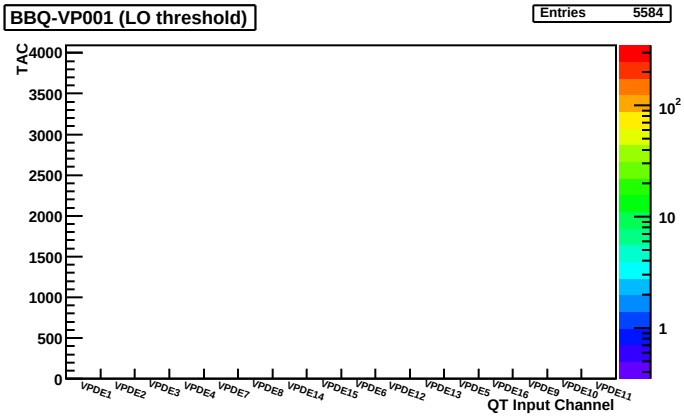

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5584
ADC
QT Input Channel

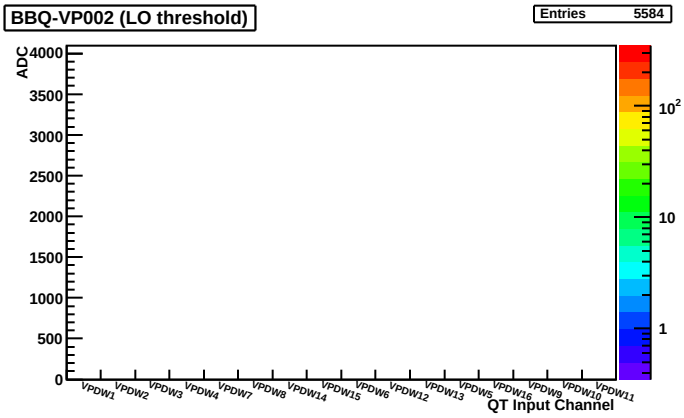

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5584
TAC
QT Input Channel

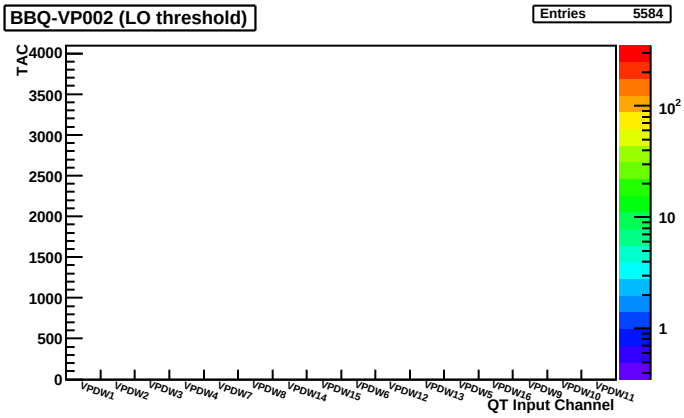

BBQ-ZD001
Entries 5584
ADC
QT Input Channel

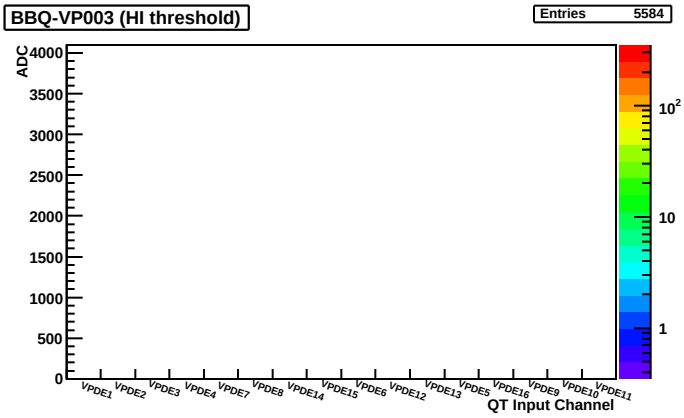

BBQ-ZD001
Entries 5584
TAC
QT Input Channel

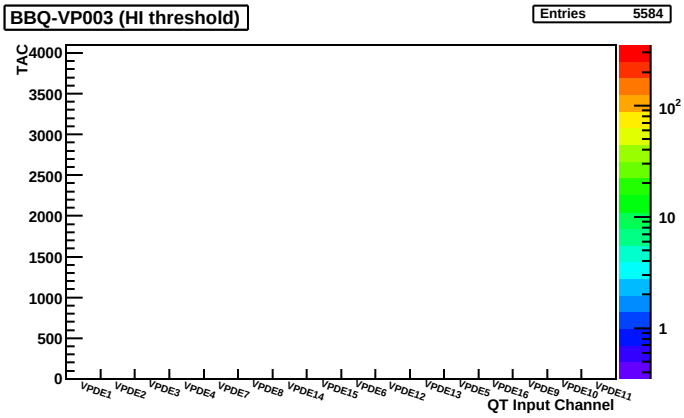

BBQ-VP001 (LO threshold)
Entries 5584
ADC
QT Input Channel

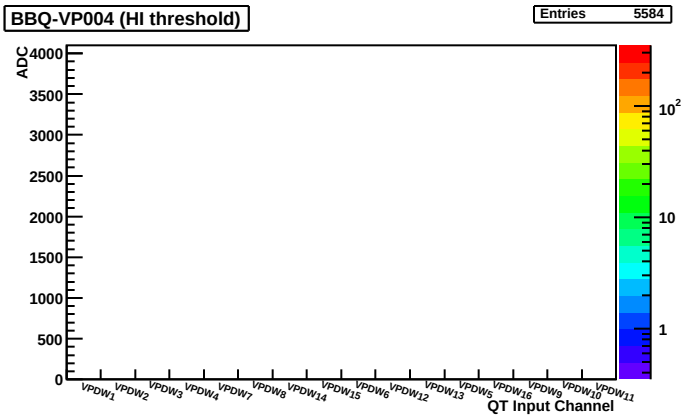

BBQ-VP001 (LO threshold)
Entries 5584
TAC
QT Input Channel

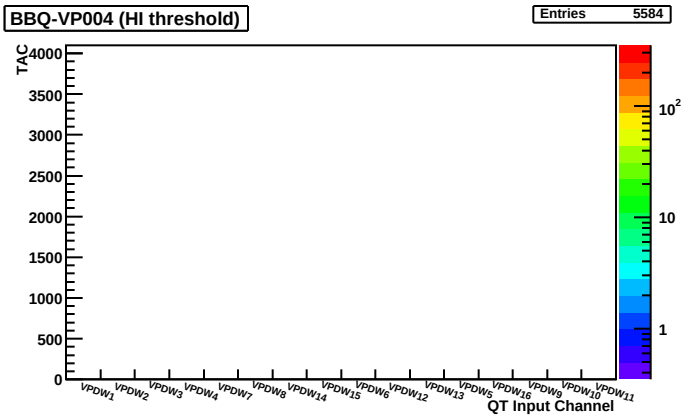

BBQ-VP002 (LO threshold)
Entries 5584
ADC
QT Input Channel

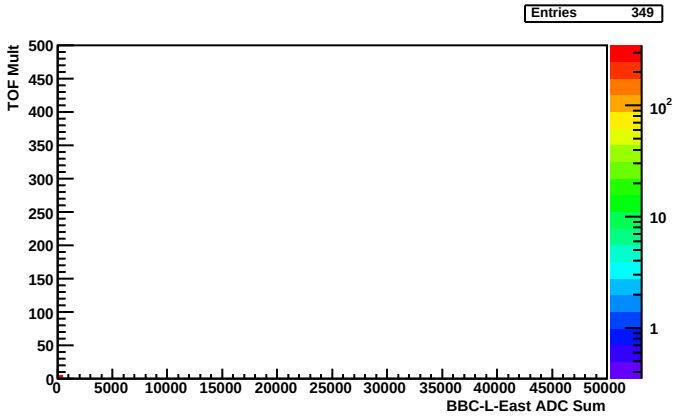

BBQ-VP002 (LO threshold)
Entries 5584
TAC
QT Input Channel

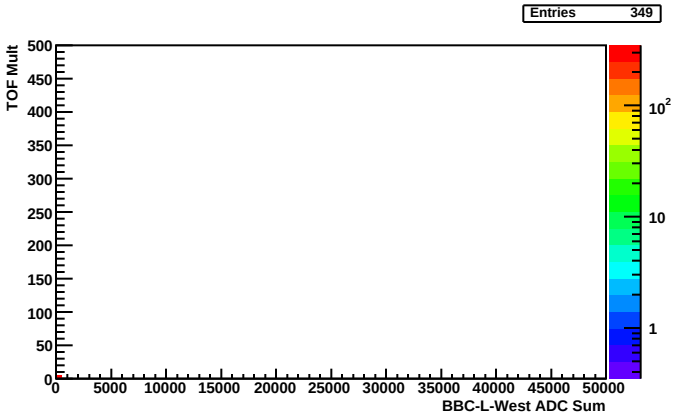

BBQ-VP003 (HI threshold)
Entries 5584
ADC
QT Input Channel

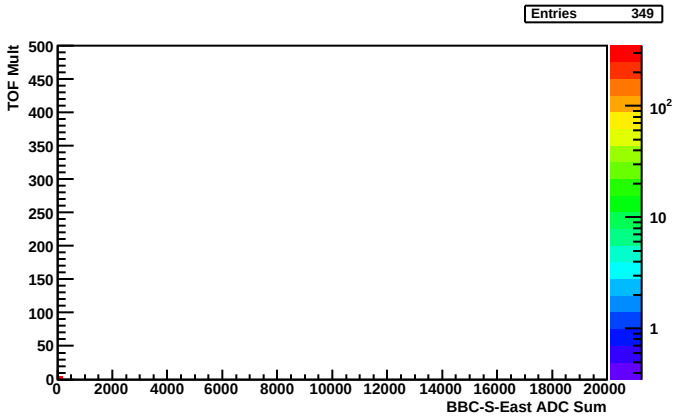

BBQ-VP003 (HI threshold)
Entries 5584
TAC
QT Input Channel

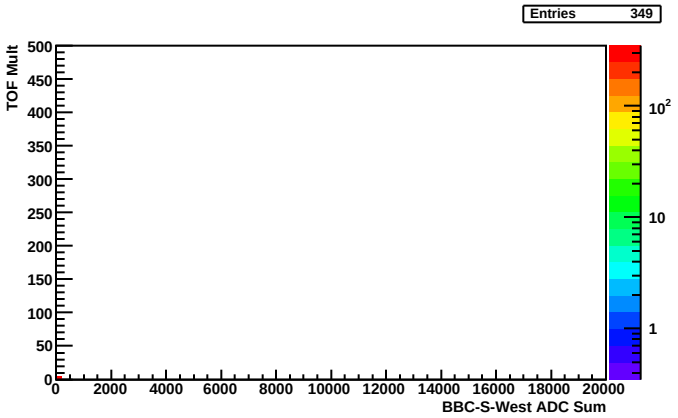

BBQ-VP004 (HI threshold)
Entries 5584
ADC
QT Input Channel

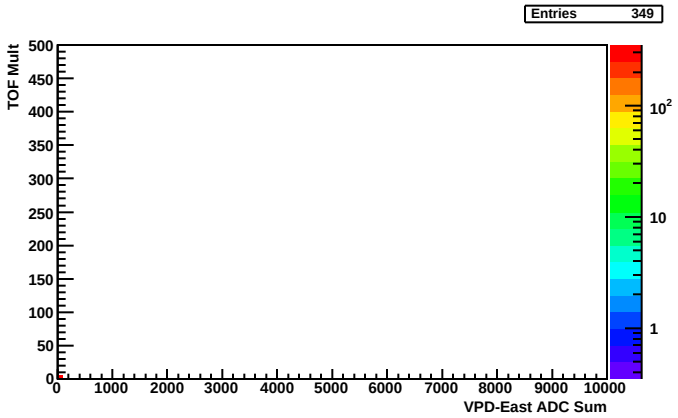

BBQ-VP004 (HI threshold)
Entries 5584
TAC
QT Input Channel

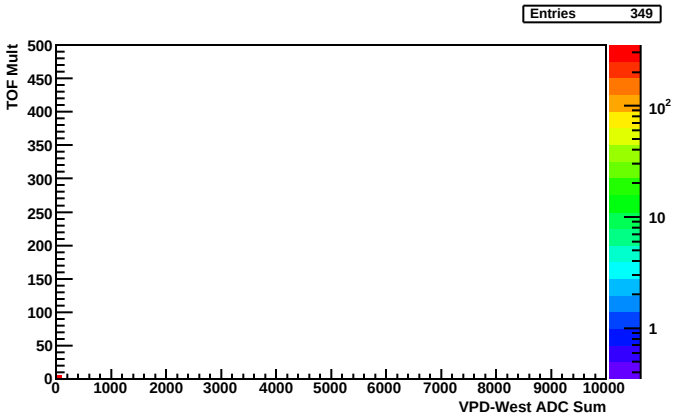

Entries 349
TOF Mult
BBC-L-East ADC Sum

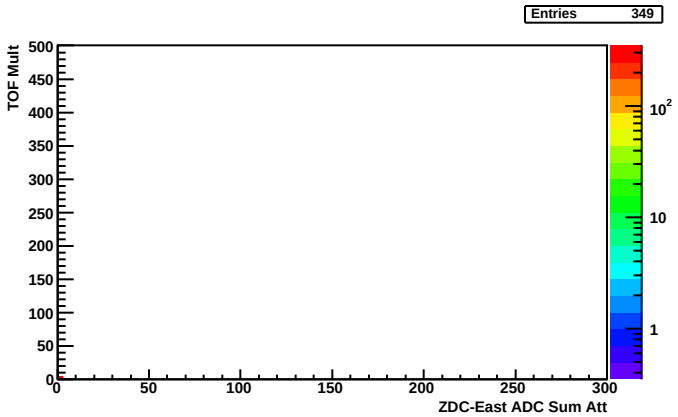

Entries 349
TOF Mult
BBC-L-West ADC Sum

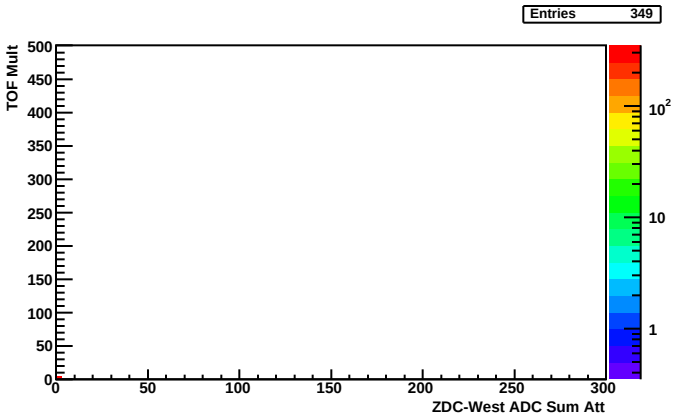

Entries 349
TOF Mult
BBC-S-East ADC Sum

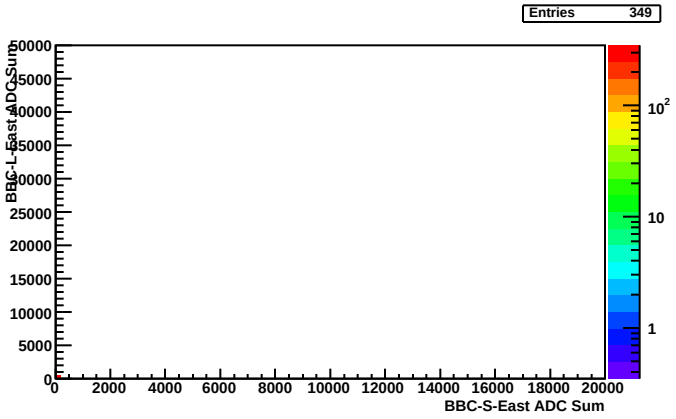

Entries 349
TOF Mult
BBC-S-West ADC Sum

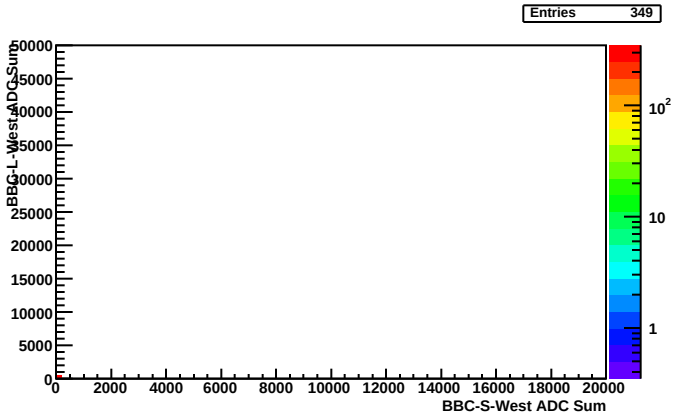

Entries 349
TOF Mult
VPD-East ADC Sum

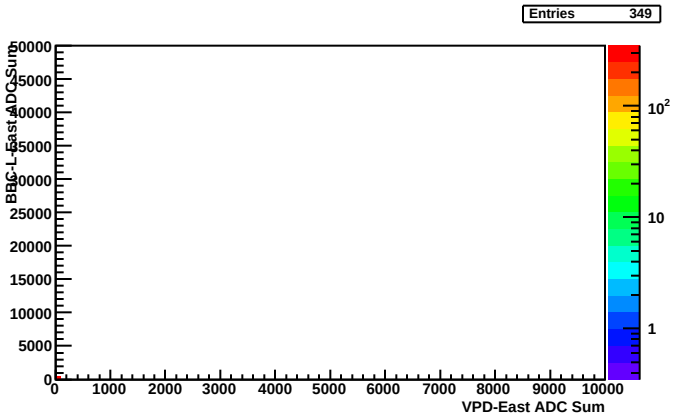

Entries 349
TOF Mult
VPD-West ADC Sum

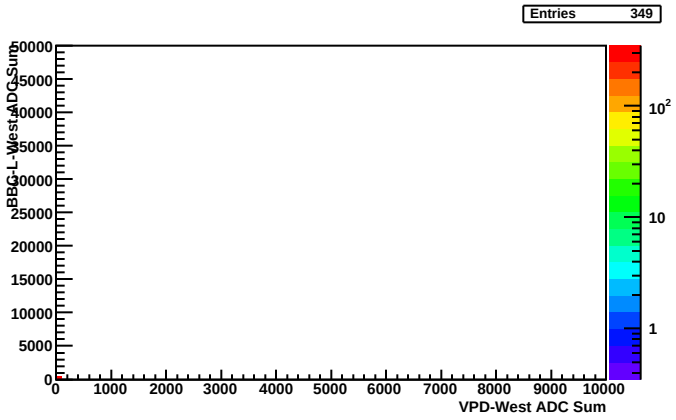

Entries 349
TOF Mult
ZDC-East ADC Sum Att

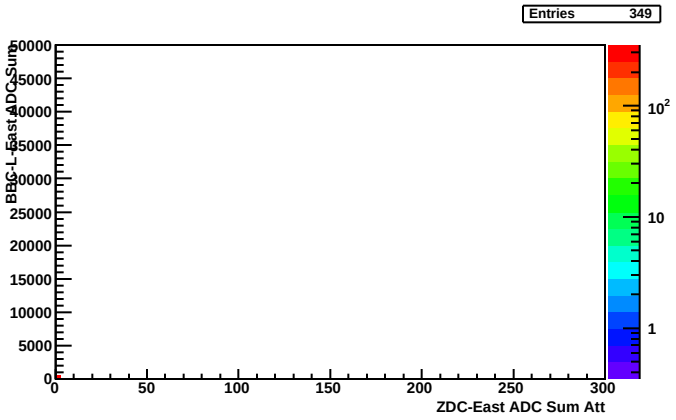

Entries 349
TOF Mult
ZDC-West ADC Sum Att

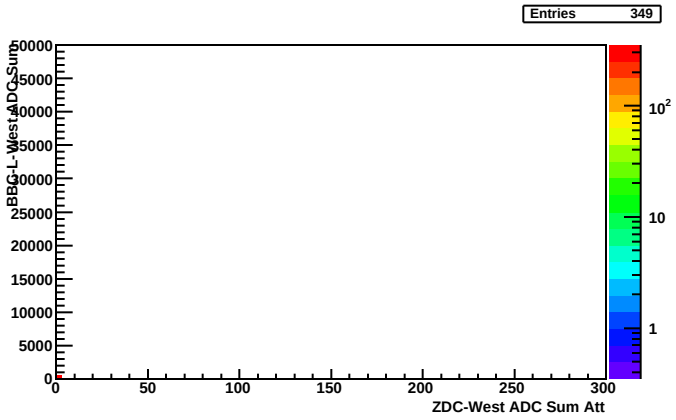

Entries 349
BBC-L-East ADC Sum
BBC-S-East ADC Sum

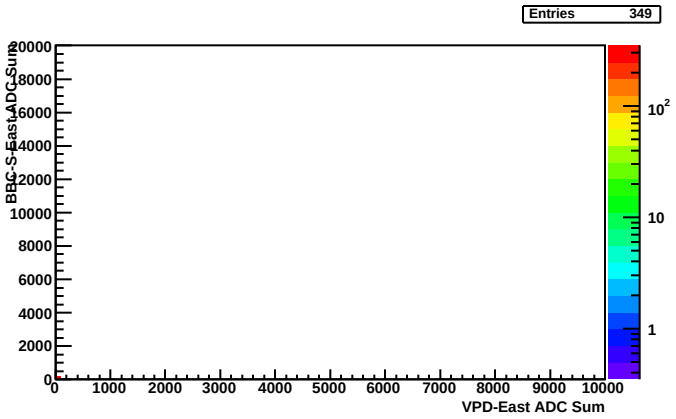

Entries 349
BBC-L-West ADC Sum
BBC-S-West ADC Sum

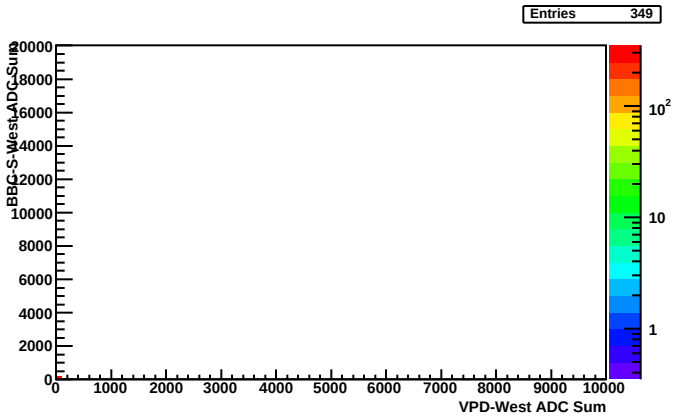

Entries 349
BBC-L-East ADC Sum
VPD-East ADC Sum

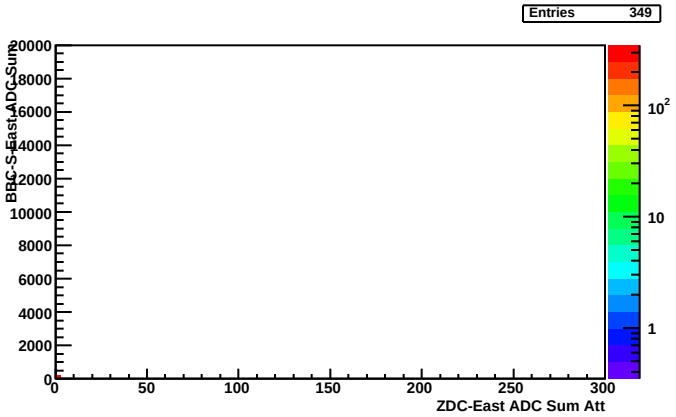

Entries 349
BBC-L-West ADC Sum
VPD-West ADC Sum

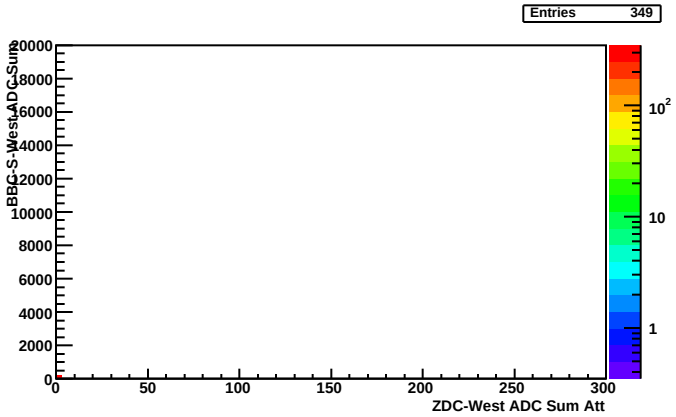

Entries 349
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

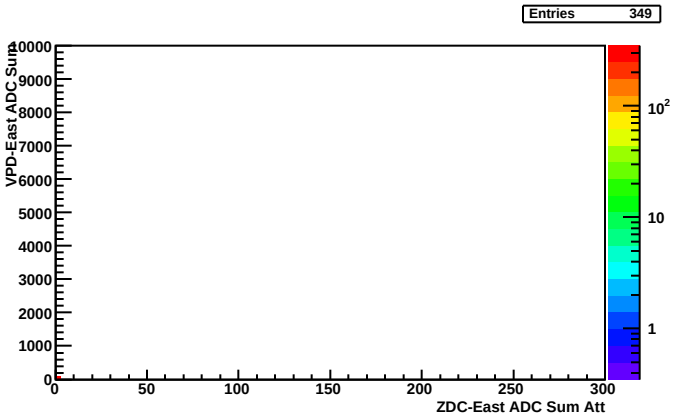

Entries 349
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

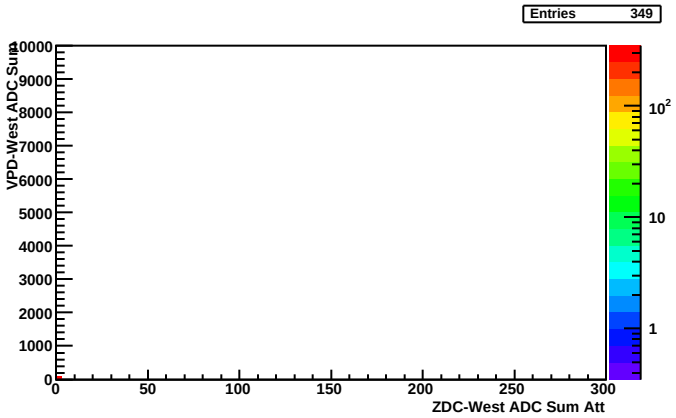

Entries 349
BBC-S-East ADC Sum
VPD-East ADC Sum

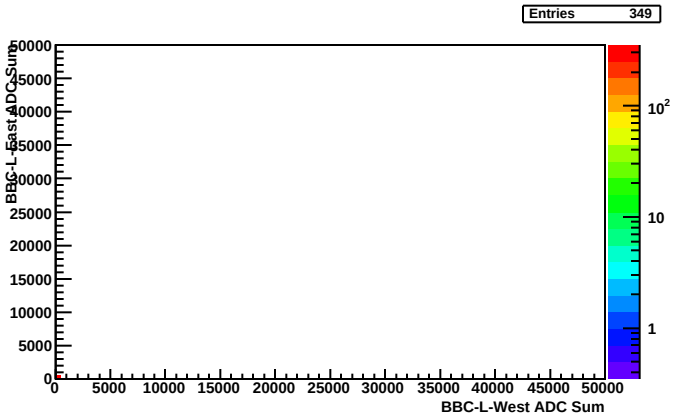

Entries 349
BBC-S-West ADC Sum
VPD-West ADC Sum

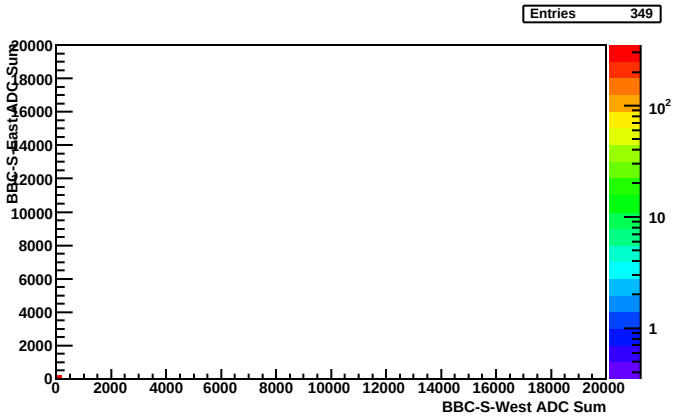

Entries 349
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

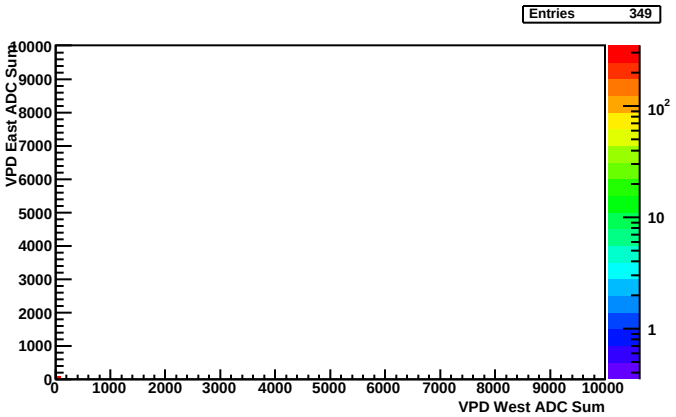

Entries 349
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

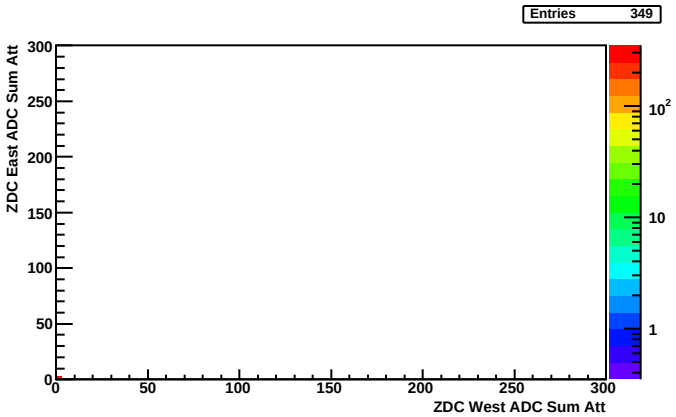

Entries 349
VPD-East ADC Sum
ZDC-East ADC Sum Att

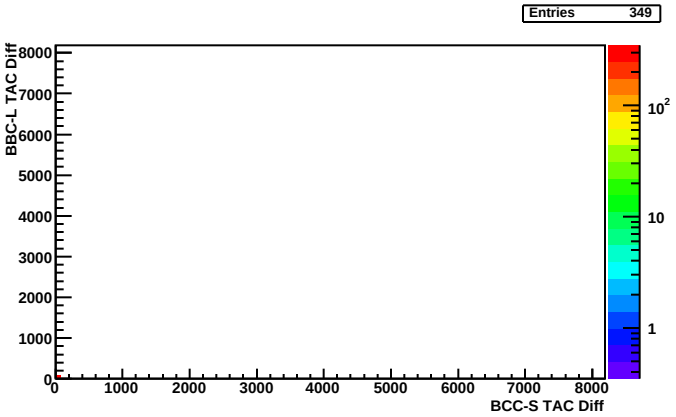

Entries 349
VPD-West ADC Sum
ZDC-West ADC Sum Att

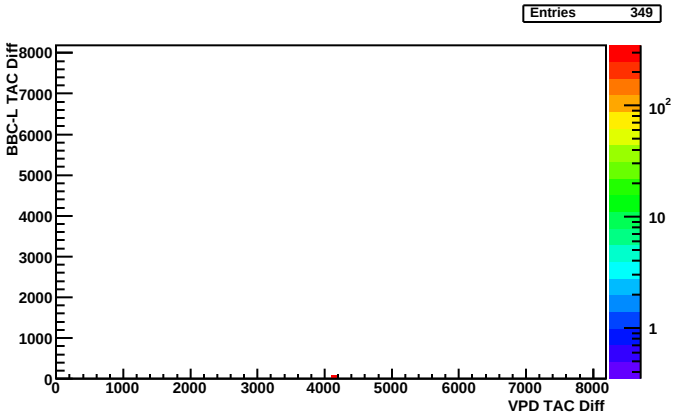

Entries 349
BBC-L-East ADC Sum
BBC-L-West ADC Sum

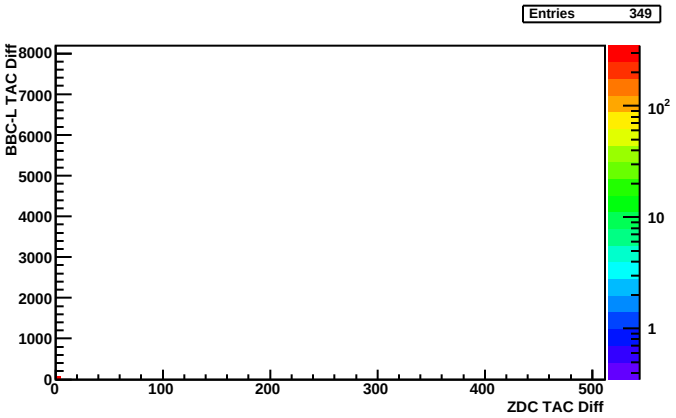

Entries 349
BBC-S-East ADC Sum
BBC-S-West ADC Sum

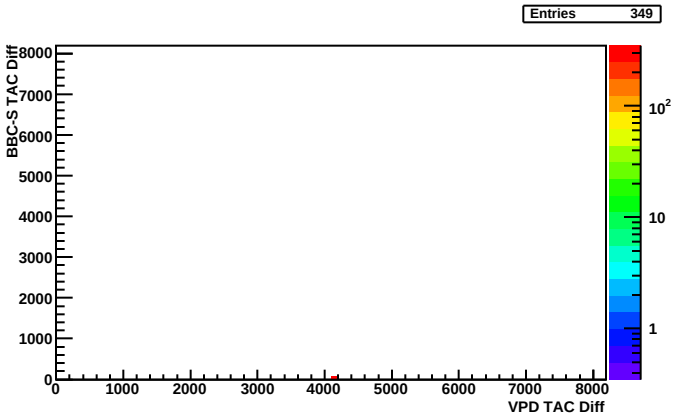

Entries 349
VPD East ADC Sum
VPD West ADC Sum

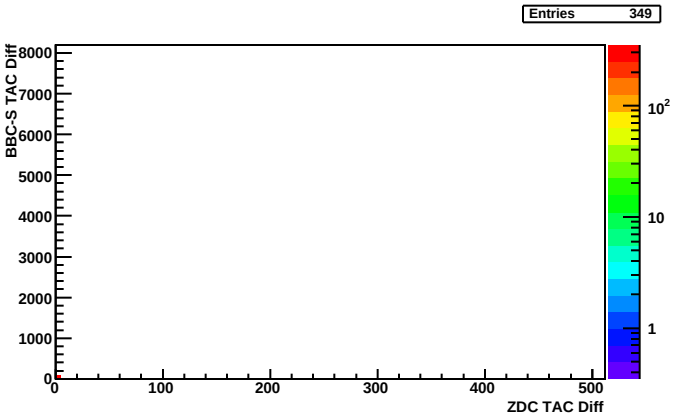

Entries 349
ZDC East ADC Sum Att
ZDC West ADC Sum Att

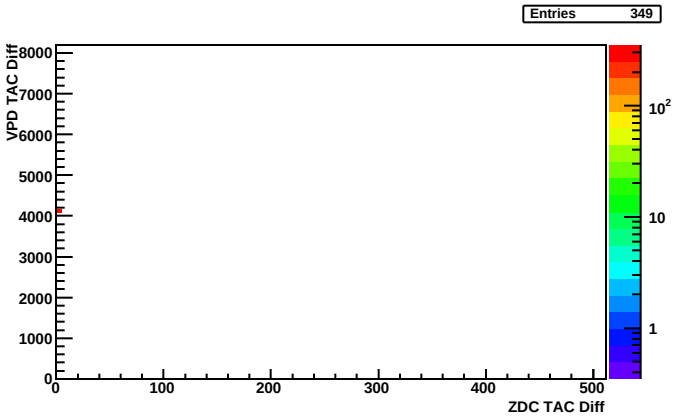

Entries 349
BBC-L TAC Diff
BCC-S TAC Diff

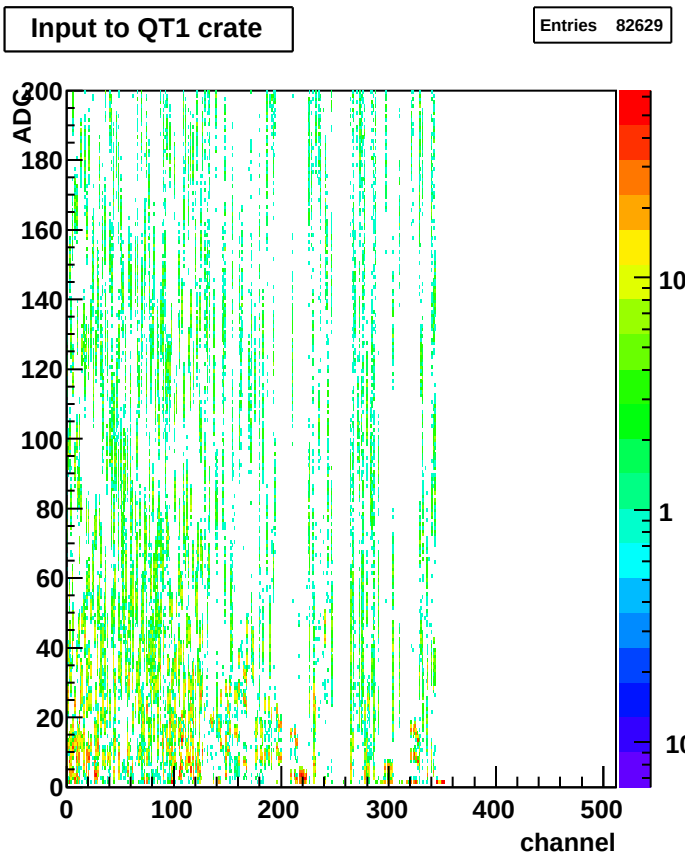

Entries 349
BBC-L TAC Diff
VPD TAC Diff

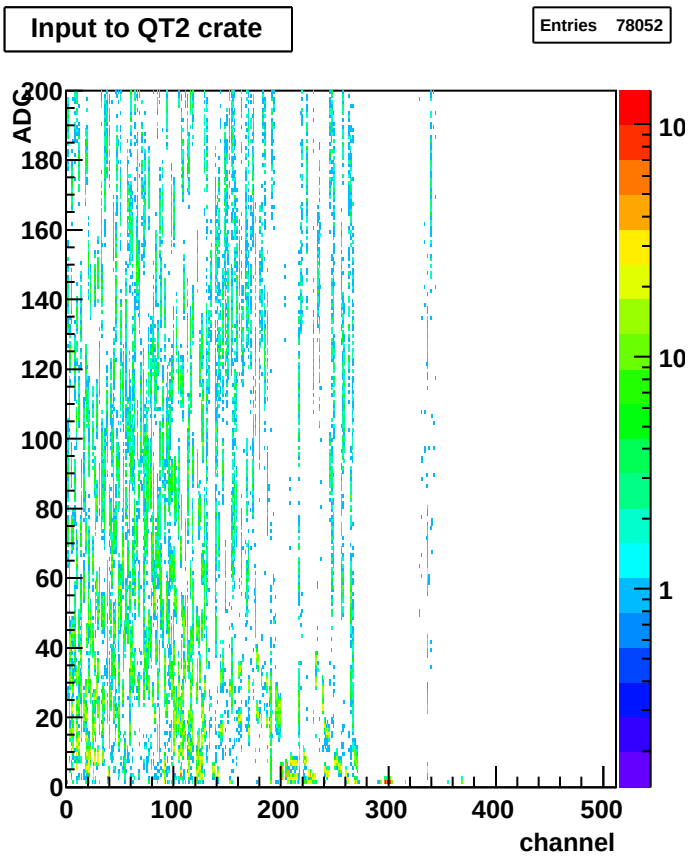

Entries 349
BBC-L TAC Diff
ZDC TAC Diff

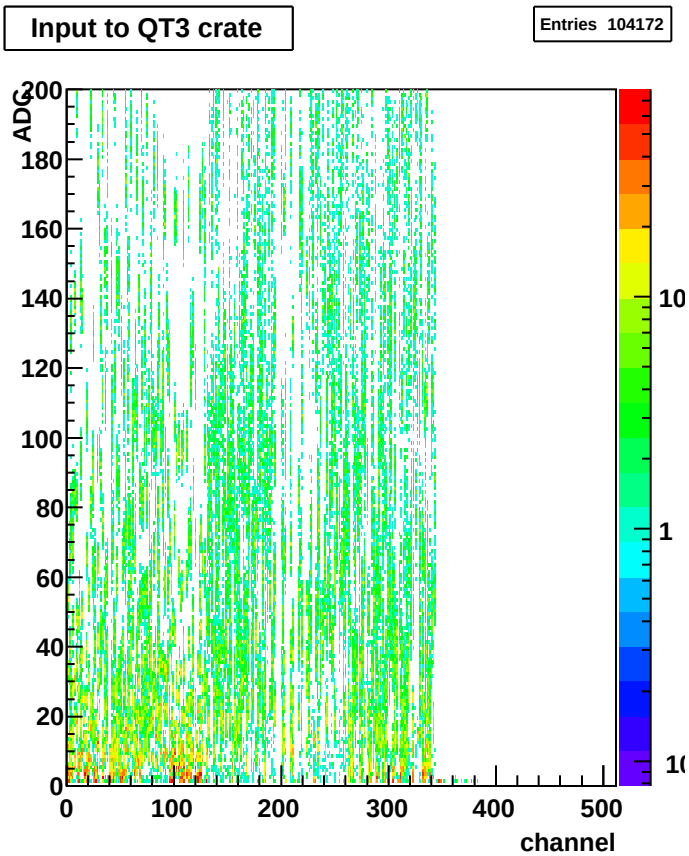

Entries 349
BBC-S TAC Diff
VPD TAC Diff

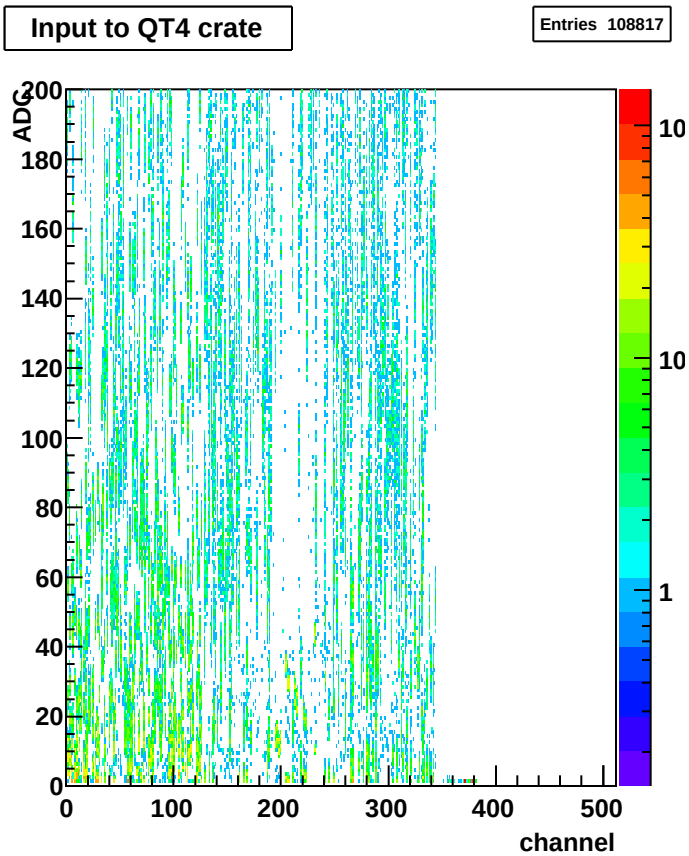

Entries 349
BBC-S TAC Diff
ZDC TAC Diff

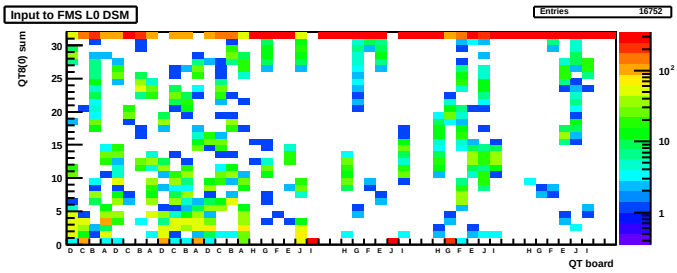

Entries 349
VPD TAC Diff
ZDC TAC Diff

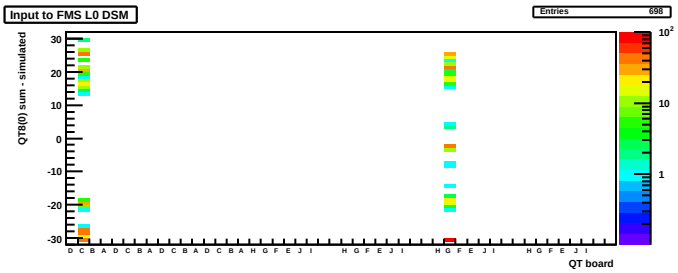

Input to QT1 crate
Entries 82629
ADC
channel

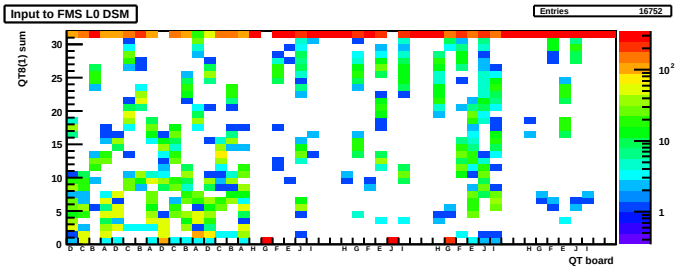

Input to QT2 crate
Entries 78052
ADC
channel

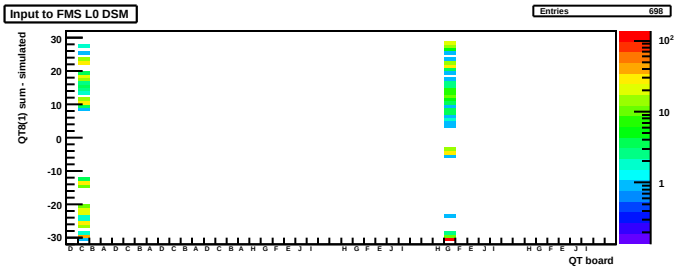

Input to QT3 crate
Entries 104172
ADC
channel

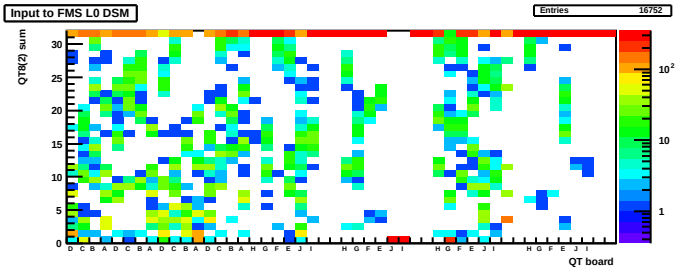

Input to QT4 crate
Entries 108817
ADC
channel

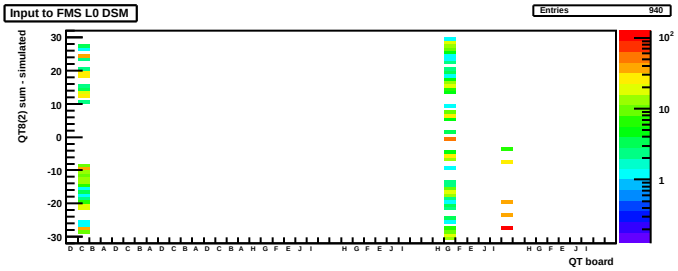

Input to FMS L0 DSM
Entries 16752
QT8(0) sum
QT board

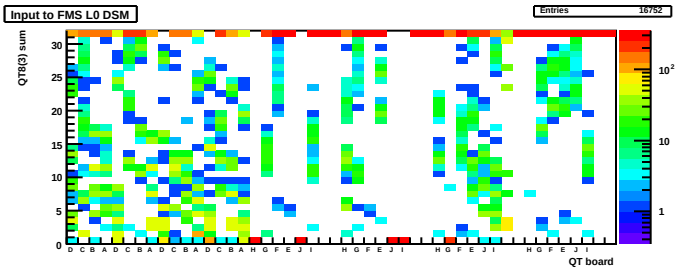

Input to FMS L0 DSM
Entries 698
QT8(0) sum - simulated
QT board

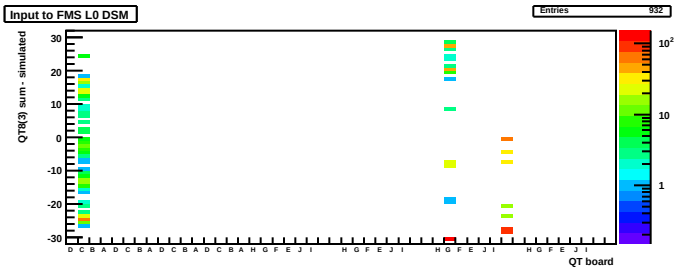

Input to FMS L0 DSM
Entries 16752
QT8(1) sum
QT board

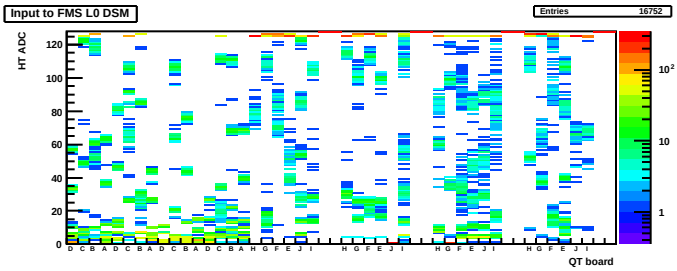

Input to FMS L0 DSM
Entries 698
QT8(1) sum - simulated
QT board

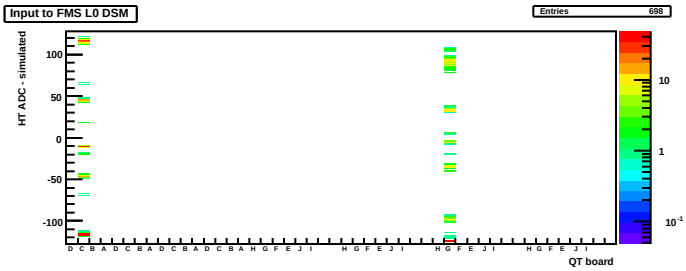

Input to FMS L0 DSM
Entries 16752
QT8(2) sum
QT board

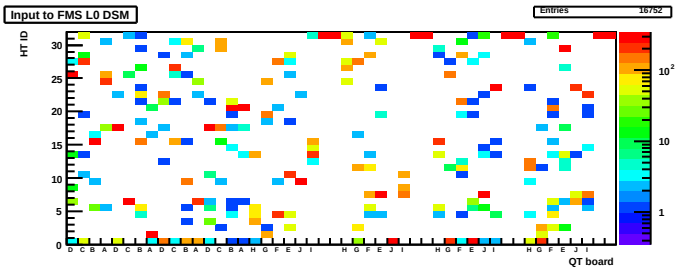

Input to FMS L0 DSM
Entries 940
QT8(2) sum - simulated
QT board

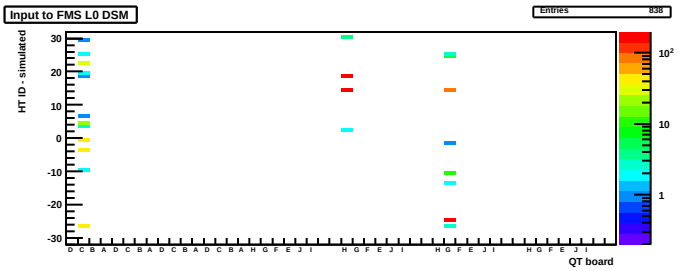

Input to FMS L0 DSM
Entries 16752
QT8(3) sum
QT board

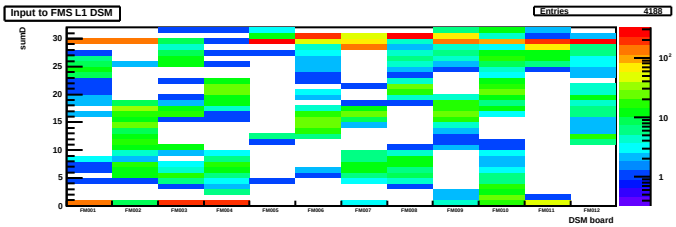

Input to FMS L0 DSM
Entries 932
QT8(3) sum - simulated
QT board

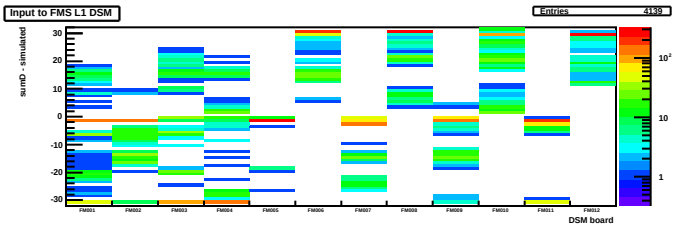

Input to FMS L0 DSM
Entries 16752
HT ADC
QT board

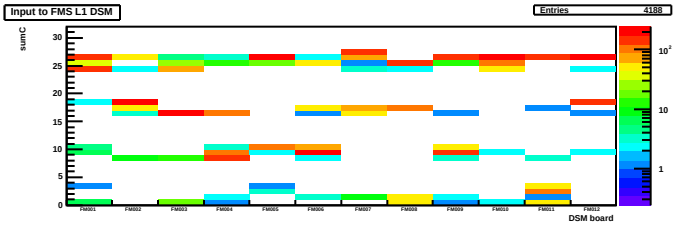

Input to FMS L0 DSM
Entries 698
HT ADC - simulated
QT board

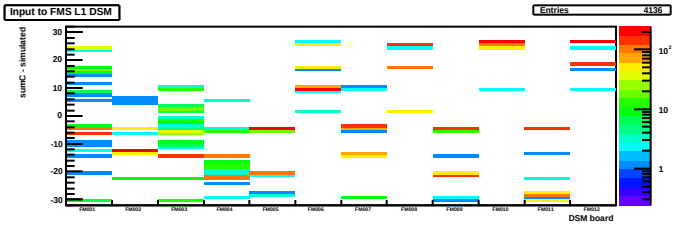

Input to FMS L0 DSM
Entries 16752
HT ID
QT board

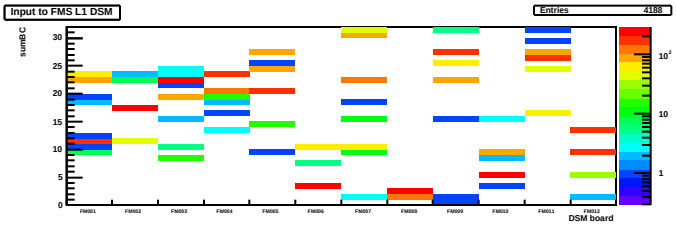

Input to FMS L0 DSM
Entries 838
HT ID - simulated
QT board

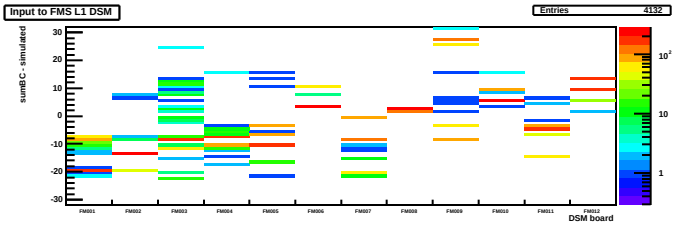

Input to FMS L1 DSM
Entries 4188
sumD
DSM board

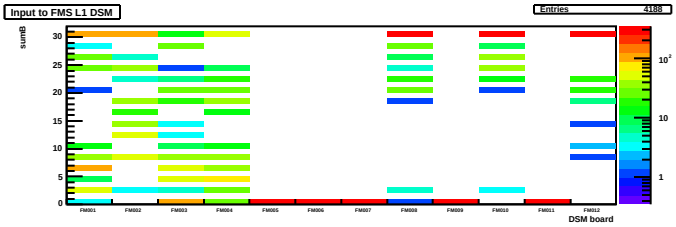

Input to FMS L1 DSM
Entries 4139
sumD - simulated
DSM board

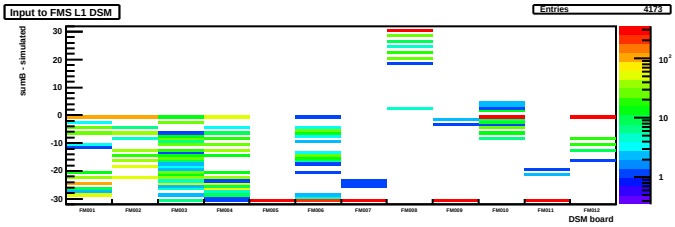

Input to FMS L1 DSM
Entries 4188
sumC
DSM board

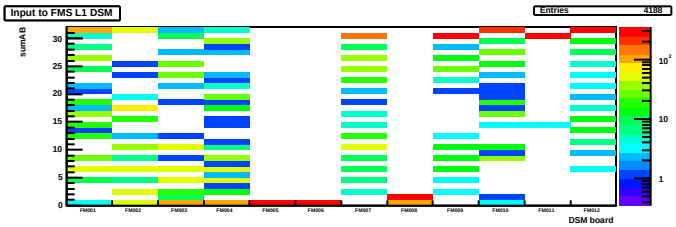

Input to FMS L1 DSM
Entries 4136
sumC - simulated
DSM board

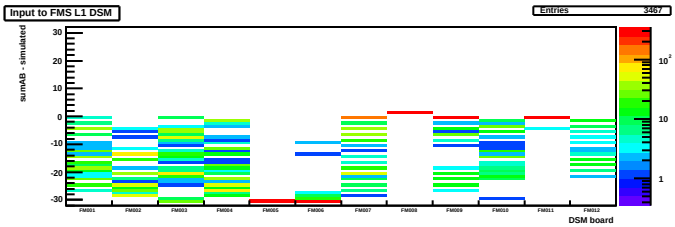

Input to FMS L1 DSM
Entries 4188
sumBC
DSM board

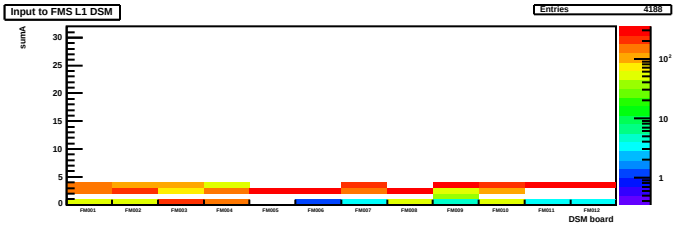

Input to FMS L1 DSM
Entries 4132
sumBC - simulated
DSM board

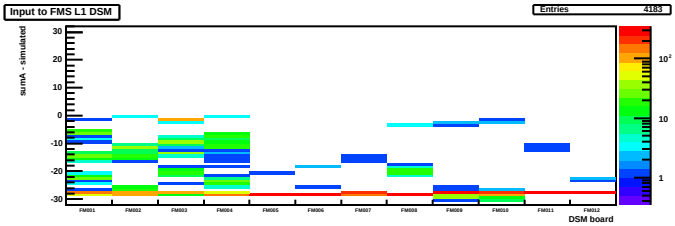

Input to FMS L1 DSM
Entries 4188
sumB
DSM board

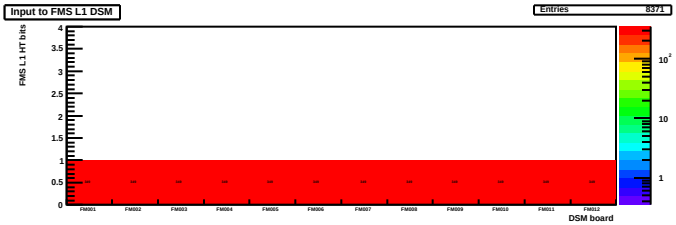

Input to FMS L1 DSM
Entries 4173
sumB - simulated
DSM board

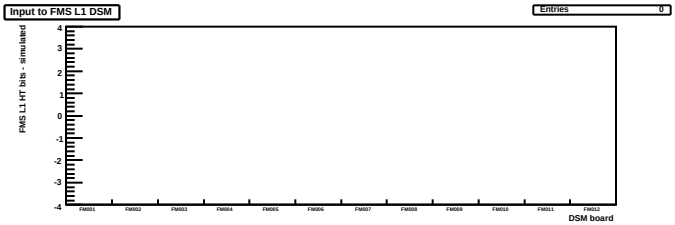

Input to FMS L1 DSM
Entries 4188
sumAB
DSM board

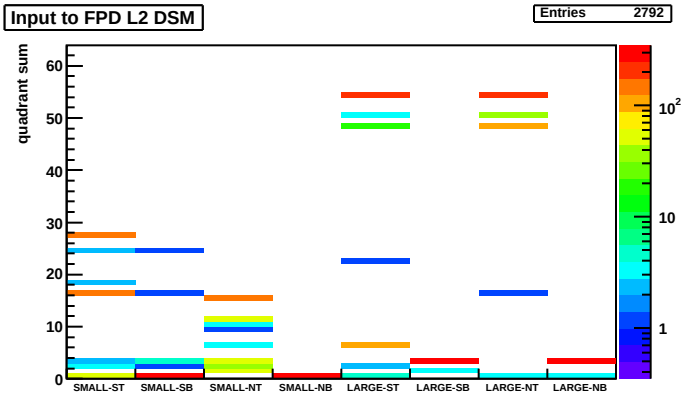

Input to FMS L1 DSM
Entries 3467
sumAB - simulated
DSM board

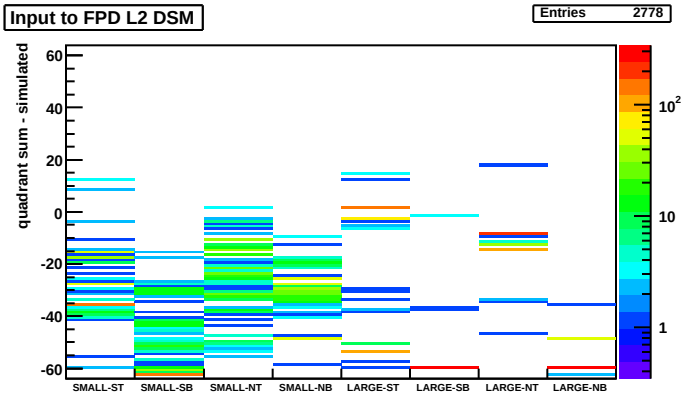

Input to FMS L1 DSM
Entries 4188
sumA
DSM board

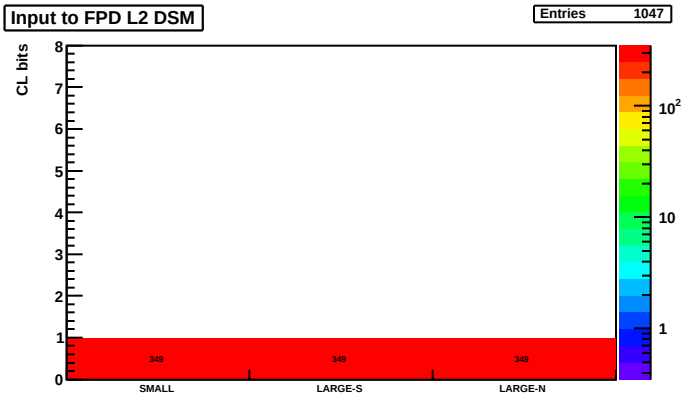

Input to FMS L1 DSM
Entries 4183
sumA - simulated
DSM board

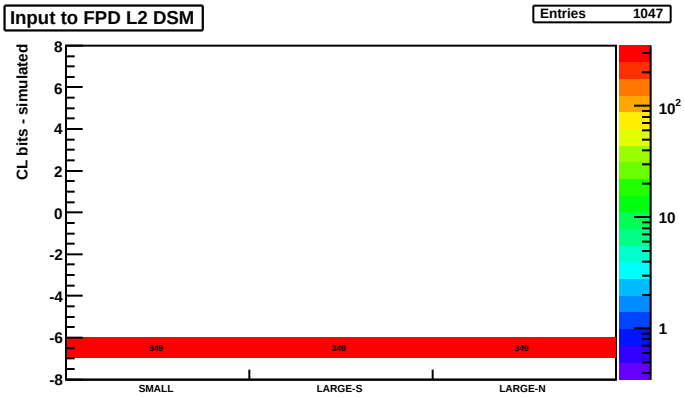

Input to FMS L1 DSM
Entries 8371
FMS L1 HT bits
DSM board

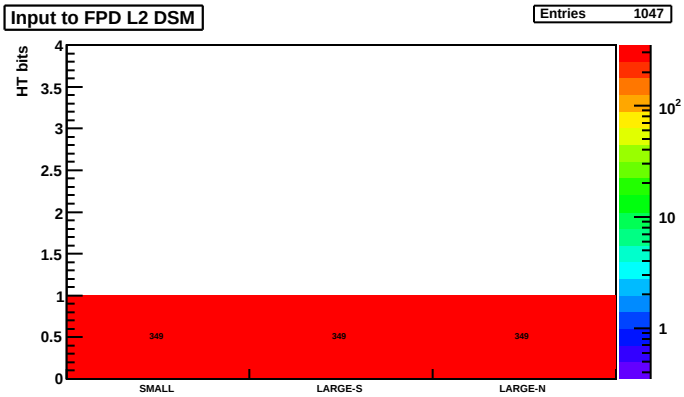

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

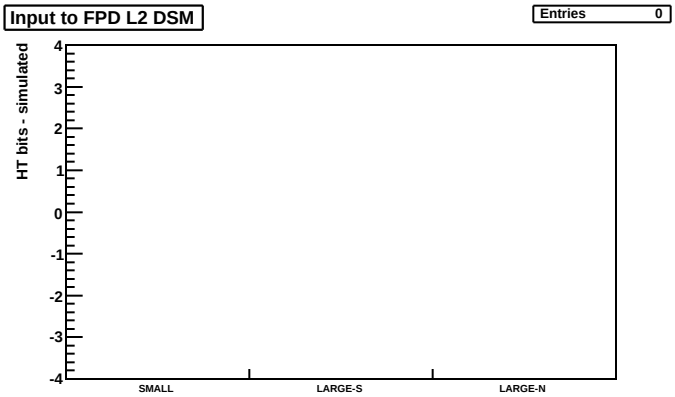

Input to FPD L2 DSM
Entries 2792
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

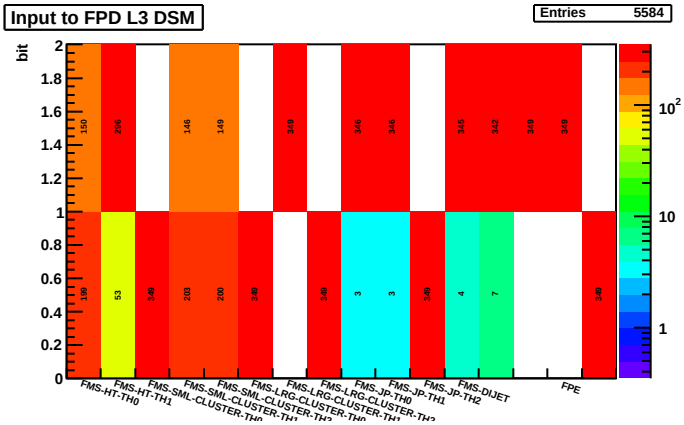

Input to FPD L2 DSM
Entries 2778
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

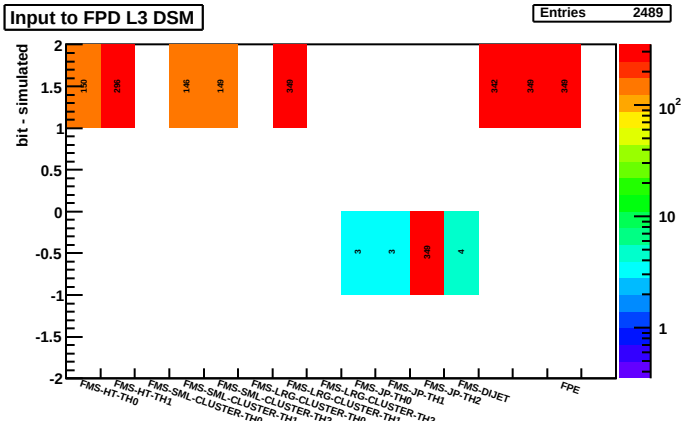

Input to FPD L2 DSM
Entries 1047
CL bits
349
349
349
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1047
CL bits - simulated
349
349
349
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1047
HT bits
349
349
349
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N


Input to FPD L3 DSM
Entries 5584
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE


Input to FPD L3 DSM
Entries 2489
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE