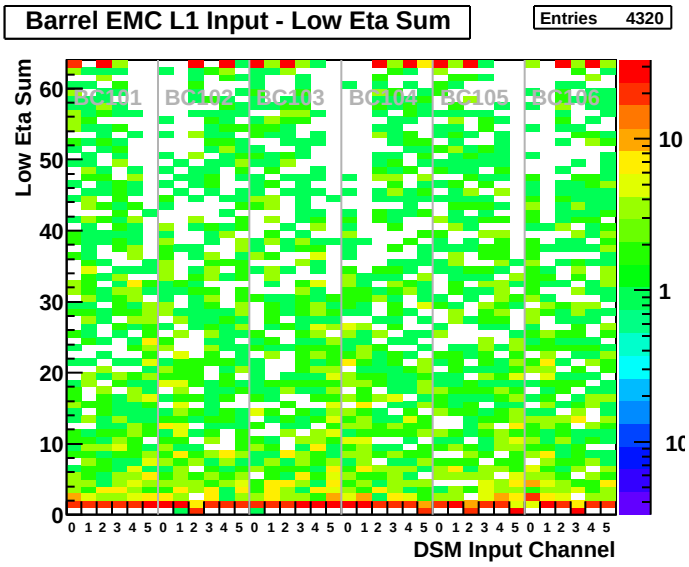
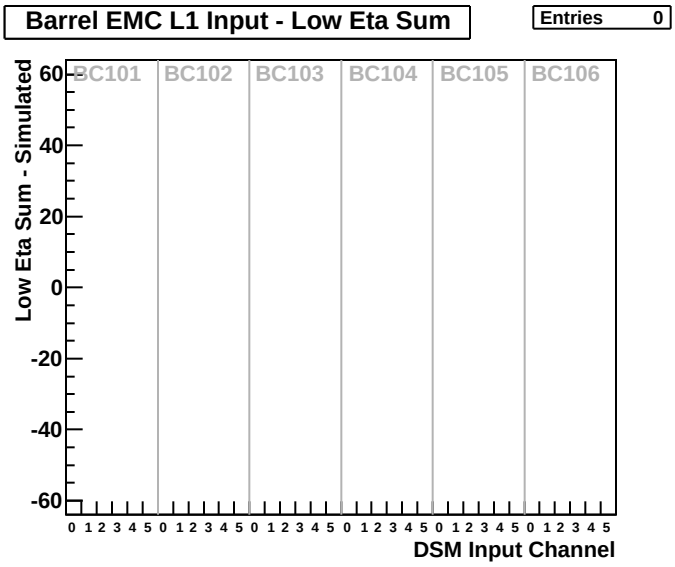
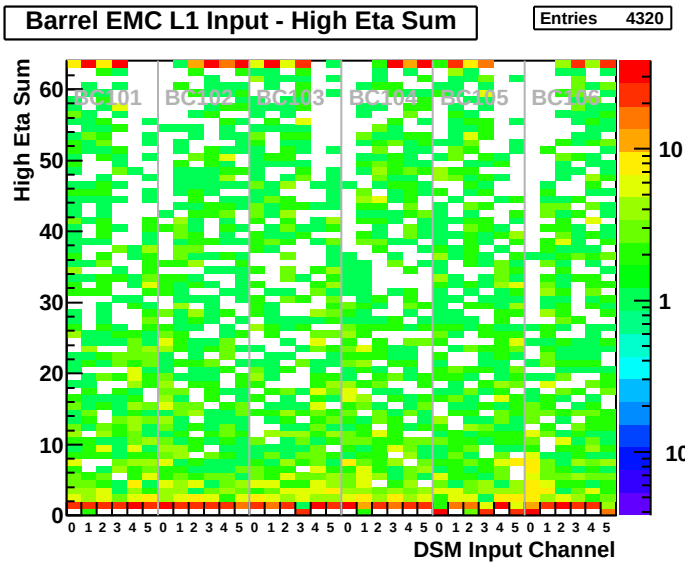
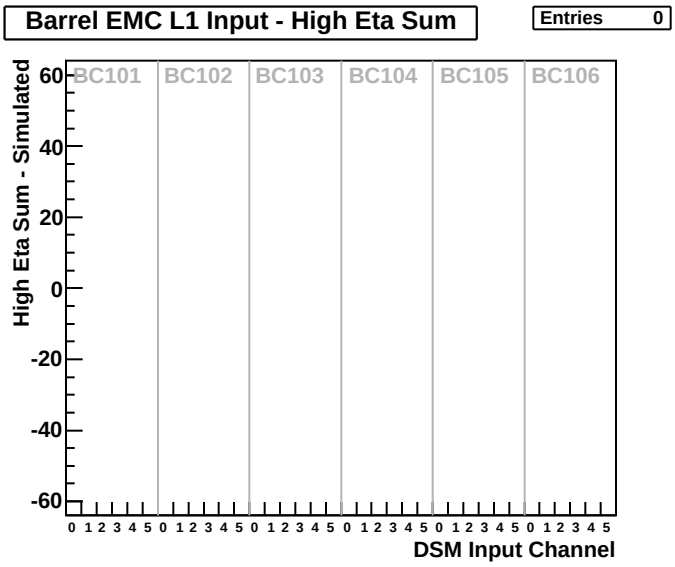
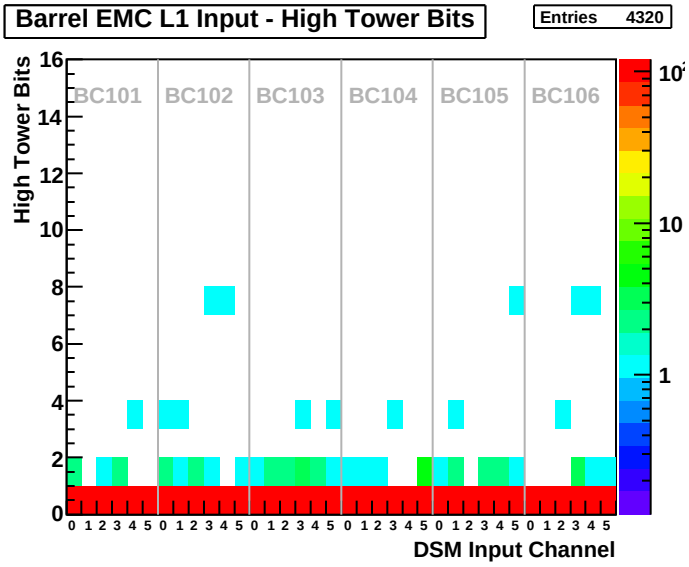
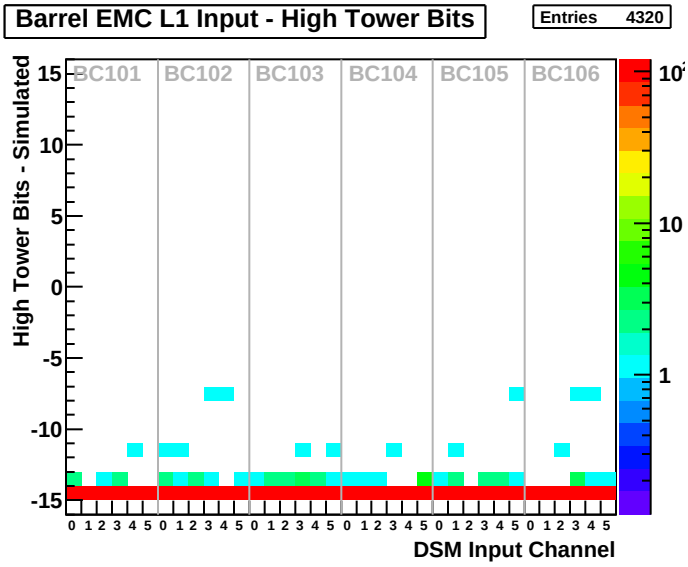

Barrel EMC L1 Input - Low Eta Sum
Entries 4320
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

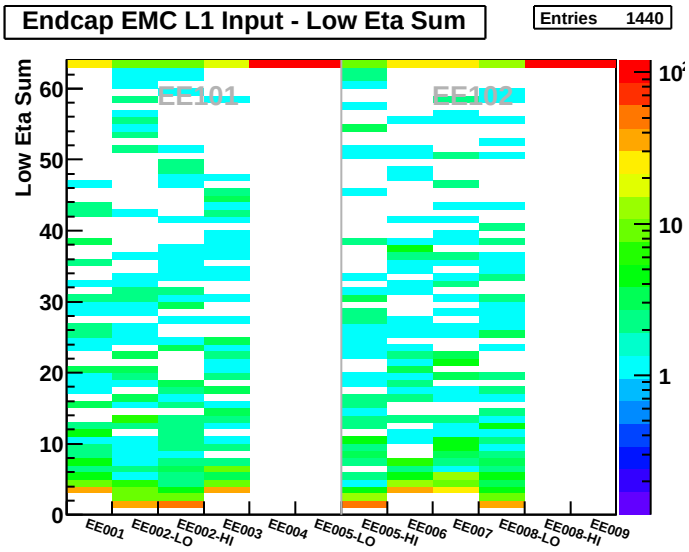

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

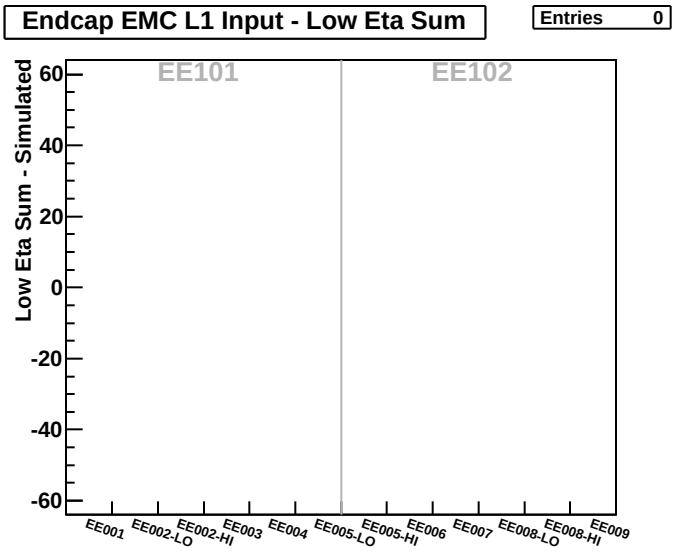

Barrel EMC L1 Input - High Eta Sum
Entries 4320
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

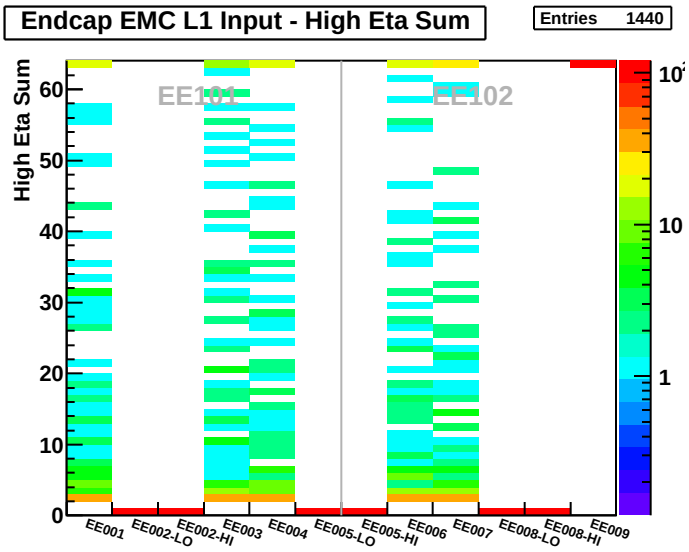

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

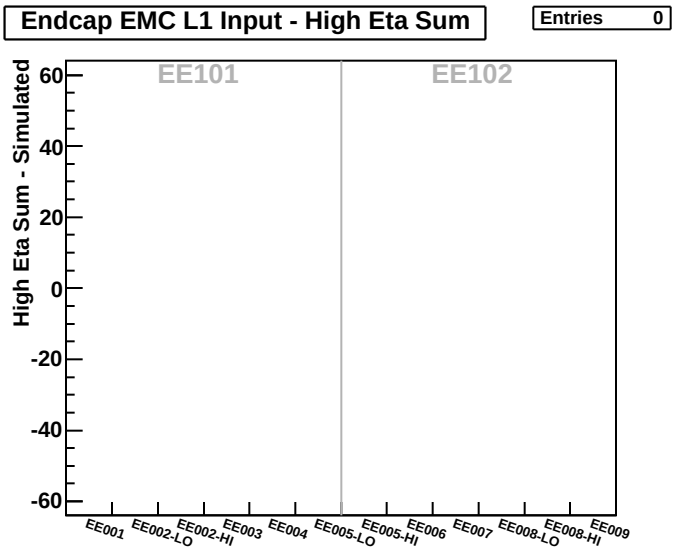

Barrel EMC L1 Input - High Tower Bits
Entries 4320
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

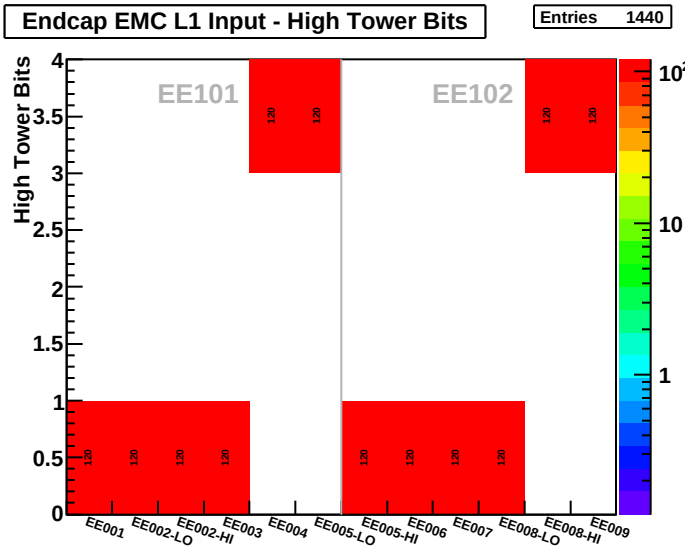

Barrel EMC L1 Input - High Tower Bits
Entries 4320
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

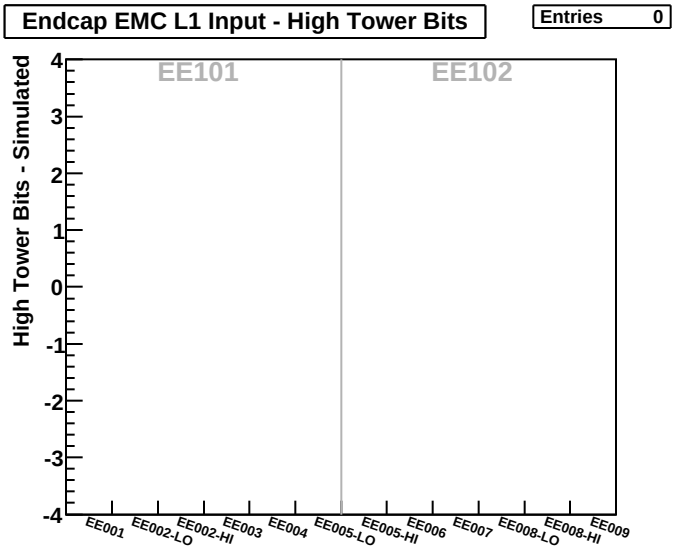

Endcap EMC L1 Input - Low Eta Sum
Entries 1440
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

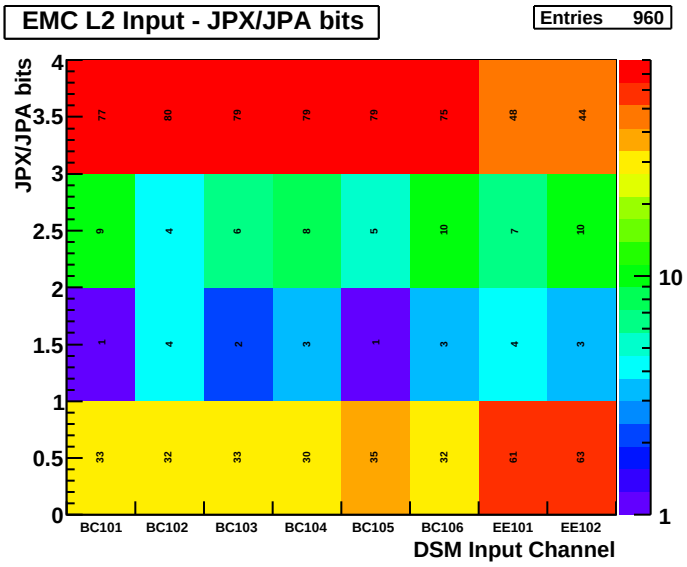

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

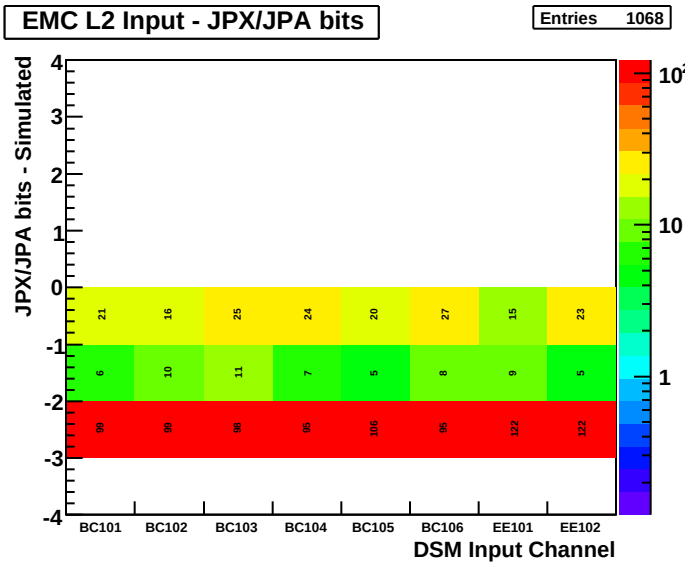

Endcap EMC L1 Input - High Eta Sum
Entries 1440
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

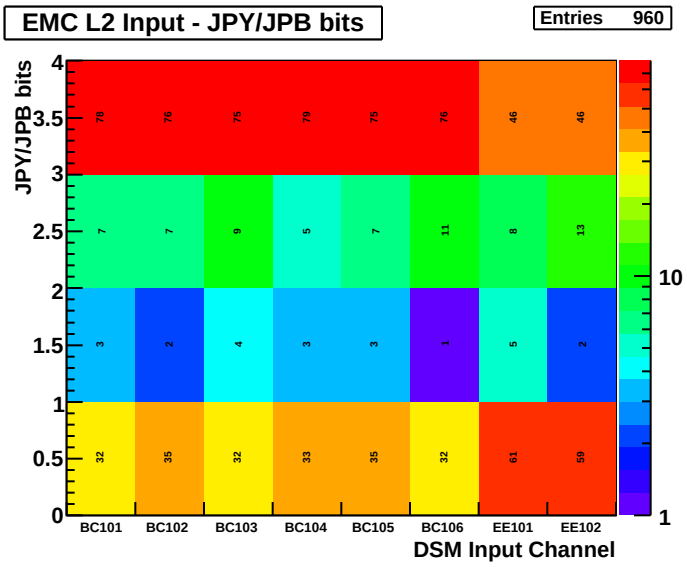

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

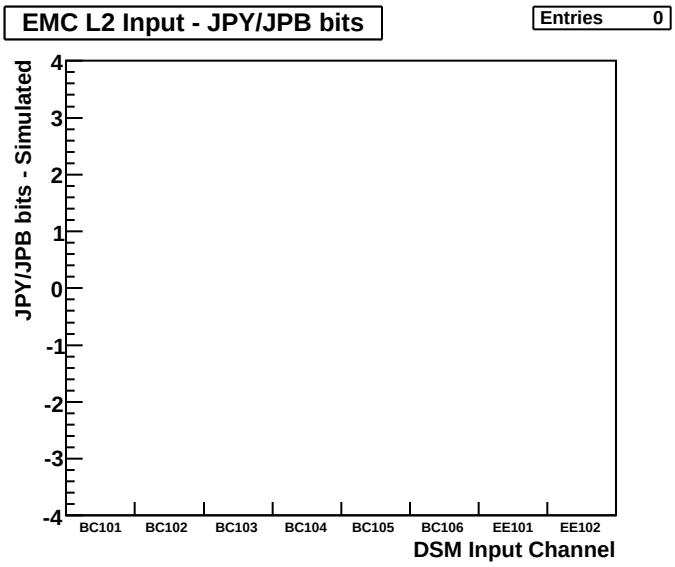

Endcap EMC L1 Input - High Tower Bits
Entries 1440
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

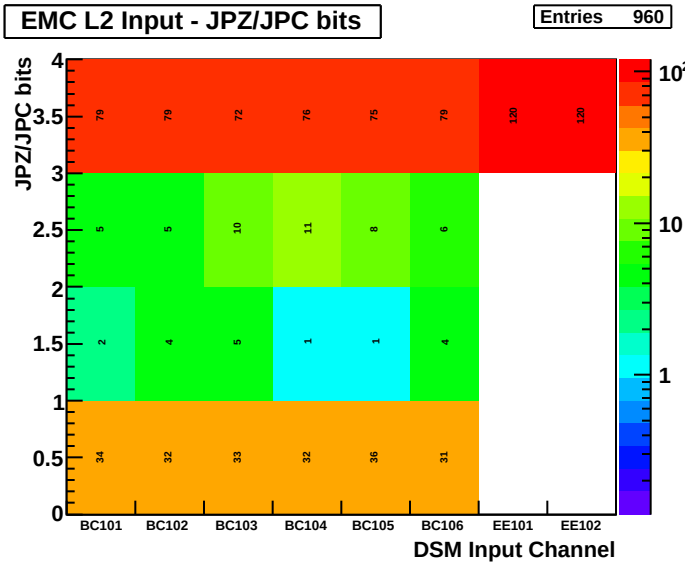

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

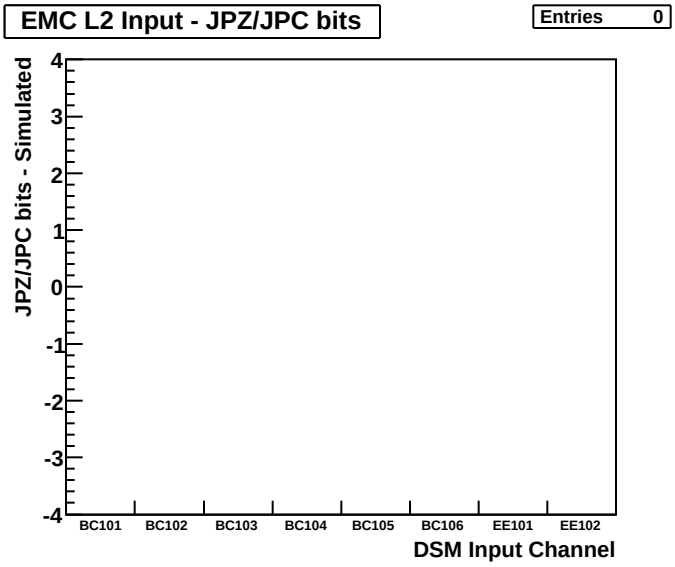

EMC L2 Input - JPX/JPA bits
Entries 960
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
77 80 79 79 79 75 48 44
9 4 6 8 5 10 7 10
1 4 2 3 1 3 4 3
33 32 33 30 35 32 61 63

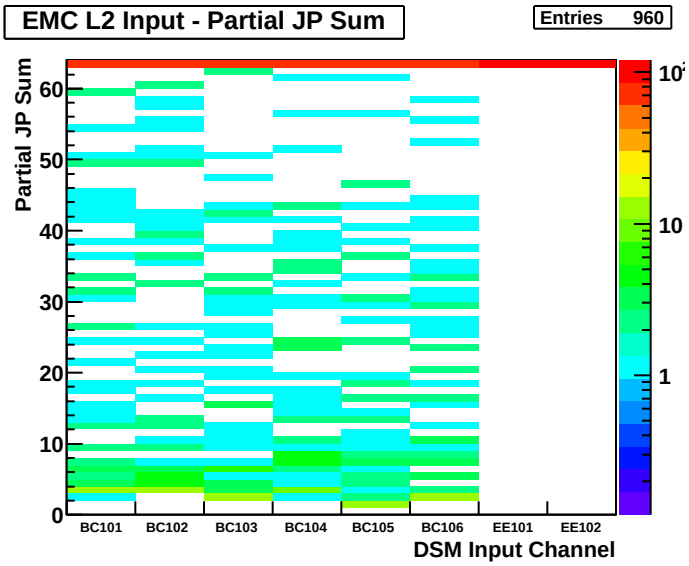

EMC L2 Input - JPX/JPA bits
Entries 1068
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
21 16 25 24 20 27 15 23
6 10 11 7 5 8 9 5
99 99 98 95 106 95 122 122

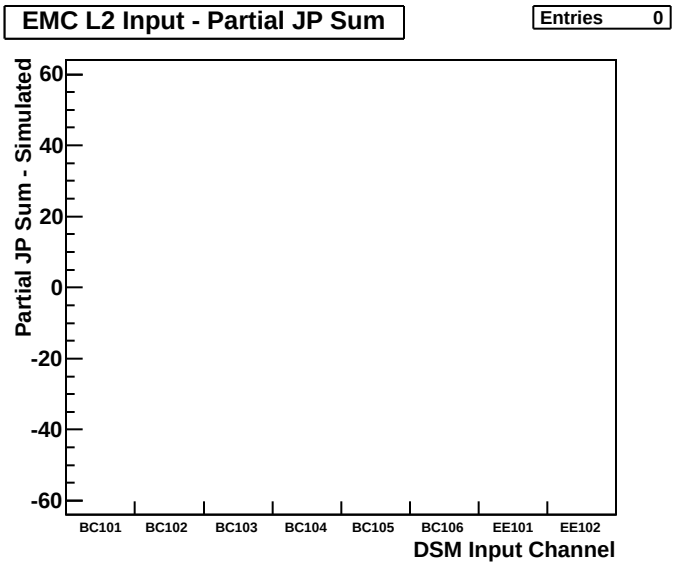

EMC L2 Input - JPY/JPB bits
Entries 960
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
78 76 75 79 75 76 46 46
7 7 9 5 7 11 8 13
3 2 4 3 3 1 5 2
32 35 32 33 35 32 61 59

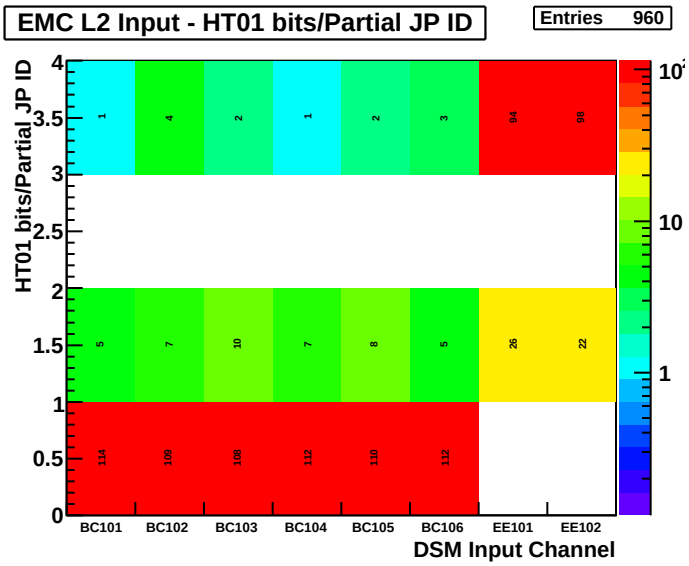

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

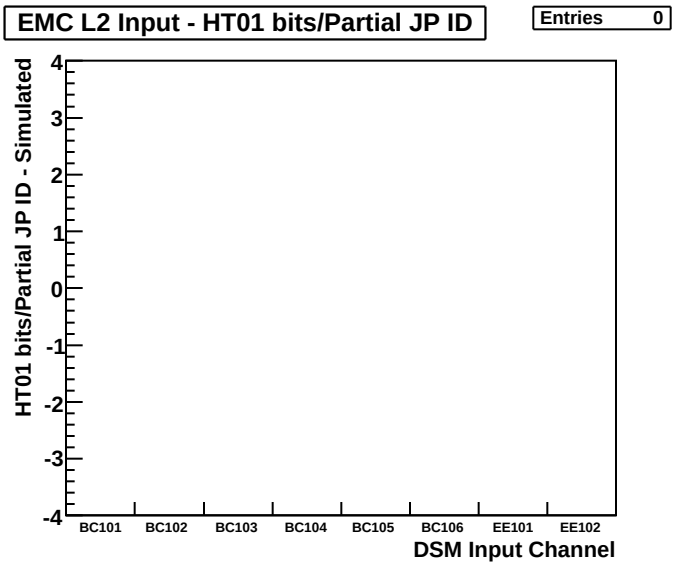

EMC L2 Input - JPZ/JPC bits
Entries 960
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
79 79 72 76 75 79 120 120
5 5 10 11 8 6
2 4 5 1 1 4
34 32 33 32 36 31

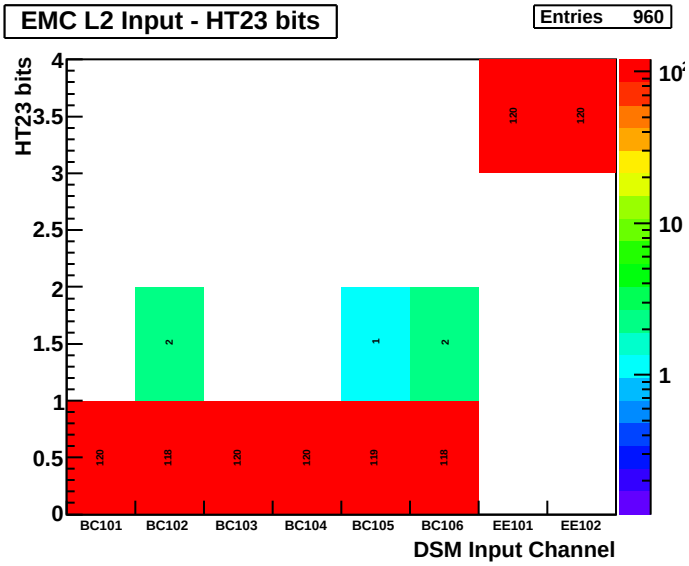

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

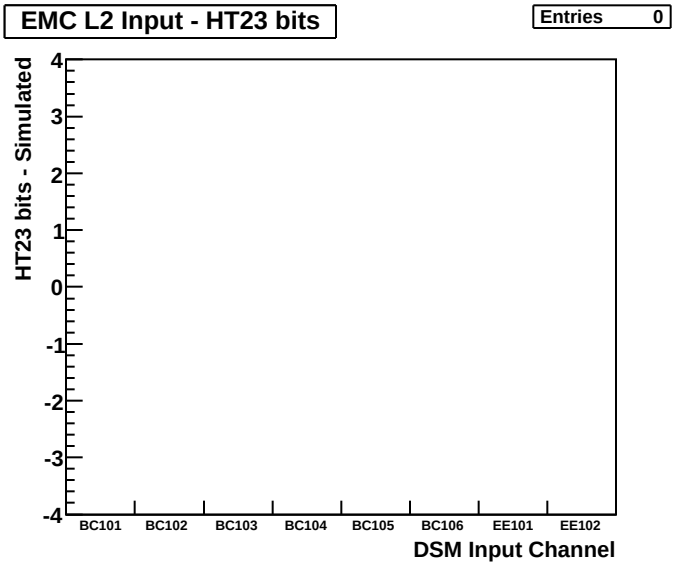

EMC L2 Input - Partial JP Sum
Entries 960
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

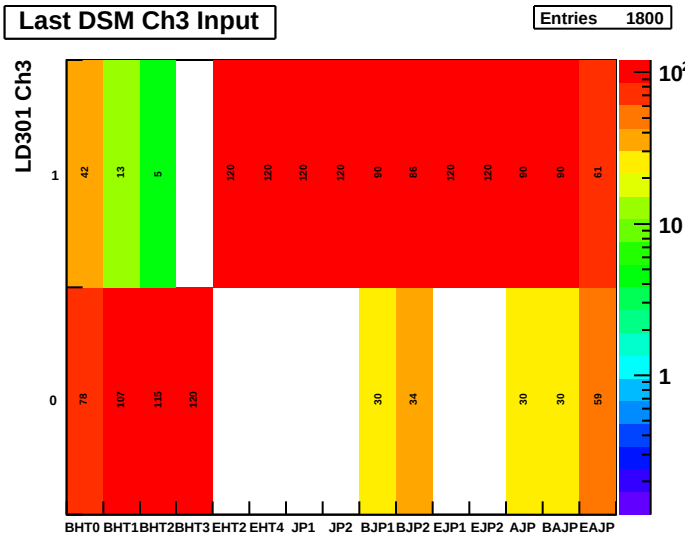

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

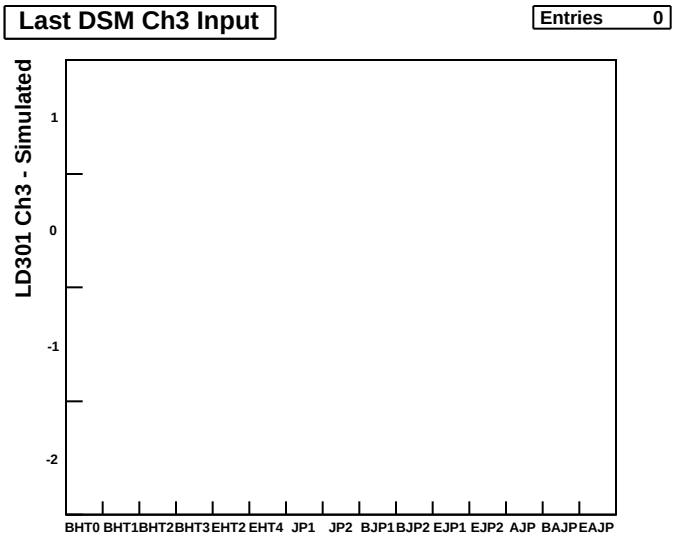

EMC L2 Input - HT01 bits/Partial JP ID
Entries 960
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

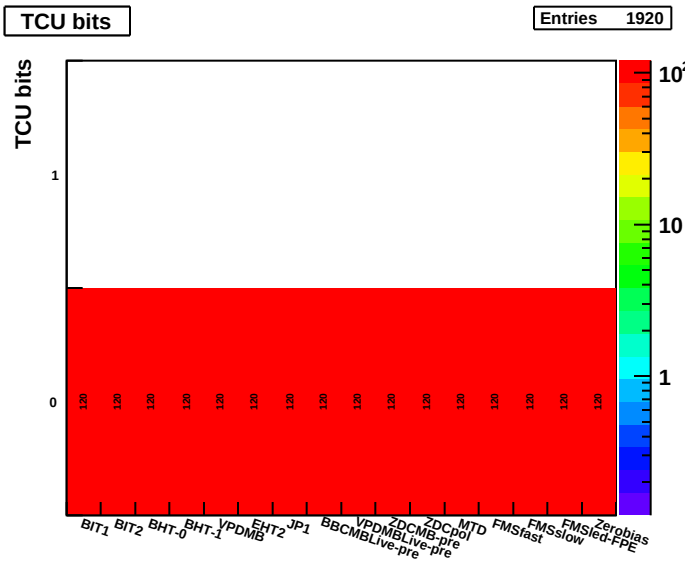

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

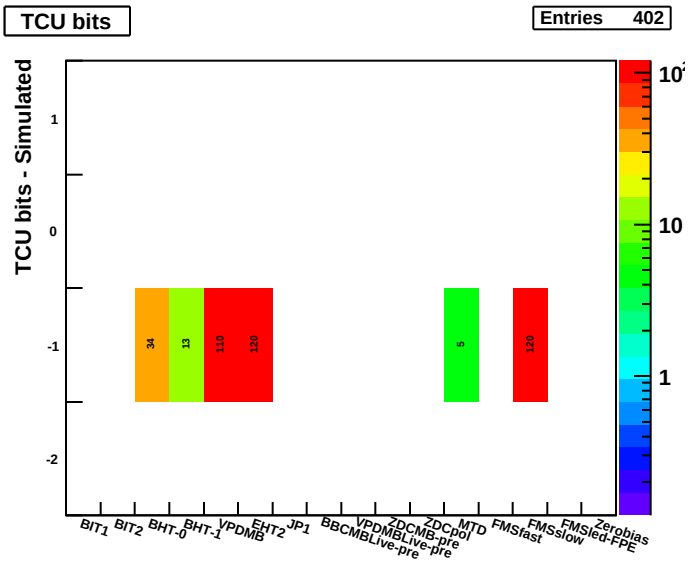

EMC L2 Input - HT23 bits
Entries 960
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

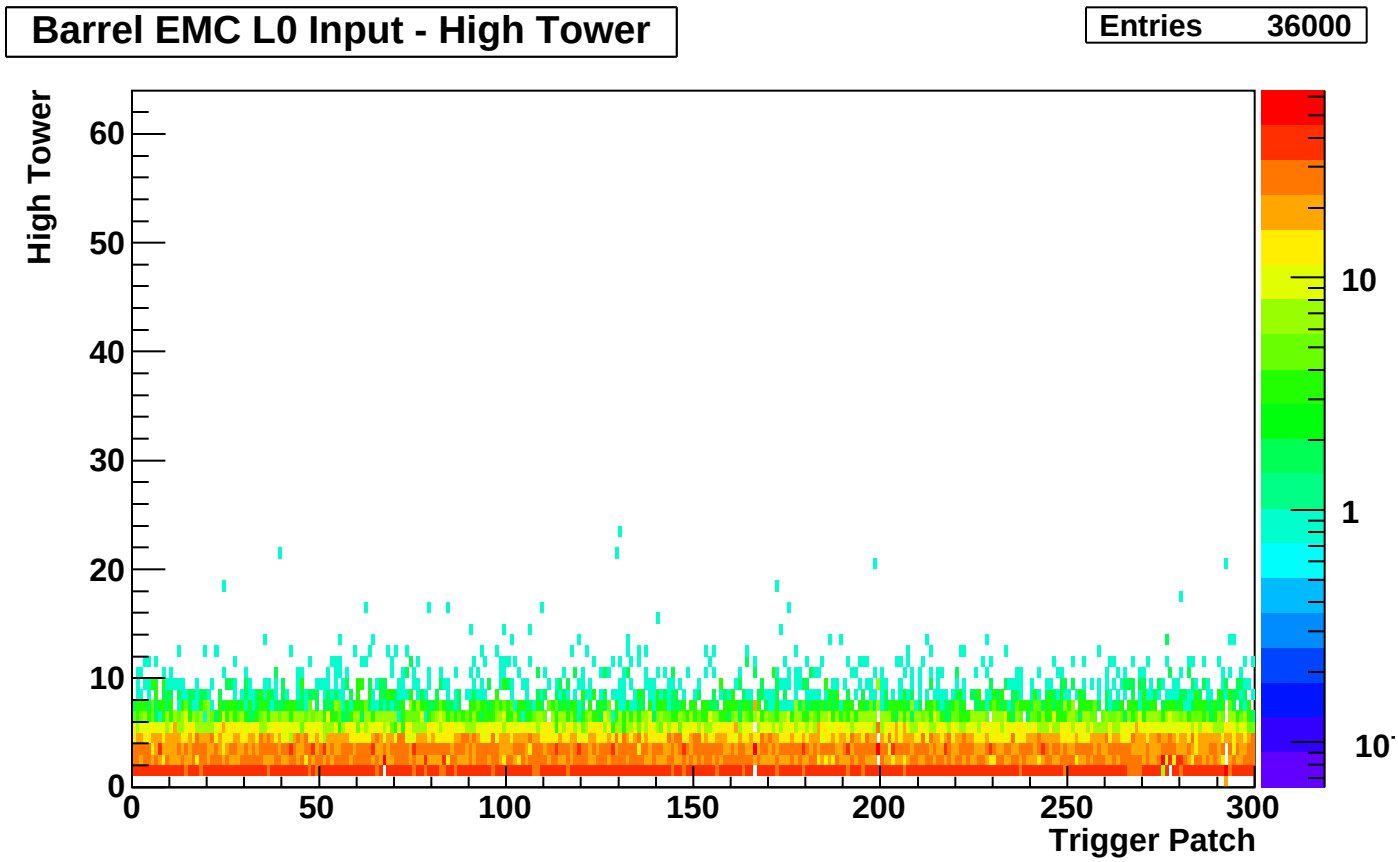

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

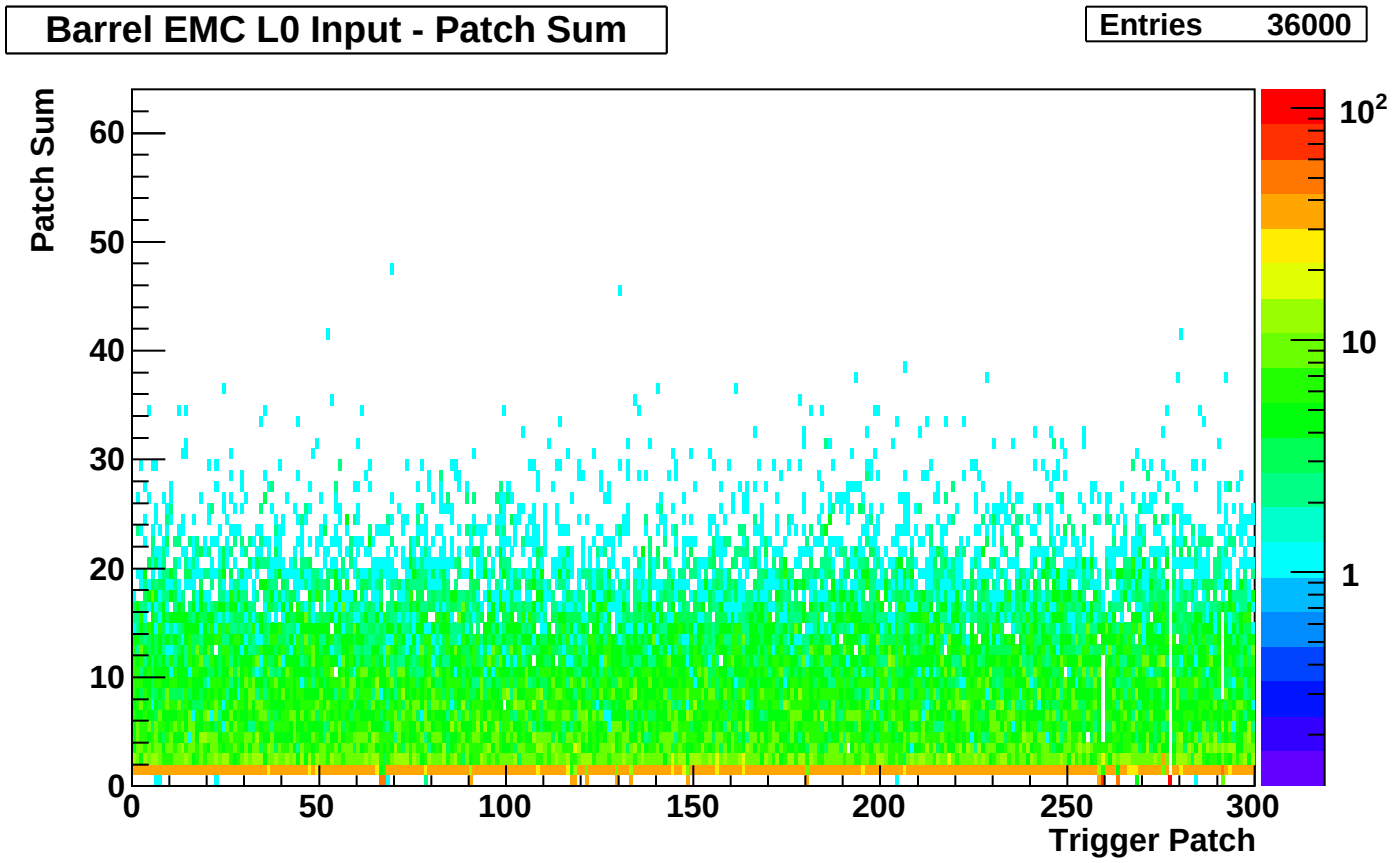

Last DSM Ch3 Input
Entries 1800
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

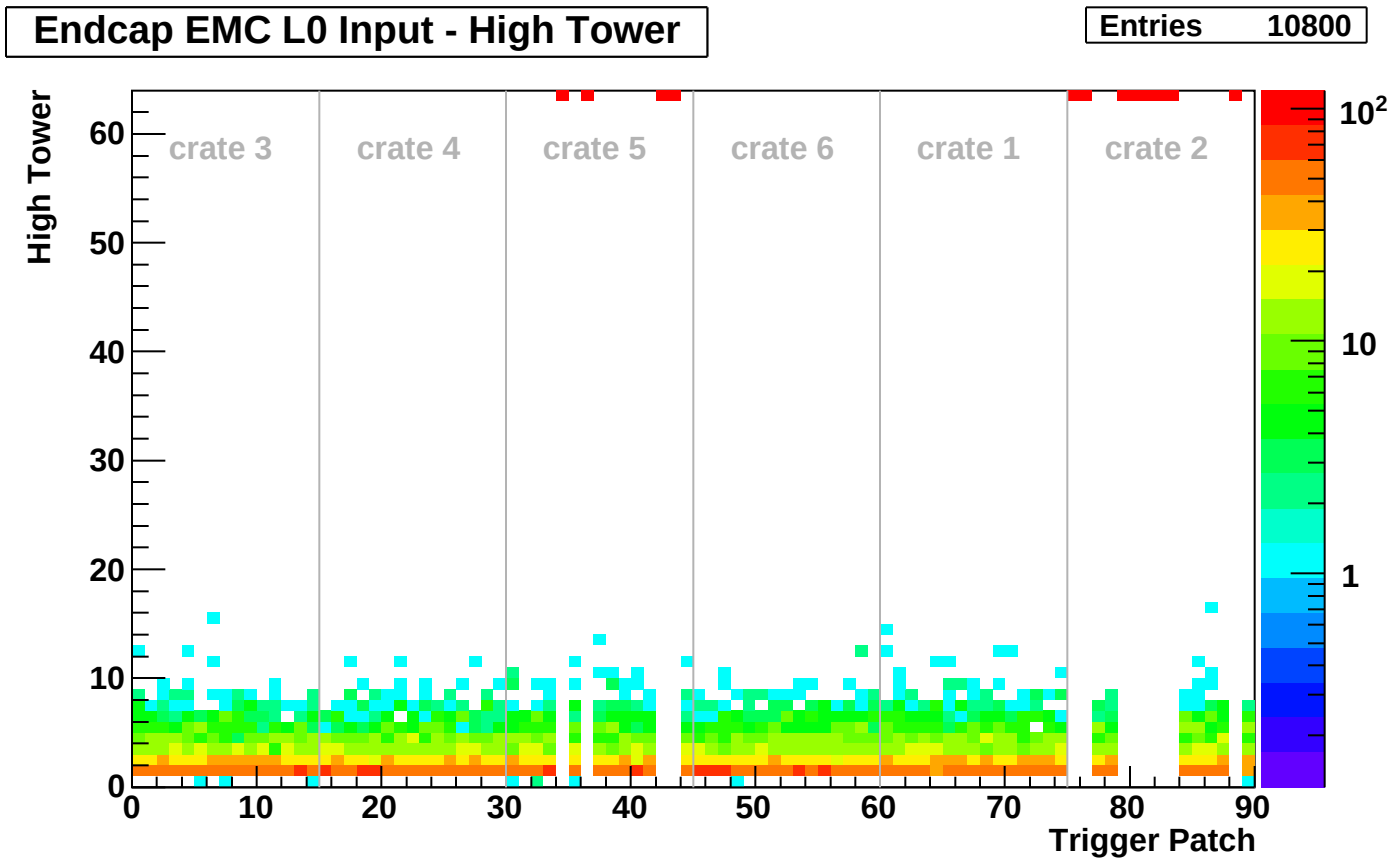

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

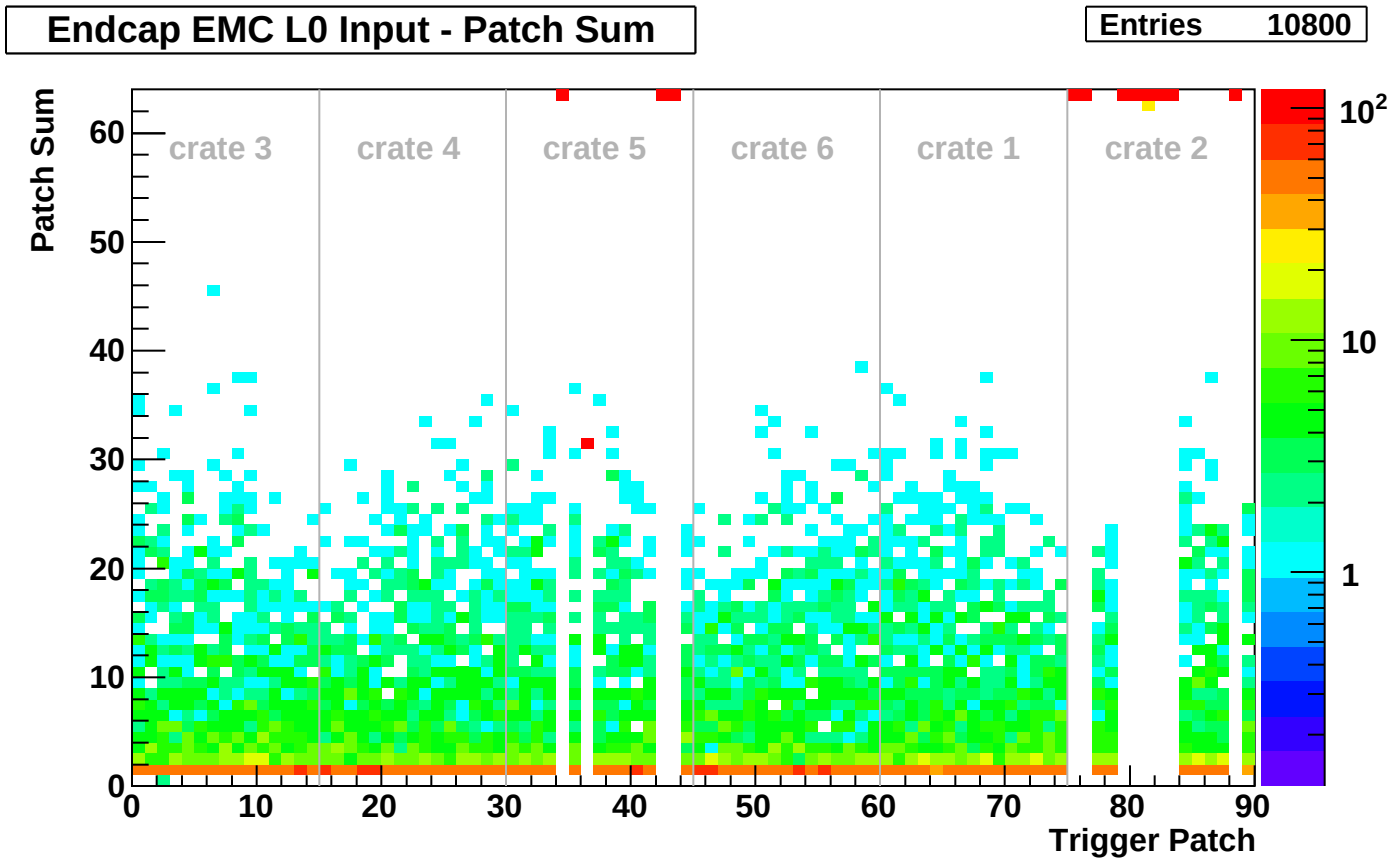

TCU bits
Entries 1920
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

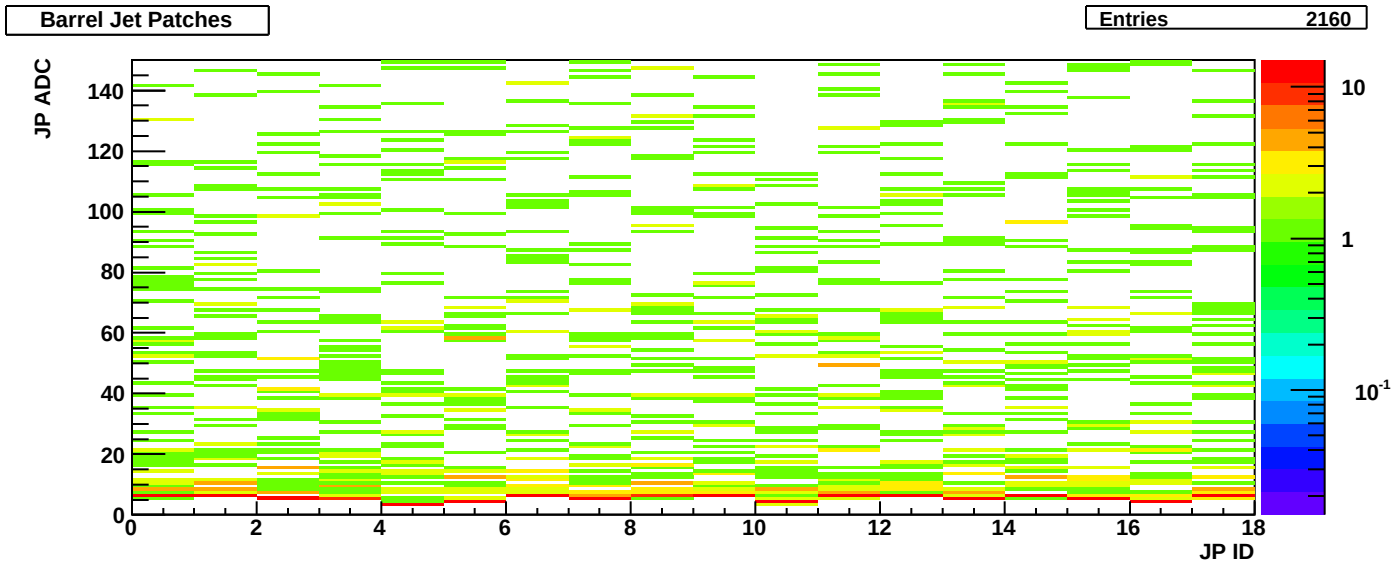

TCU bits
Entries 402
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

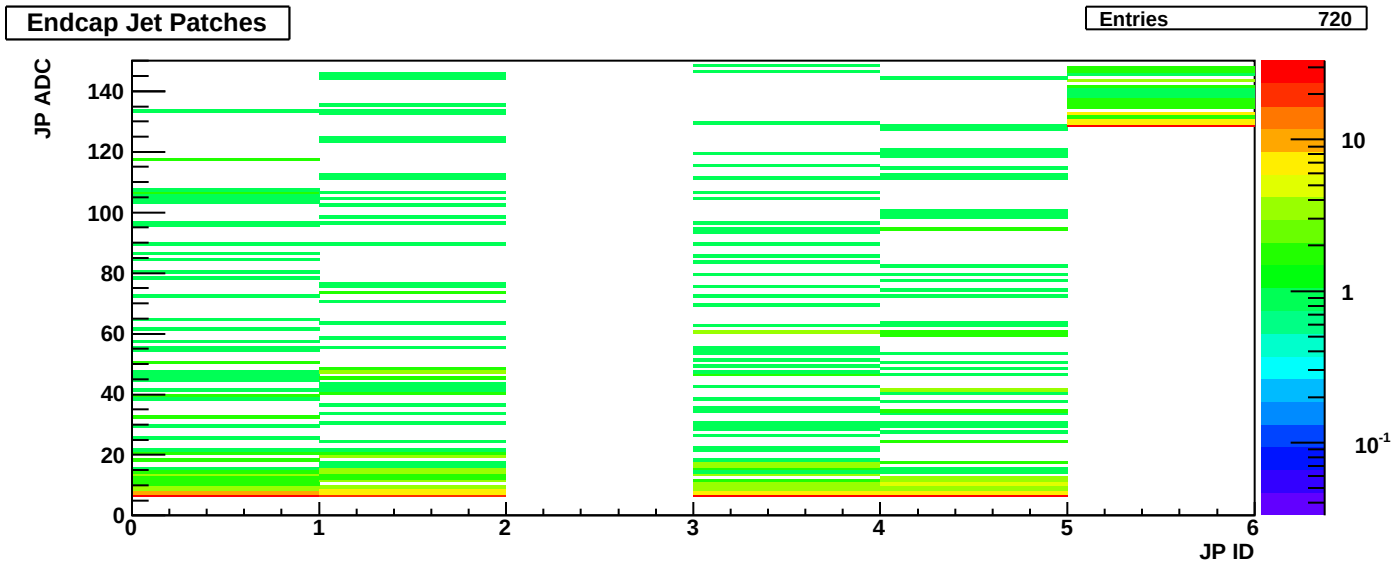

Barrel EMC L0 Input - High Tower
Entries 36000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
10
1

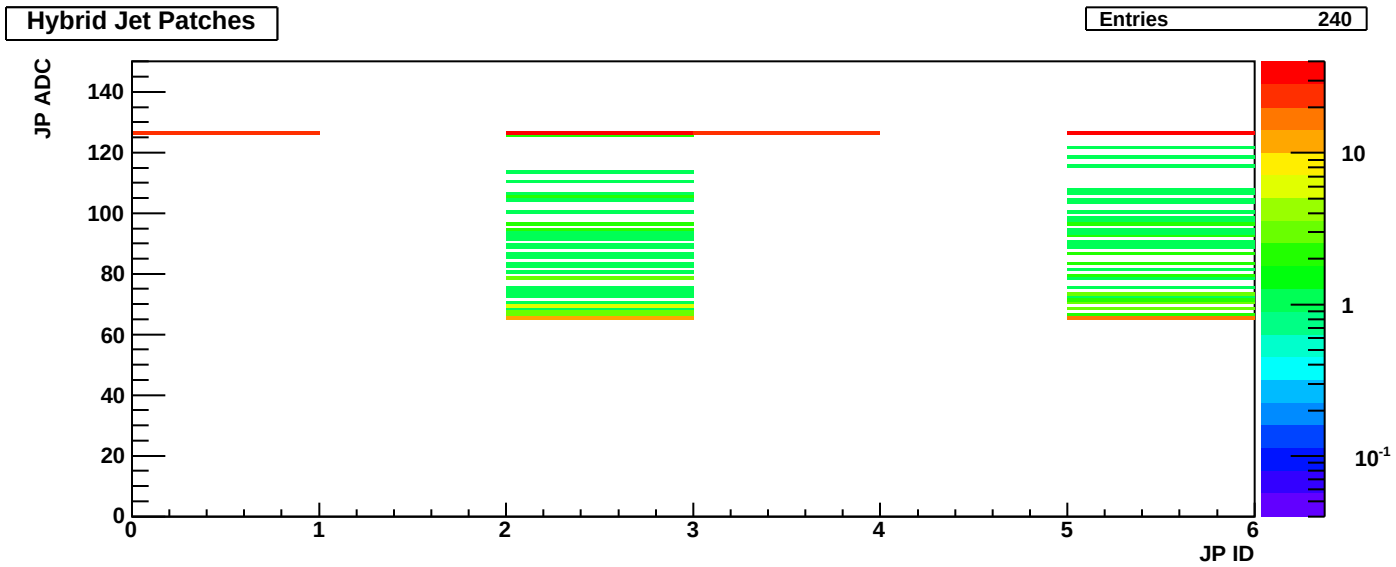

Barrel EMC L0 Input - Patch Sum
Entries 36000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
10^2
10
1

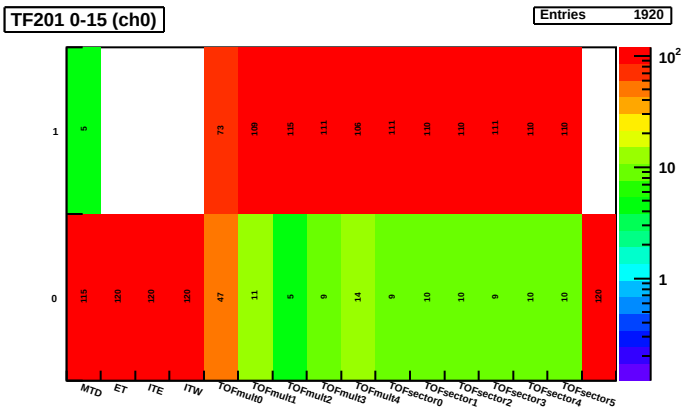

Endcap EMC L0 Input - High Tower
Entries 10800
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

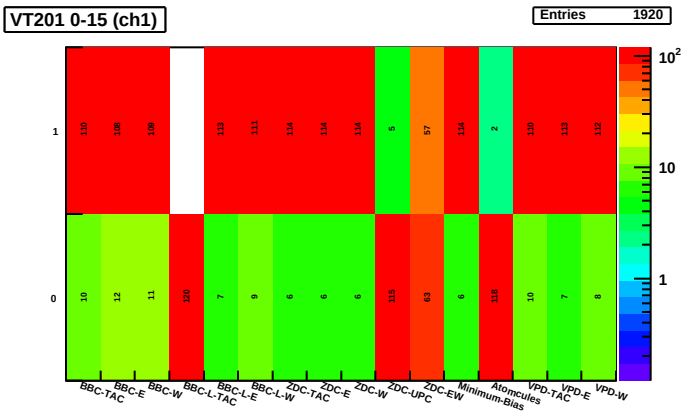

Endcap EMC L0 Input - Patch Sum
Entries 10800
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

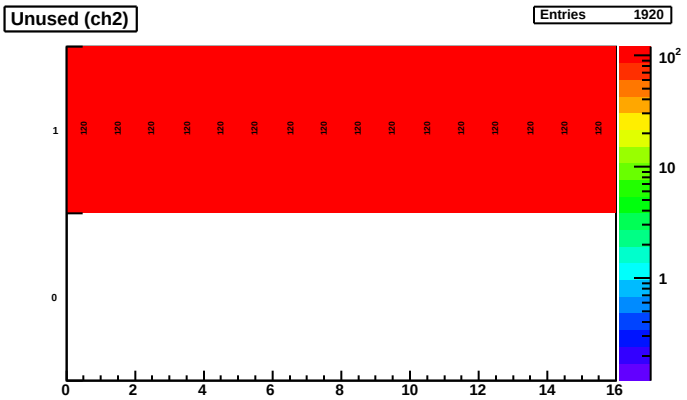

Barrel Jet Patches
Entries 2160
JP ADC
JP ID

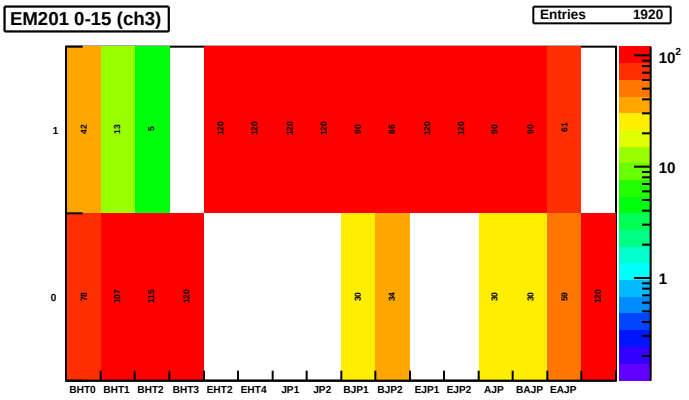

Endcap Jet Patches
Entries 720
JP ADC
JP ID

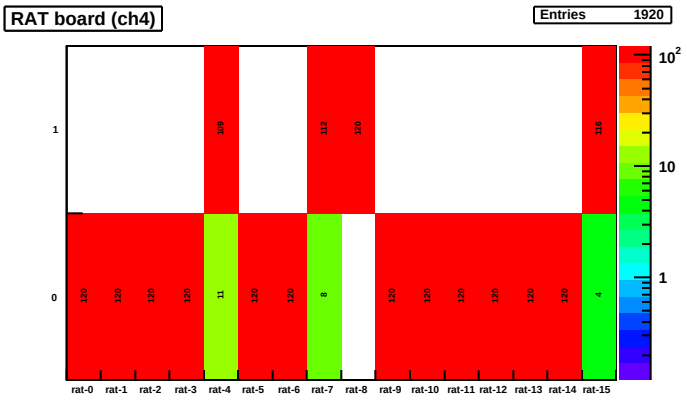

Hybrid Jet Patches
Entries 240
JP ADC
JP ID

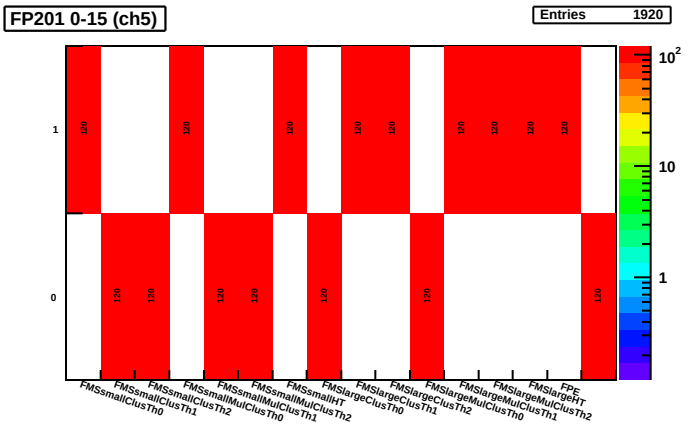

TF201 0-15 (ch0)
Entries 1920
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

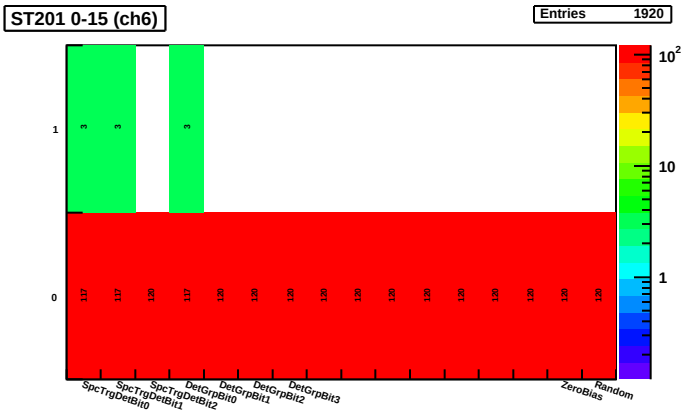

VT201 0-15 (ch1)
Entries 1920
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

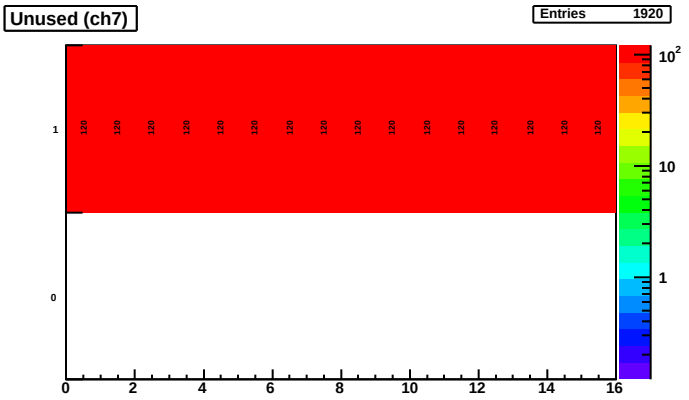

Unused (ch2)
Entries 1920

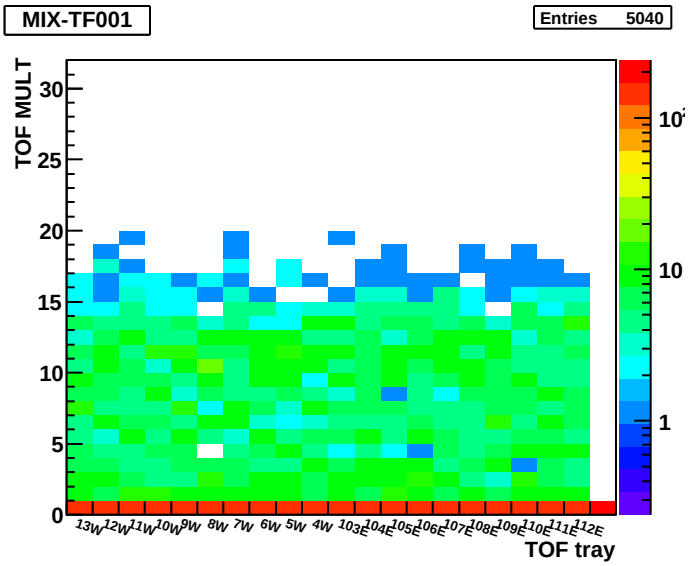

EM201 0-15 (ch3)
Entries 1920
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

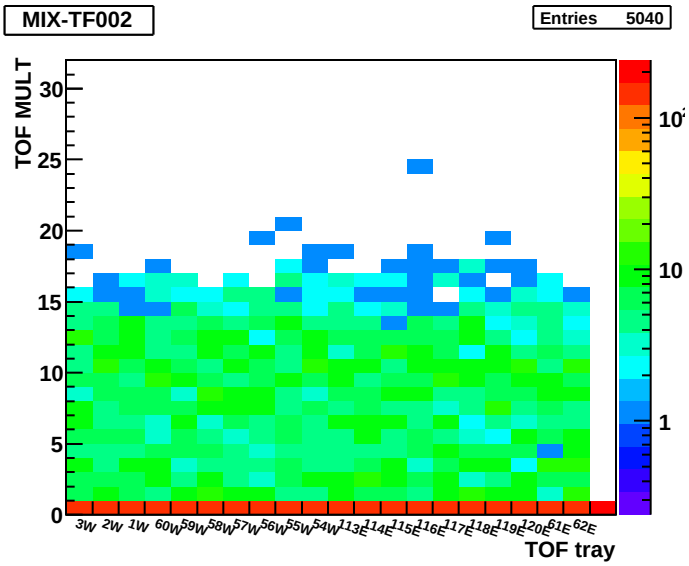

RAT board (ch4)
Entries 1920
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

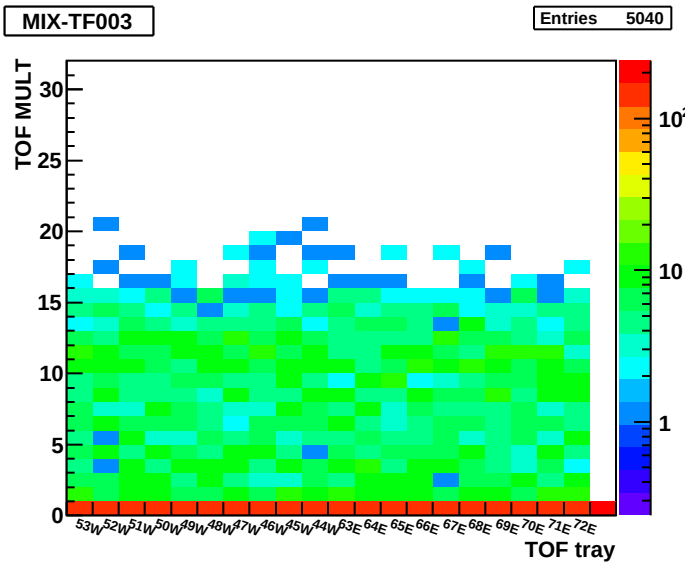

FP201 0-15 (ch5)
Entries 1920
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

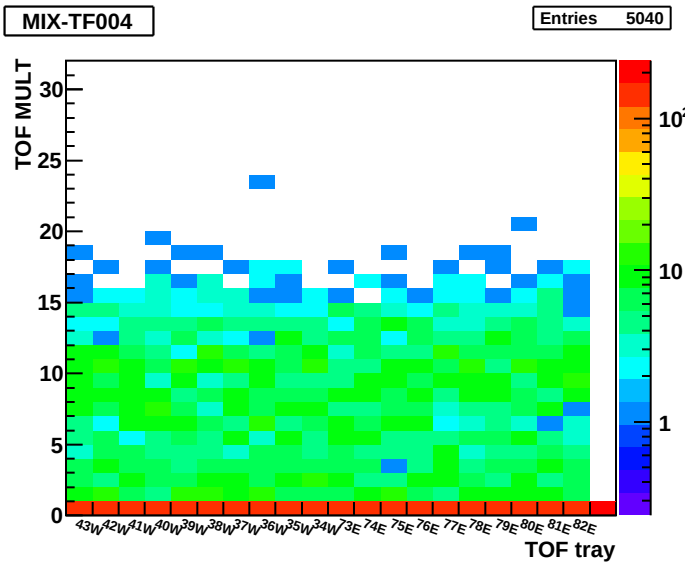

ST201 0-15 (ch6)
Entries 1920
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

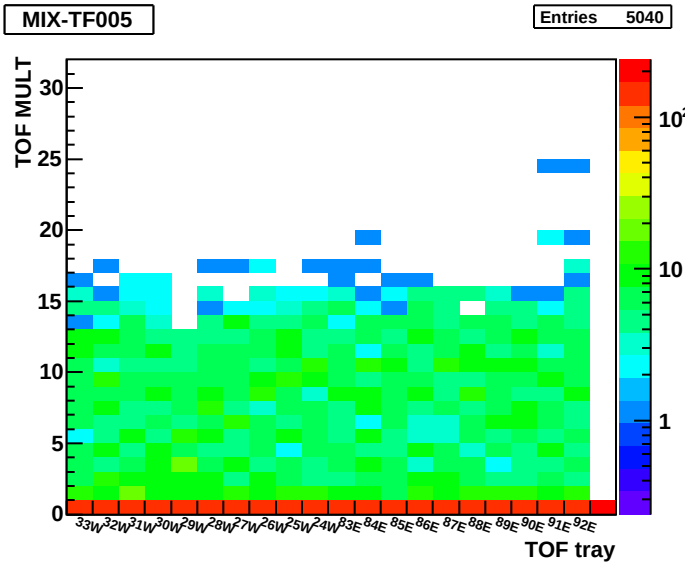

Unused (ch7)
Entries 1920

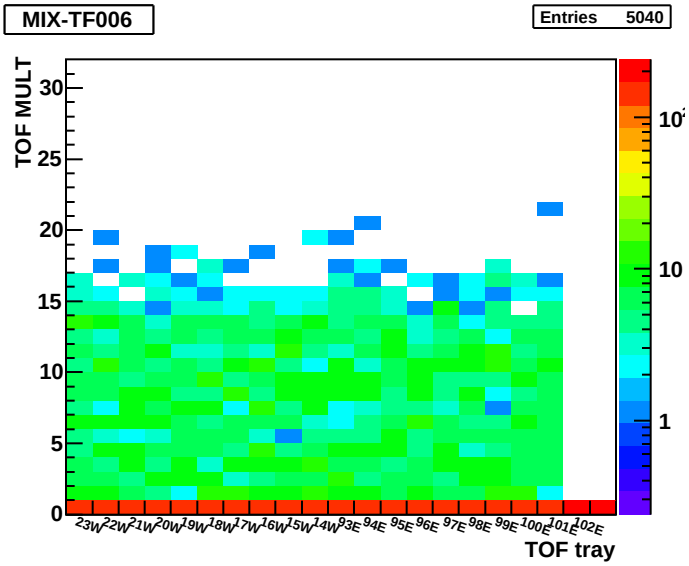

MIX-TF001
Entries 5040
TOF MULT
TOF tray

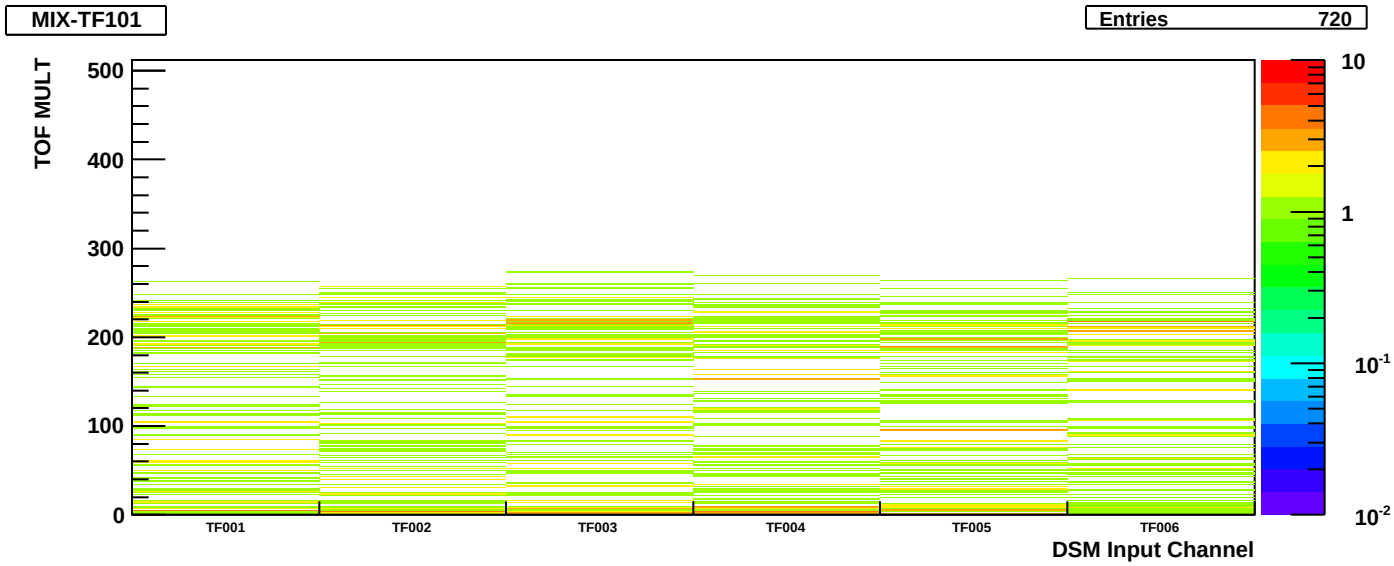

MIX-TF002
Entries 5040
TOF MULT
TOF tray

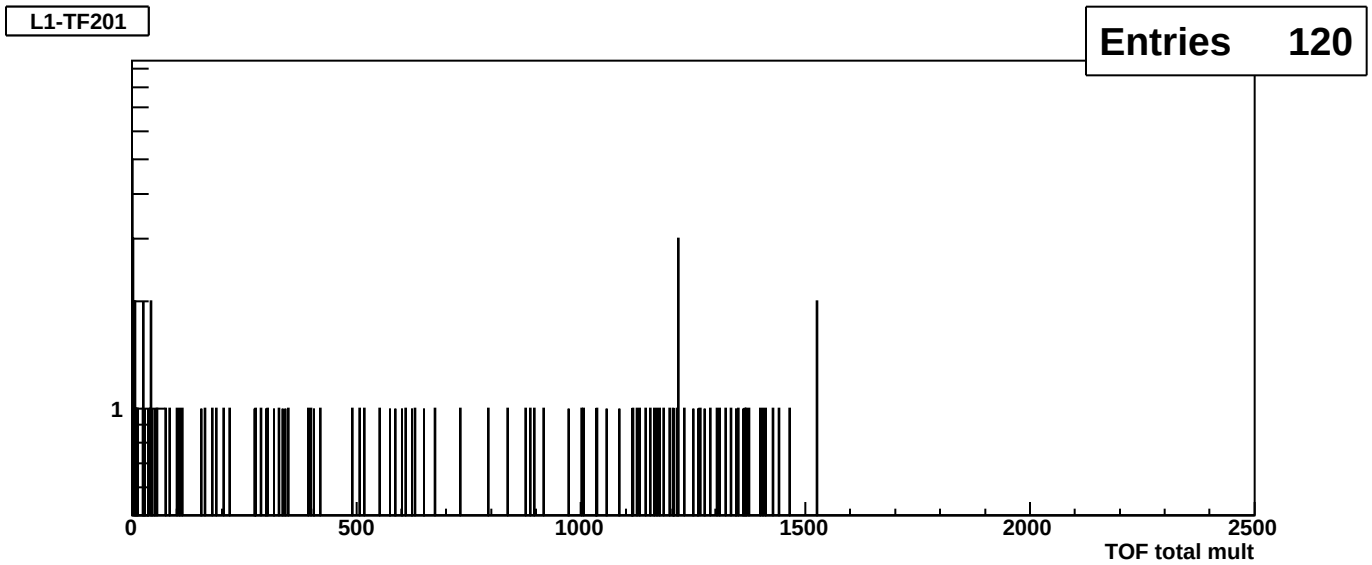

MIX-TF003
Entries 5040
TOF MULT
TOF tray

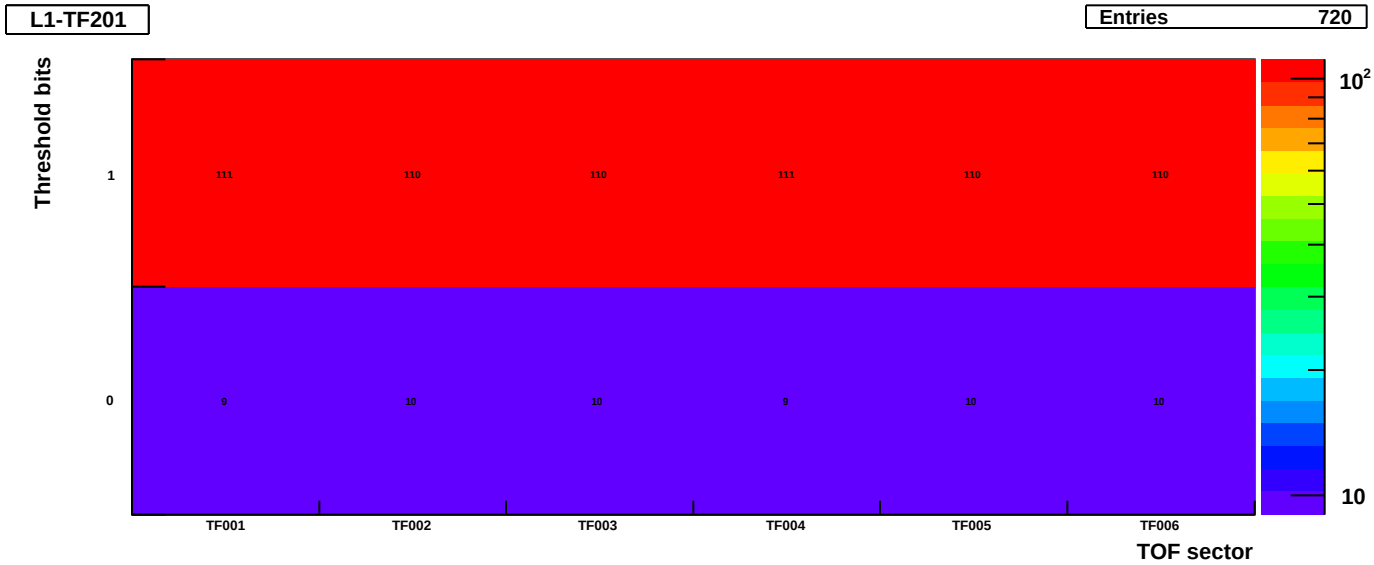

MIX-TF004
Entries 5040
TOF MULT
TOF tray

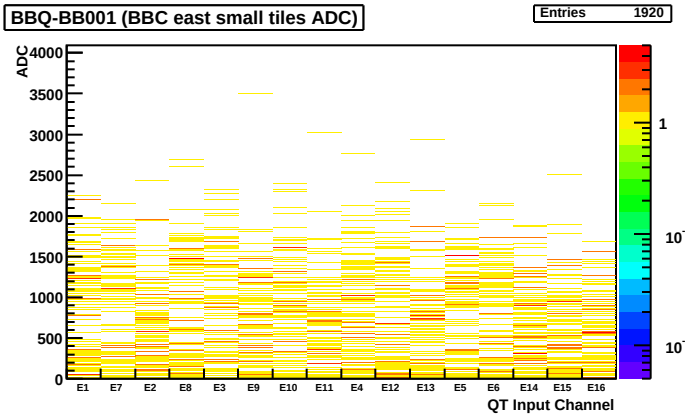

MIX-TF005
Entries 5040
TOF MULT
TOF tray

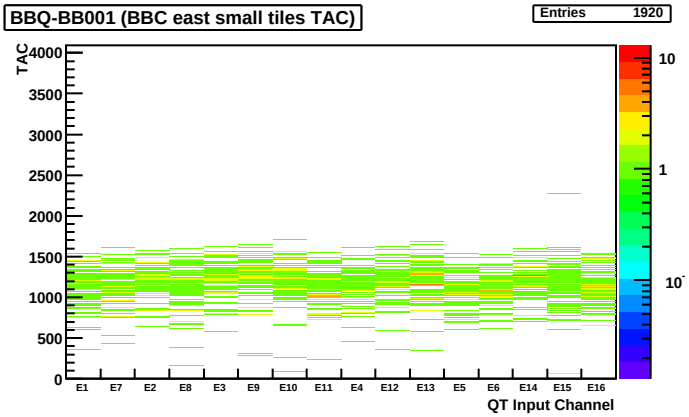

MIX-TF006
Entries 5040
TOF MULT
TOF tray

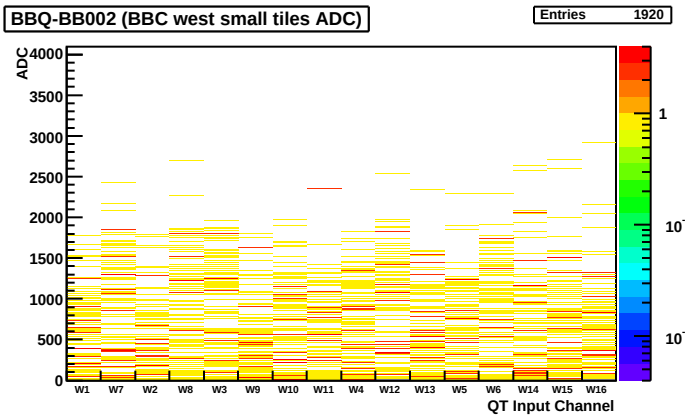

MIX-TF101
Entries 720
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10
1
10^{-1}
10^{-2}

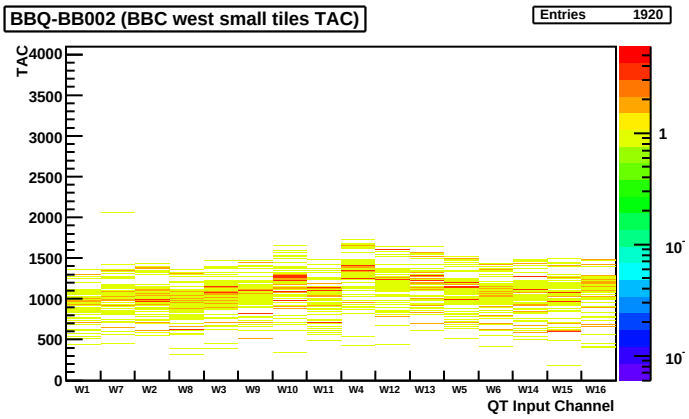

L1-TF201
Entries 120
1
0
500
1000
1500
2000
2500
TOF total mult

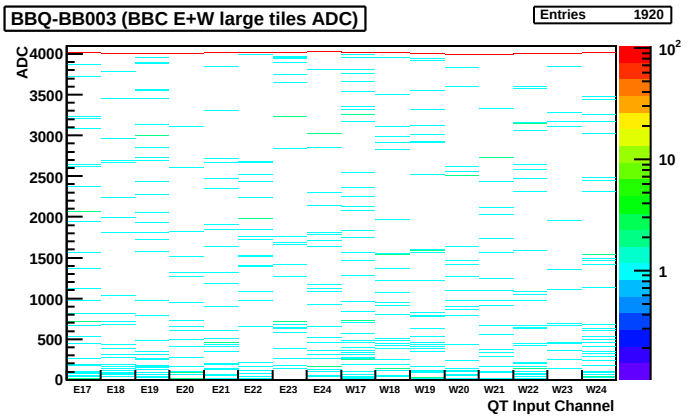

L1-TF201
Entries 720
Threshold bits
1
0
111
110
110
111
110
110
9
10
10
9
10
10
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^{2}
10

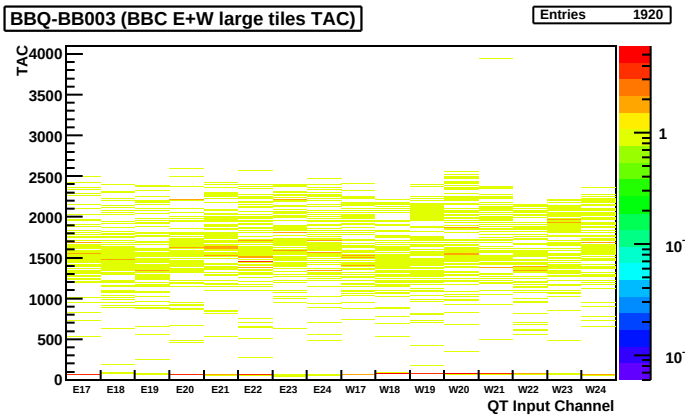

BBQ-BB001 (BBC east small tiles ADC)
Entries 1920
ADC
QT Input Channel

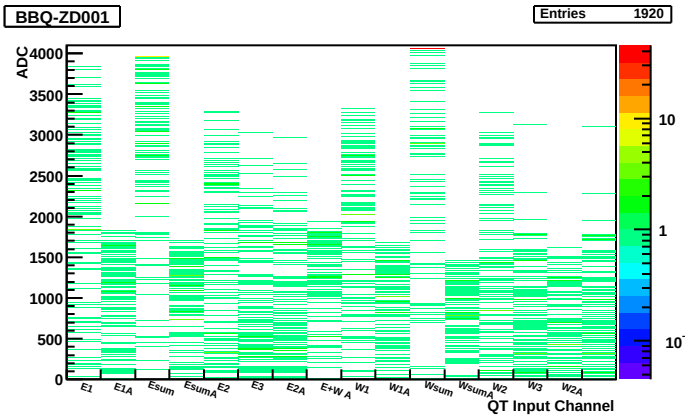

BBQ-BB001 (BBC east small tiles TAC)
Entries 1920
TAC
QT Input Channel

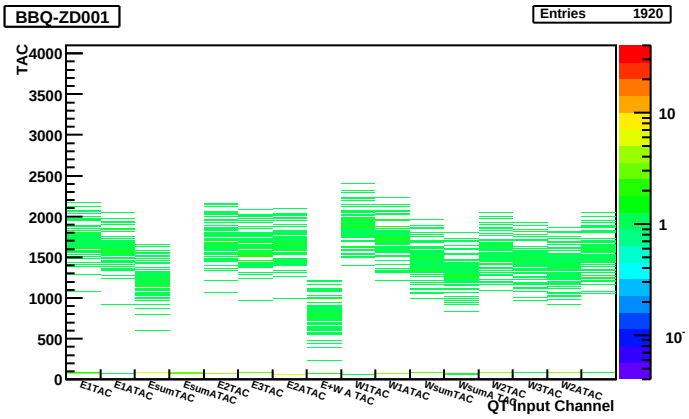

BBQ-BB002 (BBC west small tiles ADC)
Entries 1920
ADC
QT Input Channel

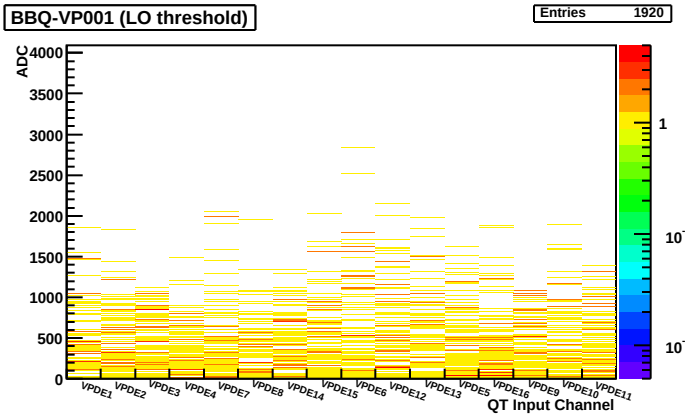

BBQ-BB002 (BBC west small tiles TAC)
Entries 1920
TAC
QT Input Channel

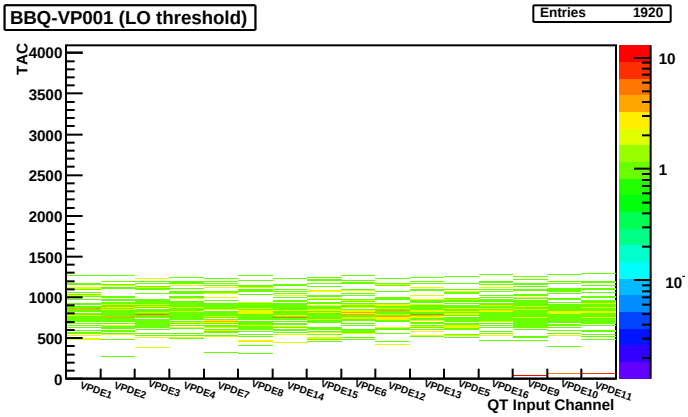

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1920
ADC
QT Input Channel

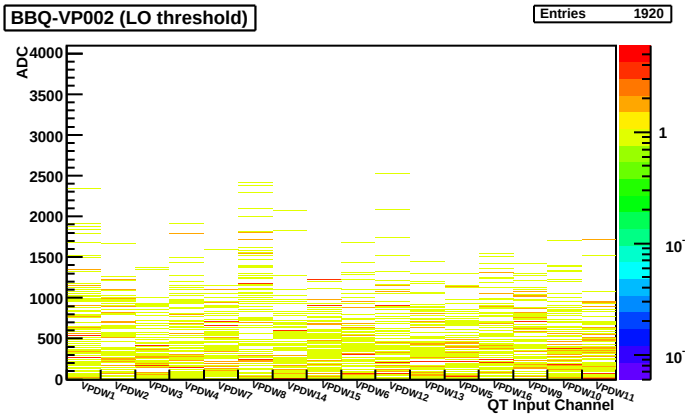

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1920
TAC
QT Input Channel

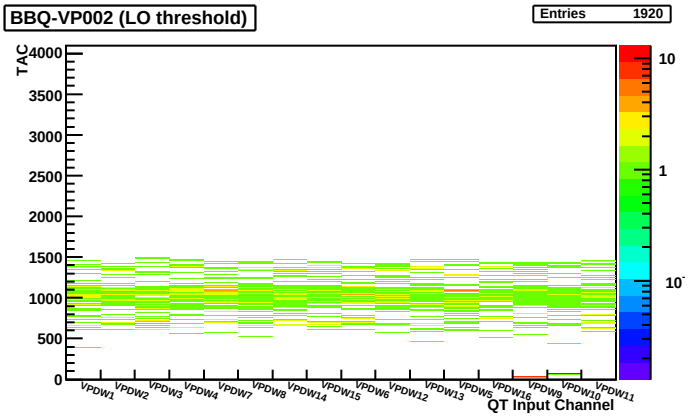

BBQ-ZD001
Entries 1920
ADC
QT Input Channel

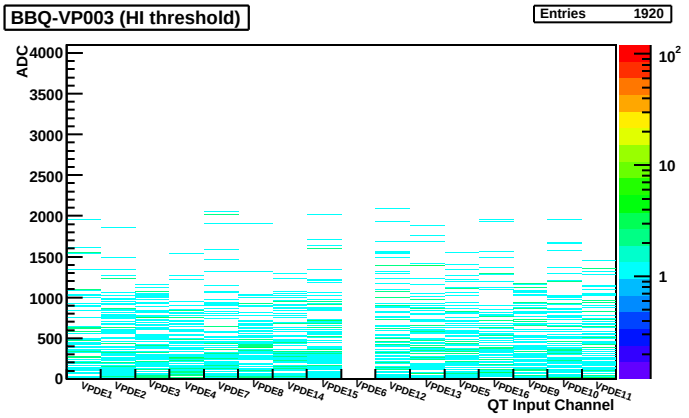

BBQ-ZD001
Entries 1920
TAC
QT Input Channel

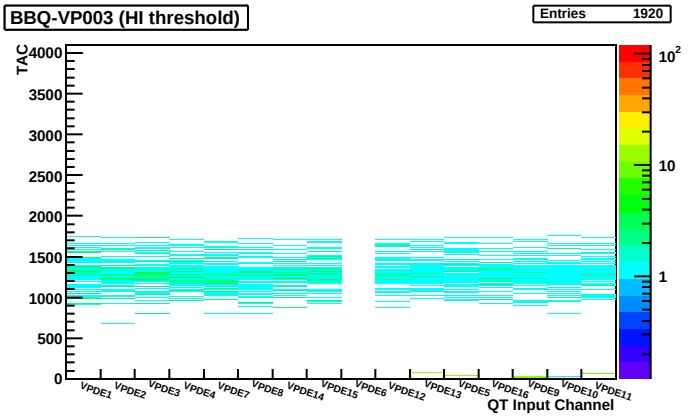

BBQ-VP001 (LO threshold)
Entries 1920
ADC
QT Input Channel

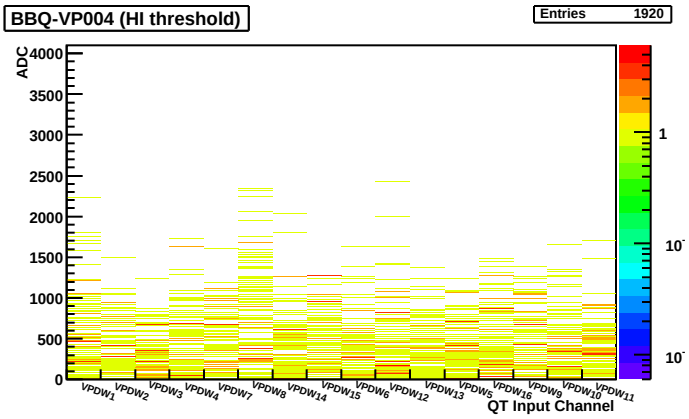

BBQ-VP001 (LO threshold)
Entries 1920
TAC
QT Input Channel

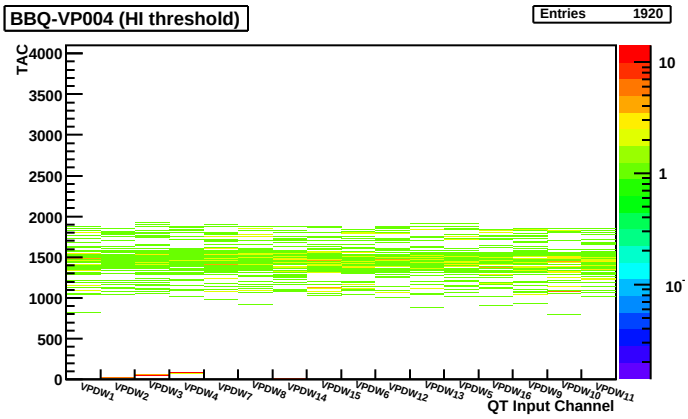

BBQ-VP002 (LO threshold)
Entries 1920
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 1920
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1920
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1920
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1920
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1920
TAC
QT Input Channel