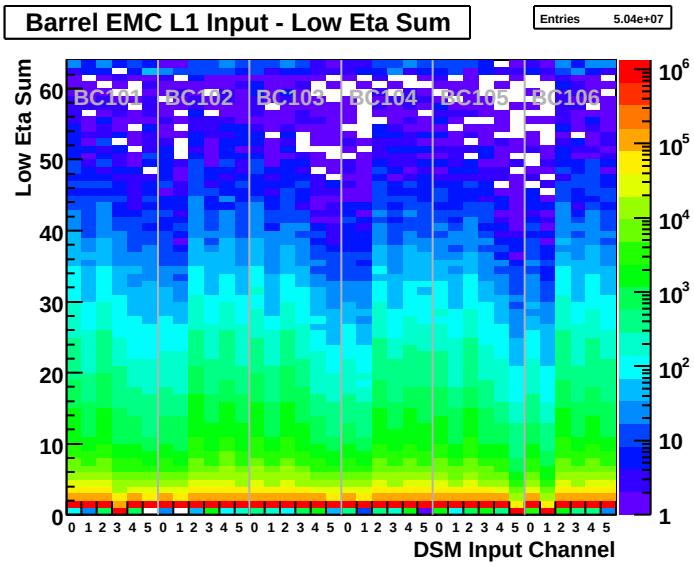
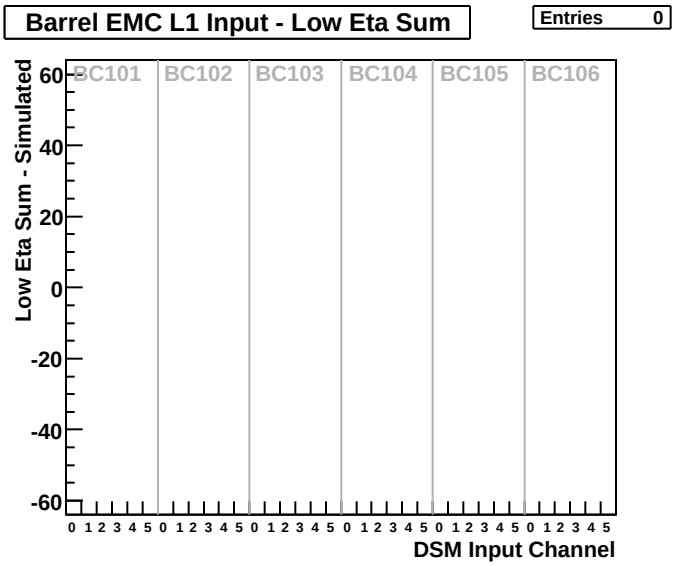
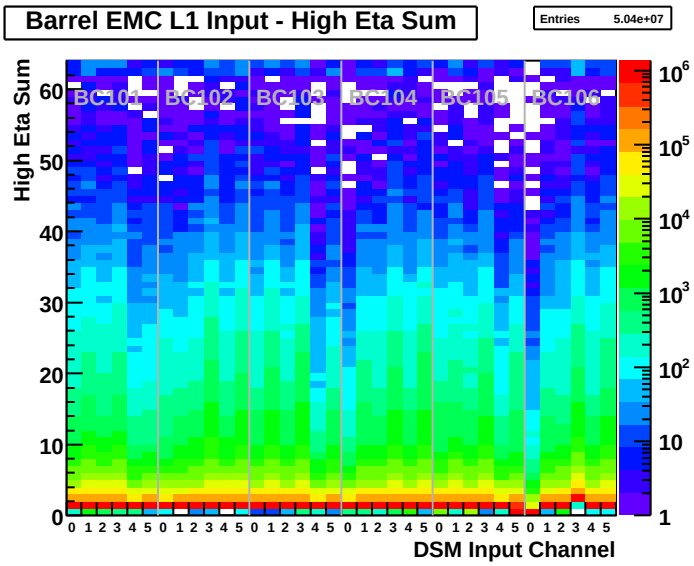
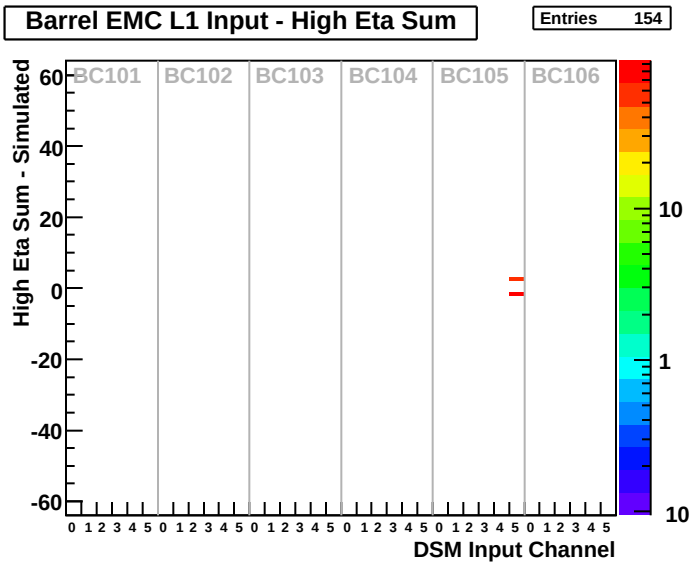
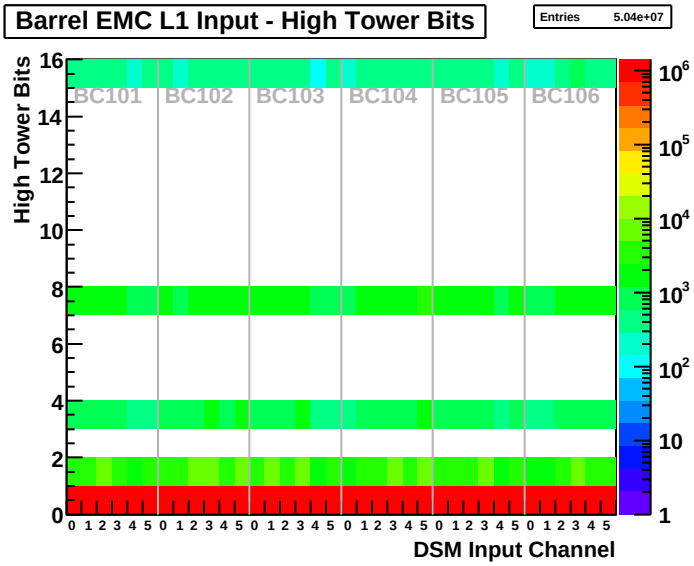
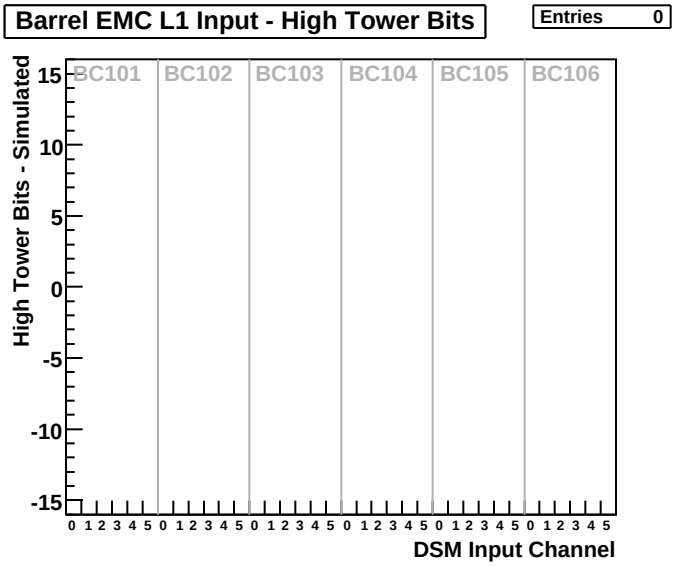

Barrel EMC L1 Input - Low Eta Sum
Entries 5.04e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

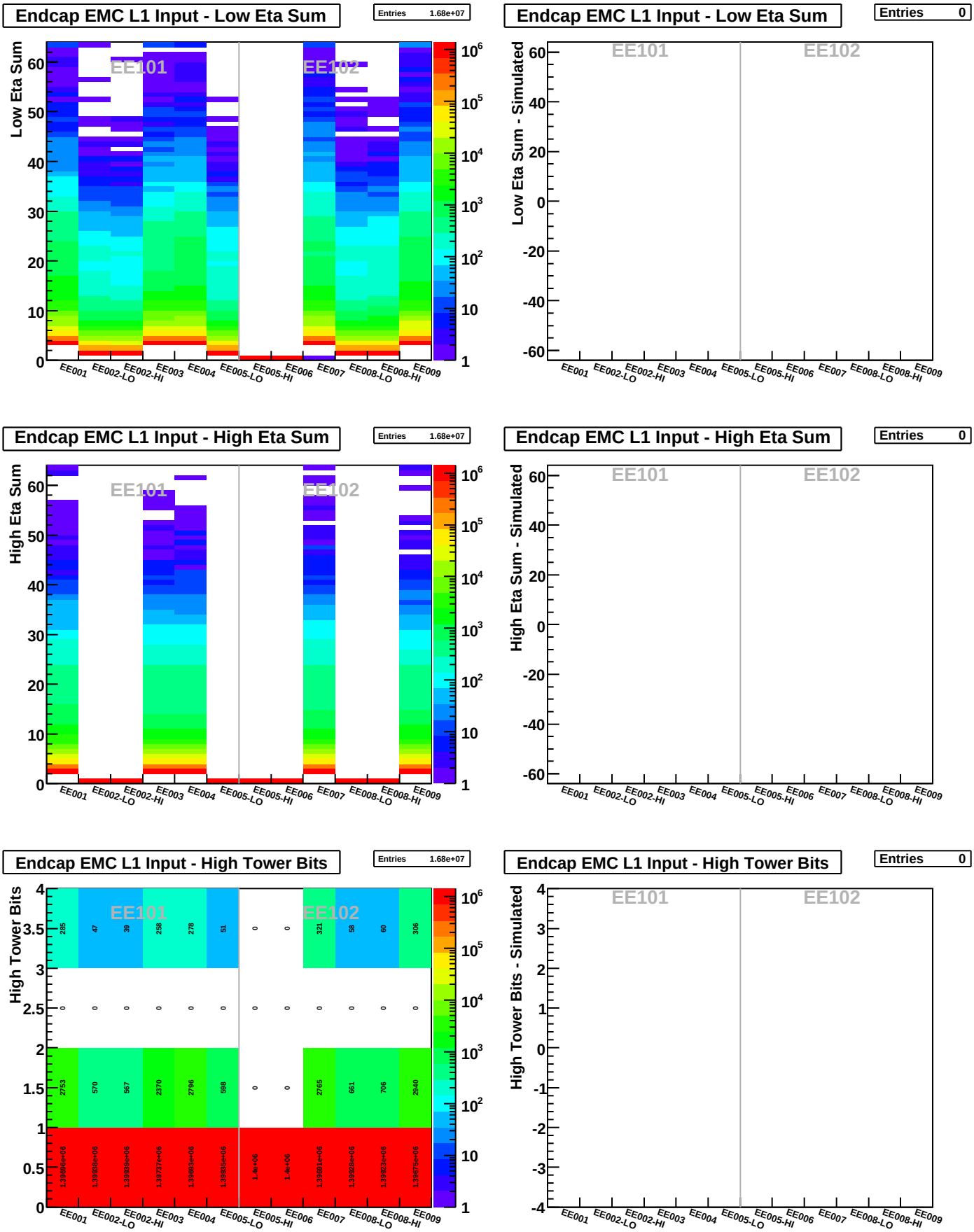

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

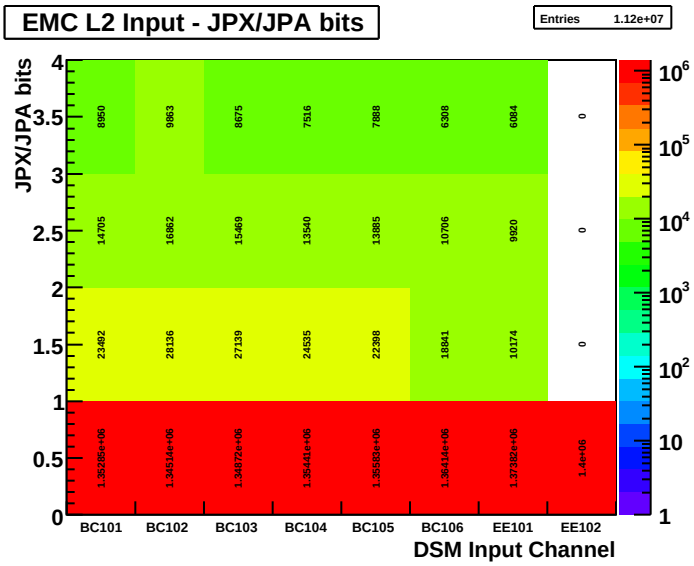

Barrel EMC L1 Input - High Eta Sum
Entries 5.04e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

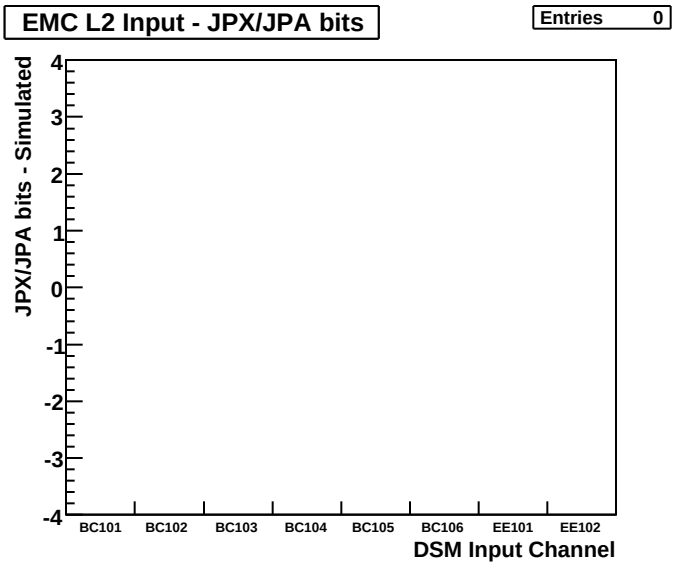

Barrel EMC L1 Input - High Eta Sum
Entries 154
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

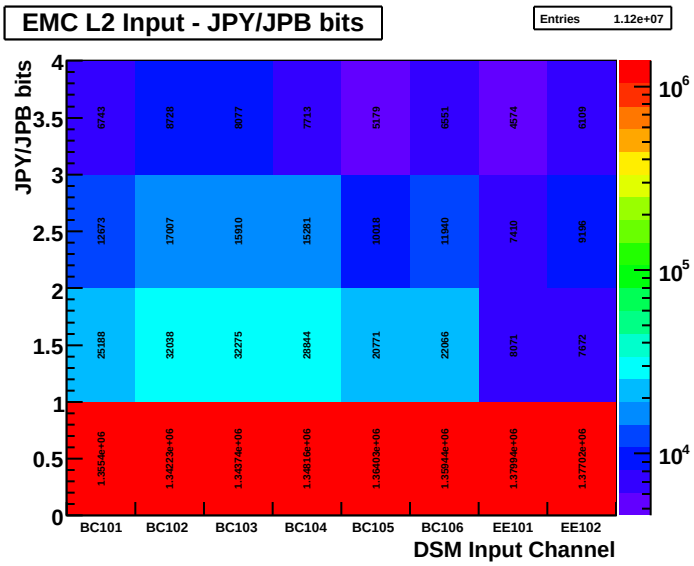

Barrel EMC L1 Input - High Tower Bits
Entries 5.04e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

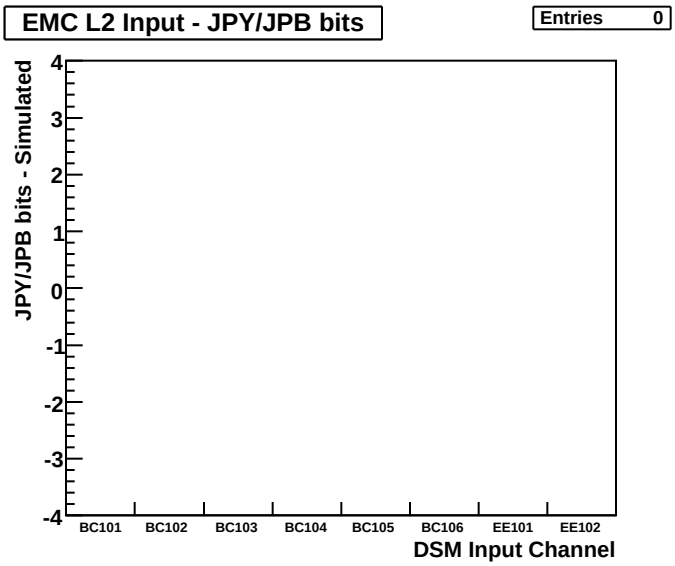

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

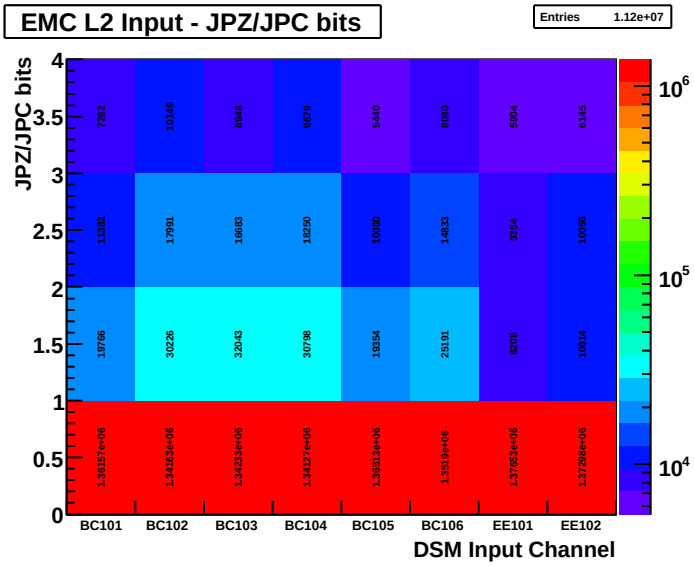

Endcap EMC L1 Input - Low Eta Sum
Entries 1.68e+07
Low Eta Sum
EE101
EE102
Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
Endcap EMC L1 Input - High Eta Sum
Entries 1.68e+07
High Eta Sum
Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
Endcap EMC L1 Input - High Tower Bits
Entries 1.68e+07
High Tower Bits
Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

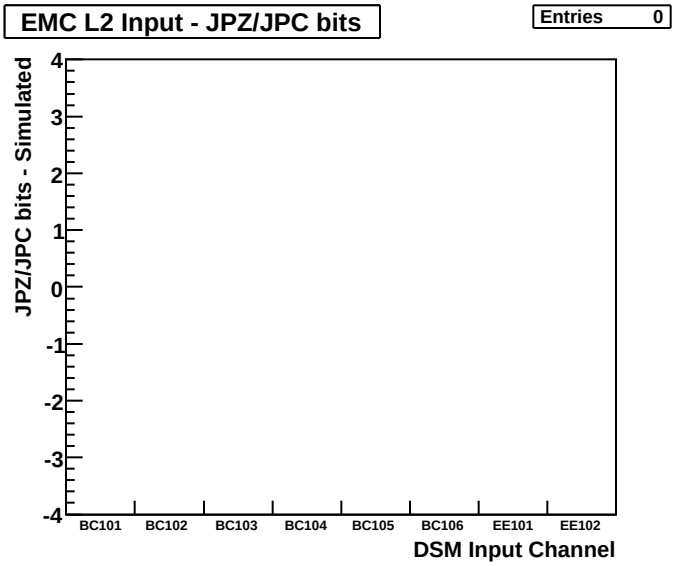

EMC L2 Input - JPX/JPA bits
Entries 1.12e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8950 9863 8675 7516 7888 6308 6084 0
14705 16862 15469 13540 13885 10706 9920 0
23492 28136 27139 24535 22398 18841 10174 0
1.35285e+06 1.34514e+06 1.34872e+06 1.35441e+06 1.35583e+06 1.36414e+06 1.37382e+06 1.4e+06

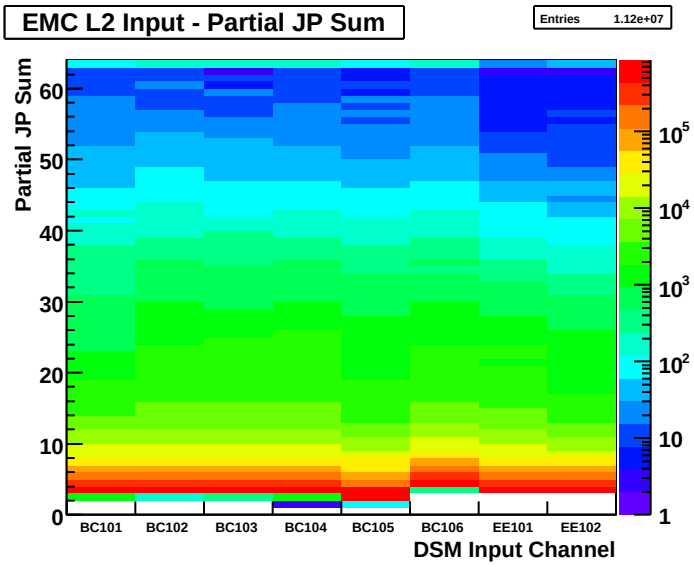

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

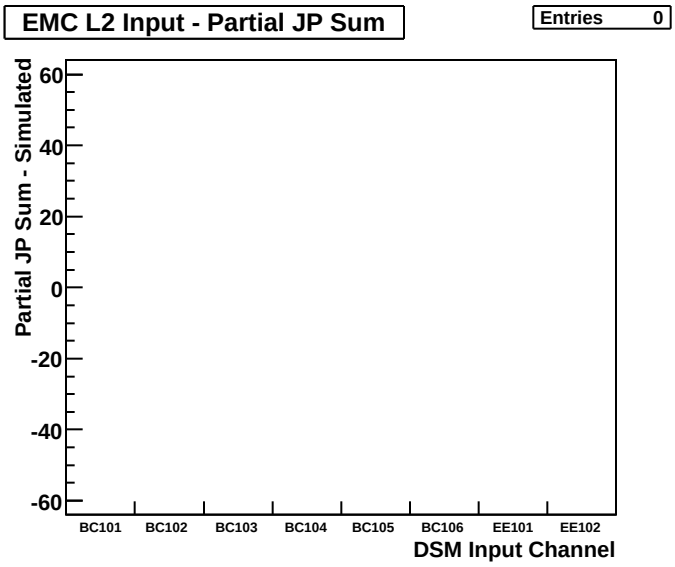

EMC L2 Input - JPY/JPB bits
Entries 1.12e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6743 8726 8077 7713 5179 6551 4574 6109
12673 17007 15910 15281 10018 11940 7410 9196
25188 32038 32275 28944 20771 22066 8071 7672
1.3554e+06 1.34223e+06 1.34374e+06 1.34816e+06 1.36403e+06 1.35944e+06 1.37094e+06 1.37702e+06

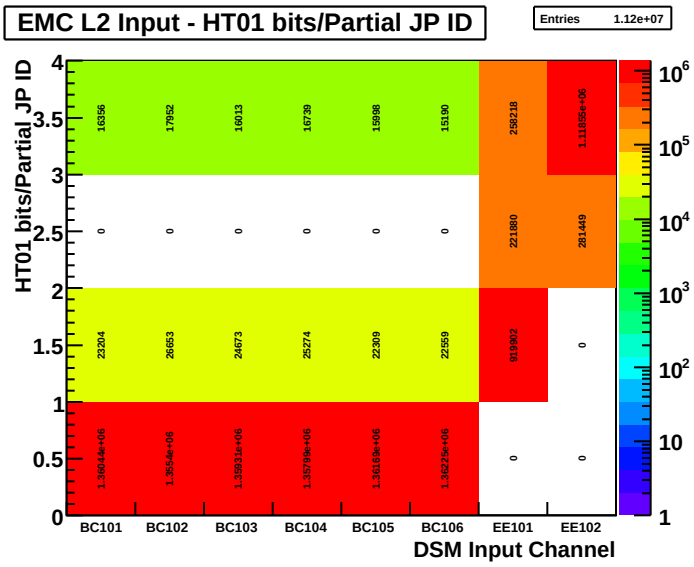

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

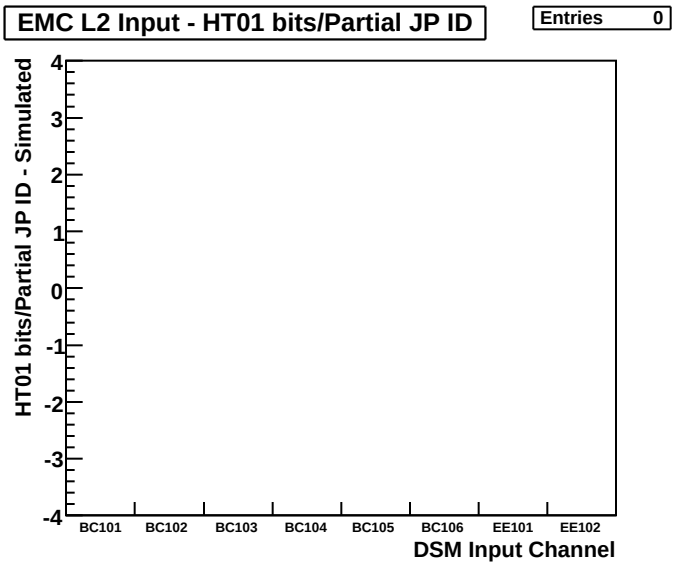

EMC L2 Input - JPZ/JPC bits
Entries 1.12e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7282 10149 8940 9679 5410 8080 5904 6145
11382 17991 16683 18250 10080 14633 9354 10056
19766 30226 32043 30798 19354 25191 8208 10814
1.36157e+06 1.34163e+06 1.34233e+06 1.34127e+06 1.36513e+06 1.3519e+06 1.37653e+06 1.37298e+06

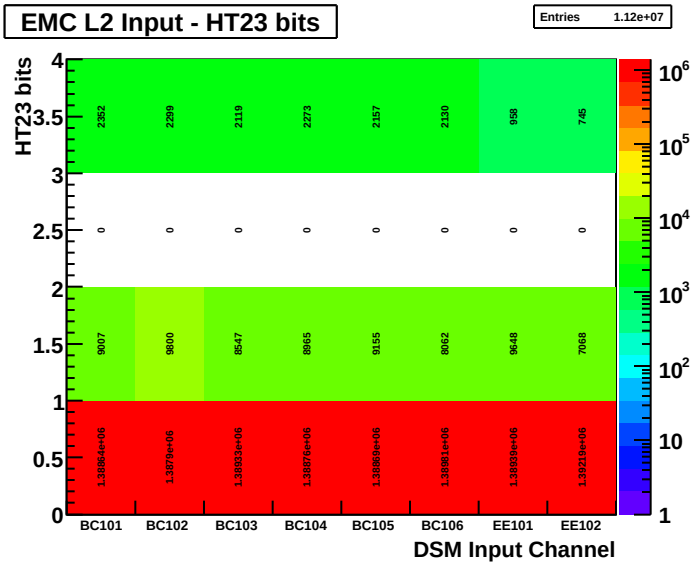

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

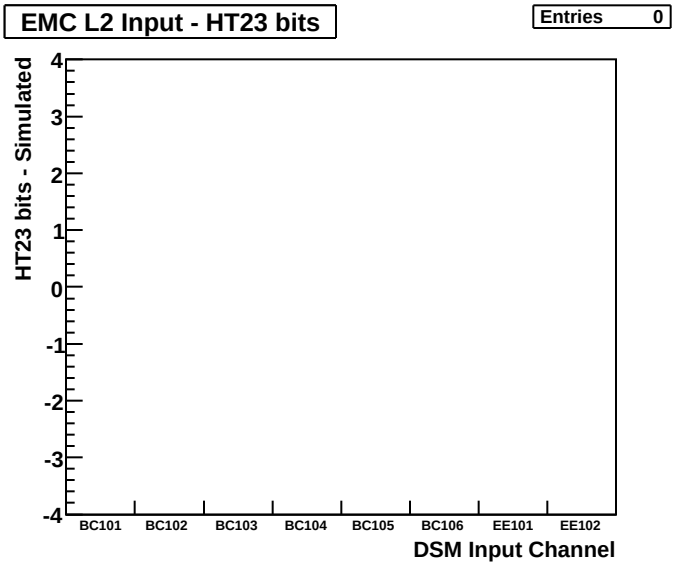

EMC L2 Input - Partial JP Sum
Entries 1.12e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

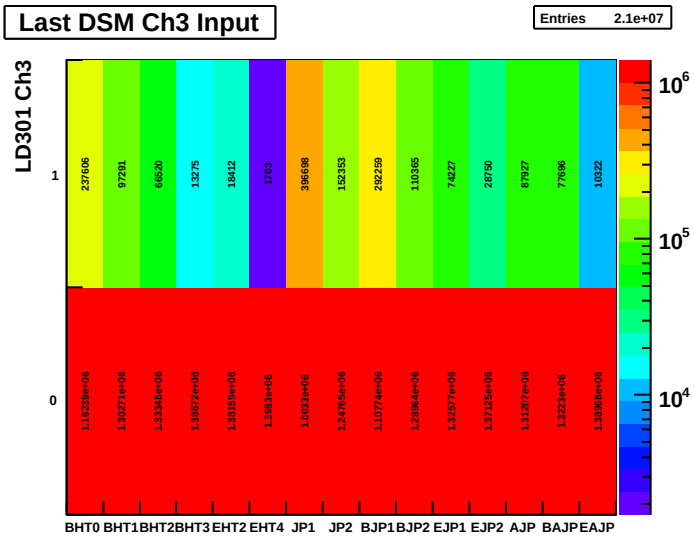

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

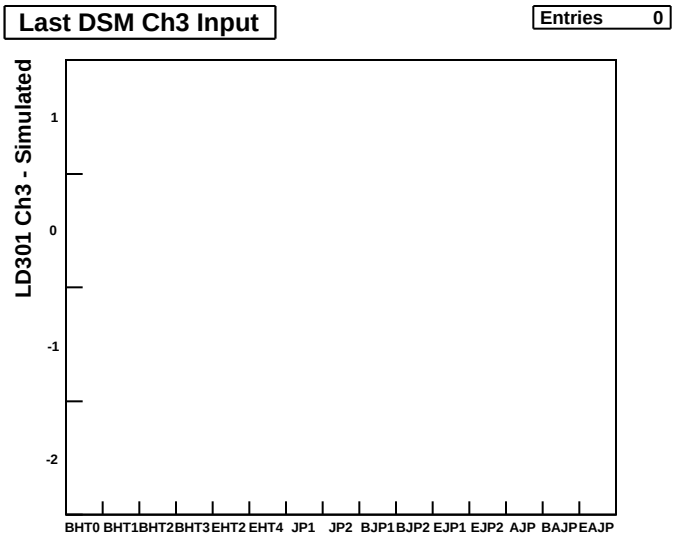

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.12e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

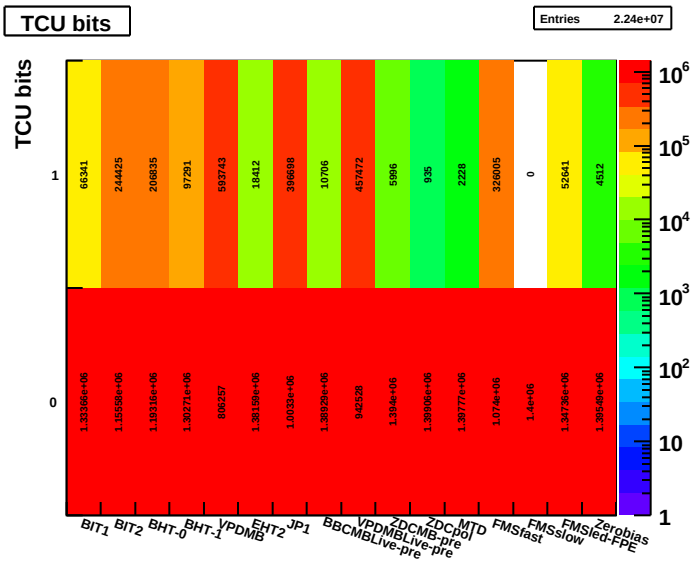

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

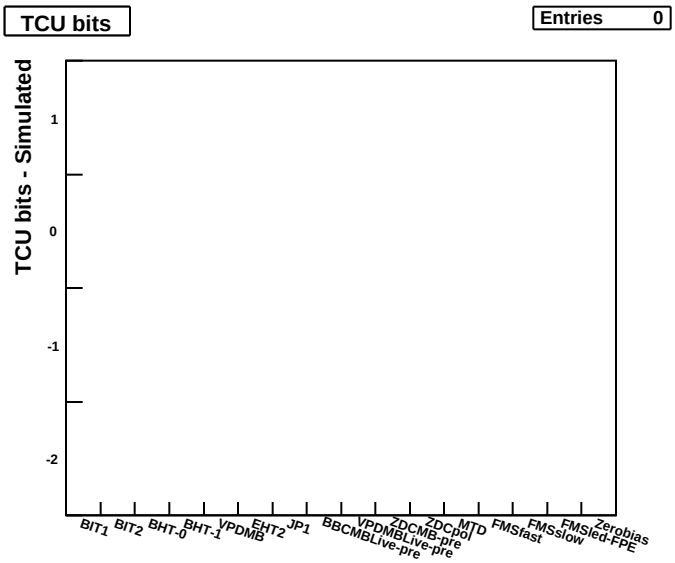

EMC L2 Input - HT23 bits
Entries 1.12e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

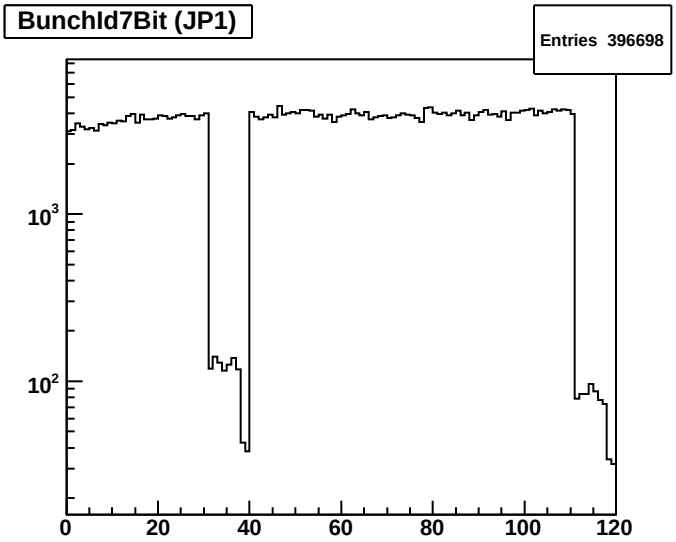

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

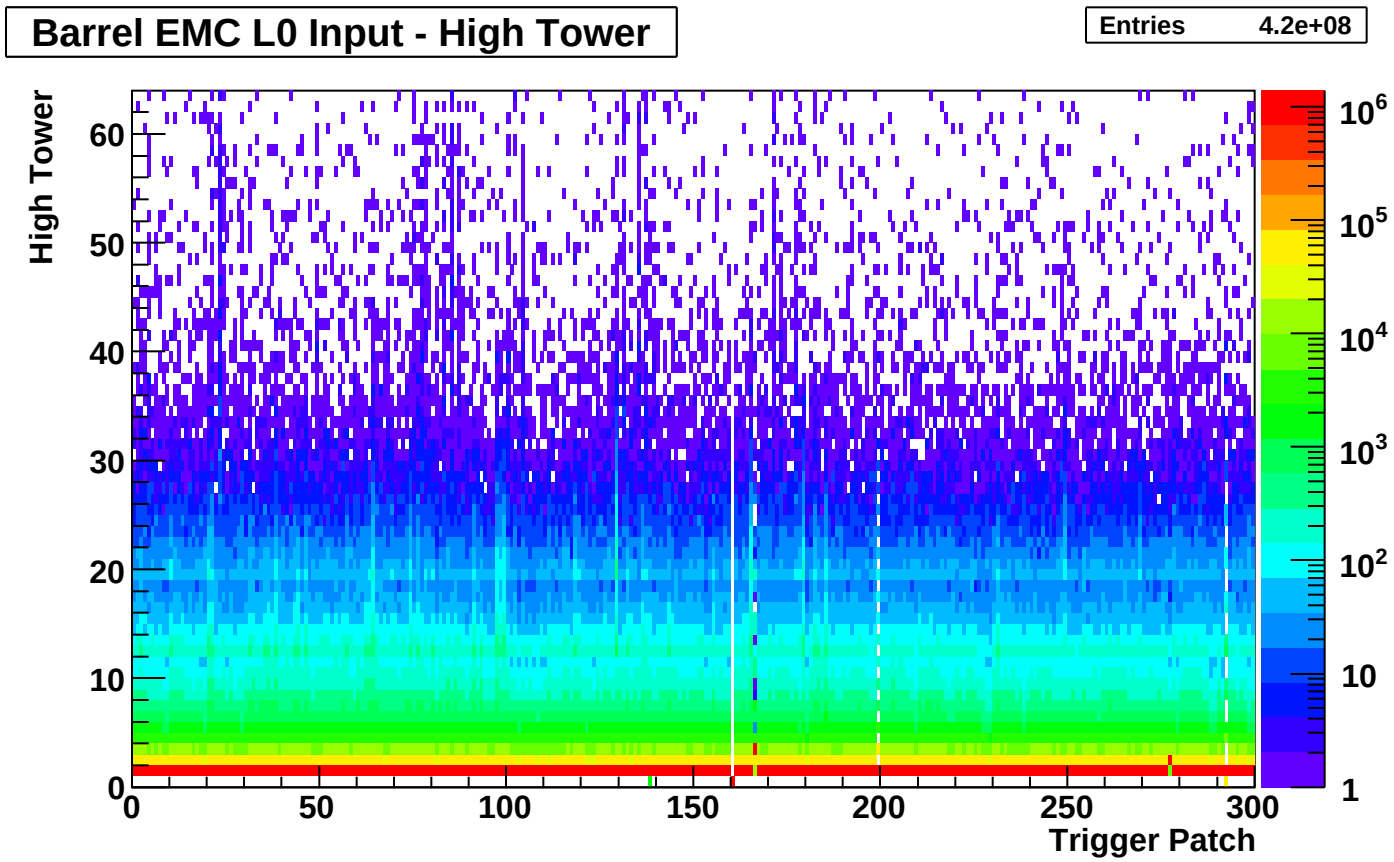

Last DSM Ch3 Input
Entries 2.1e+07
LD301 Ch3
237606 97291 66520 13275 18412 1703 396698 152353 292259 110365 74227 26750 87927 77696 10322
1.16239e+06 1.30271e+06 1.33348e+06 1.38672e+06 1.38159e+06 1.3983e+06 1.0033e+06 1.24765e+06 1.10774e+06 1.28964e+06 1.32577e+06 1.37125e+06 1.31207e+06 1.3223e+06 1.38968e+06
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

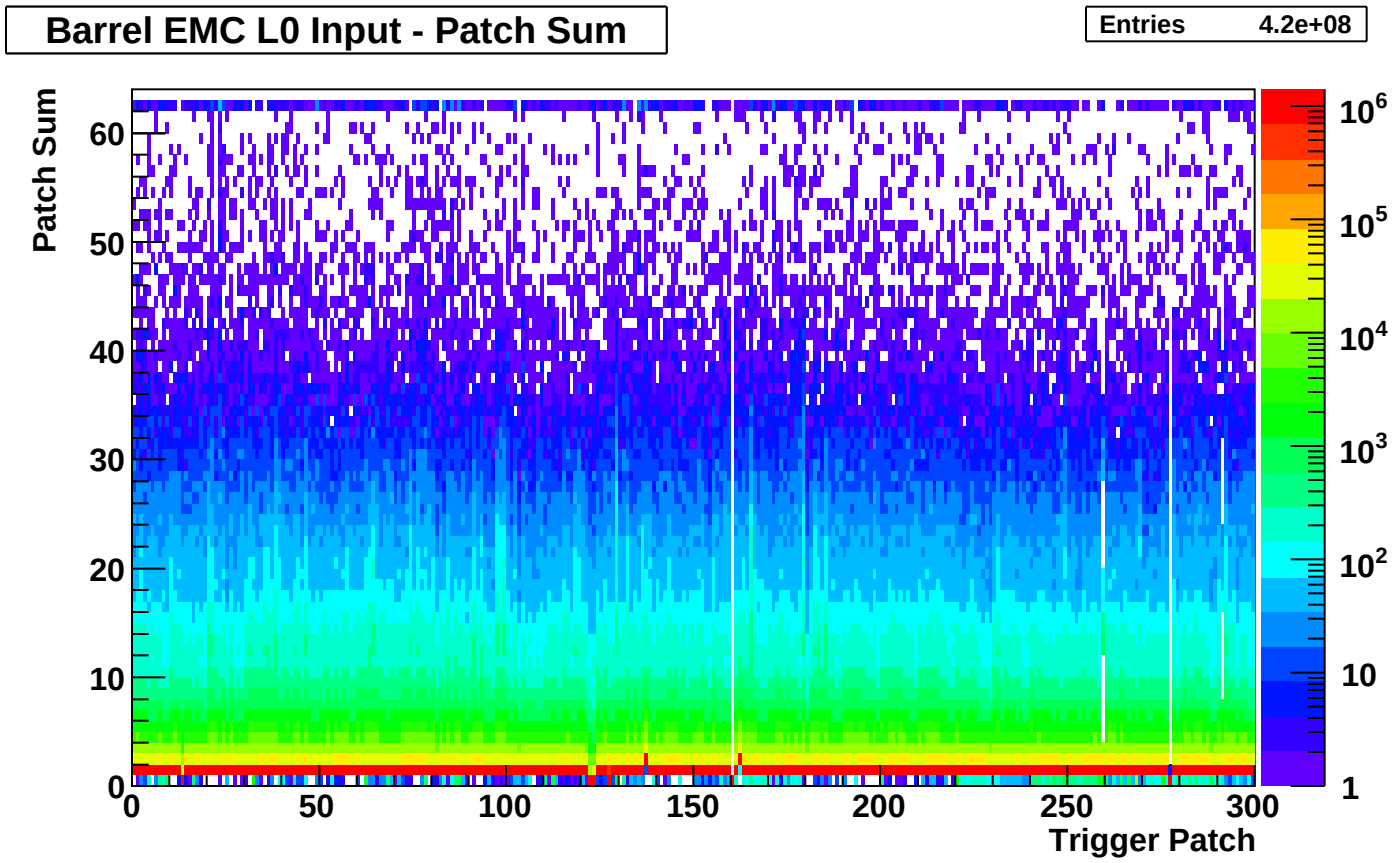

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

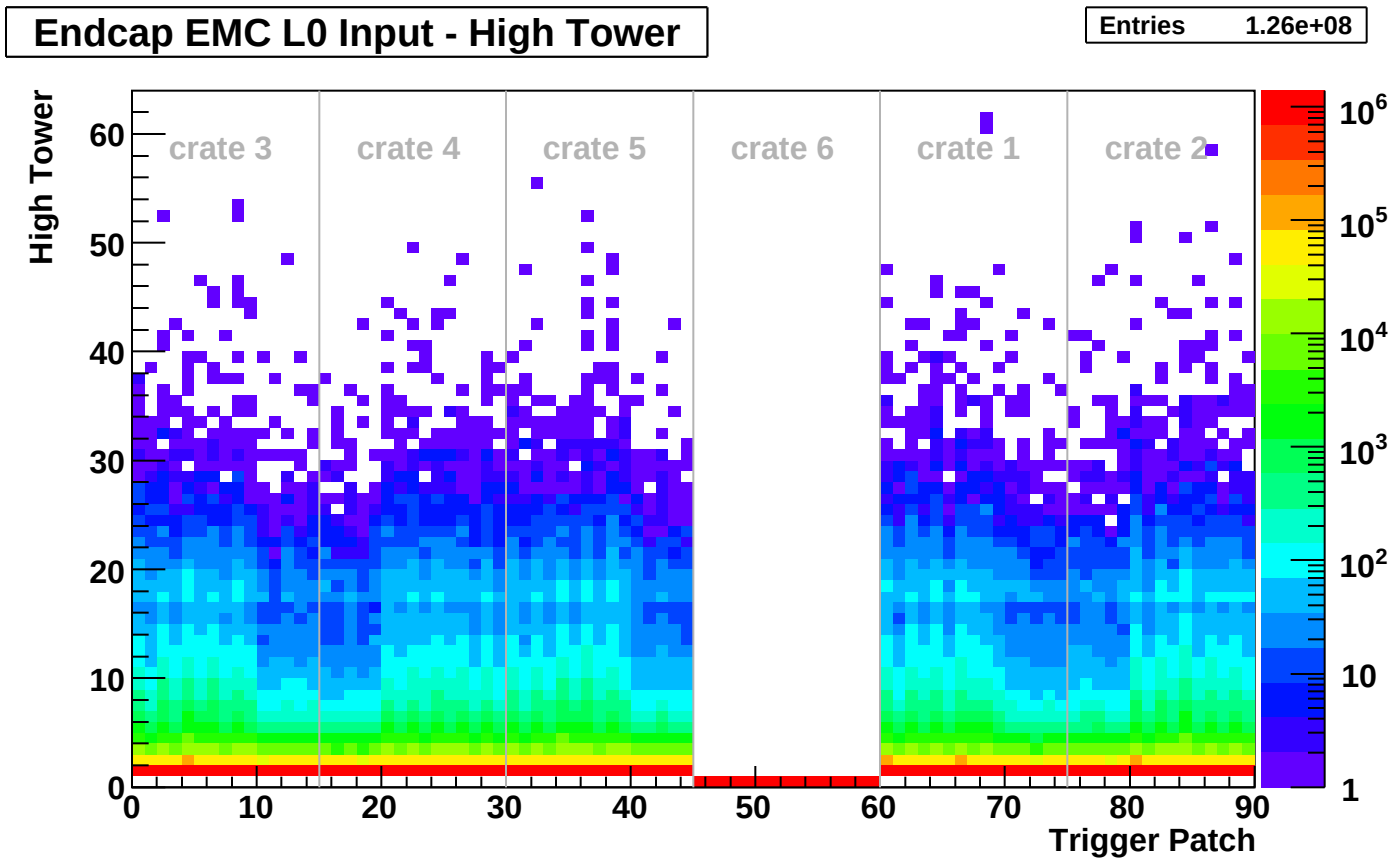

TCU bits
Entries 2.24e+07
66341 244425 206835 97291 593743 18412 396698 10706 457472 5996 935 2228 326005 0 52641 4512
1.33366e+06 1.15558e+06 1.18316e+06 1.30271e+06 806257 1.38159e+06 1.0033e+06 1.38939e+06 942528 1.394e+06 1.39906e+06 1.38777e+06 1.074e+06 1.4e+06 1.34736e+06 1.30549e+06
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

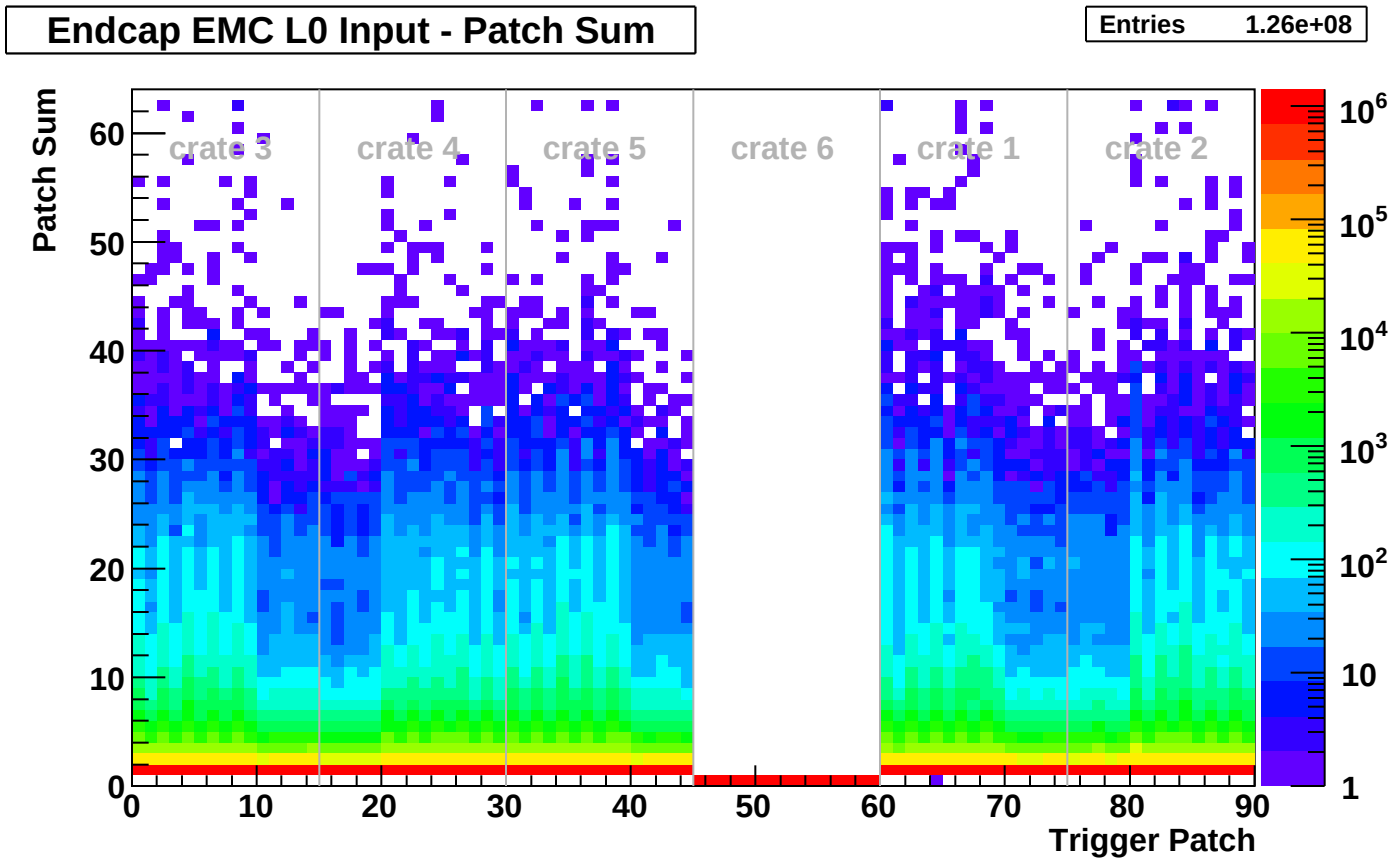

TCU bits
Entries 0
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

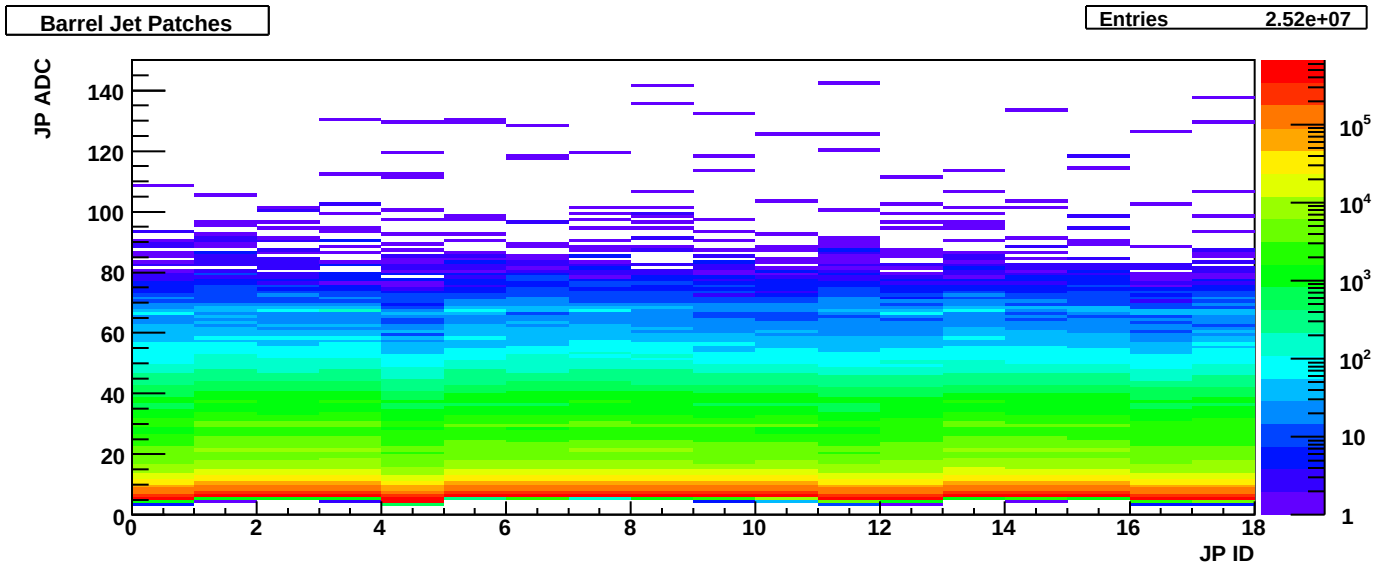

BunchId7Bit (JP1)
Entries 396698

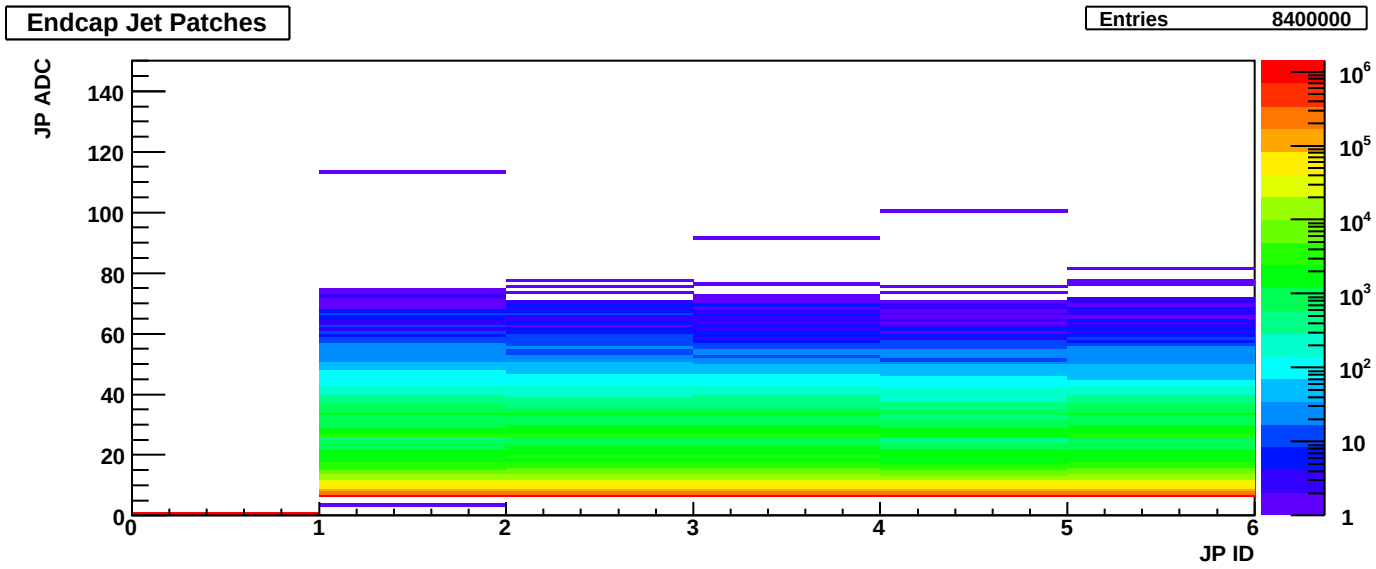

Barrel EMC L0 Input - High Tower
Entries 4.2e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

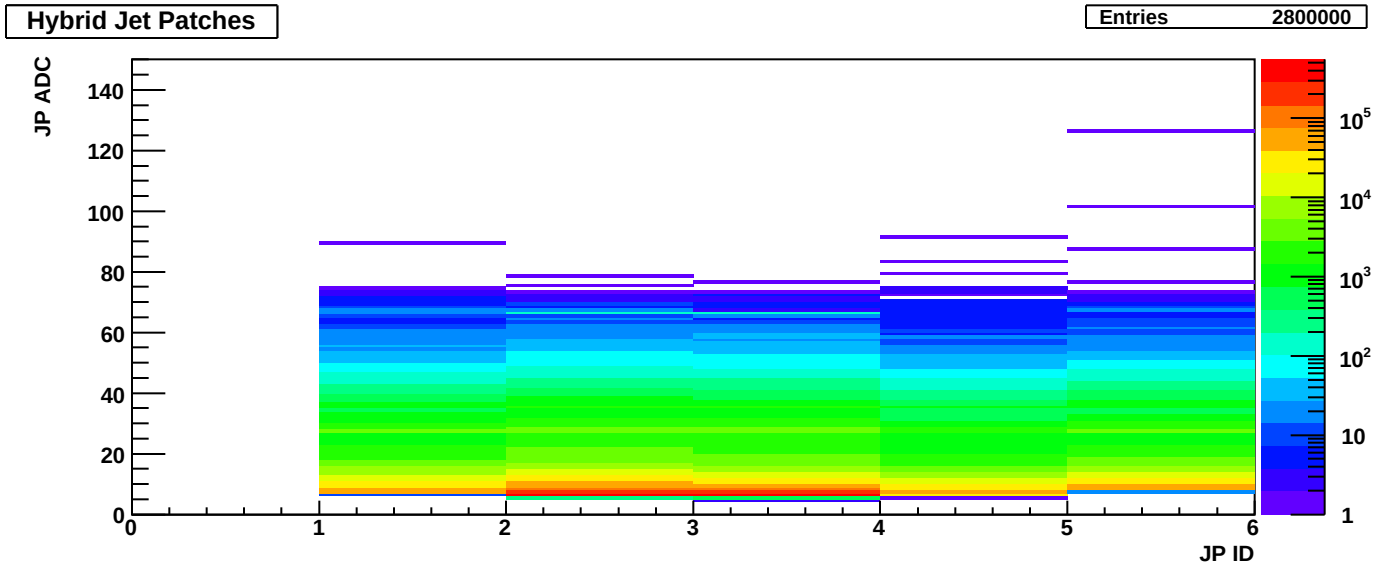

Barrel EMC L0 Input - Patch Sum
Entries 4.2e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

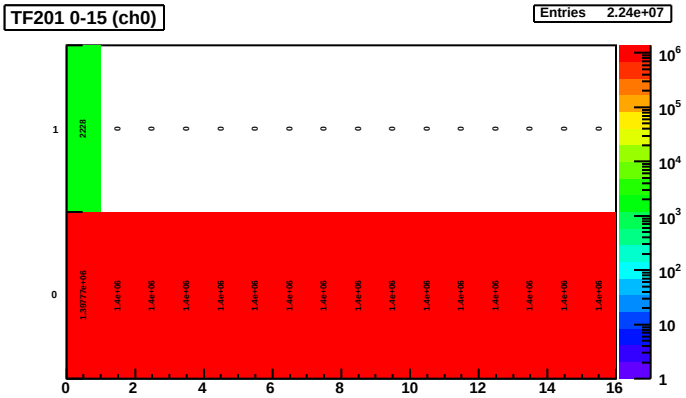

Endcap EMC L0 Input - High Tower
Entries 1.26e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

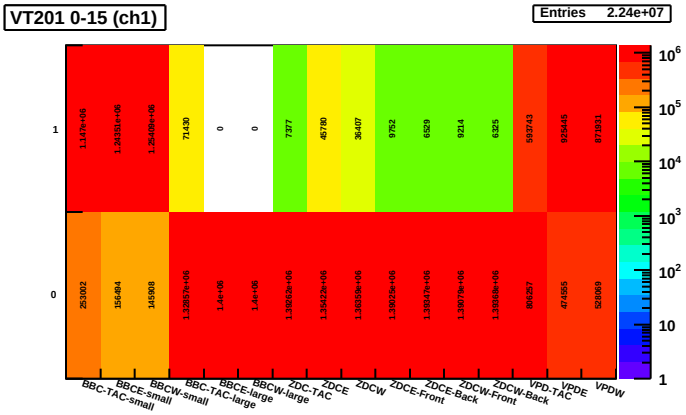

Endcap EMC L0 Input - Patch Sum
Entries 1.26e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

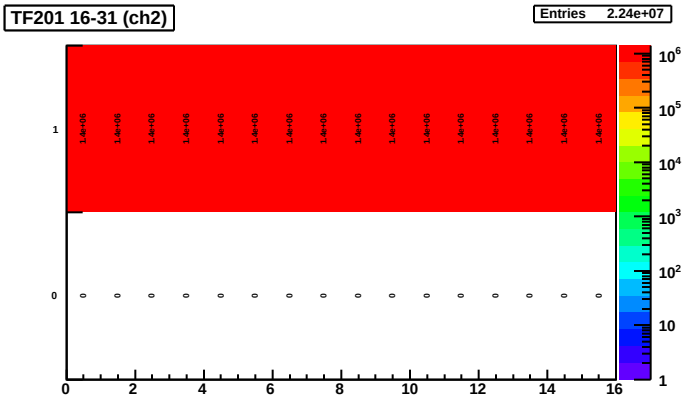

Barrel Jet Patches
Entries 2.52e+07
JP ADC
JP ID

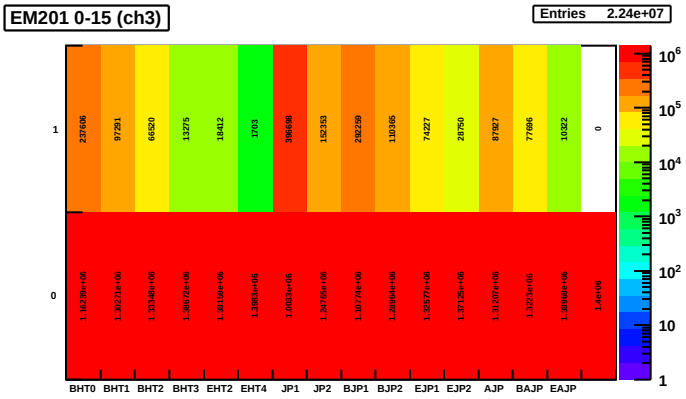

Endcap Jet Patches
Entries 8400000
JP ADC
JP ID

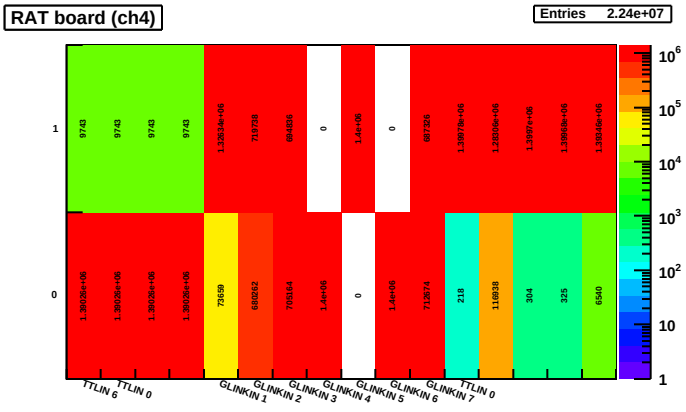

Hybrid Jet Patches
Entries 2800000
JP ADC
JP ID

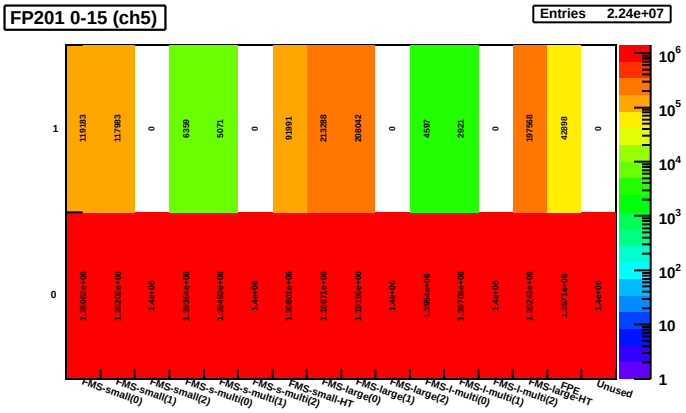

TF201 0-15 (ch0)
Entries 2.24e+07

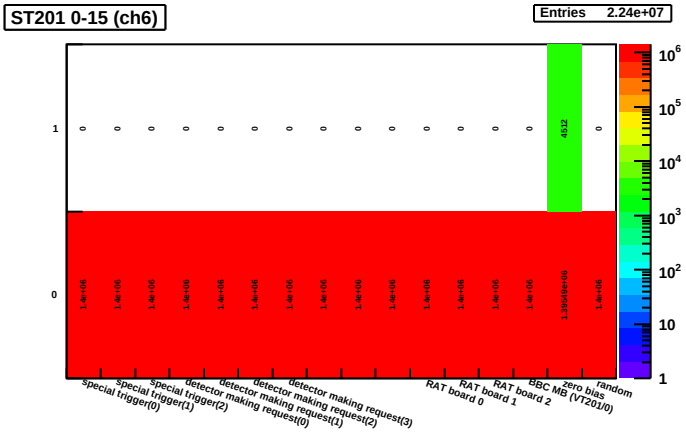

VT201 0-15 (ch1)
Entries 2.24e+07
BBC-TAC-small
BBCE-small
BBCW-small
BBC-TAC-large
BBCE-large
BBCW-large
ZDC-TAC
ZDCE
ZDCW
ZDCE-Front
ZDCE-Back
ZDCW-Front
ZDCW-Back
VPD-TAC
VPDE
VPDW

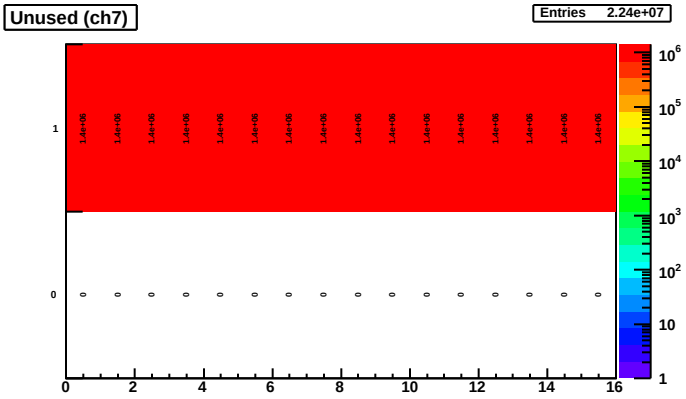

TF201 16-31 (ch2)
Entries 2.24e+07


EM201 0-15 (ch3)
Entries 2.24e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP


RAT board (ch4)
Entries 2.24e+07
TTLIN 6
TTLIN 0
GLINKIN 1
GLINKIN 2
GLINKIN 3
GLINKIN 4
GLINKIN 5
GLINKIN 6
GLINKIN 7
TTLIN 0


FP201 0-15 (ch5)
Entries 2.24e+07
FMS-small(0)
FMS-small(1)
FMS-small(2)
FMS-s-multi(0)
FMS-s-multi(1)
FMS-s-multi(2)
FMS-small-HT
FMS-large(0)
FMS-large(1)
FMS-large(2)
FMS-l-multi(0)
FMS-l-multi(1)
FMS-l-multi(2)
FMS-large-HT
FPE
Unused


ST201 0-15 (ch6)
Entries 2.24e+07
Special trigger(0)
Special trigger(1)
Special trigger(2)
detector making request(0)
detector making request(1)
detector making request(2)
detector making request(3)
RAT board 0
RAT board 1
RAT board 2
BBC MB (VT201/0)
zero bias
random


Unused (ch7)
Entries 2.24e+07