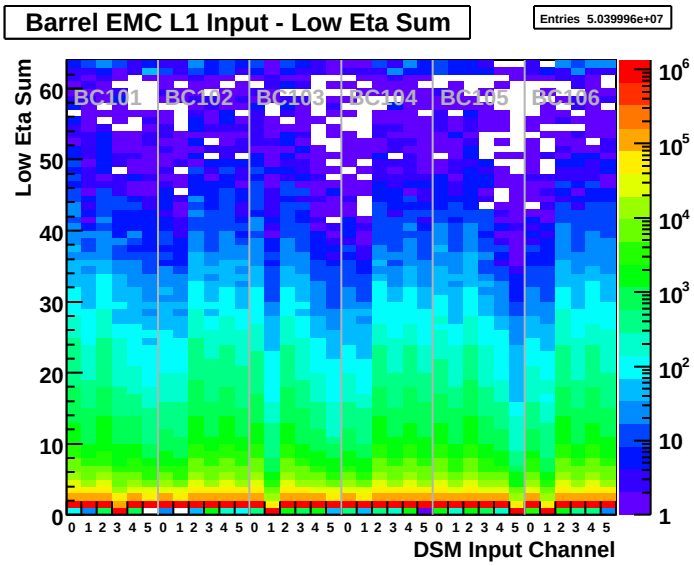
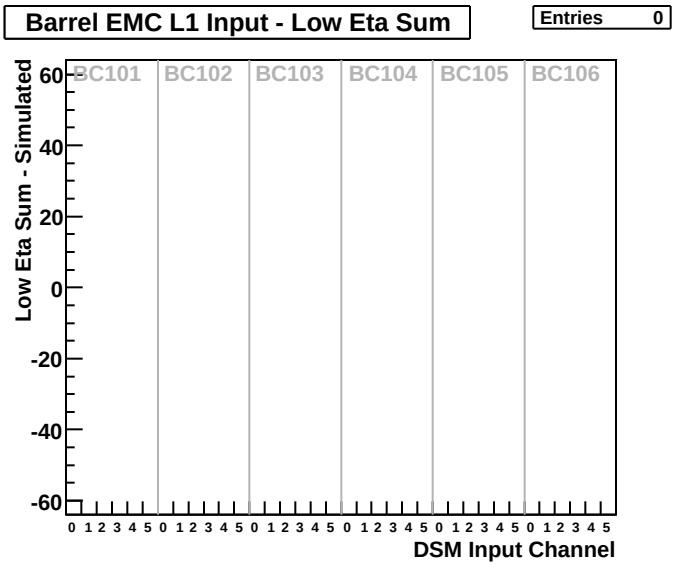
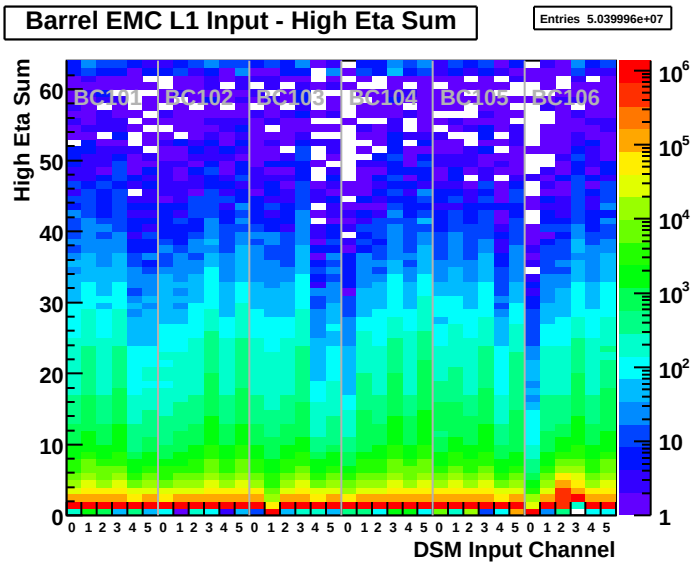
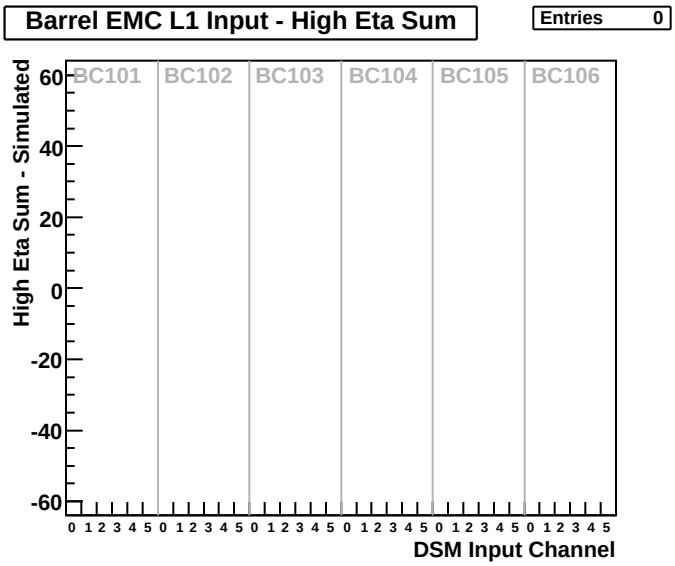
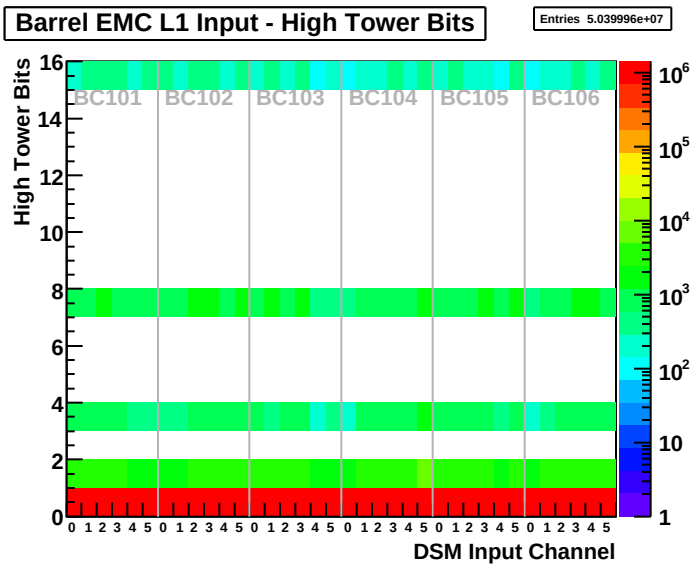
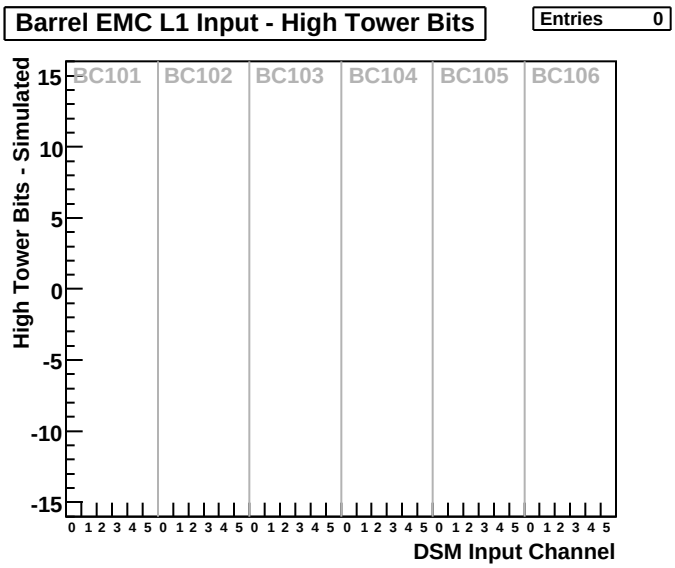

Barrel EMC L1 Input - Low Eta Sum
Entries 5.039996e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

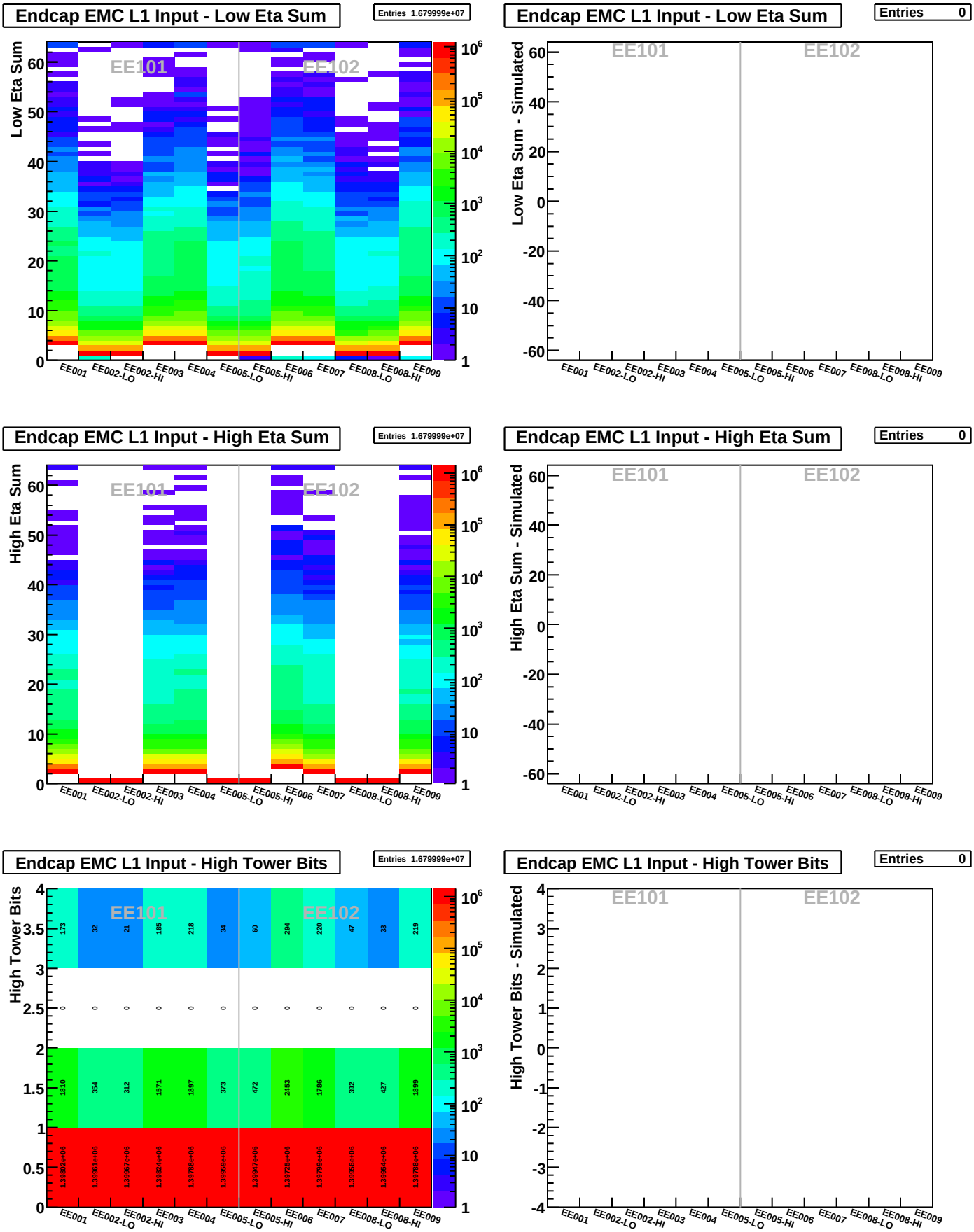

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

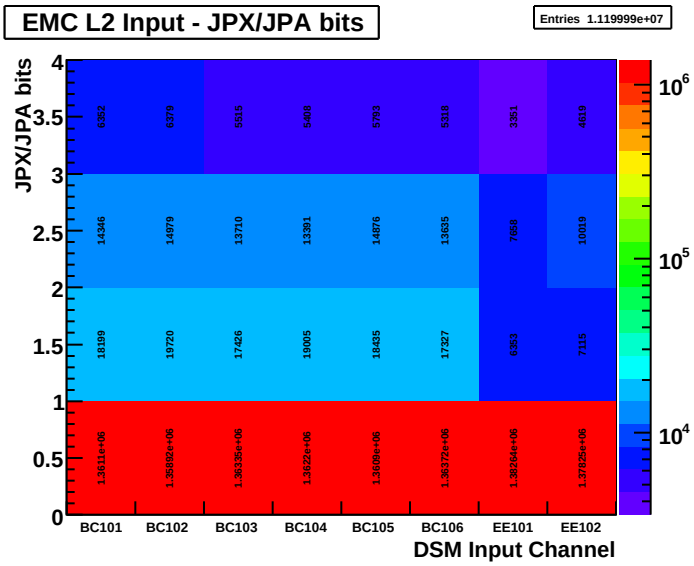

Barrel EMC L1 Input - High Eta Sum
Entries 5.039996e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

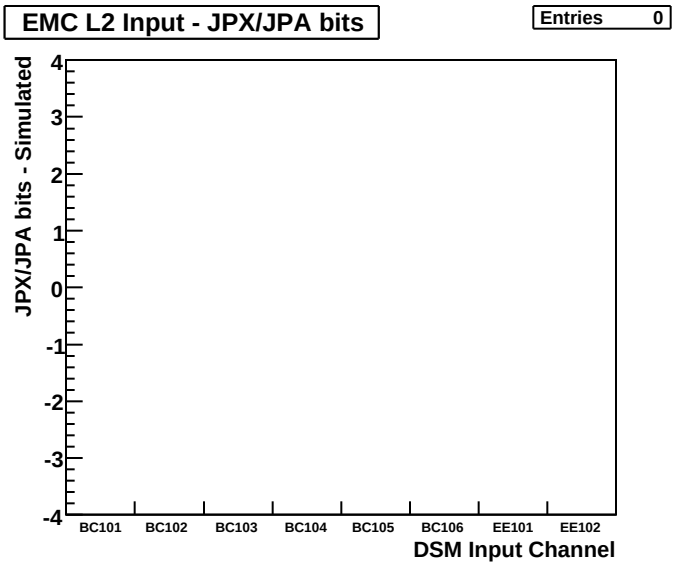

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

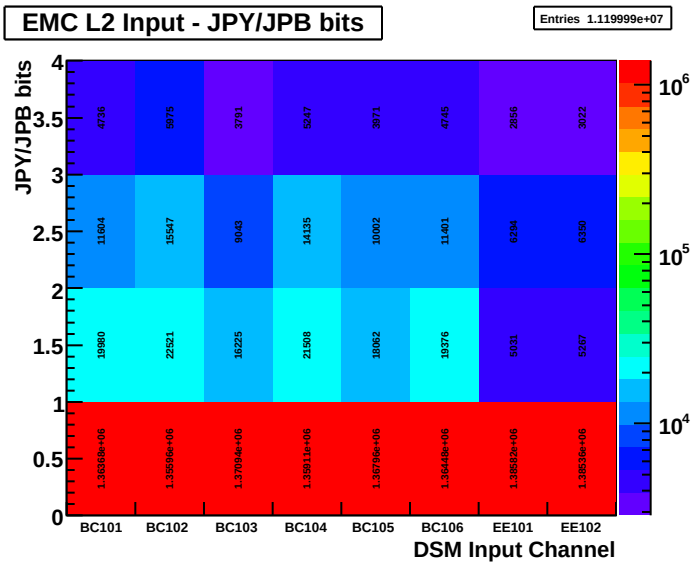

Barrel EMC L1 Input - High Tower Bits
Entries 5.039996e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

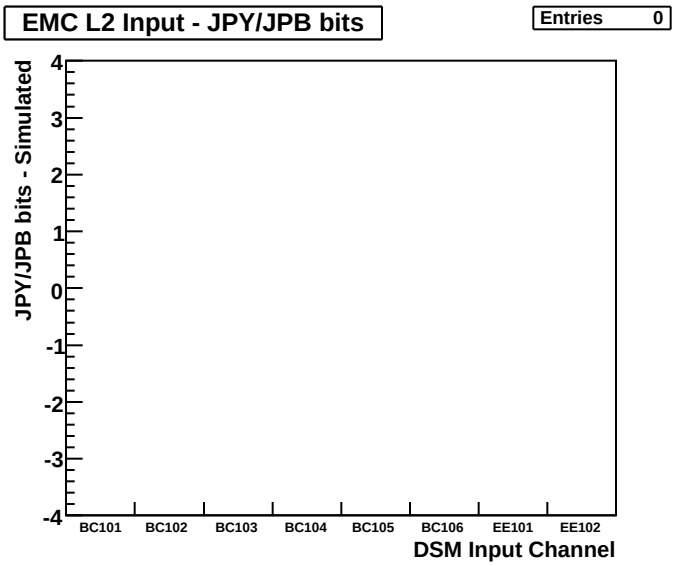

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

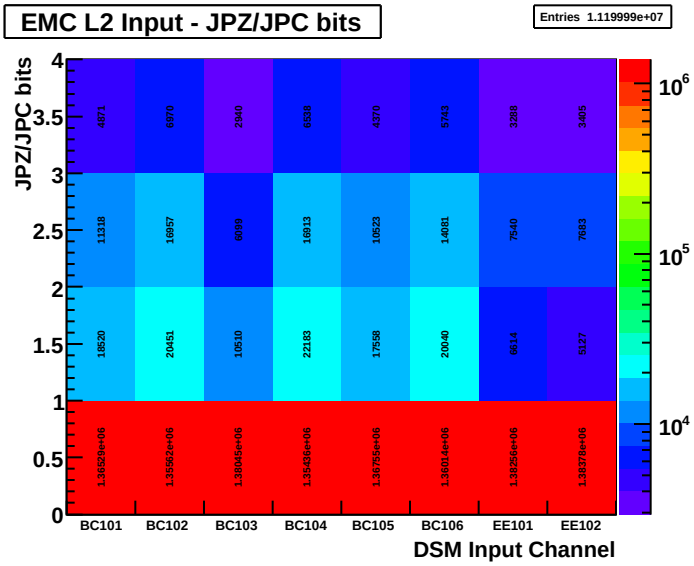

Endcap EMC L1 Input - Low Eta Sum
Entries 1.679999e+07
Low Eta Sum
EE101
EE102
Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
Endcap EMC L1 Input - High Eta Sum
Entries 1.679999e+07
High Eta Sum
Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
Endcap EMC L1 Input - High Tower Bits
Entries 1.679999e+07
High Tower Bits
Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

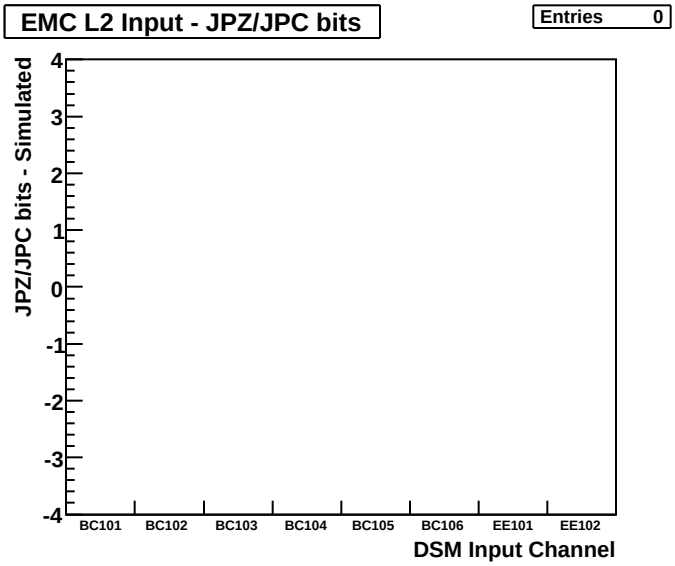

EMC L2 Input - JPX/JPA bits
Entries 1.119999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6352 6379 5815 5408 5783 5318 3351 4619
14346 14979 13710 13391 14876 13635 7658 10019
18199 19720 17426 19005 18435 17327 6353 7115
1.3611e+06 1.35892e+06 1.36335e+06 1.3622e+06 1.3609e+06 1.36372e+06 1.38264e+06 1.37825e+06

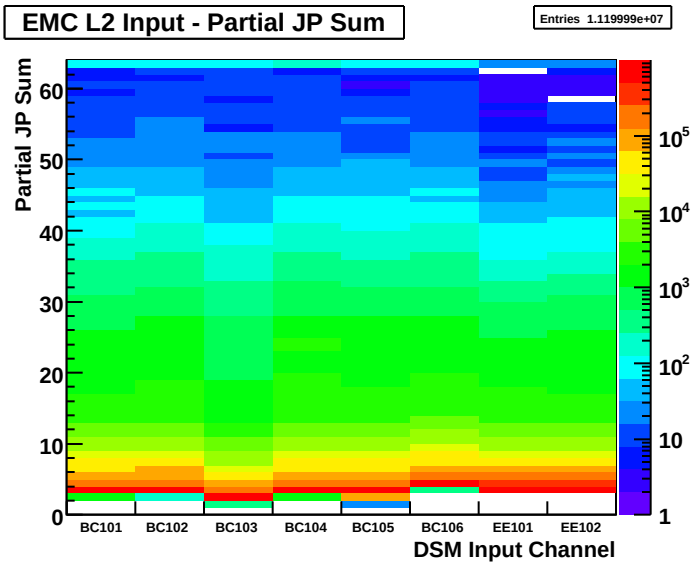

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

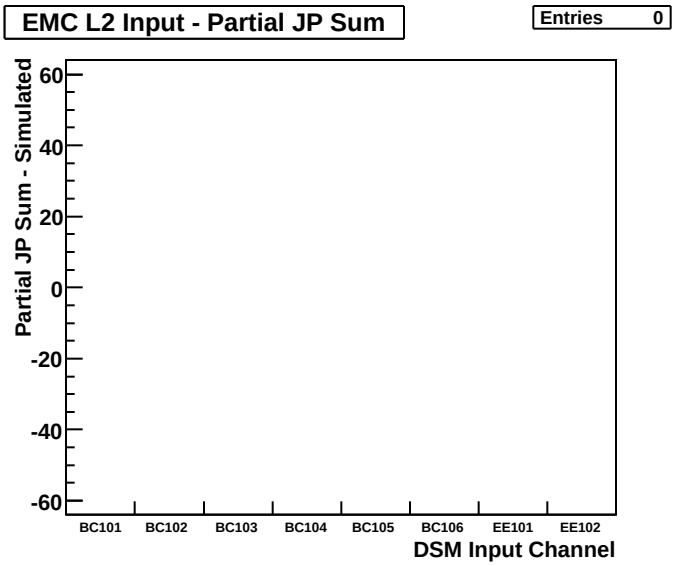

EMC L2 Input - JPY/JPB bits
Entries 1.119999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4736 5975 3791 5247 3971 4745 2856 3022
11604 15547 9043 14135 10002 11401 6294 6350
19980 22521 16225 21508 18062 19376 5031 5267
1.36368e+06 1.35590e+06 1.37094e+06 1.35911e+06 1.36786e+06 1.36449e+06 1.38592e+06 1.38536e+06

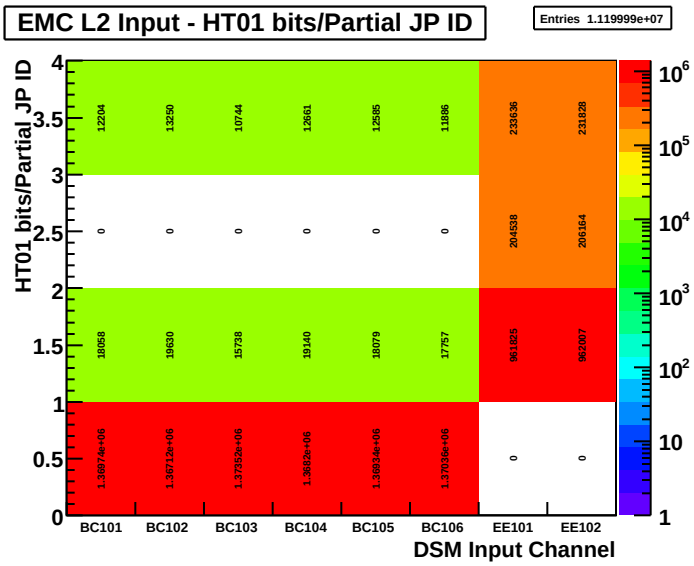

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

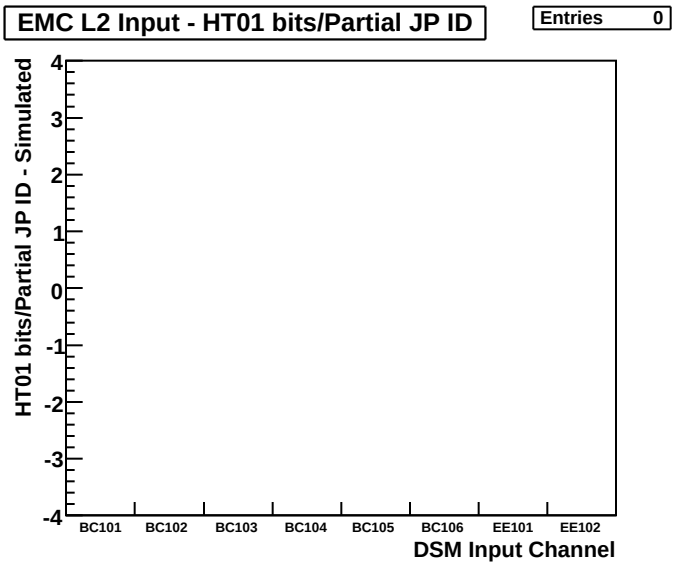

EMC L2 Input - JPZ/JPC bits
Entries 1.119999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4871 6970 2940 6538 4370 5743 3288 3405
11318 16957 6099 16913 10523 14081 7540 7683
18520 20451 10510 22183 17558 20040 6614 5127
1.36529e+06 1.35562e+06 1.38045e+06 1.35436e+06 1.36755e+06 1.36014e+06 1.38256e+06 1.38378e+06

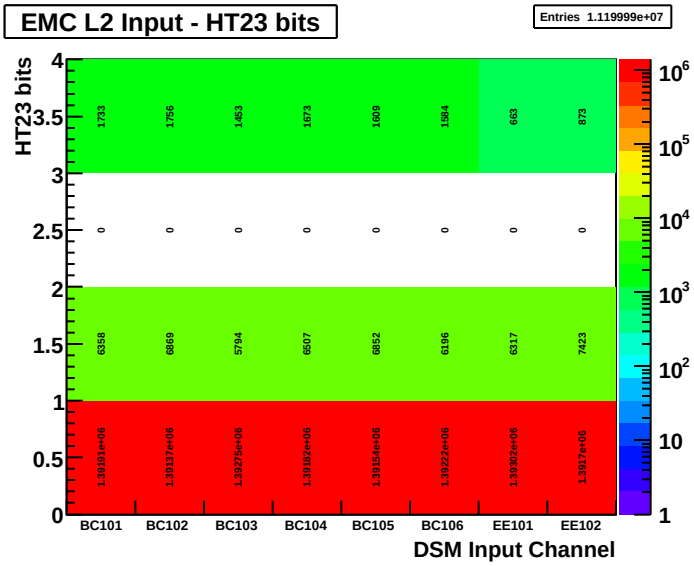

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

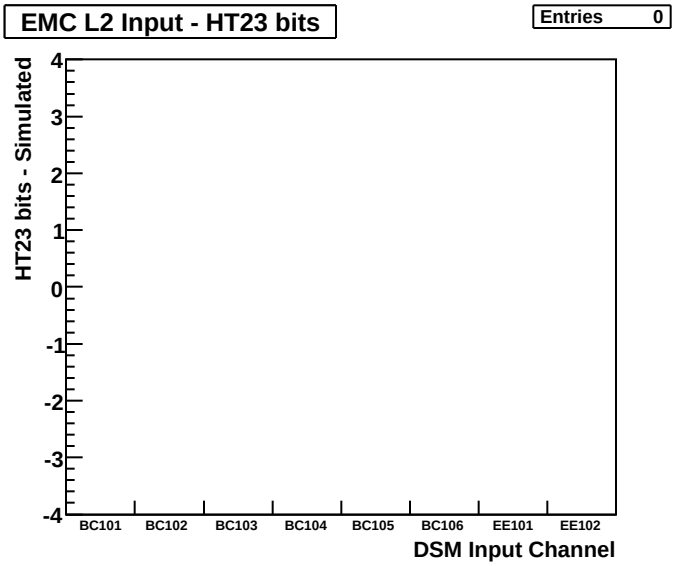

EMC L2 Input - Partial JP Sum
Entries 1.119999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

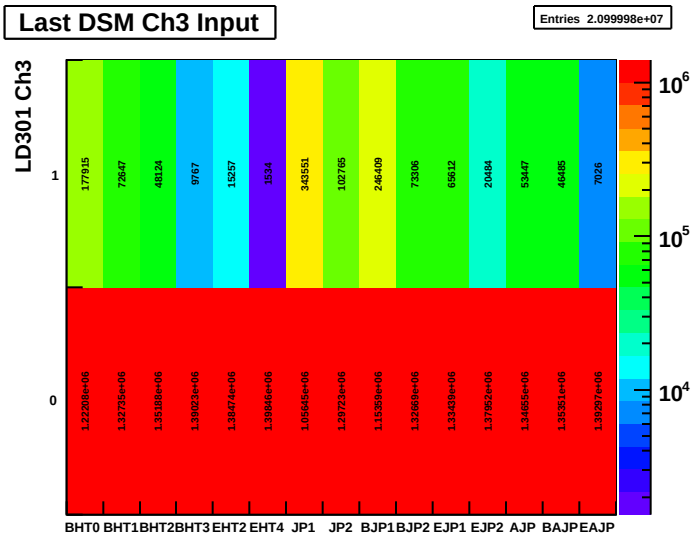

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

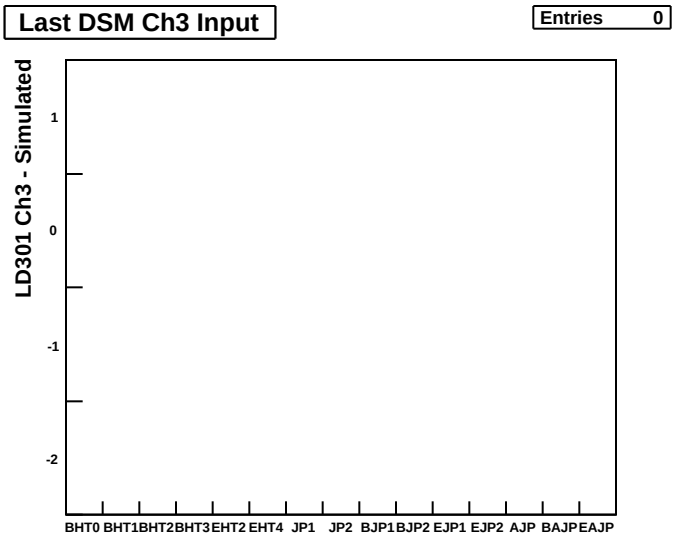

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.119999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

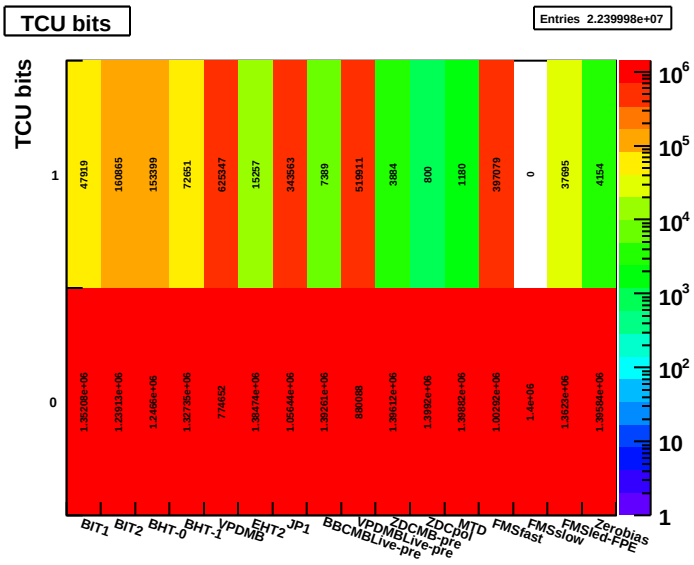

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

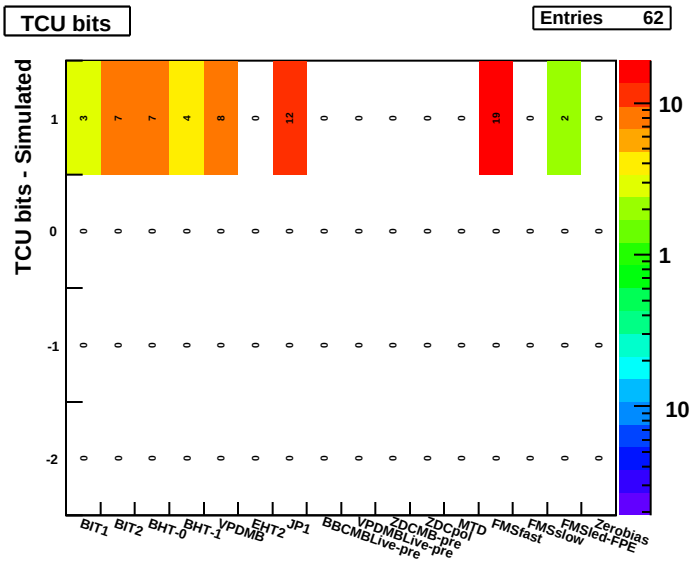

EMC L2 Input - HT23 bits
Entries 1.119999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

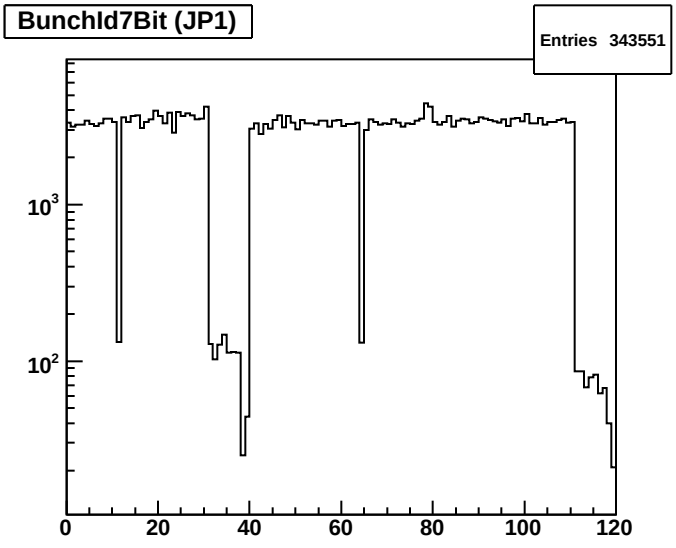

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

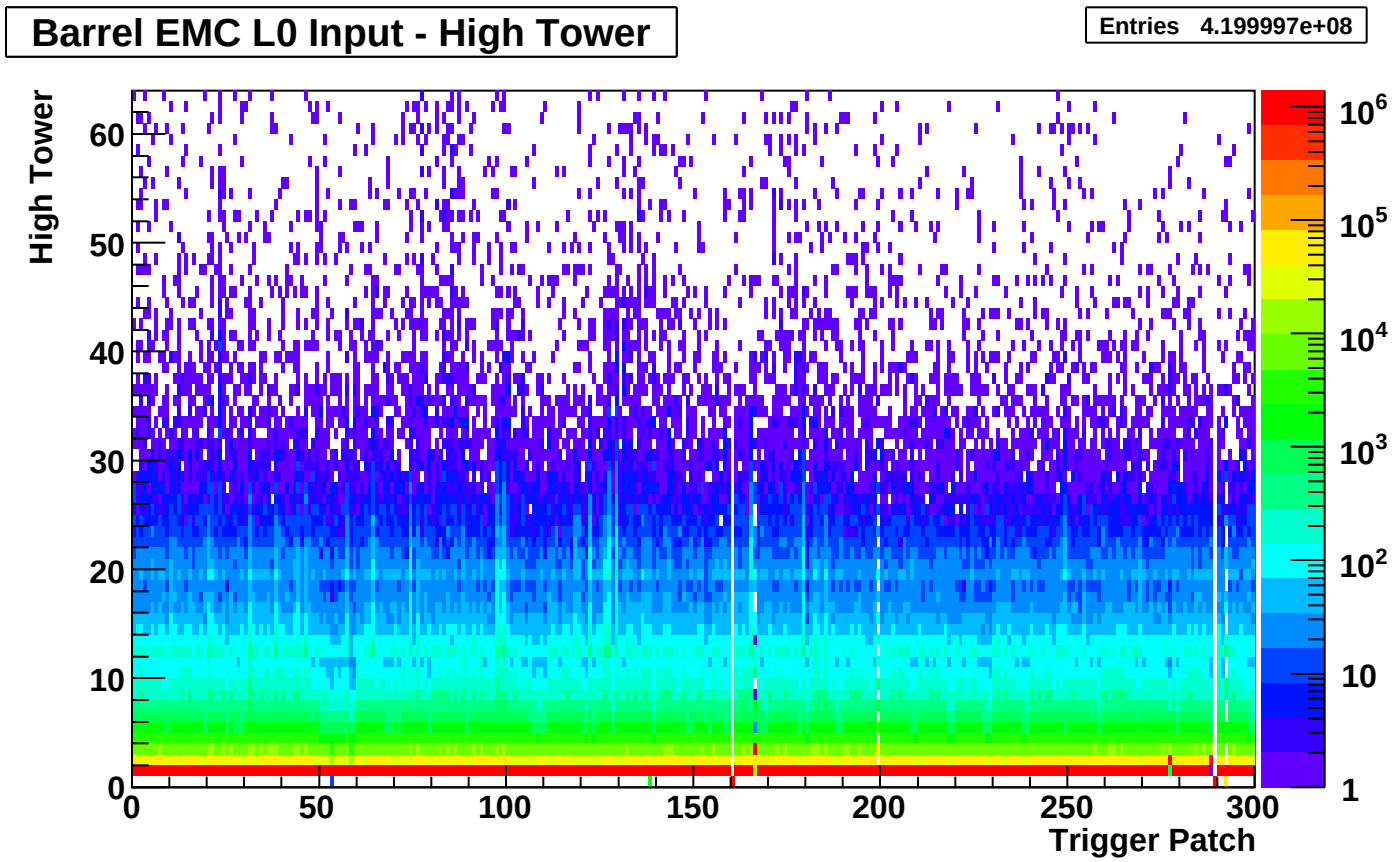

Last DSM Ch3 Input
Entries 2.099998e+07
LD301 Ch3
1
0
177915 72647 48124 9767 15257 1534 343551 102765 246409 73306 65612 20484 53447 46485 7026
1.22208e+06 1.32735e+06 1.35188e+06 1.39023e+06 1.38474e+06 1.39846e+06 1.05645e+06 1.29723e+06 1.15359e+06 1.32669e+06 1.33439e+06 1.37952e+06 1.34655e+06 1.35351e+06 1.39297e+06
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
10^6
10^5
10^4

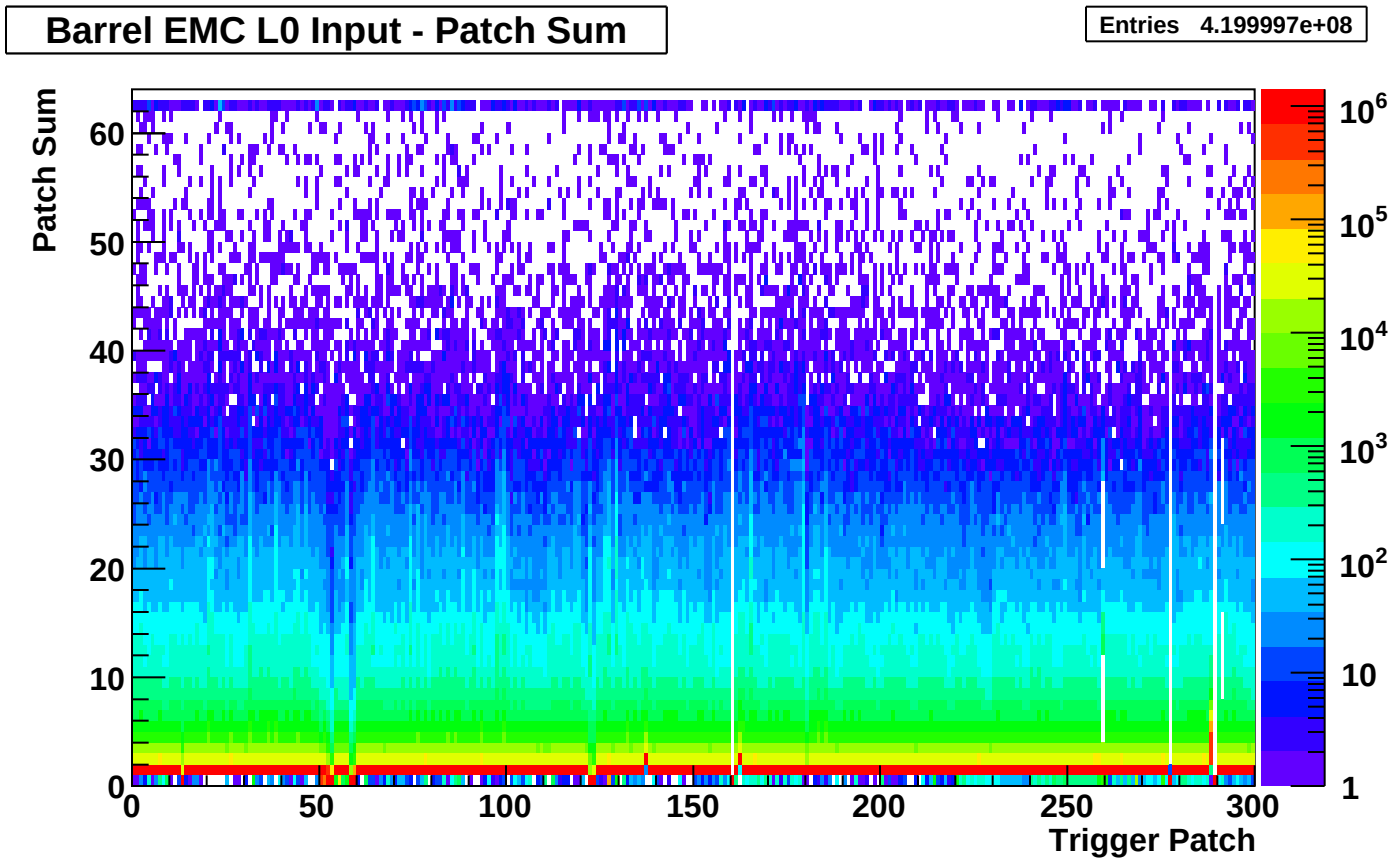

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

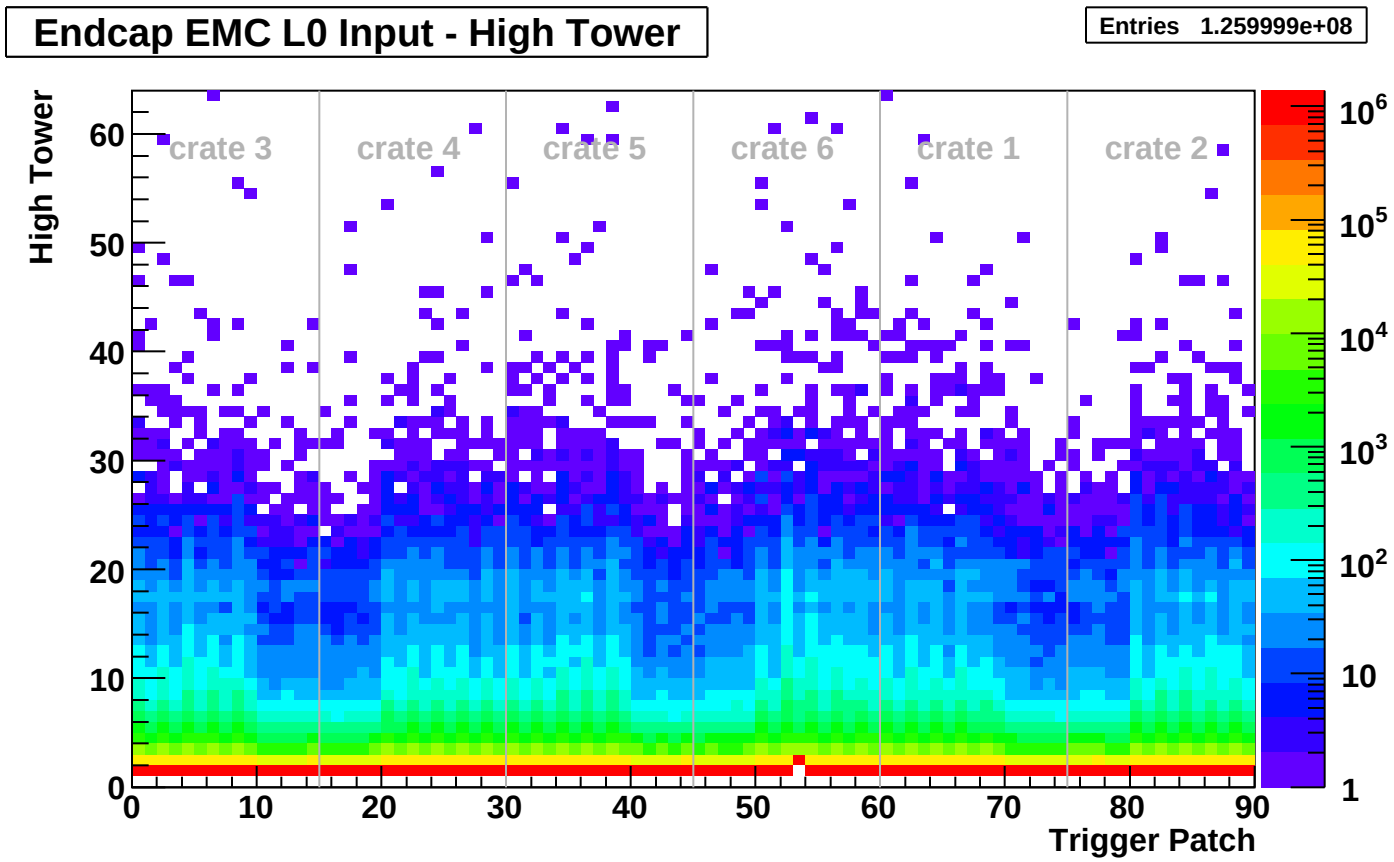

TCU bits
Entries 2.239998e+07
TCU bits
1
0
47919 160865 153399 72651 625347 15257 343563 7389 519911 3884 800 1180 397079 0 37695 4154
1.35208e+06 1.23913e+06 1.12466e+06 1.32735e+06 774652 1.38474e+06 1.05644e+06 1.39261e+06 880088 1.39612e+06 1.3992e+06 1.38082e+06 1.00292e+06 1.4e+06 1.3623e+06 1.39594e+06
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10^6
10^5
10^4
10^3
10^2
10
1

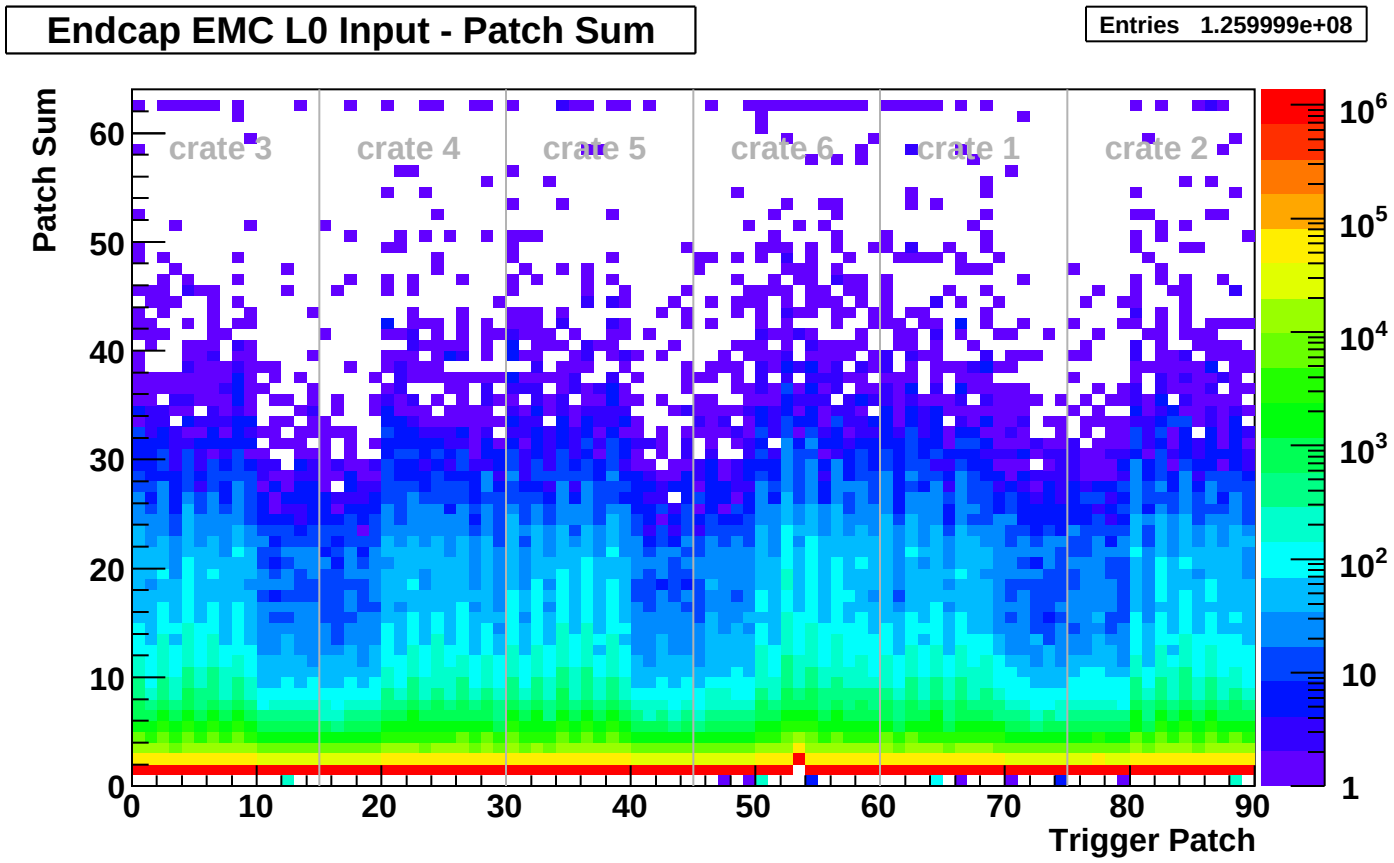

TCU bits
Entries 62
TCU bits - Simulated
1
0
-1
-2
3 7 7 4 8 0 12 0 0 0 0 0 19 0 2 0
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1
10

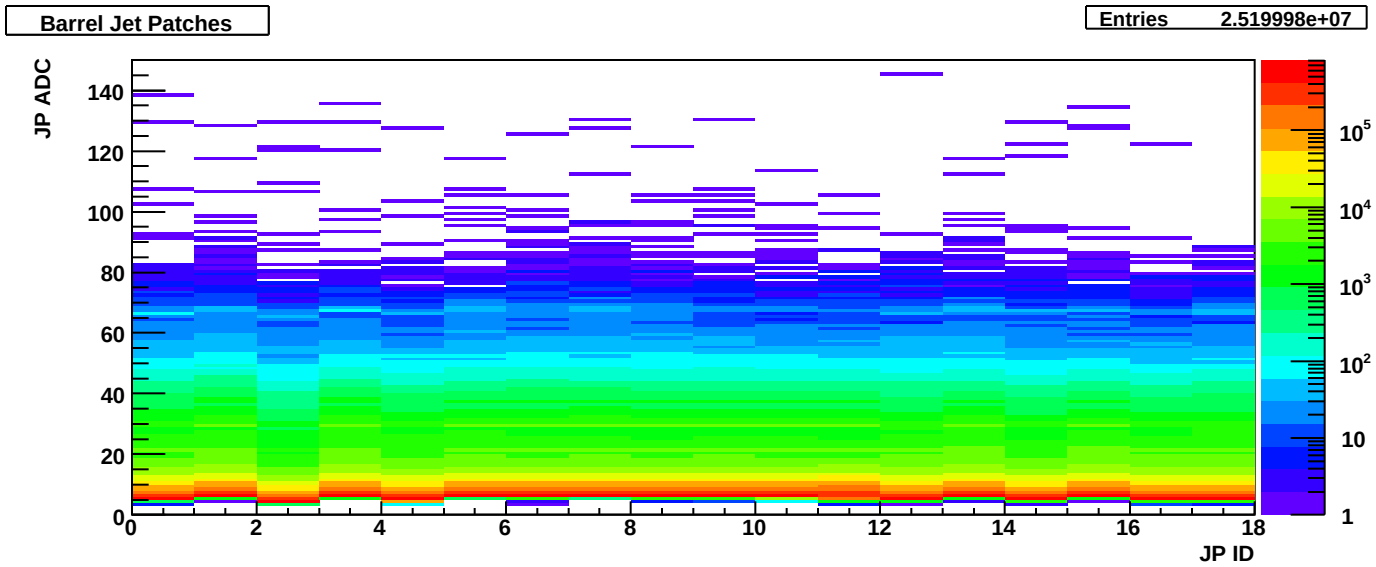

BunchId7Bit (JP1)
Entries 343551
10^3
10^2
0
20
40
60
80
100
120

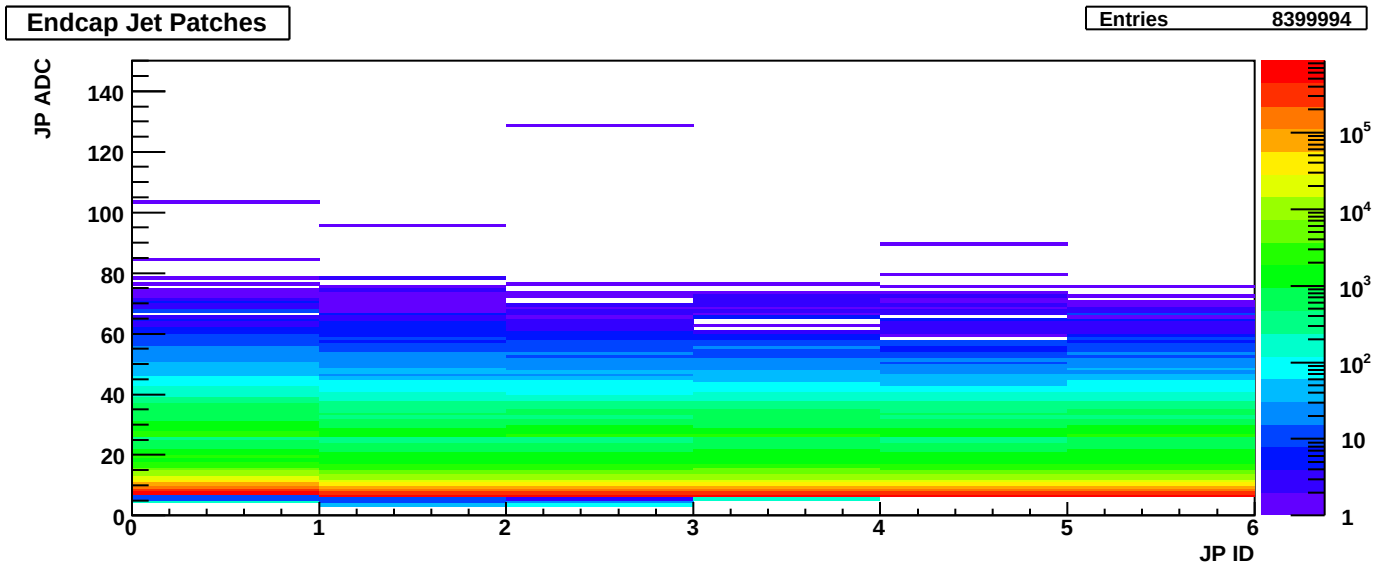

Barrel EMC L0 Input - High Tower
Entries 4.199997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

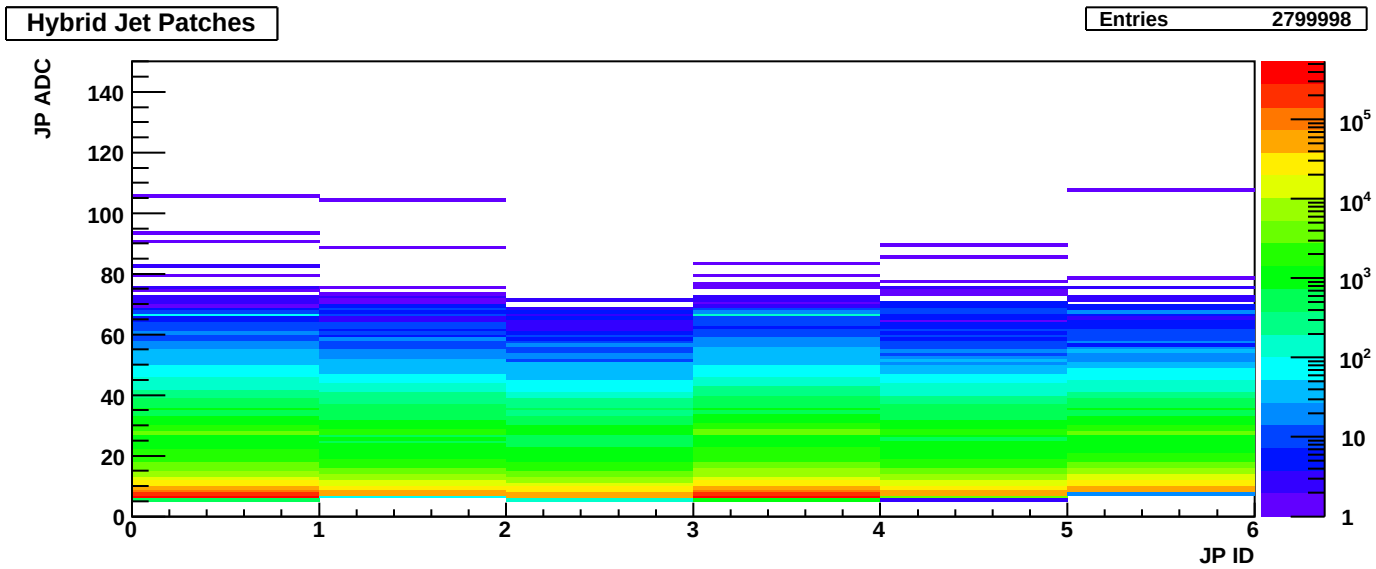

Barrel EMC L0 Input - Patch Sum
Entries 4.199997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6


Endcap EMC L0 Input - High Tower
Entries 1.259999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

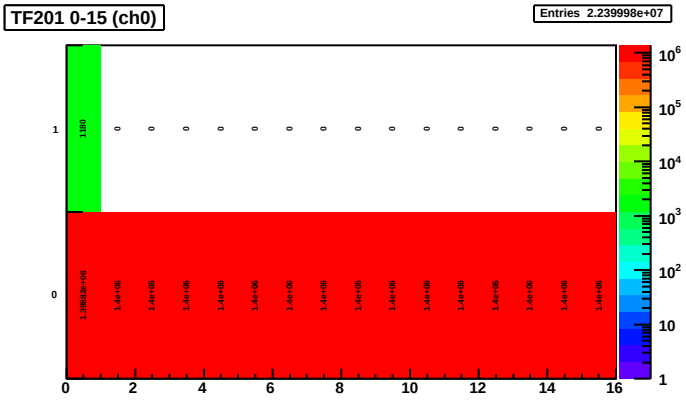

Endcap EMC L0 Input - Patch Sum
Entries 1.259999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 2.519998e+07
JP ADC
JP ID

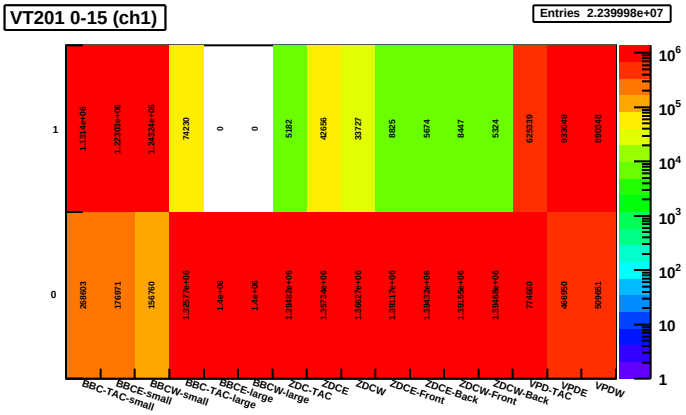

Endcap Jet Patches
Entries 8399994
JP ADC
JP ID


Hybrid Jet Patches
Entries 2799998
JP ADC
JP ID

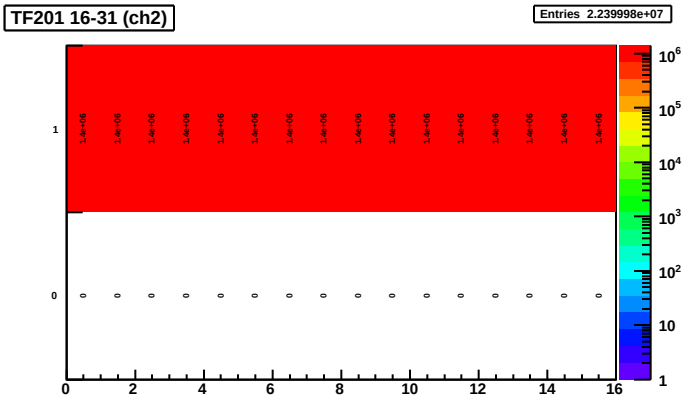
TF201 0-15 (ch0)



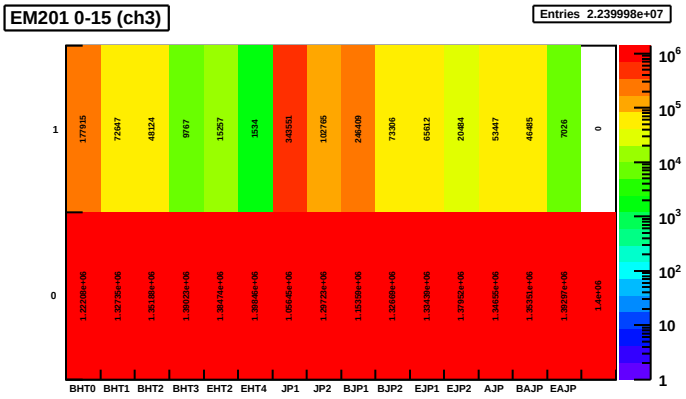
VT201 0-15 (ch1)



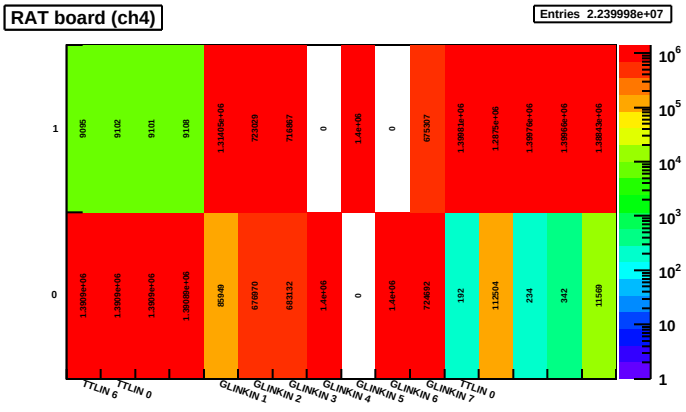
TF201 16-31 (ch2)



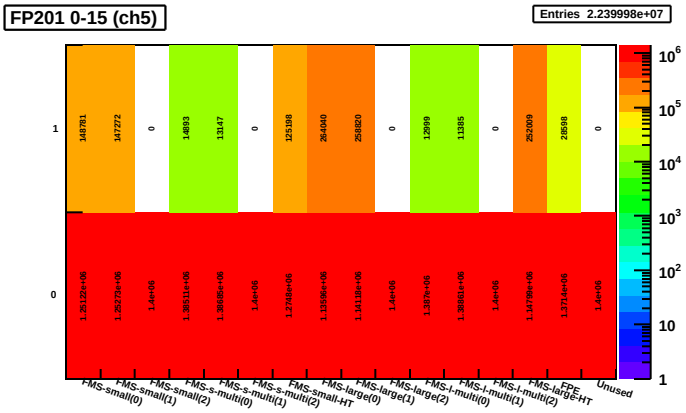
EM201 0-15 (ch3)



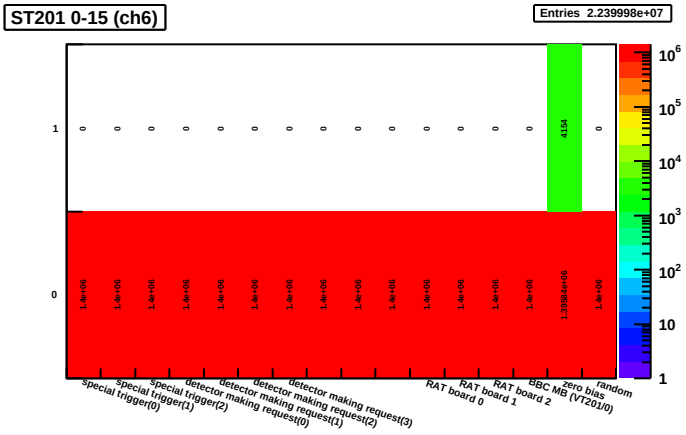
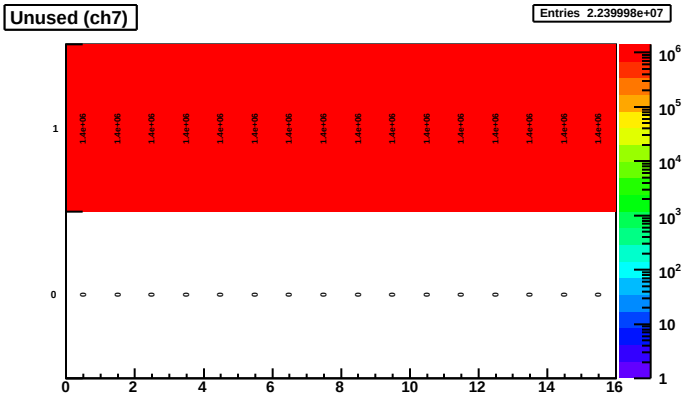
RAT board (ch4)



FP201 0-15 (ch5)



ST201 0-15 (ch6)



Unused (ch7)