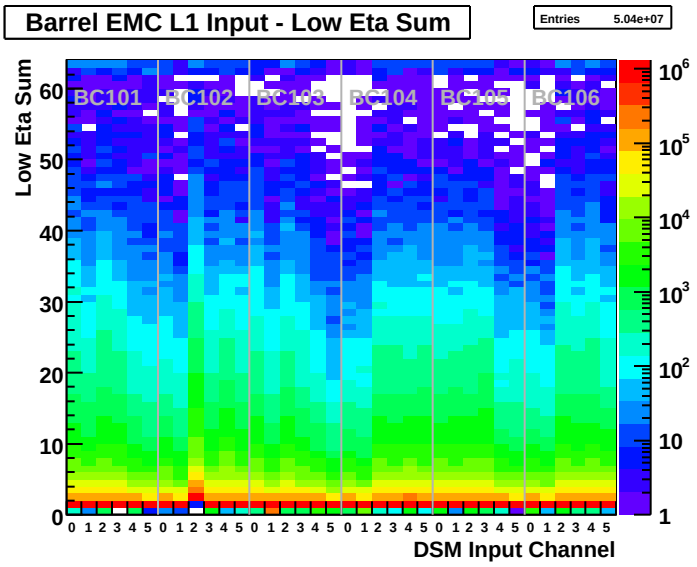
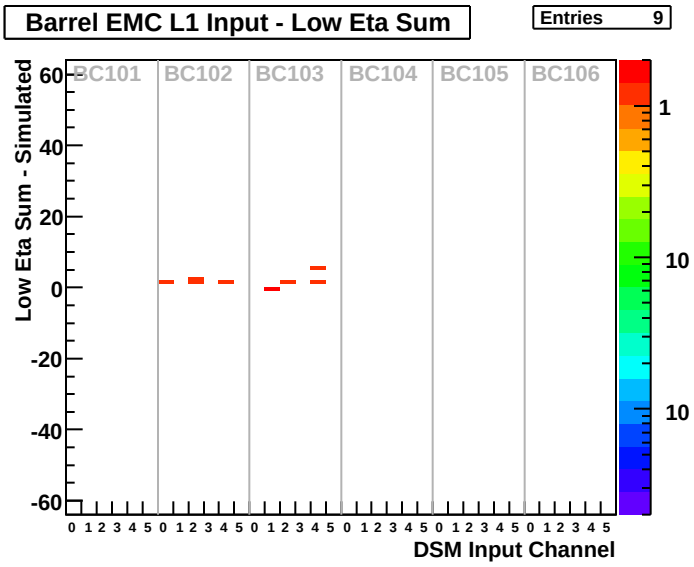
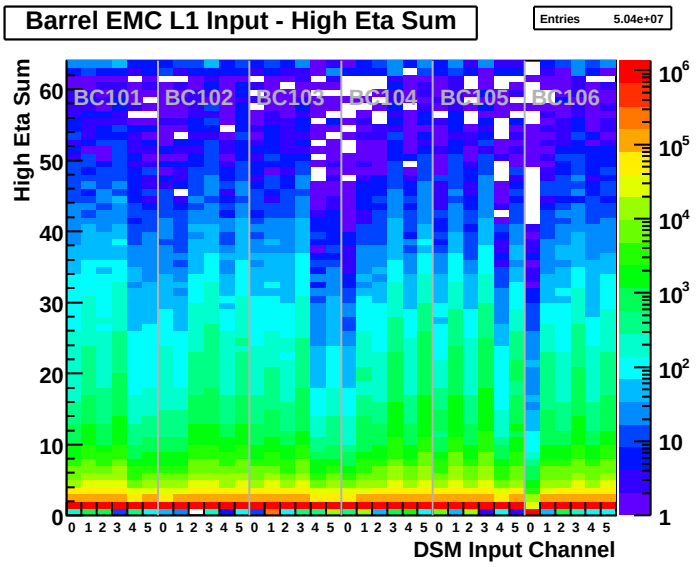
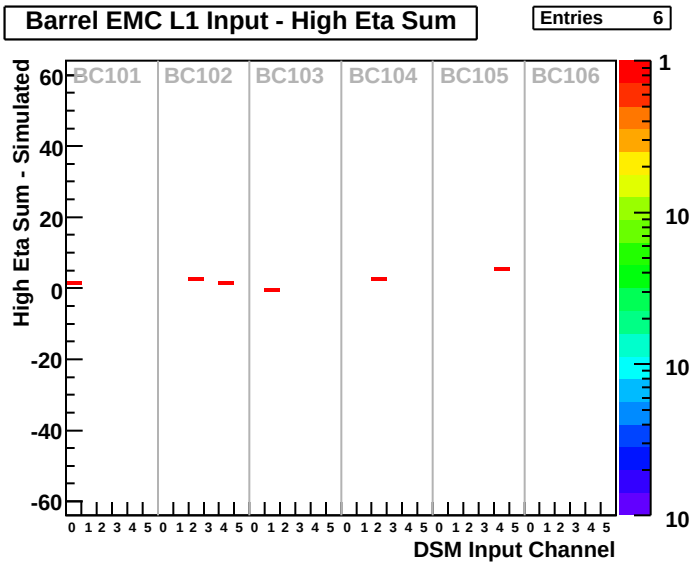
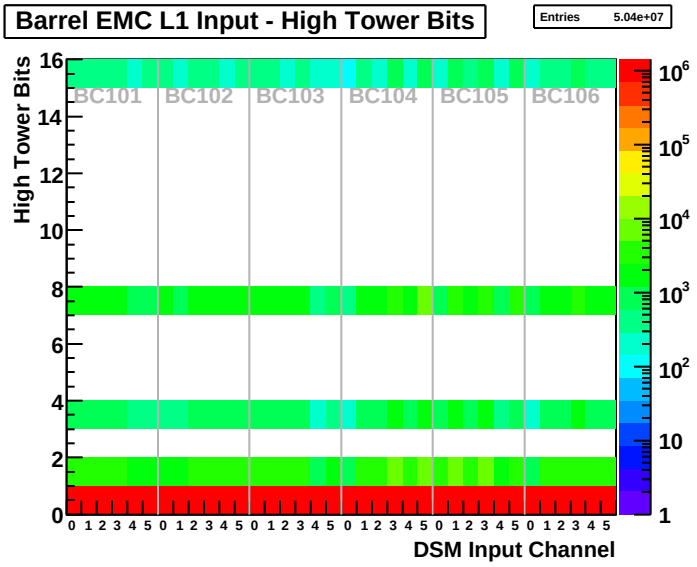
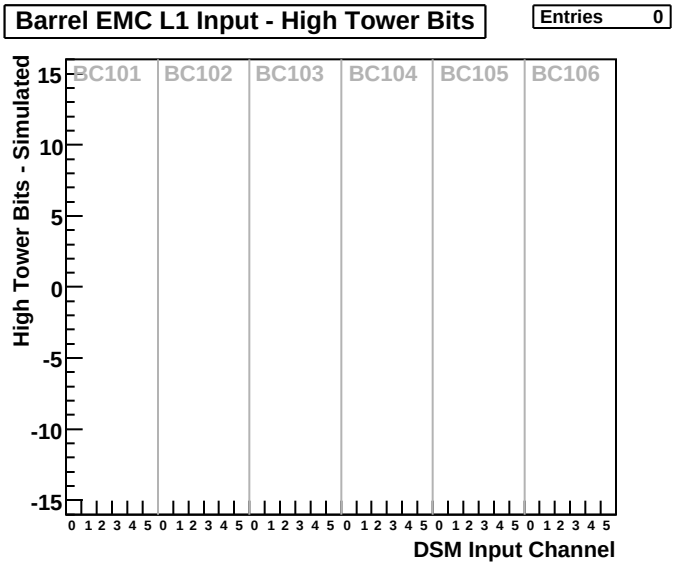

Barrel EMC L1 Input - Low Eta Sum
Entries 5.04e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

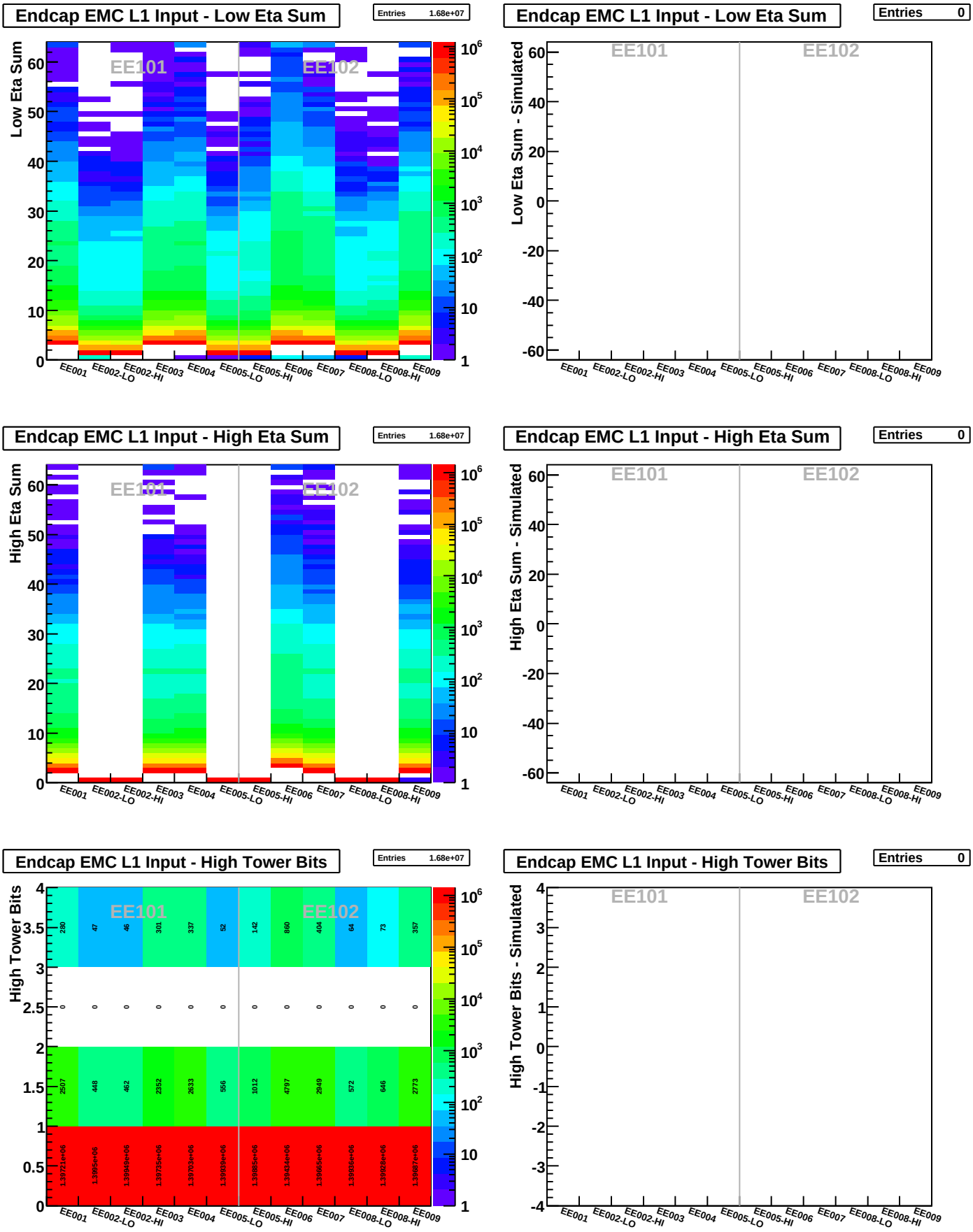

Barrel EMC L1 Input - Low Eta Sum
Entries 9
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

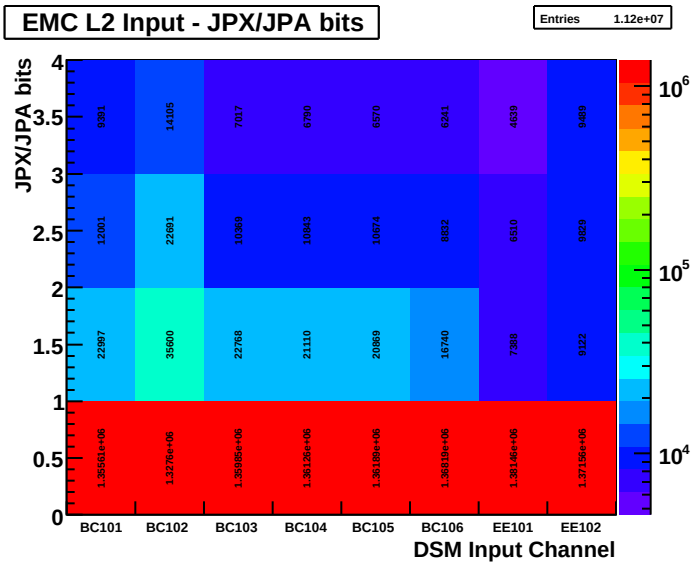

Barrel EMC L1 Input - High Eta Sum
Entries 5.04e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

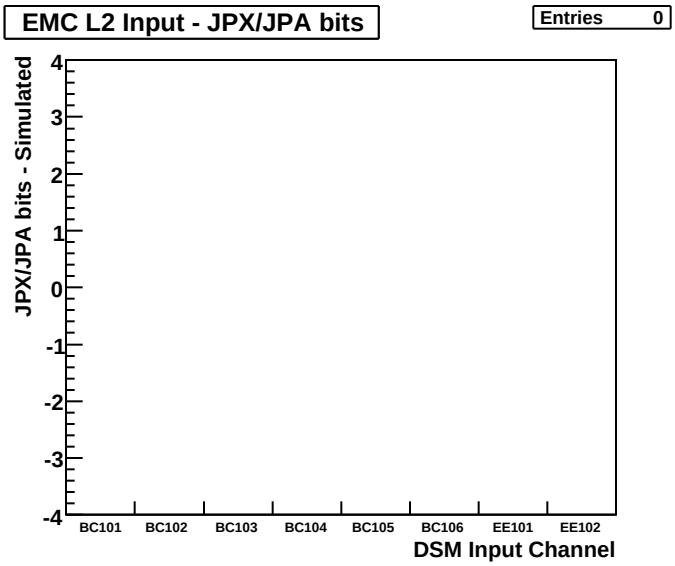

Barrel EMC L1 Input - High Eta Sum
Entries 6
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

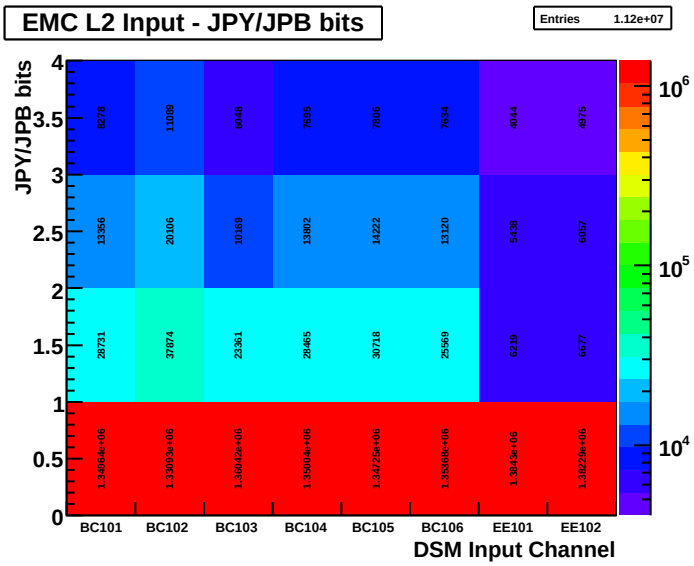

Barrel EMC L1 Input - High Tower Bits
Entries 5.04e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

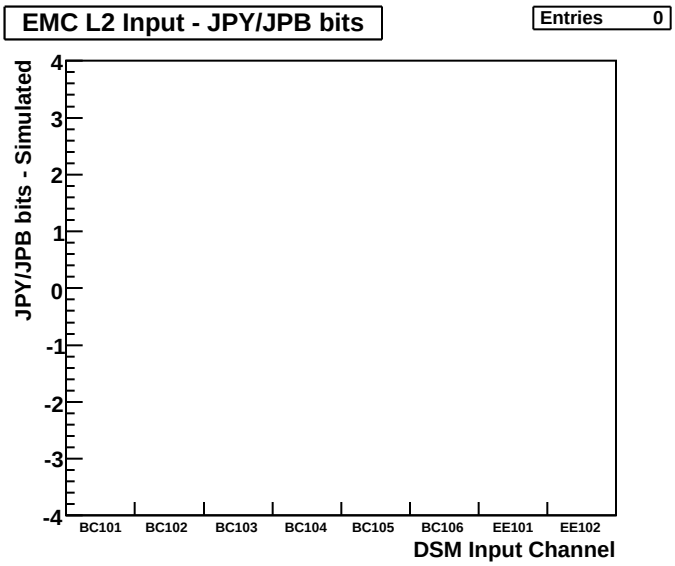

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

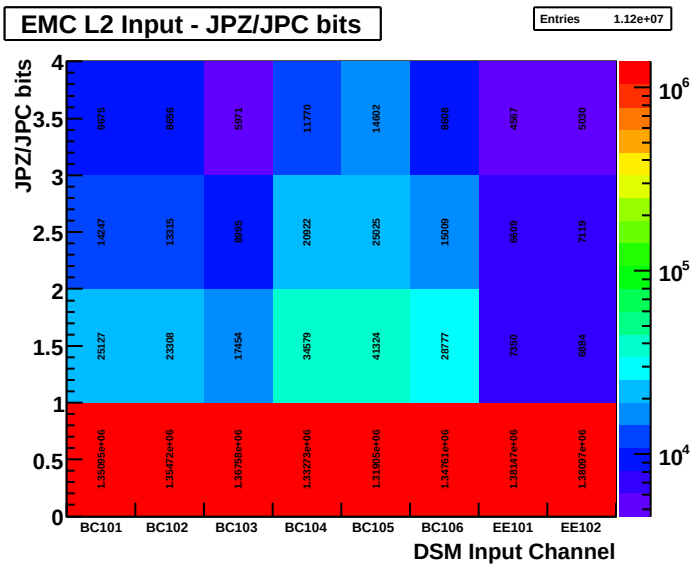

Endcap EMC L1 Input - Low Eta Sum
Entries 1.68e+07
Low Eta Sum
EE101
EE102
Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
Endcap EMC L1 Input - High Eta Sum
Entries 1.68e+07
High Eta Sum
Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
Endcap EMC L1 Input - High Tower Bits
Entries 1.68e+07
High Tower Bits
Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

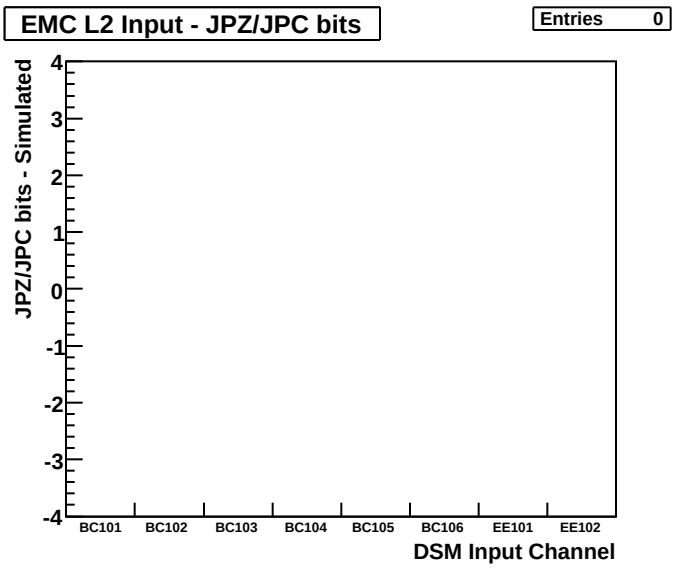

EMC L2 Input - JPX/JPA bits
Entries 1.12e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9381 14105 7017 6790 6570 6241 4639 9489
12001 22691 10369 10843 10674 8832 6510 9829
22997 35600 22768 21110 20869 16740 7388 9122
1.35561e+06 1.32706e+06 1.35985e+06 1.36126e+06 1.36189e+06 1.36819e+06 1.38146e+06 1.37156e+06

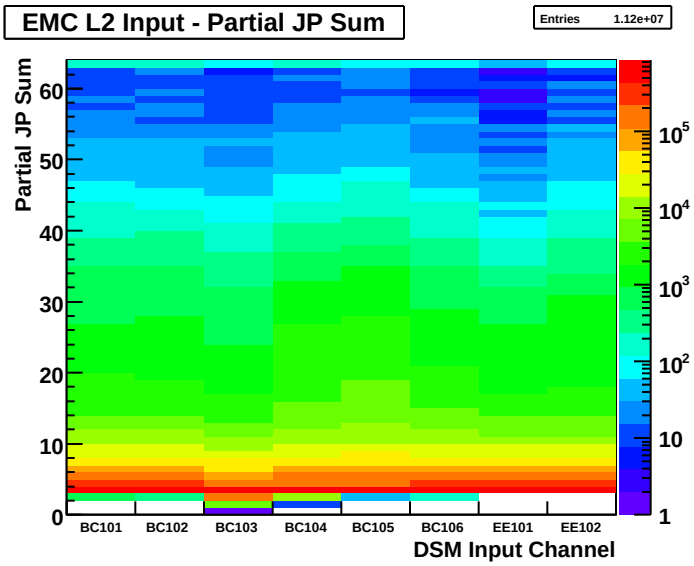

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

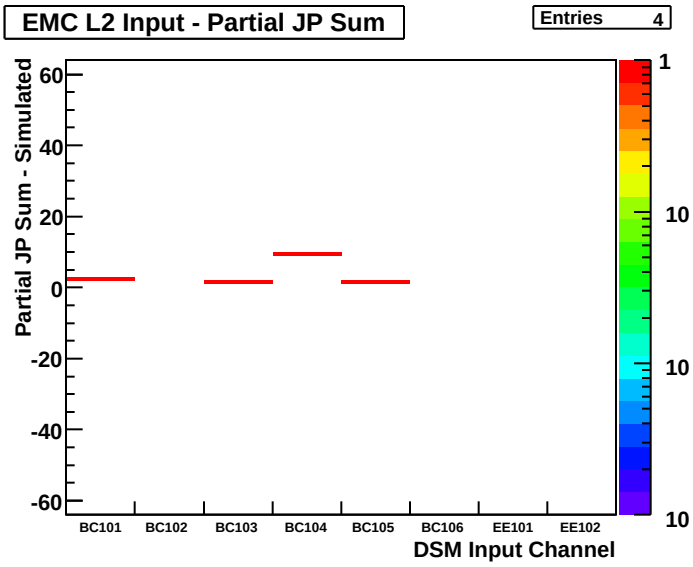

EMC L2 Input - JPY/JPB bits
Entries 1.12e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8276 11089 6048 7695 7806 7634 4044 4975
13356 20106 10169 13802 14222 13120 5438 6057
28731 37874 23361 28465 30718 25569 6219 6677
1.34964e+06 1.33093e+06 1.36042e+06 1.35004e+06 1.34725e+06 1.35169e+06 1.3843e+06 1.38229e+06

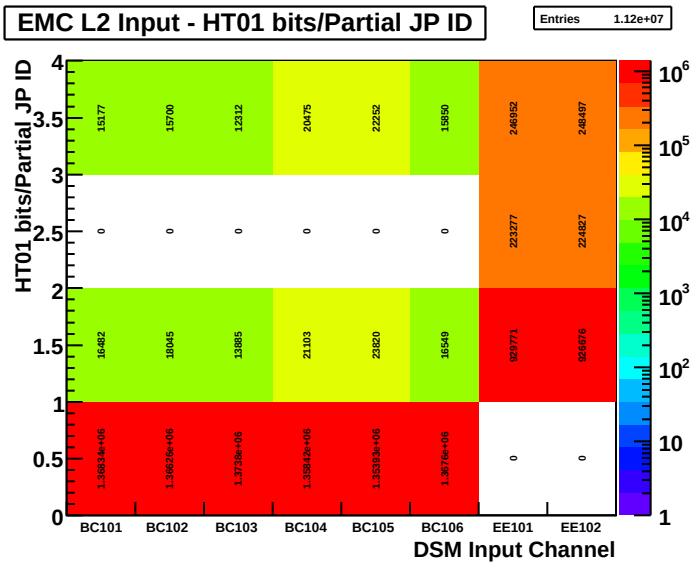

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

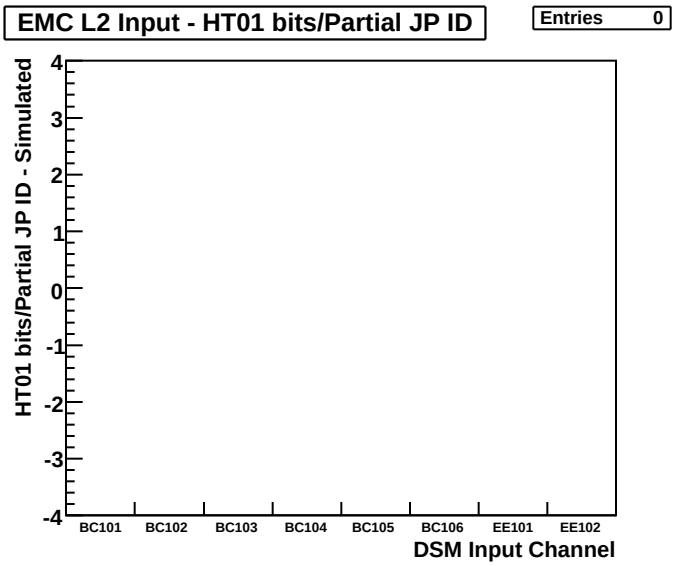

EMC L2 Input - JPZ/JPC bits
Entries 1.12e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9675 8656 5971 11770 14602 8608 4567 5030
14247 13315 8695 20922 25025 15009 6609 7119
25127 23308 17454 34579 41324 28777 7350 6834
1.33095e+06 1.35472e+06 1.36758e+06 1.33273e+06 1.31905e+06 1.34761e+06 1.38147e+06 1.38097e+06

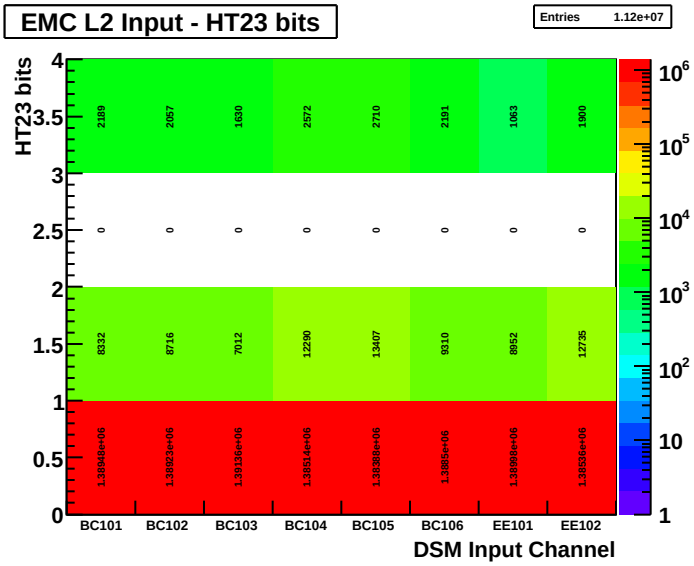

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

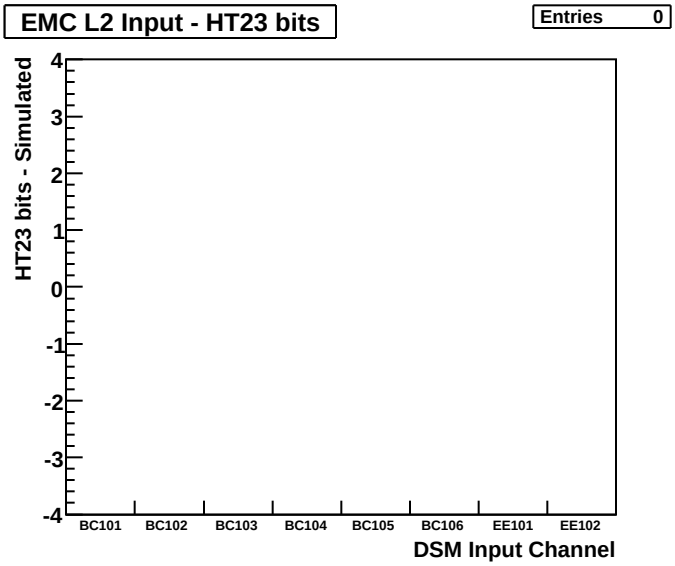

EMC L2 Input - Partial JP Sum
Entries 1.12e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

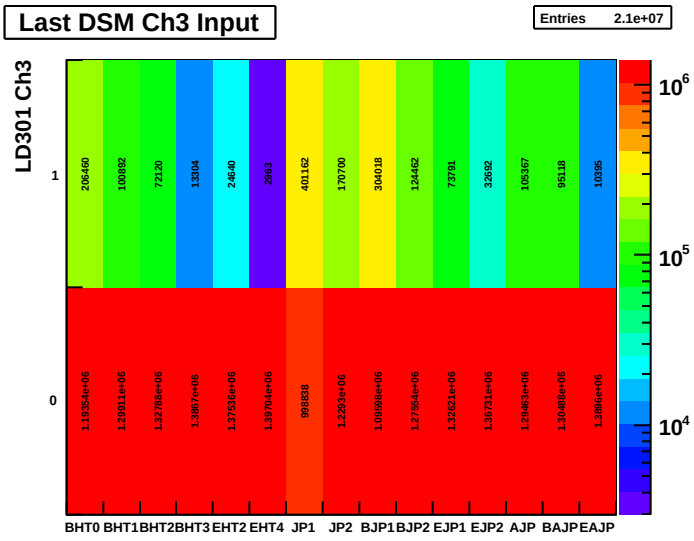

EMC L2 Input - Partial JP Sum
Entries 4
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

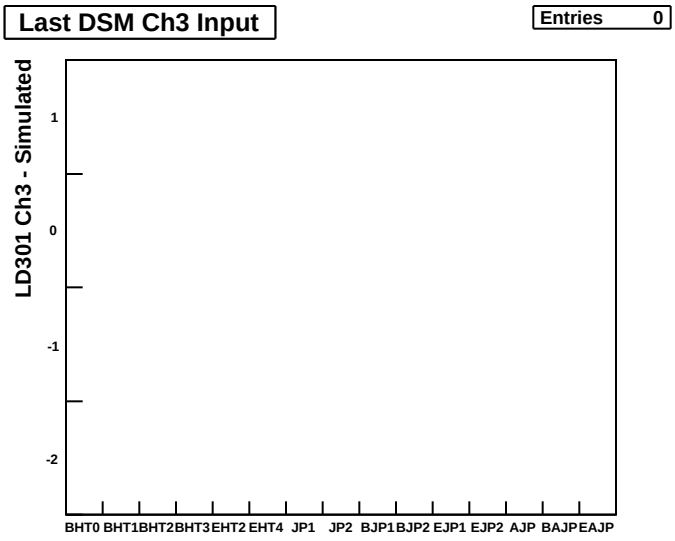

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.12e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

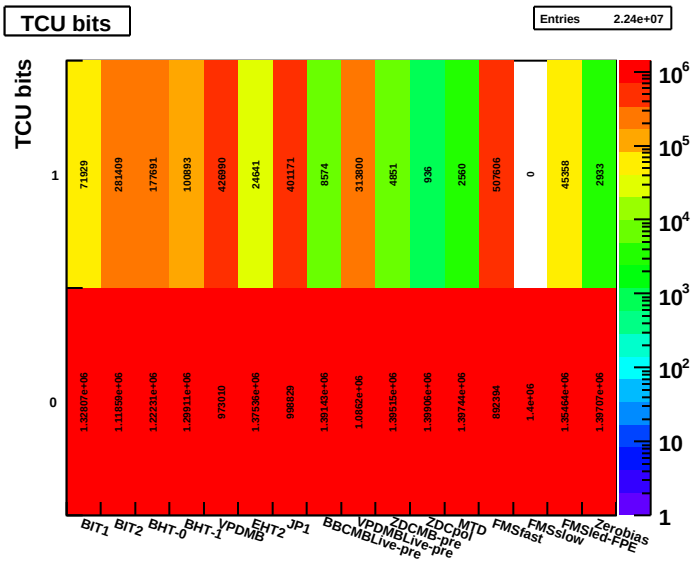

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

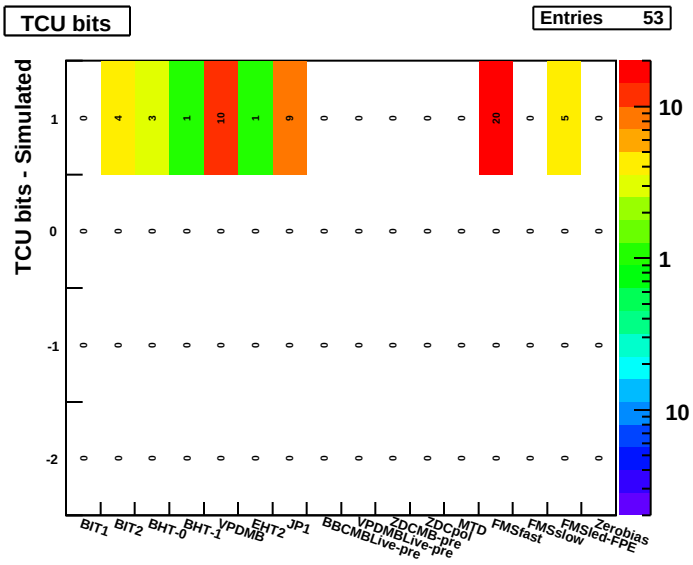

EMC L2 Input - HT23 bits
Entries 1.12e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

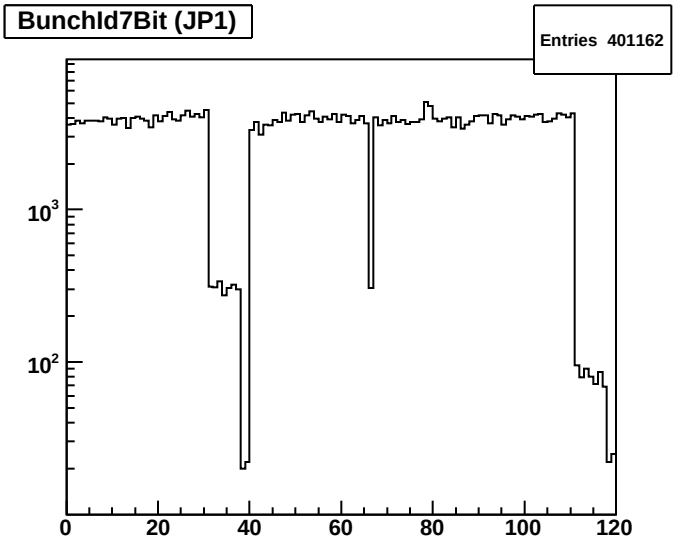

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

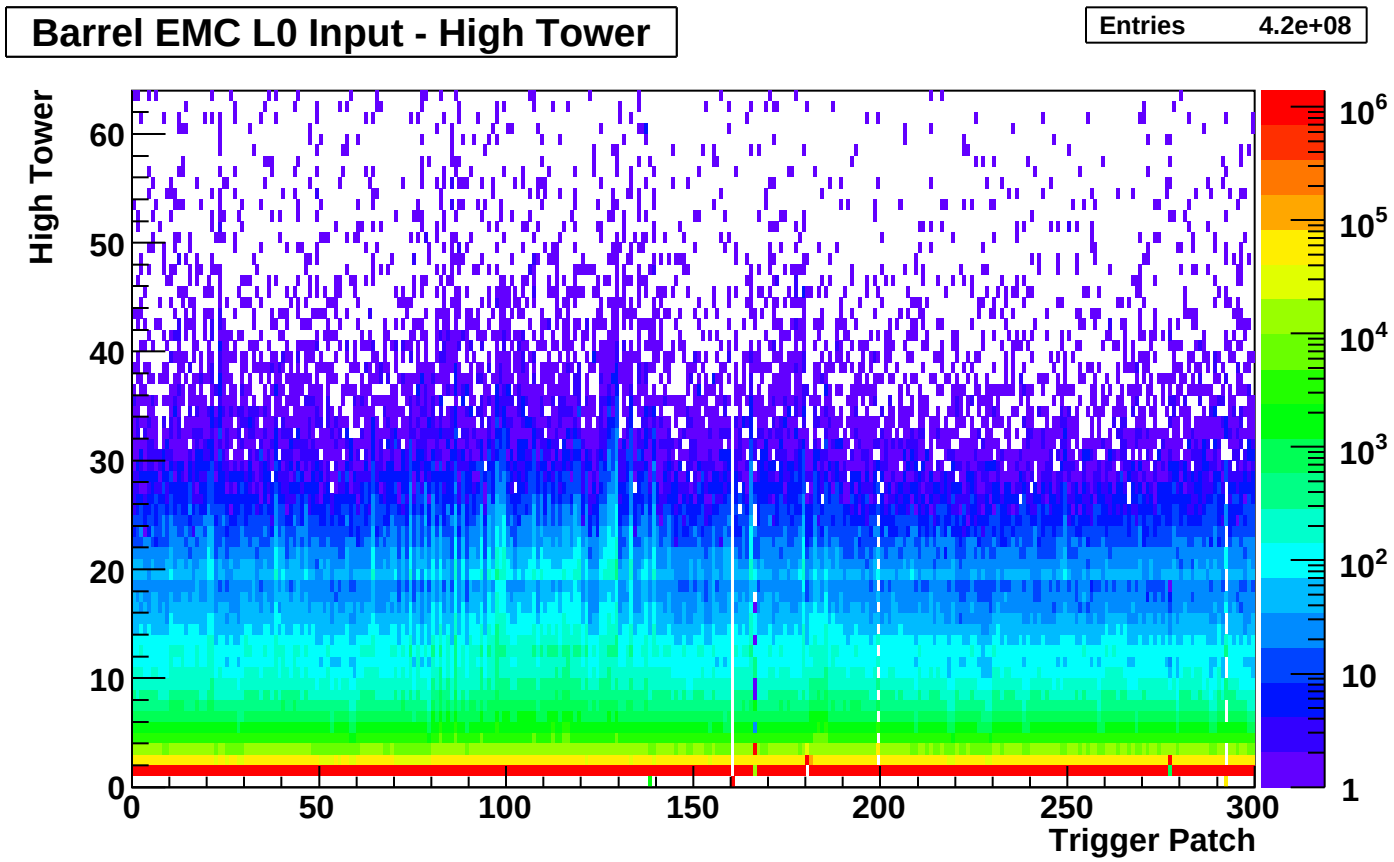

Last DSM Ch3 Input
Entries 2.1e+07
LD301 Ch3
1
0
206460
100892
72120
13304
24640
2363
401162
170700
304018
124462
73791
32692
105367
95118
10395
1.19354e+06
1.29911e+06
1.32788e+06
1.3867e+06
1.37536e+06
1.39704e+06
998838
1.2293e+06
1.09598e+06
1.27954e+06
1.32621e+06
1.36731e+06
1.29463e+06
1.30488e+06
1.3886e+06
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
10^6
10^5
10^4

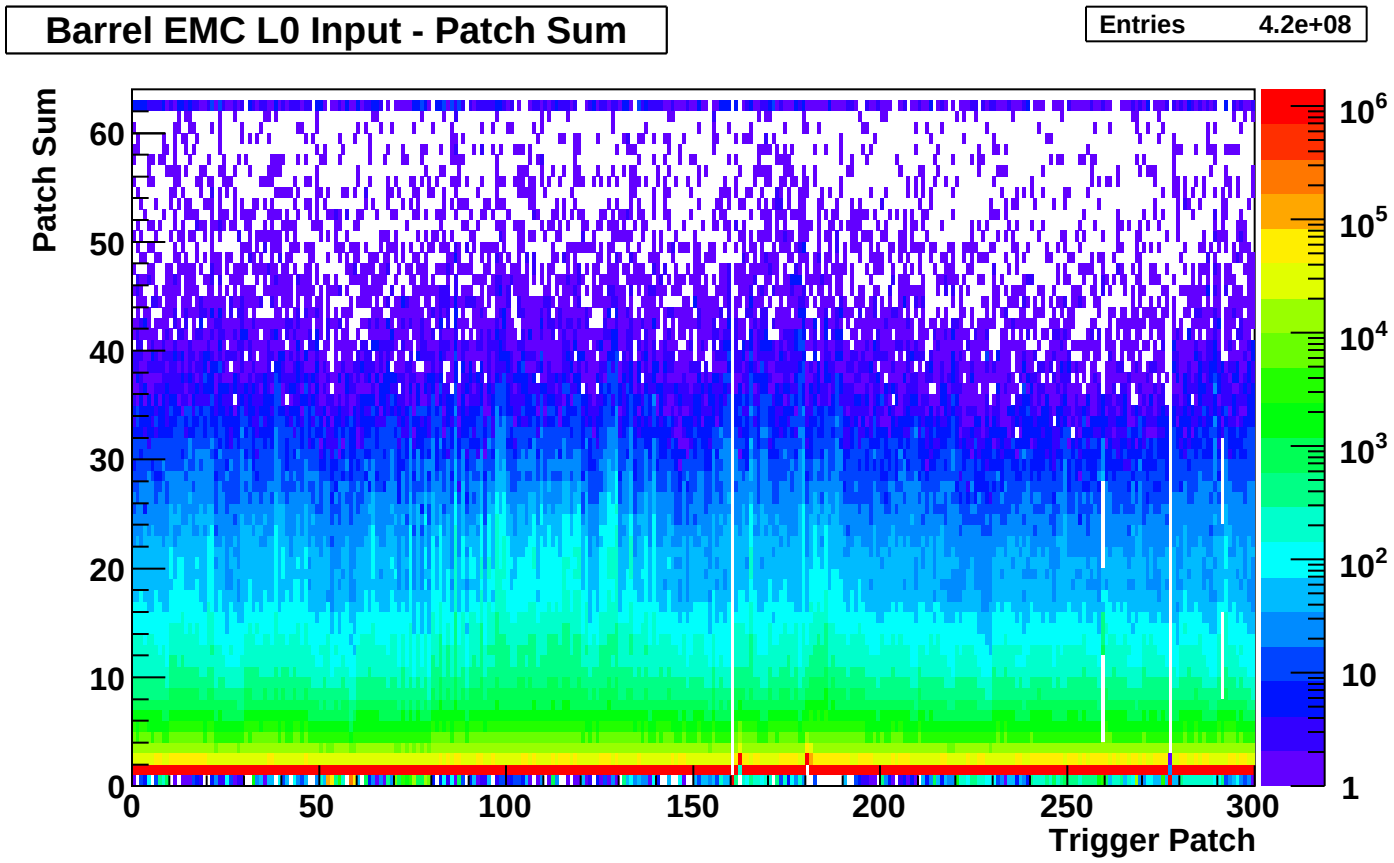

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

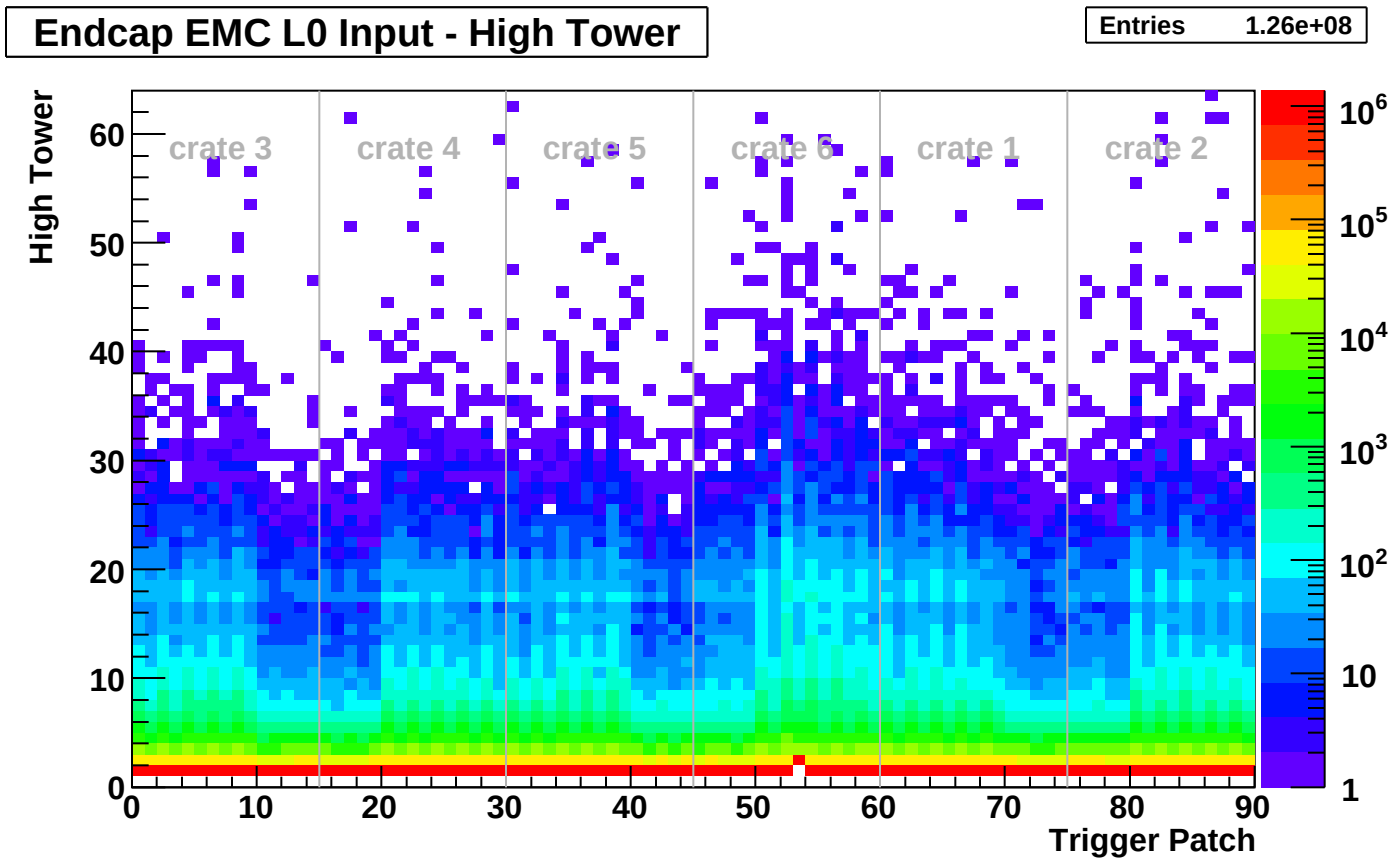

TCU bits
Entries 2.24e+07
TCU bits
1
0
71929
281409
177691
100893
426990
24641
401171
8574
313800
4851
936
2560
507606
0
45358
2933
1.32807e+06
1.11859e+06
1.22231e+06
1.29911e+06
973010
1.37538e+06
998829
1.39143e+06
1.0862e+06
1.39515e+06
1.39906e+06
1.39744e+06
832394
1.4e+06
1.35464e+06
1.39707e+06
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10^6
10^5
10^4
10^3
10^2
10
1

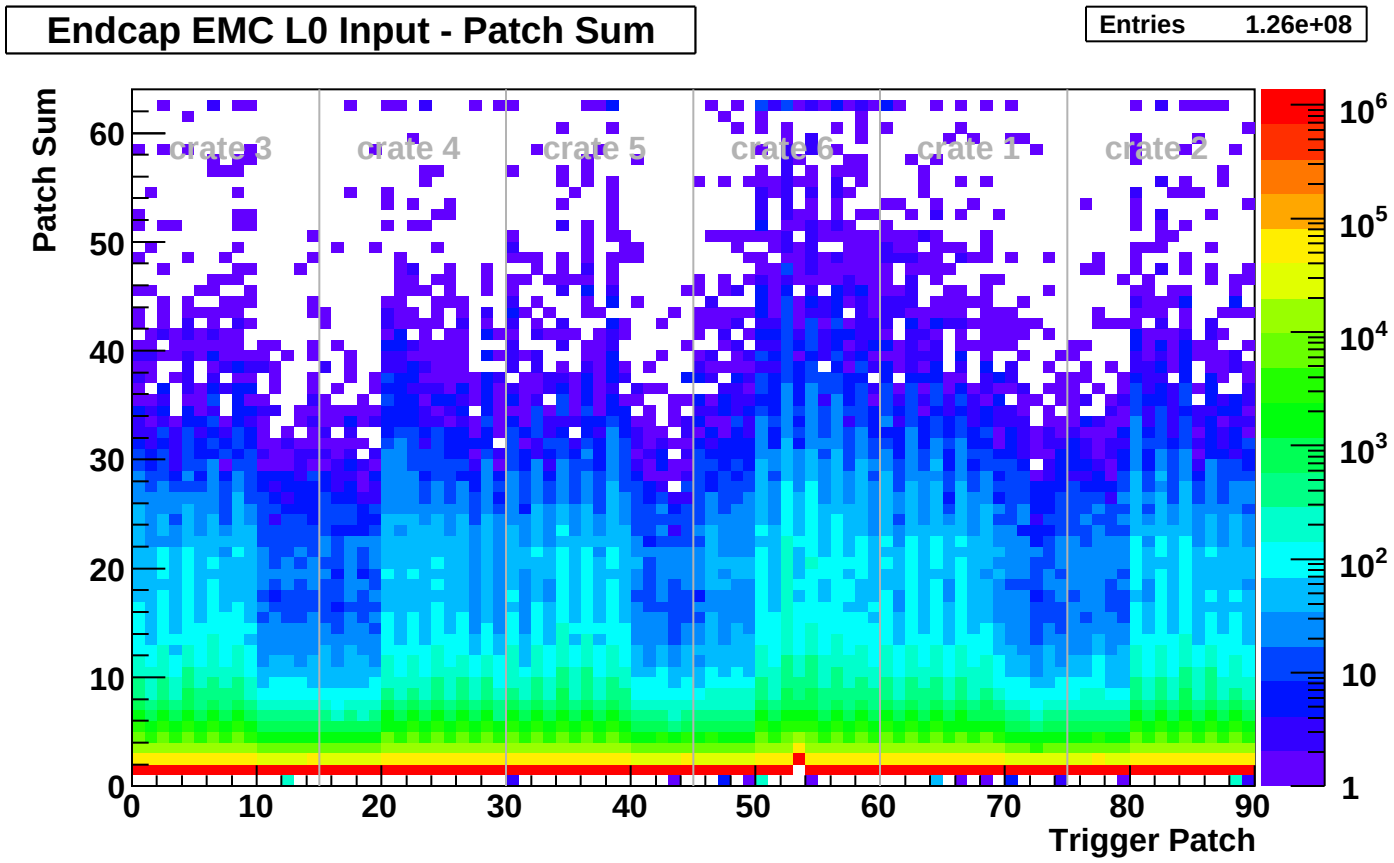

TCU bits
Entries 53
TCU bits - Simulated
1
0
-1
-2
0 4 3 1 10 1 9 0 0 0 0 0 20 0 5 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1
10

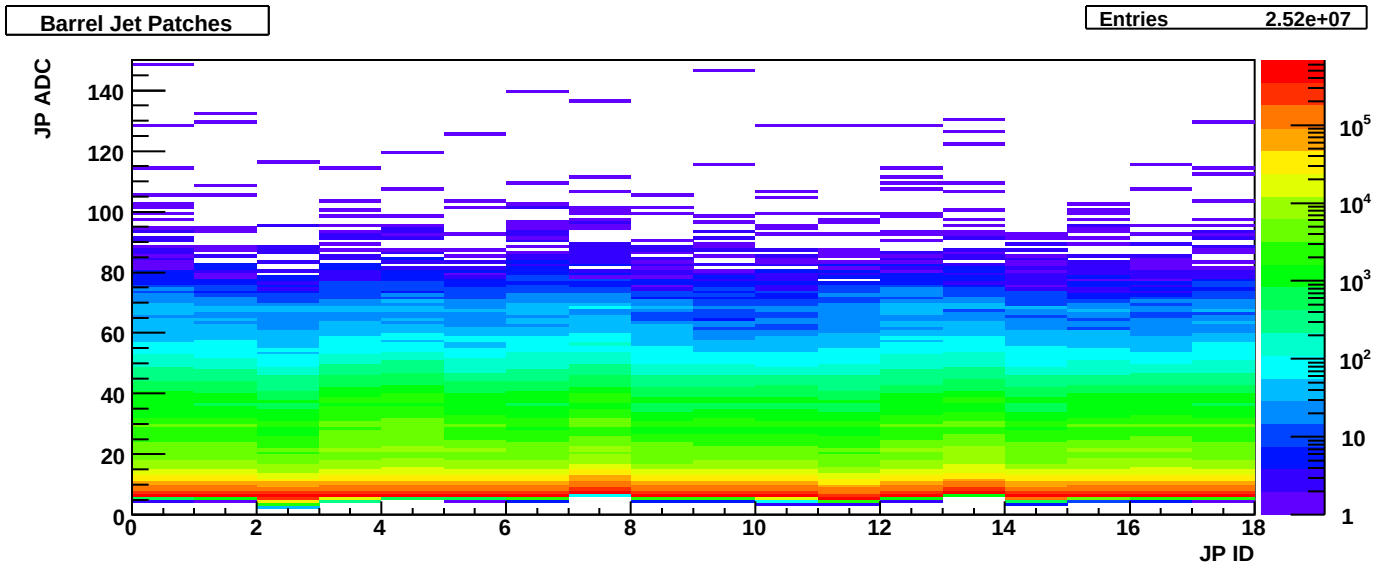

BunchId7Bit (JP1)
Entries 401162
10^3
10^2
0
20
40
60
80
100
120

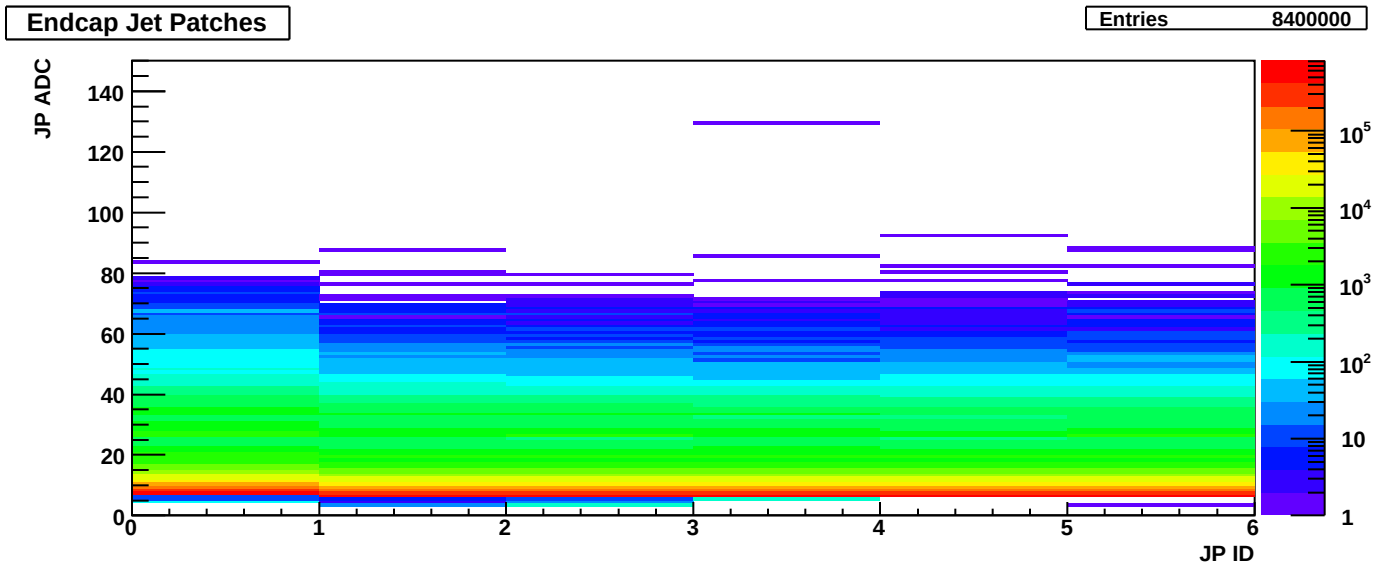

Barrel EMC L0 Input - High Tower
Entries 4.2e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

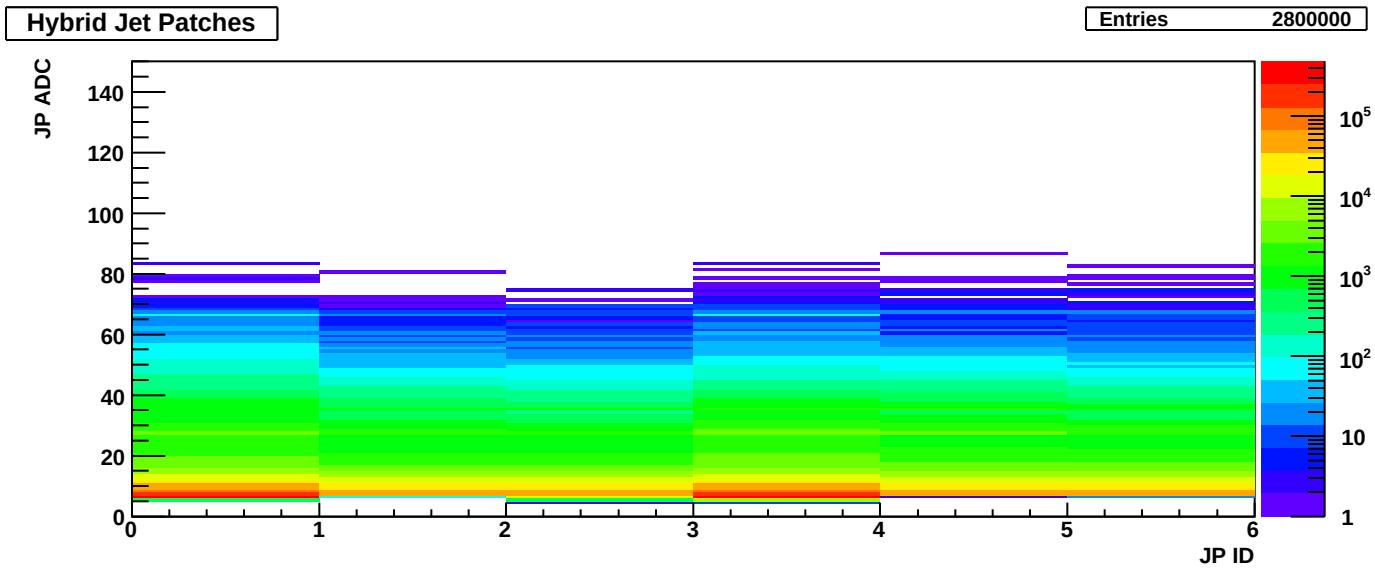

Barrel EMC L0 Input - Patch Sum
Entries 4.2e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

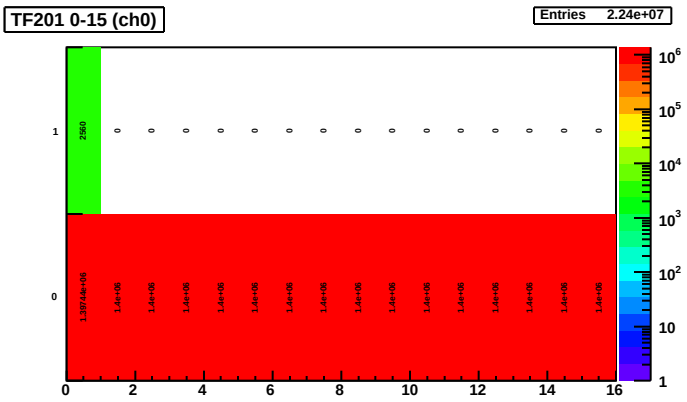

Endcap EMC L0 Input - High Tower
Entries 1.26e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

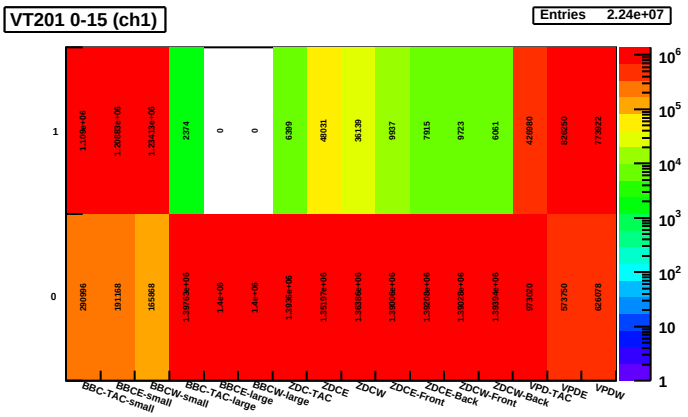

Endcap EMC L0 Input - Patch Sum
Entries 1.26e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

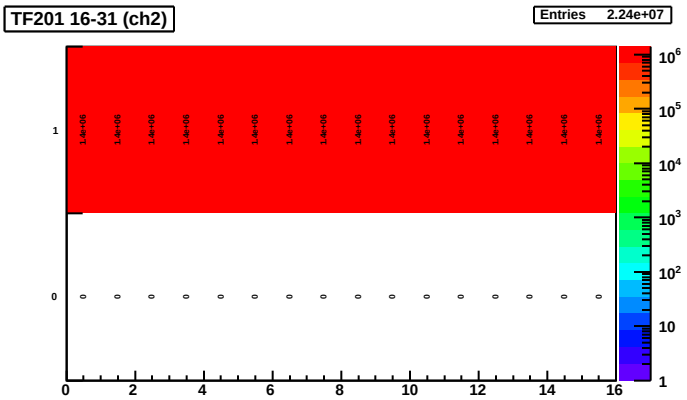

Barrel Jet Patches
Entries 2.52e+07
JP ADC
JP ID

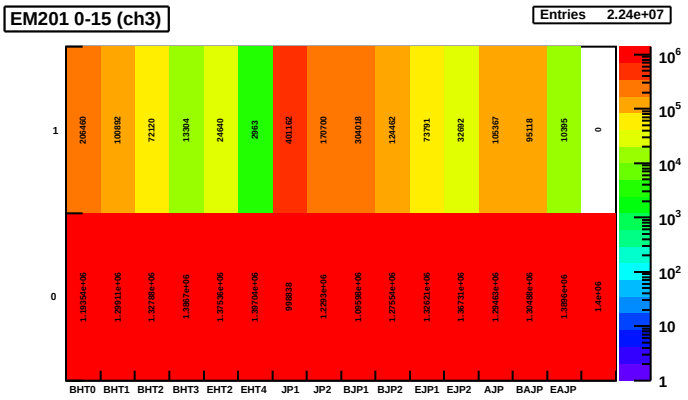

Endcap Jet Patches
Entries 8400000
JP ADC
JP ID

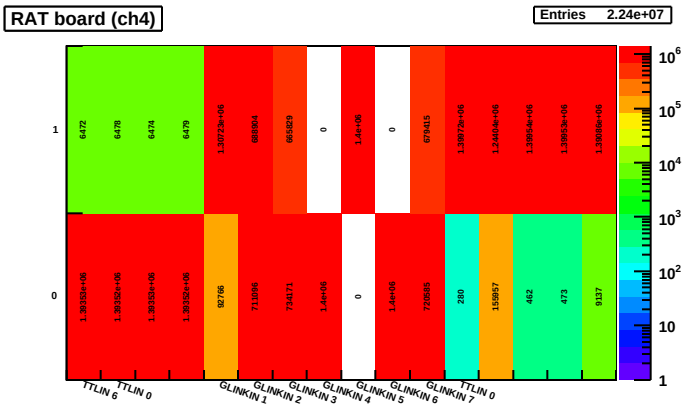

Hybrid Jet Patches
Entries 2800000
JP ADC
JP ID

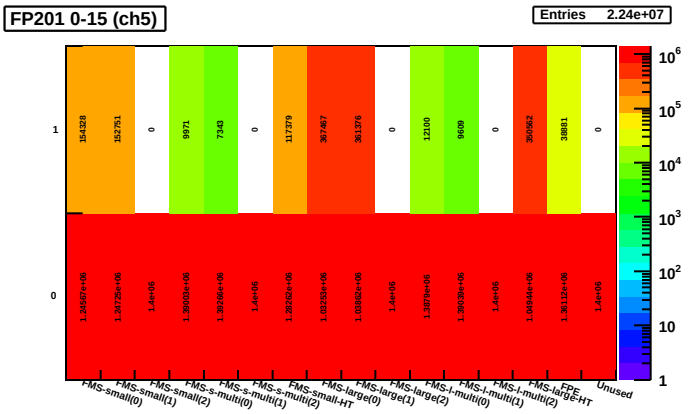

TF201 0-15 (ch0)
Entries 2.24e+07

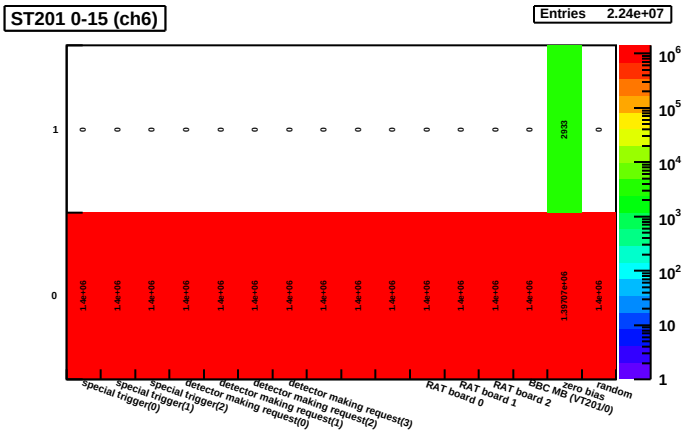

VT201 0-15 (ch1)
Entries 2.24e+07
BBC-TAC-small
BBCE-small
BBCW-small
BBC-TAC-large
BBCE-large
BBCW-large
ZDC-TAC
ZDCE
ZDCW
ZDCE-Front
ZDCE-Back
ZDCW-Front
ZDCW-Back
VPD-TAC
VPDE
VPDW

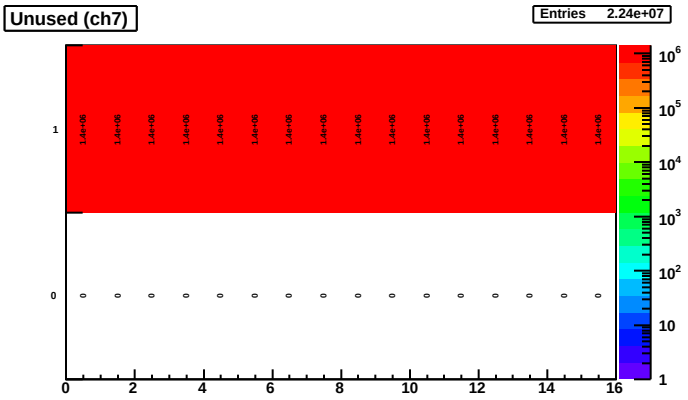

TF201 16-31 (ch2)
Entries 2.24e+07


EM201 0-15 (ch3)
Entries 2.24e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP


RAT board (ch4)
Entries 2.24e+07
TTLIN 6
TTLIN 0
GLINKIN 1
GLINKIN 2
GLINKIN 3
GLINKIN 4
GLINKIN 5
GLINKIN 6
GLINKIN 7
TTLIN 0


FP201 0-15 (ch5)
Entries 2.24e+07
FMS-small(0)
FMS-small(1)
FMS-small(2)
FMS-s-multi(0)
FMS-s-multi(1)
FMS-s-multi(2)
FMS-small-HT
FMS-large(0)
FMS-large(1)
FMS-large(2)
FMS-l-multi(0)
FMS-l-multi(1)
FMS-l-multi(2)
FMS-large-HT
FPE
Unused


ST201 0-15 (ch6)
Entries 2.24e+07
Special trigger(0)
Special trigger(1)
Special trigger(2)
detector making request(0)
detector making request(1)
detector making request(2)
detector making request(3)
RAT board 0
RAT board 1
RAT board 2
BBC MB (VT201/0)
zero bias
random


Unused (ch7)
Entries 2.24e+07