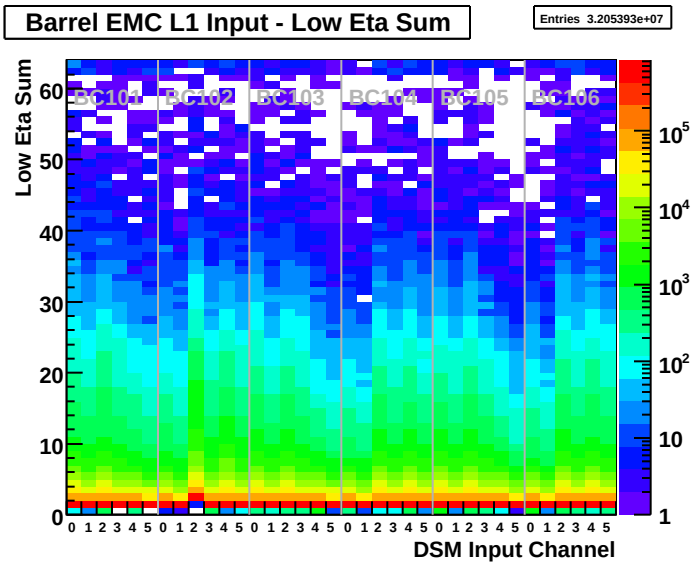
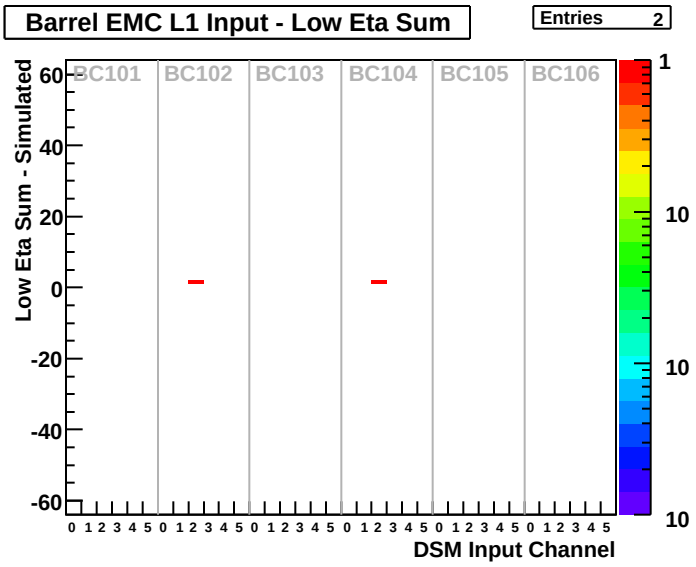
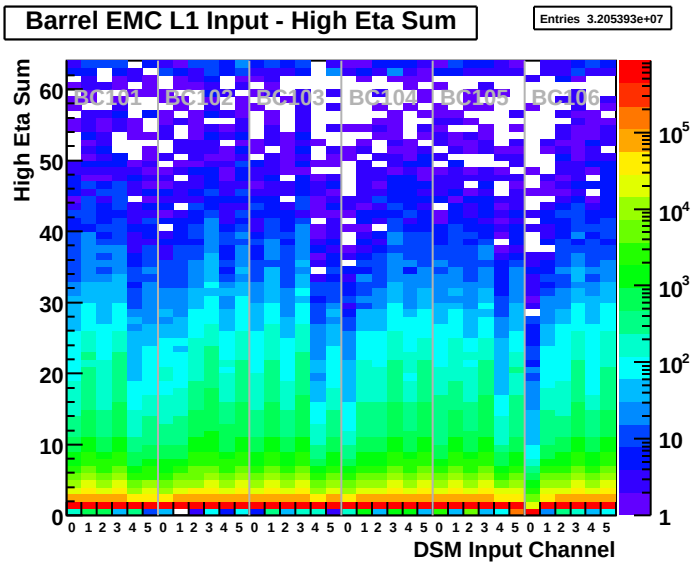
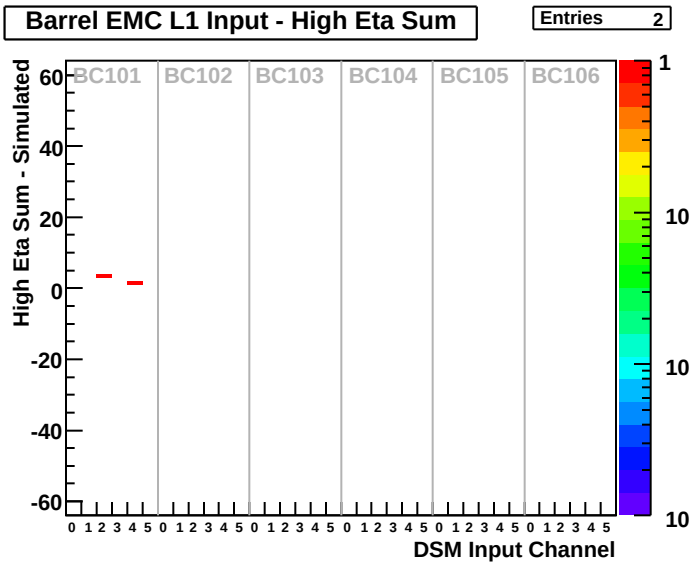
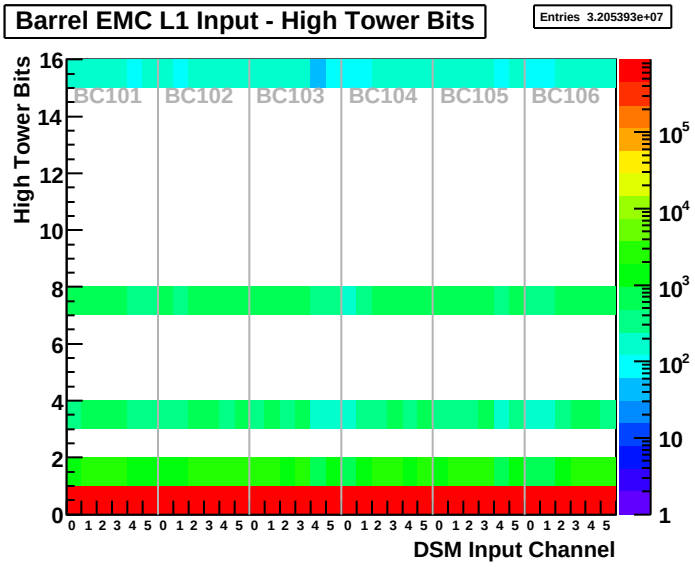
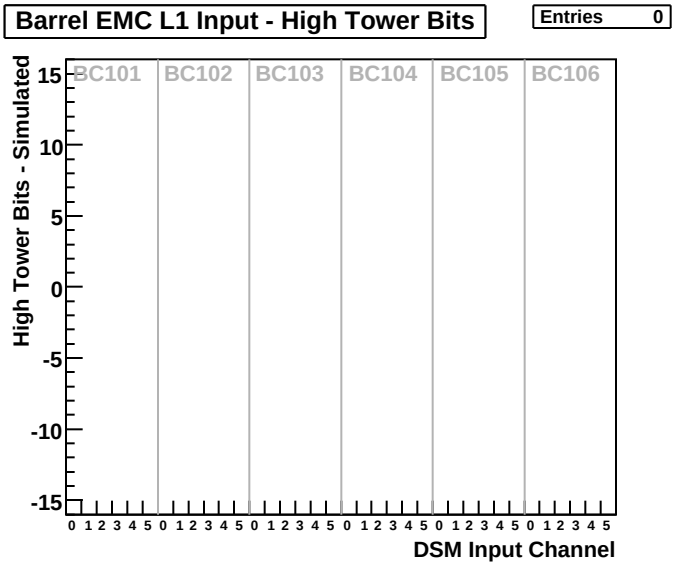

Barrel EMC L1 Input - Low Eta Sum
Entries 3.205393e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

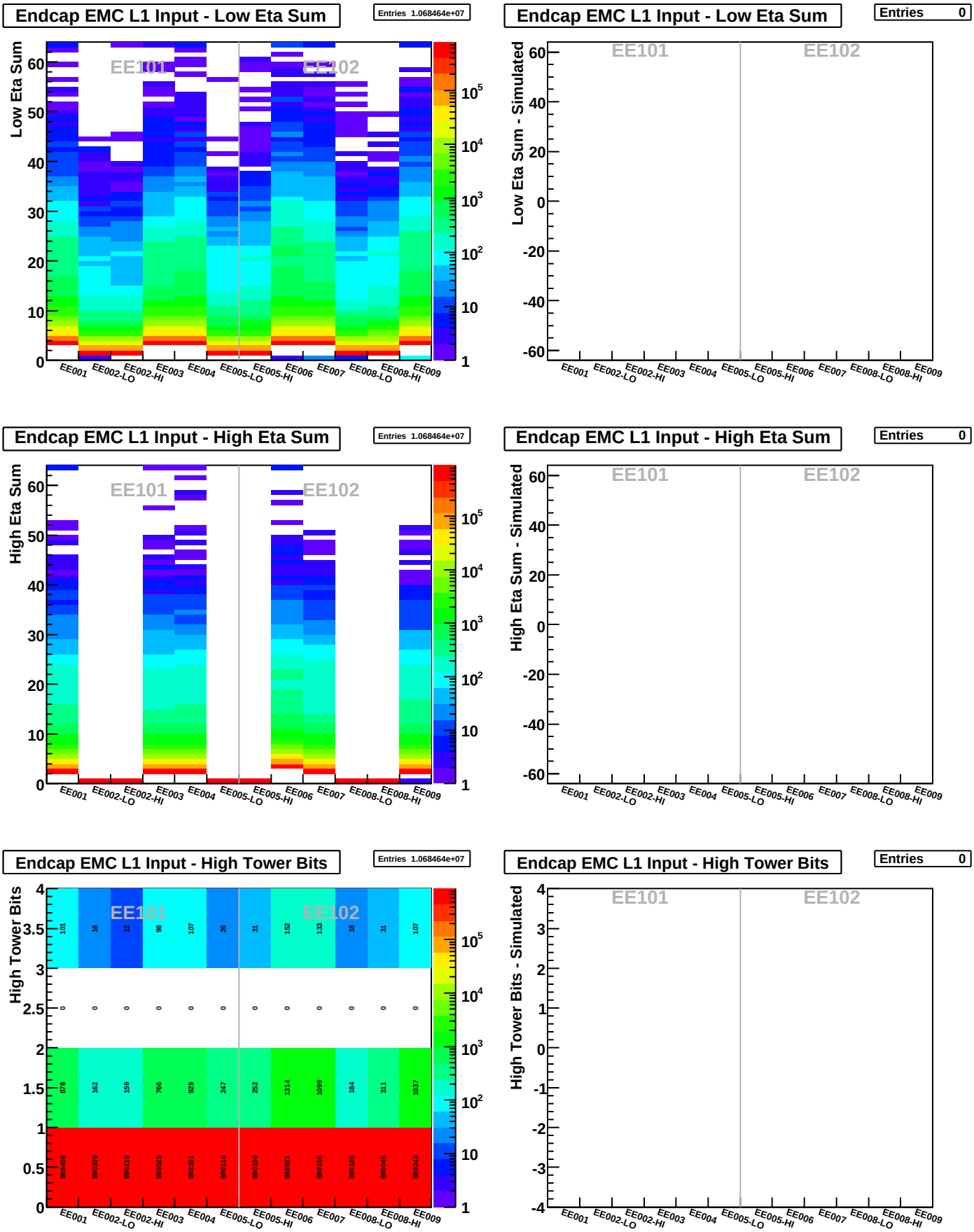

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

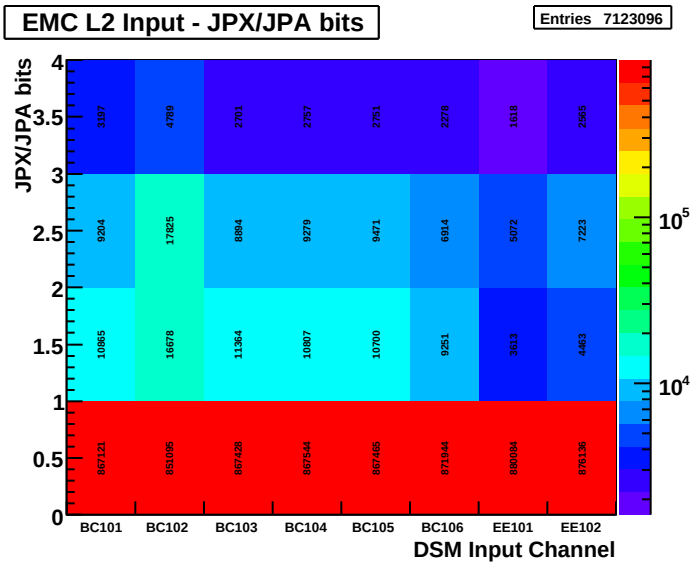

Barrel EMC L1 Input - High Eta Sum
Entries 3.205393e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

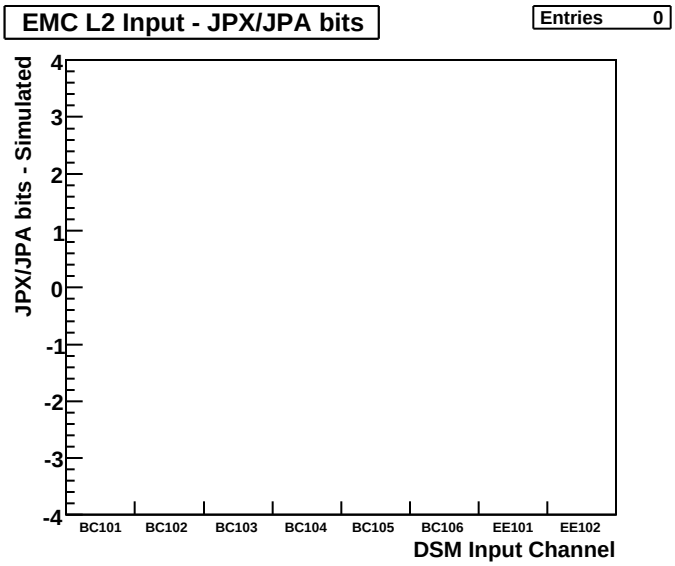

Barrel EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

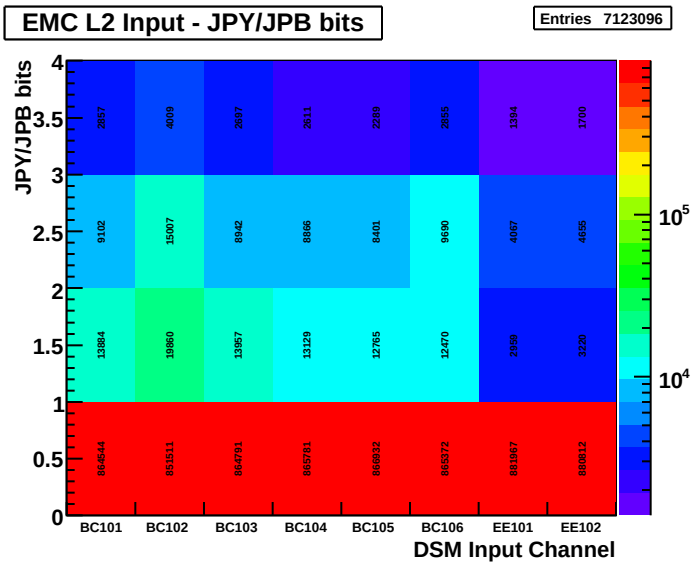

Barrel EMC L1 Input - High Tower Bits
Entries 3.205393e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

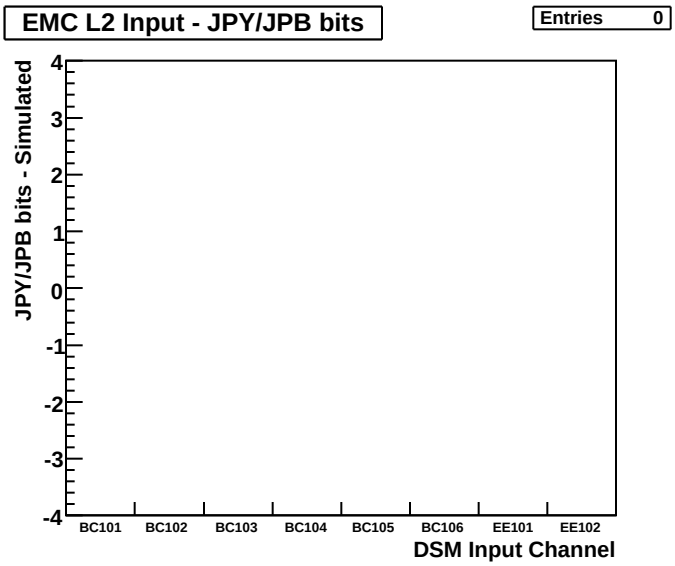

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

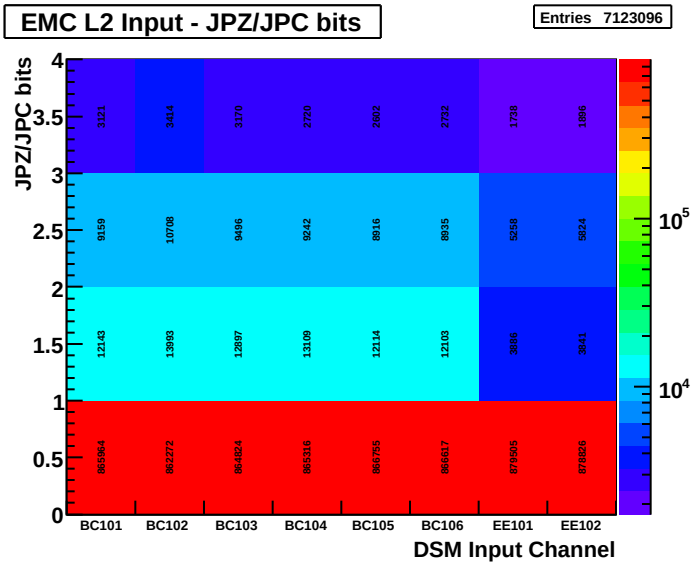

Endcap EMC L1 Input - Low Eta Sum
Entries 1.068464e+07
Low Eta Sum
EE101
EE102
Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
Endcap EMC L1 Input - High Eta Sum
Entries 1.068464e+07
High Eta Sum
Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
Endcap EMC L1 Input - High Tower Bits
Entries 1.068464e+07
High Tower Bits
Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

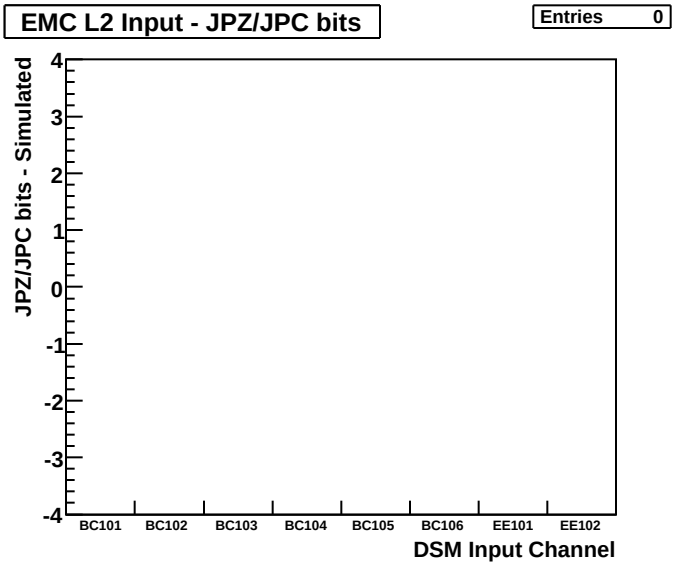

EMC L2 Input - JPX/JPA bits
Entries 7123096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3197 4789 2701 2757 2751 2278 1618 2565
9204 17825 8894 9279 9471 6914 5072 7223
10865 16678 11364 10807 10700 9251 3613 4463
867121 851095 867428 867544 867465 871944 880084 876136

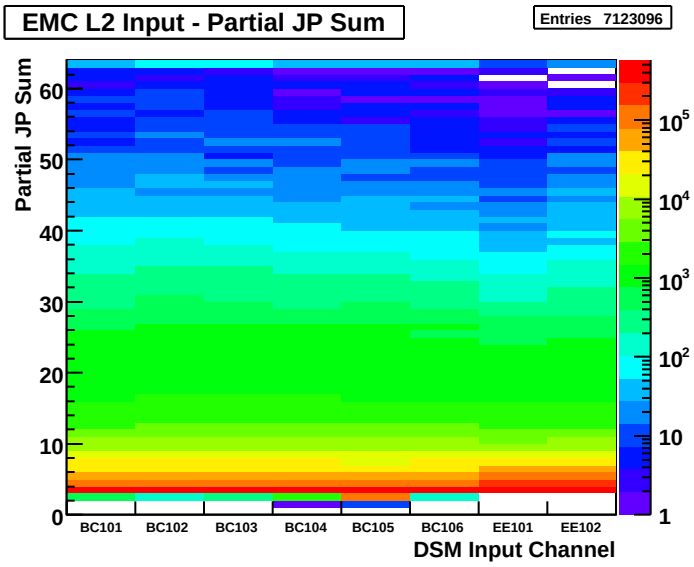

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

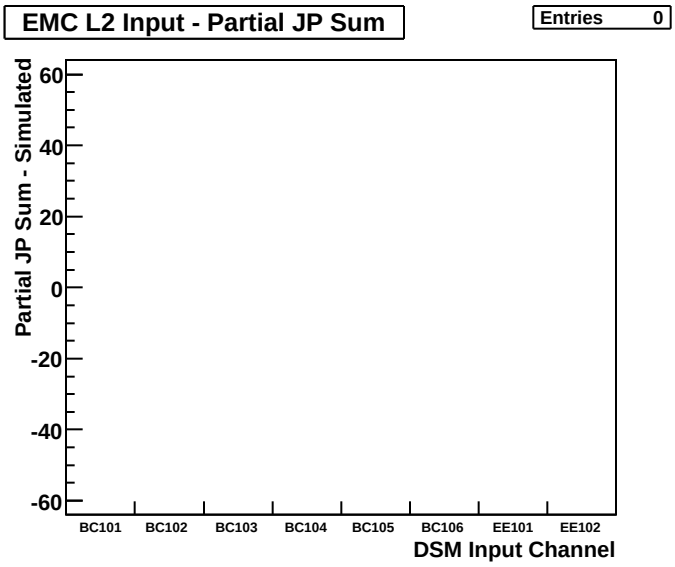

EMC L2 Input - JPY/JPB bits
Entries 7123096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2357 4009 2697 2611 2289 2855 1394 1700
9102 15007 8942 8866 8401 9690 4067 4655
13884 19860 13957 13129 12765 12470 2959 3220
864544 851511 864791 865781 866932 865372 881967 880612

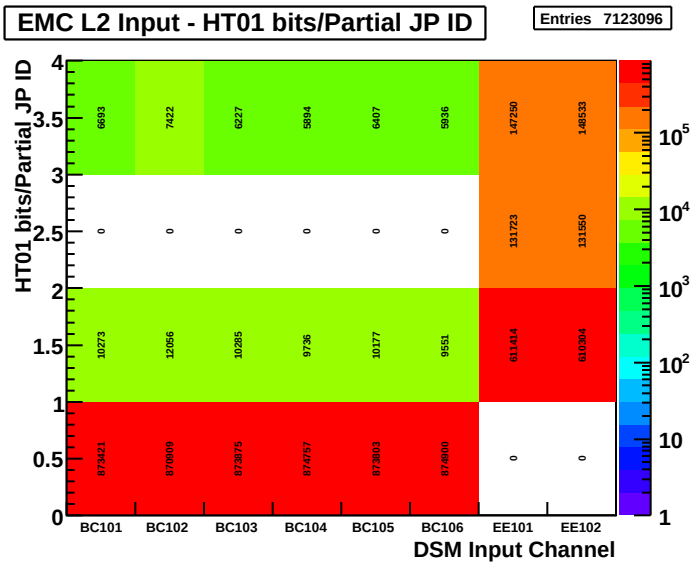

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

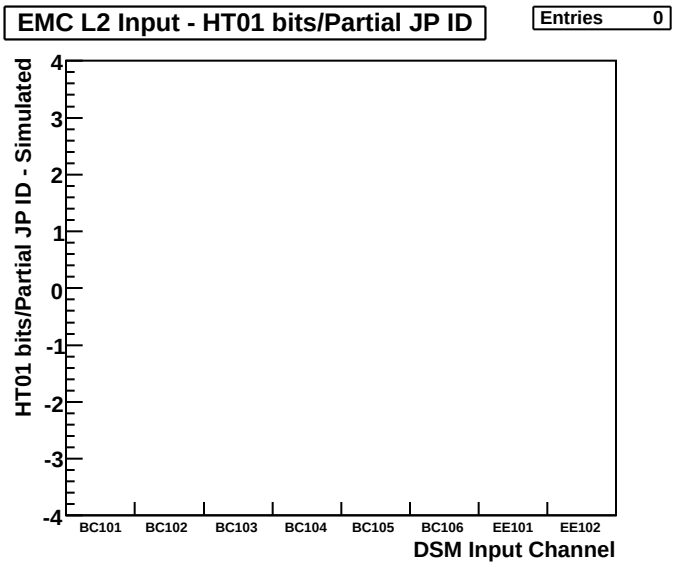

EMC L2 Input - JPZ/JPC bits
Entries 7123096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3121 3414 3170 2720 2602 2732 1738 1936
9159 10708 9496 9242 8916 8935 5258 5824
12143 13993 12897 13109 12114 12103 3386 3841
865964 862272 864824 865316 866755 866617 879905 878826

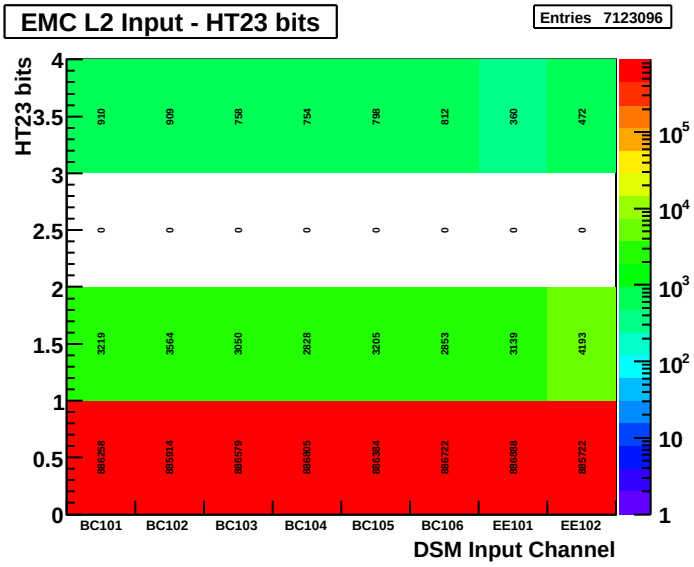

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

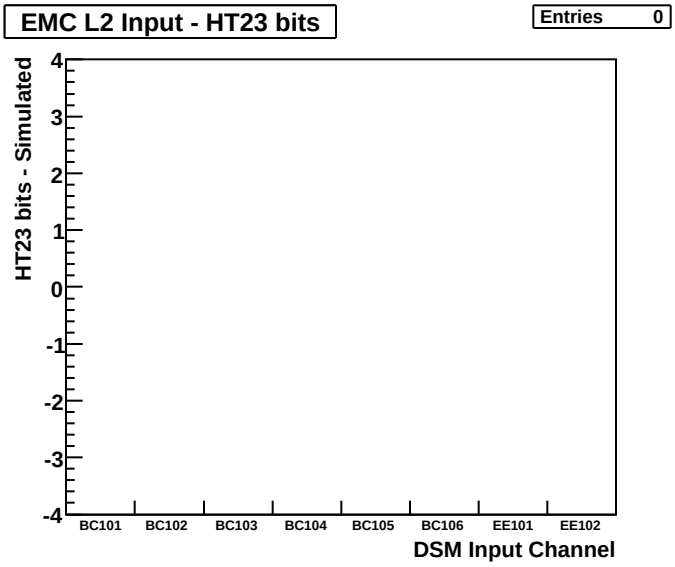

EMC L2 Input - Partial JP Sum
Entries 7123096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

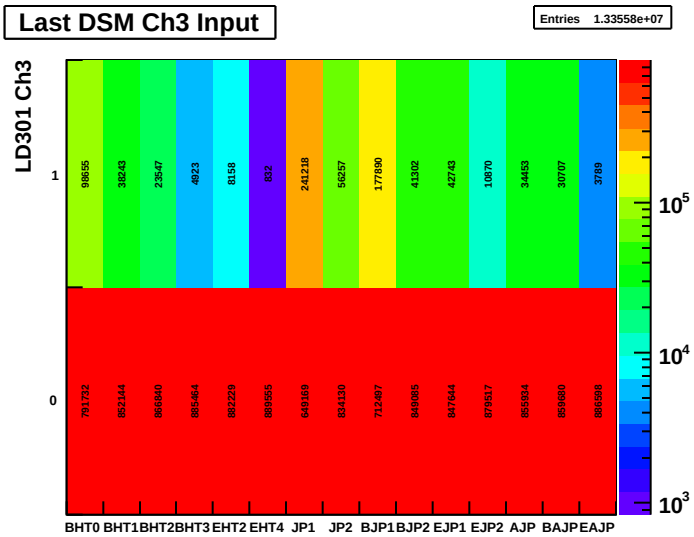

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

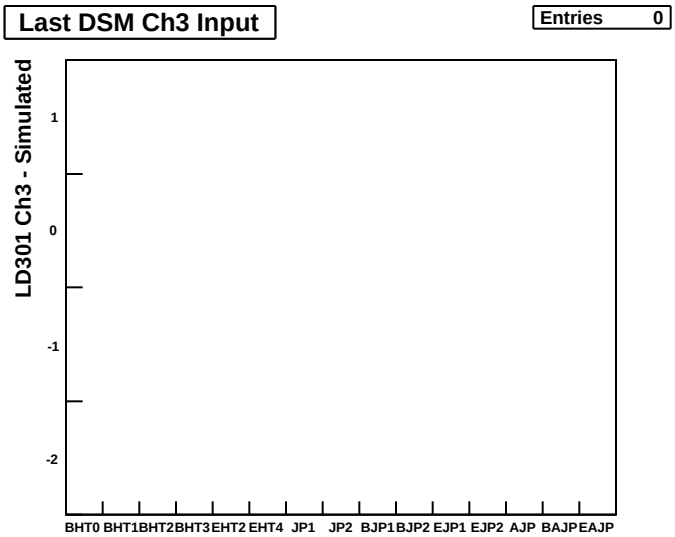

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7123096
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

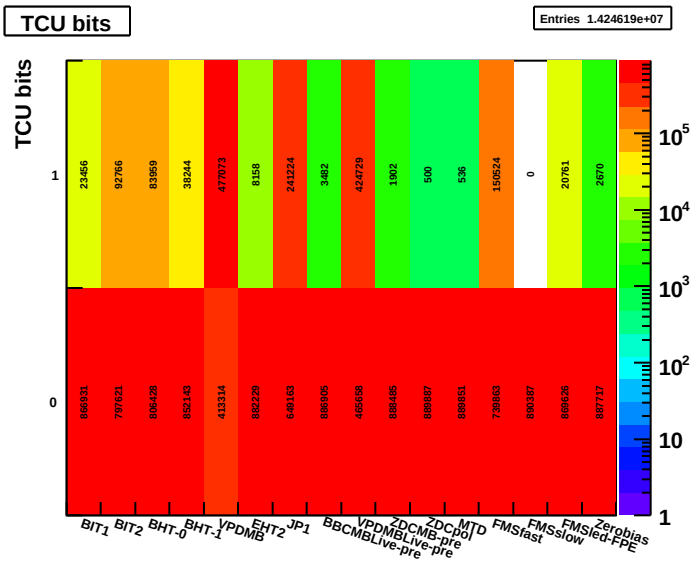

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

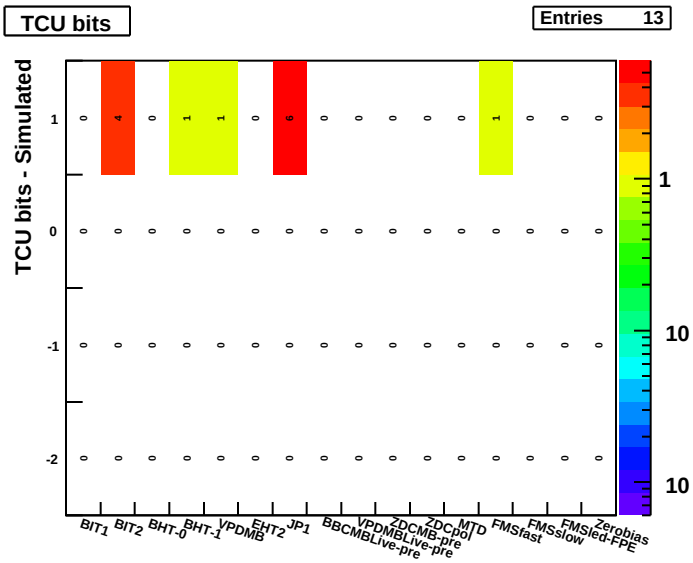

EMC L2 Input - HT23 bits
Entries 7123096
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

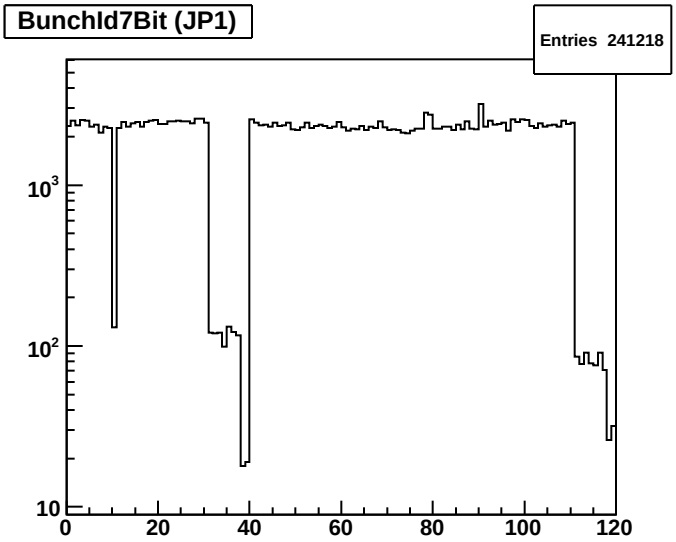

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

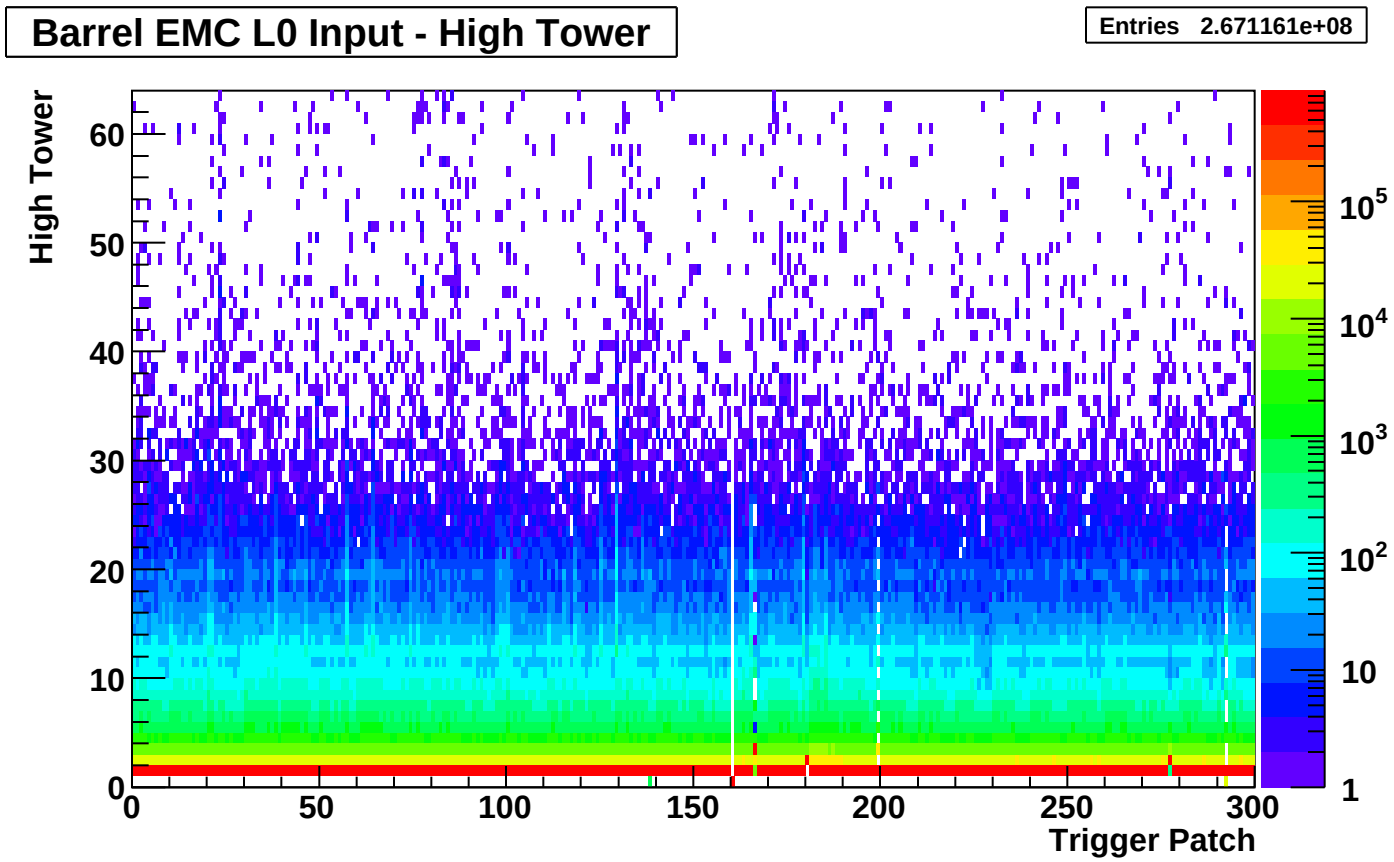

Last DSM Ch3 Input
Entries 1.33558e+07
LD301 Ch3
1
0
98655 38243 23547 4923 8158 832 241218 56257 177890 41302 42743 10870 34453 30707 3789
791732 852144 866840 885464 882229 889555 649169 834130 712497 849085 847644 879517 855934 859680 885598
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
10^5
10^4
10^3

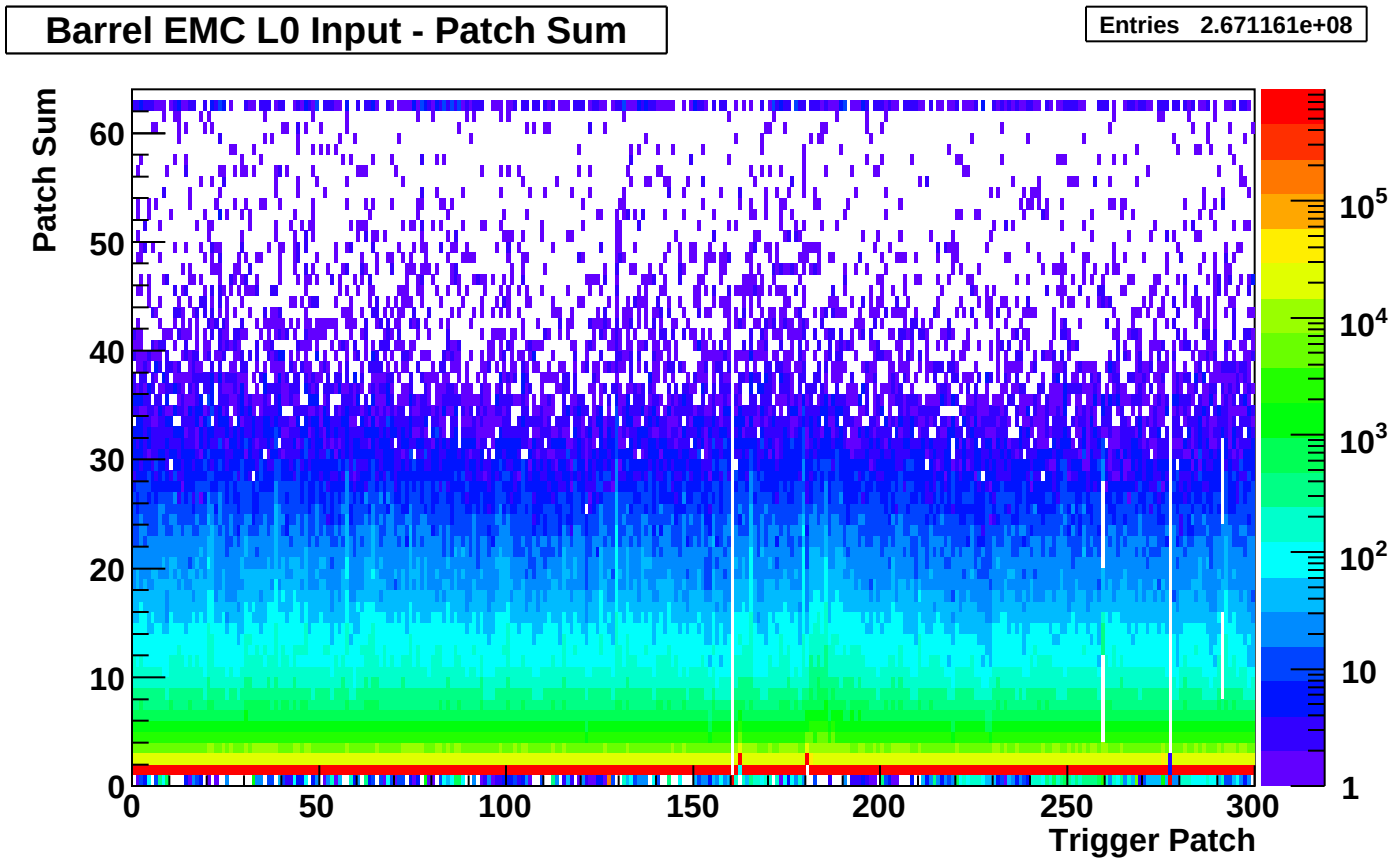

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

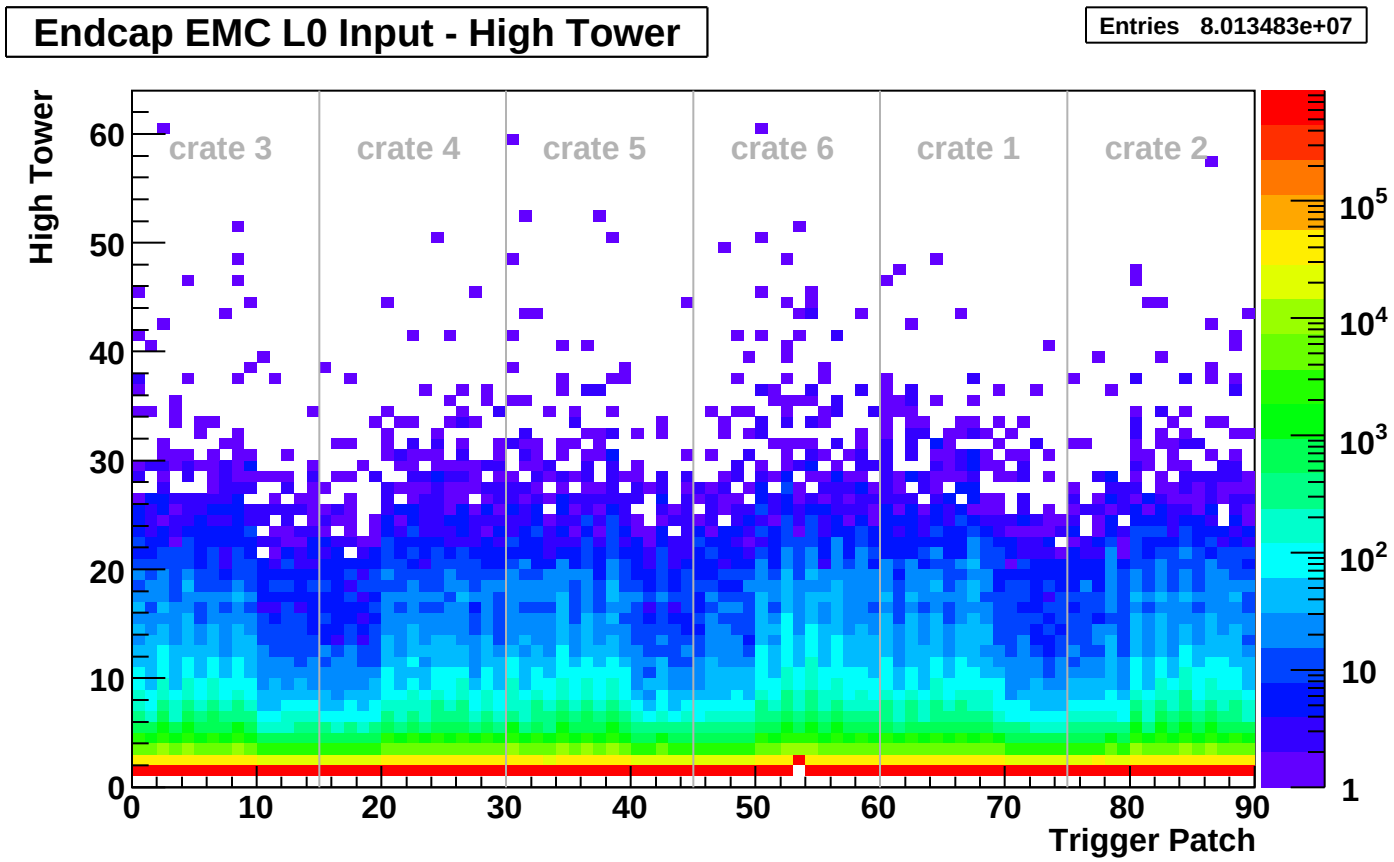

TCU bits
Entries 1.424619e+07
TCU bits
1
0
23456 92766 83959 38244 477073 8158 241224 3482 424729 1902 500 536 150524 0 20761 2670
866931 797621 806428 852143 413314 882229 649163 886905 469558 888485 888887 889851 739863 890387 869626 887717
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10^5
10^4
10^3
10^2
10
1

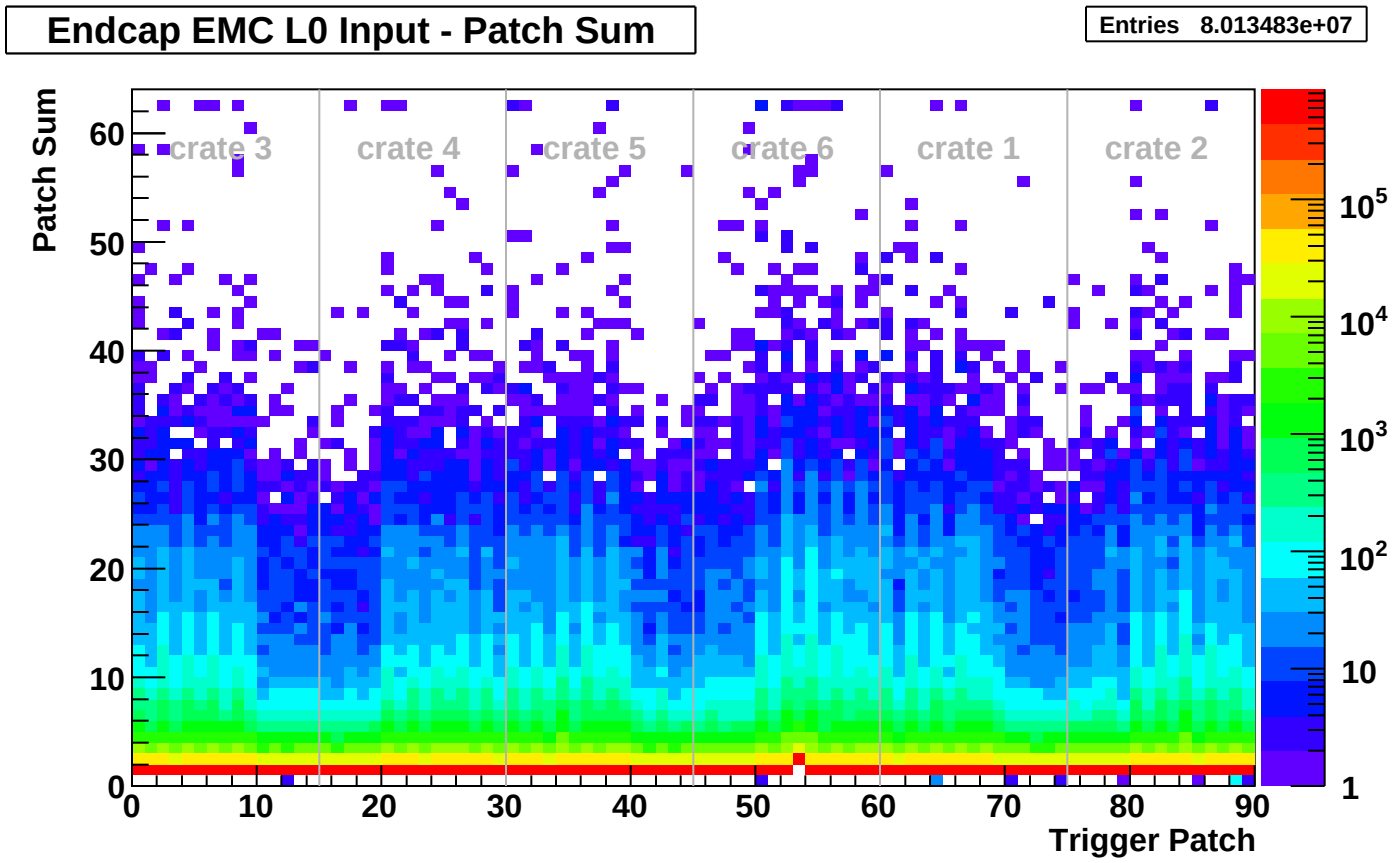

TCU bits
Entries 13
TCU bits - Simulated
1
0
-1
-2
0 4 0 1 1 0 6 0 0 0 0 0 1 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1
10
10

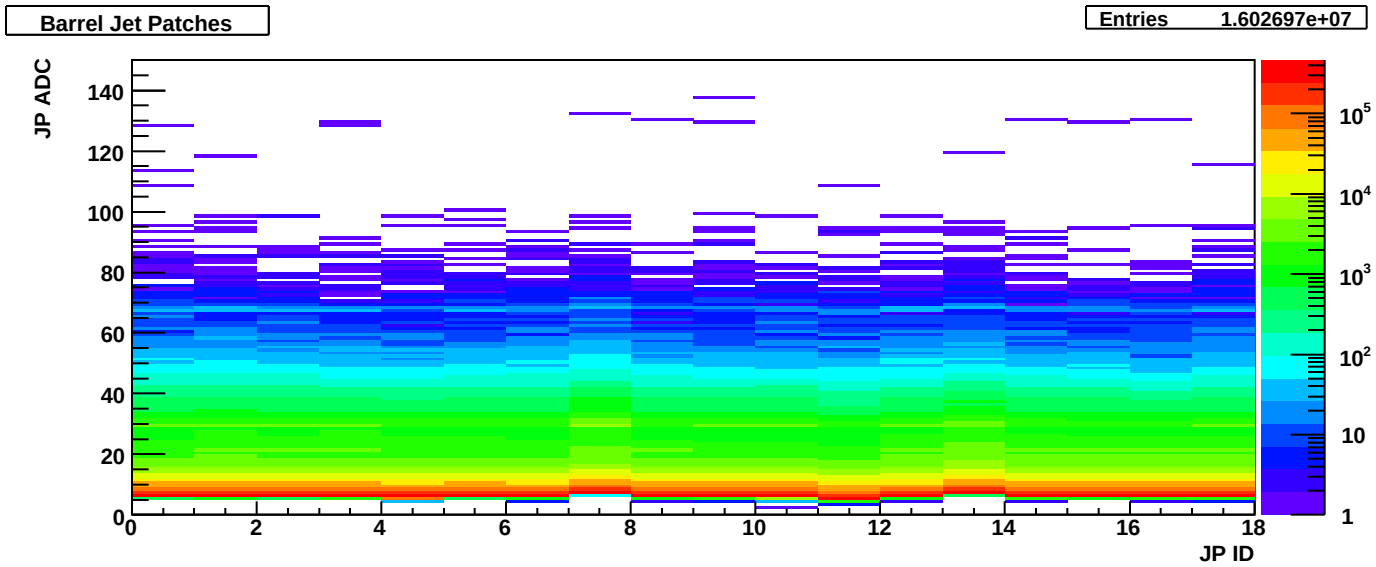

BunchId7Bit (JP1)
Entries 241218
10^3
10^2
10
0
20
40
60
80
100
120

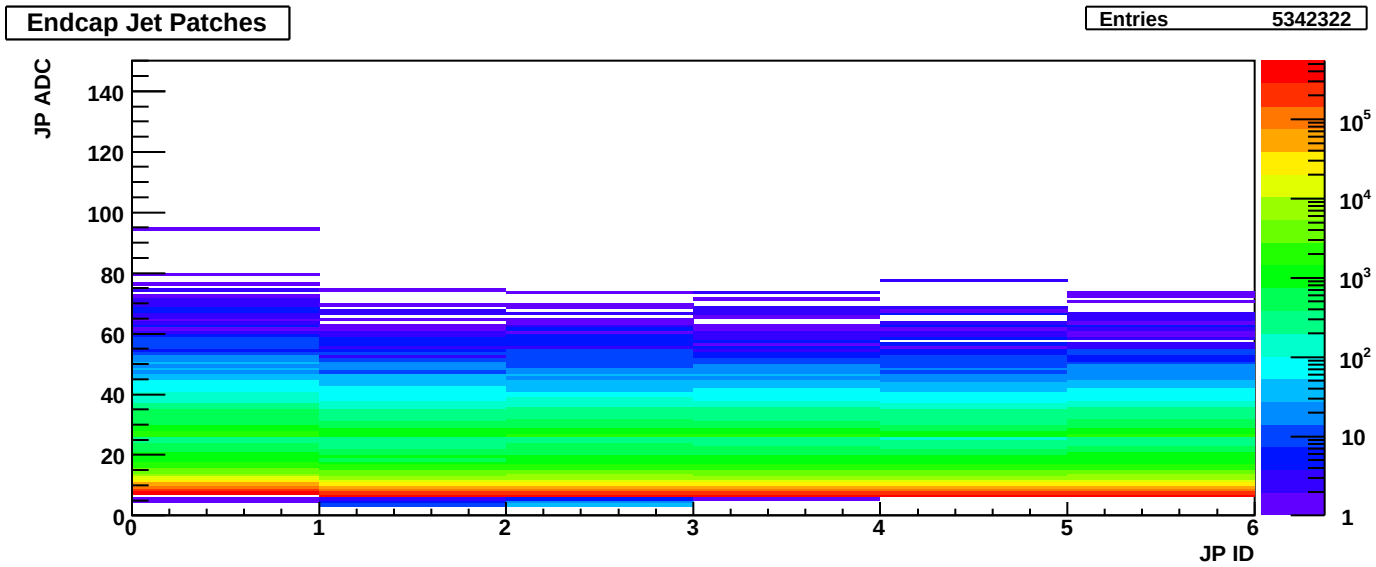

Barrel EMC L0 Input - High Tower
Entries 2.671161e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

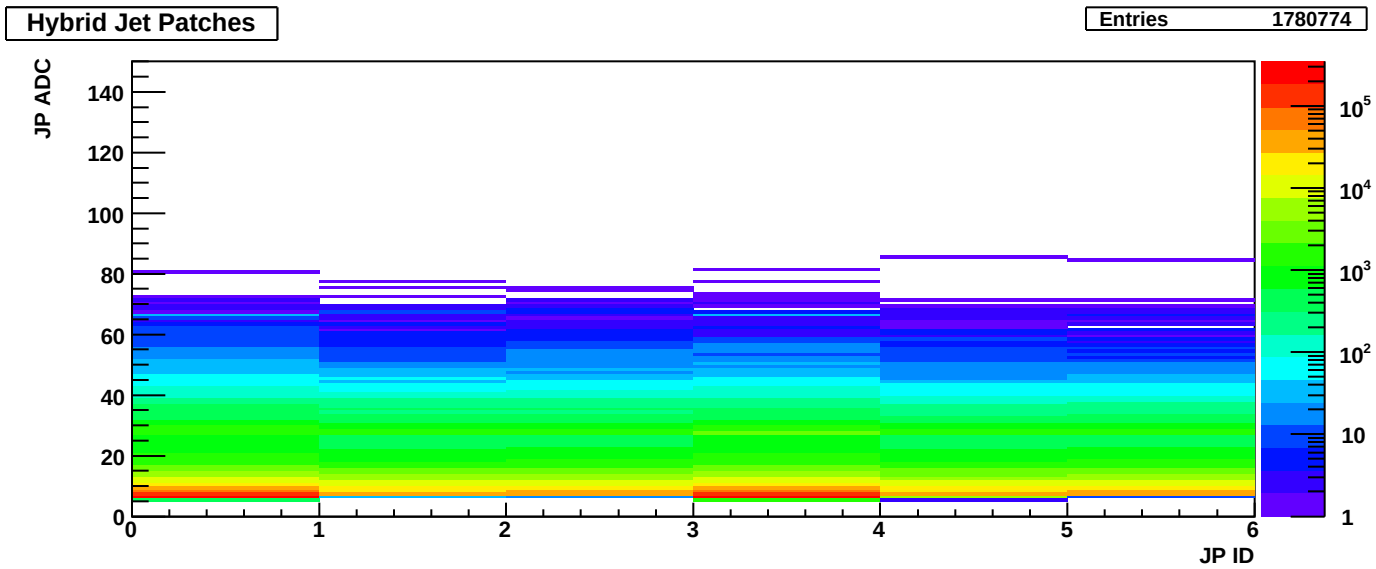

Barrel EMC L0 Input - Patch Sum
Entries 2.671161e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

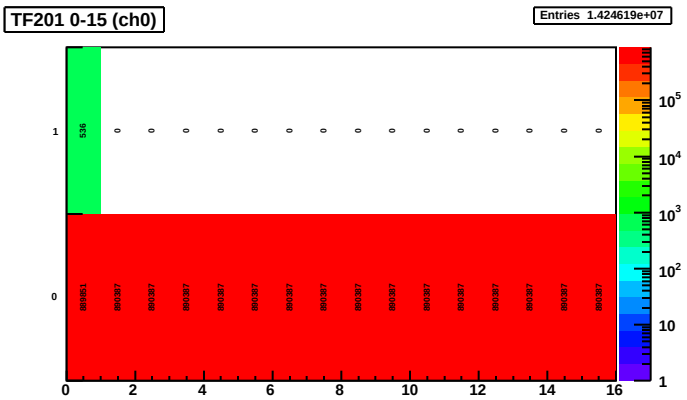

Endcap EMC L0 Input - High Tower
Entries 8.013483e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

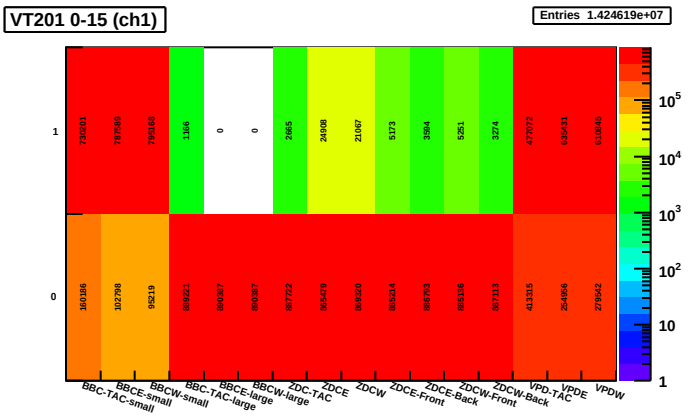

Endcap EMC L0 Input - Patch Sum
Entries 8.013483e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

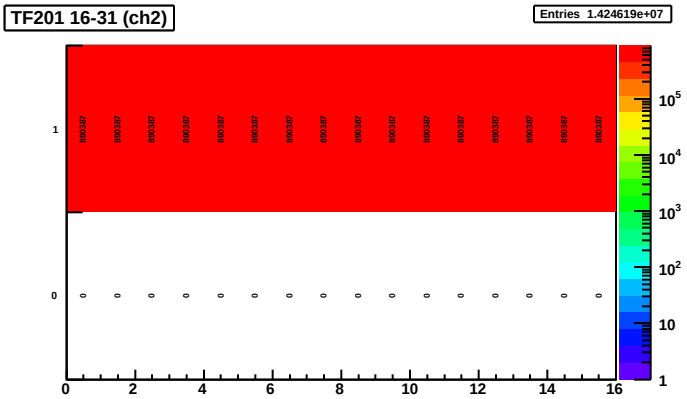

Barrel Jet Patches
Entries 1.602697e+07
JP ADC
JP ID

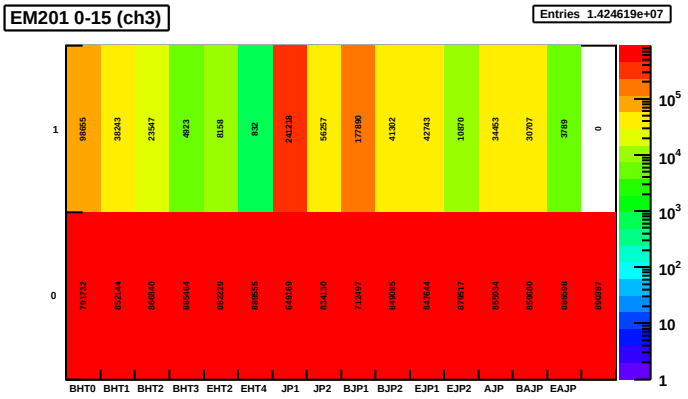

Endcap Jet Patches
Entries 5342322
JP ADC
JP ID

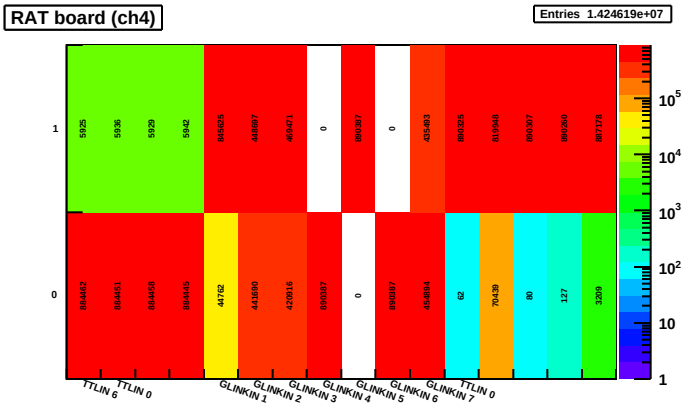

Hybrid Jet Patches
Entries 1780774
JP ADC
JP ID

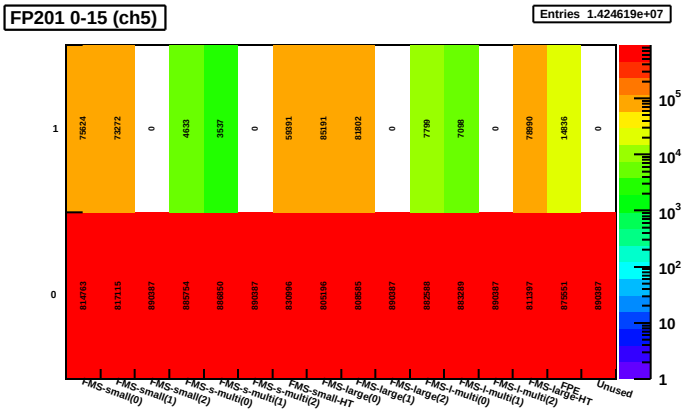

TF201 0-15 (ch0)
Entries 1.424619e+07

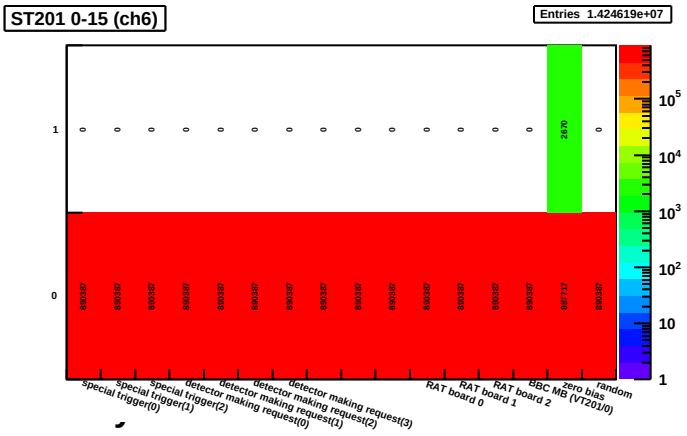

VT201 0-15 (ch1)
Entries 1.424619e+07
BBC-TAC-small
BBCE-small
BBCW-small
BBC-TAC-large
BBCE-large
BBCW-large
ZDC-TAC
ZDCE
ZDCW
ZDCE-Front
ZDCE-Back
ZDCW-Front
ZDCW-Back
VPD-TAC
VPDE
VPDW

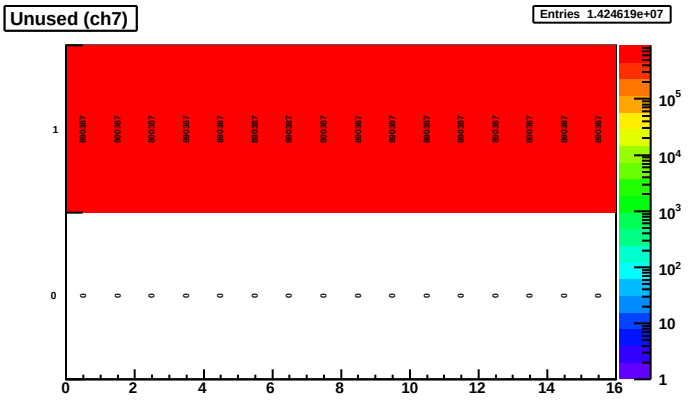

TF201 16-31 (ch2)
Entries 1.424619e+07


EM201 0-15 (ch3)
Entries 1.424619e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP


RAT board (ch4)
Entries 1.424619e+07
TTLIN 6
TTLIN 0
GLINKIN 1
GLINKIN 2
GLINKIN 3
GLINKIN 4
GLINKIN 5
GLINKIN 6
GLINKIN 7
TTLIN 0


FP201 0-15 (ch5)
Entries 1.424619e+07
FMS-small(0)
FMS-small(1)
FMS-small(2)
FMS-s-multi(0)
FMS-s-multi(1)
FMS-s-multi(2)
FMS-small-HT
FMS-large(0)
FMS-large(1)
FMS-large(2)
FMS-l-multi(0)
FMS-l-multi(1)
FMS-l-multi(2)
FMS-large-HT
FPE
Unused


ST201 0-15 (ch6)
Entries 1.424619e+07
Special trigger(0)
Special trigger(1)
Special trigger(2)
detector making request(0)
detector making request(1)
detector making request(2)
detector making request(3)
RAT board 0
RAT board 1
RAT board 2
BBC MB (VT201/0)
zero bias
random


Unused (ch7)
Entries 1.424619e+07