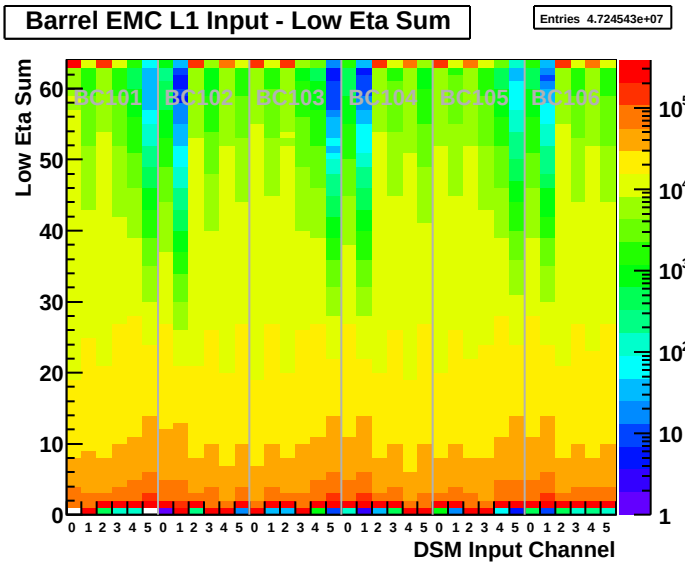
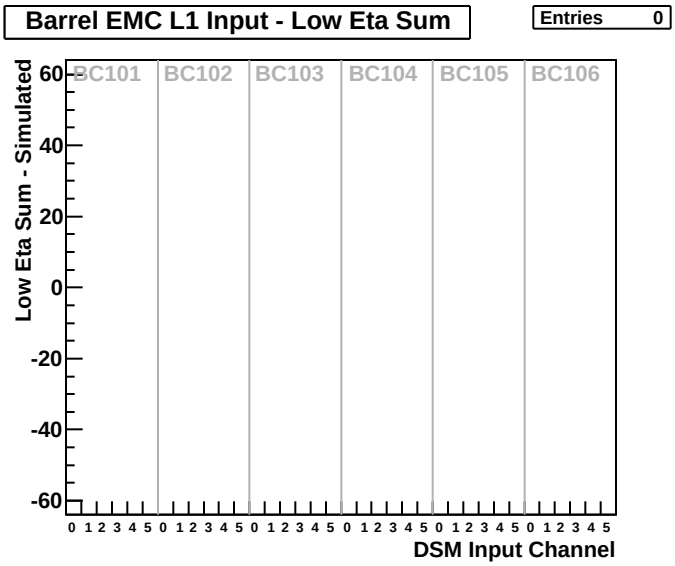
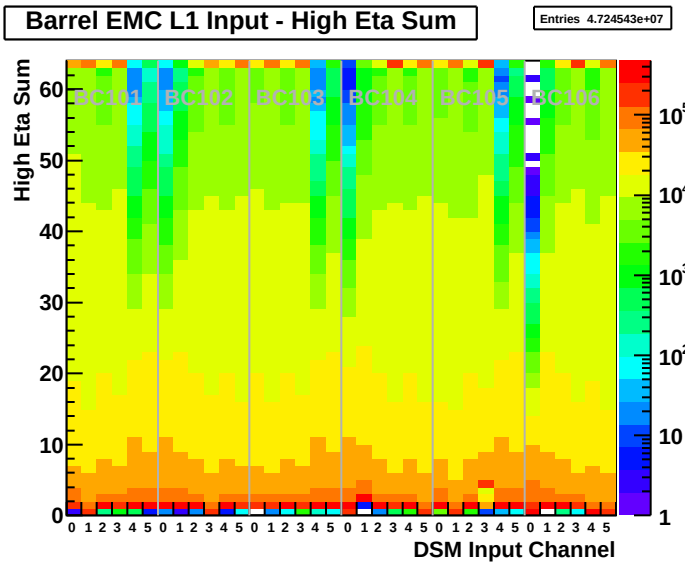
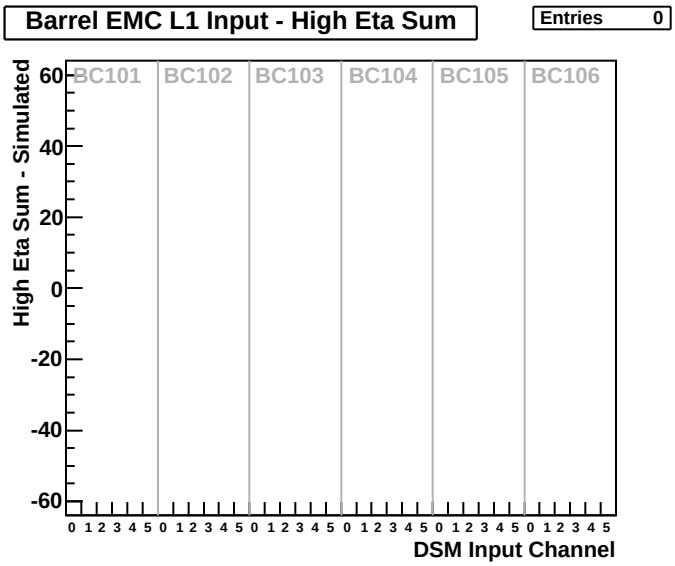
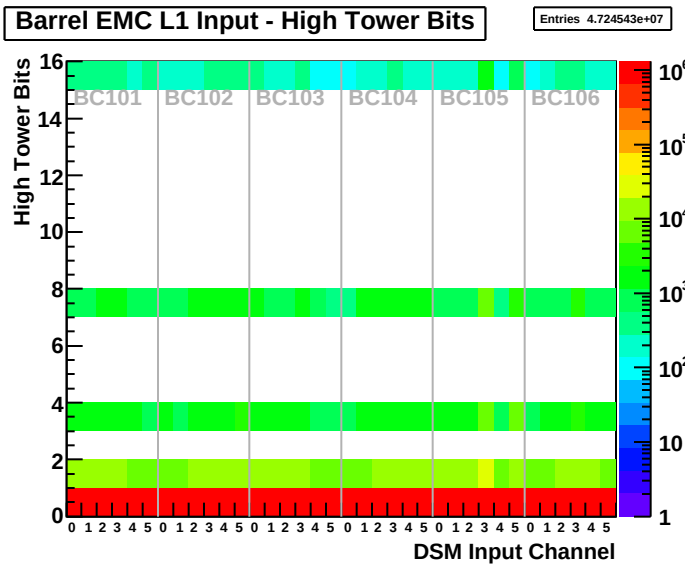
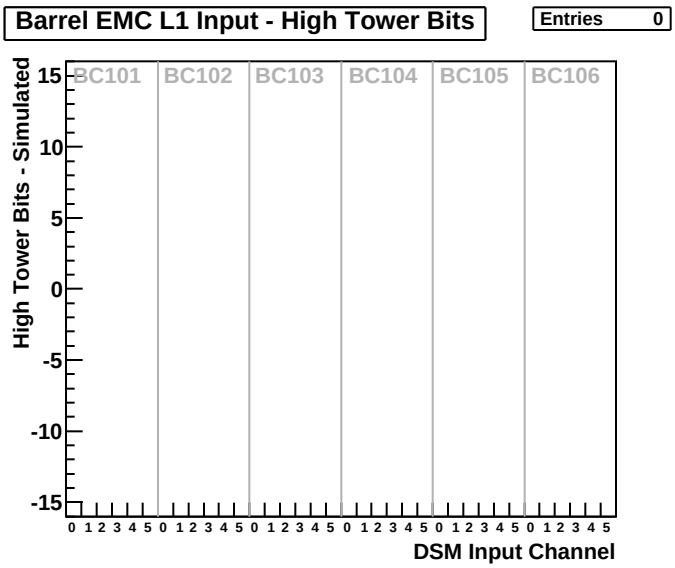

Barrel EMC L1 Input - Low Eta Sum
Entries 4.724543e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

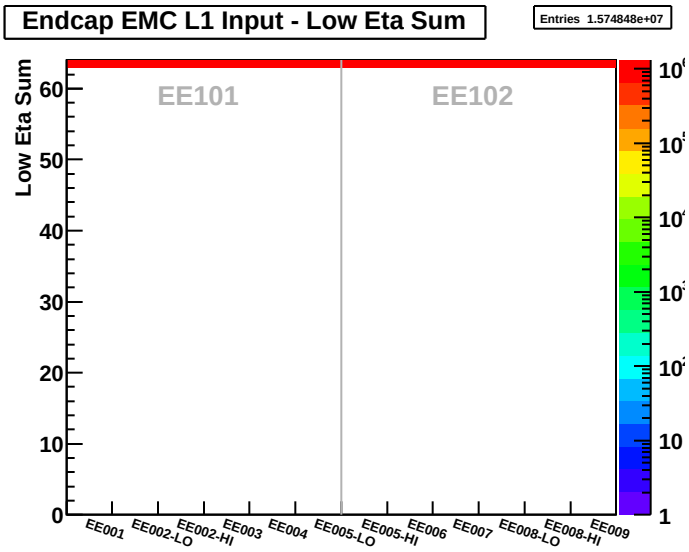

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

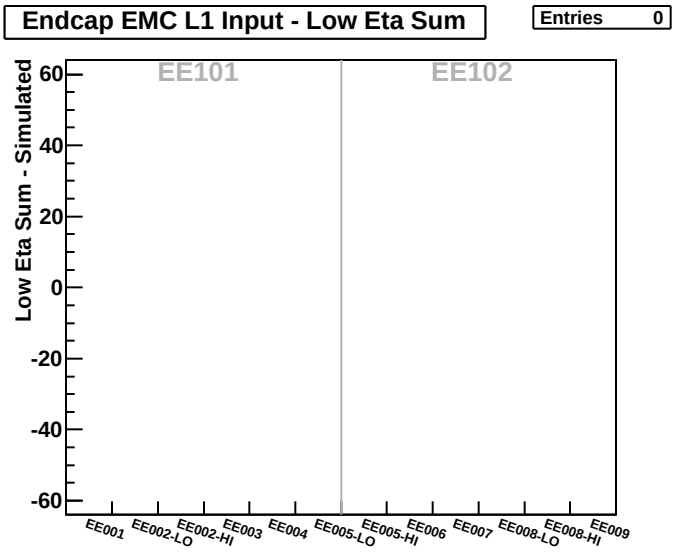

Barrel EMC L1 Input - High Eta Sum
Entries 4.724543e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

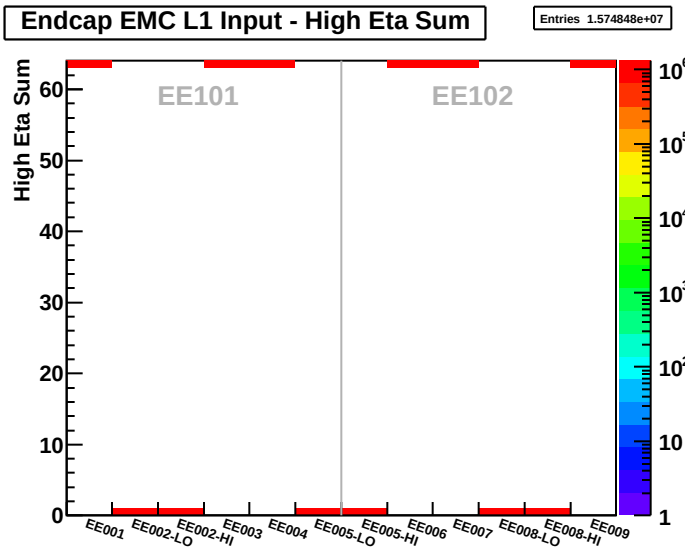

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

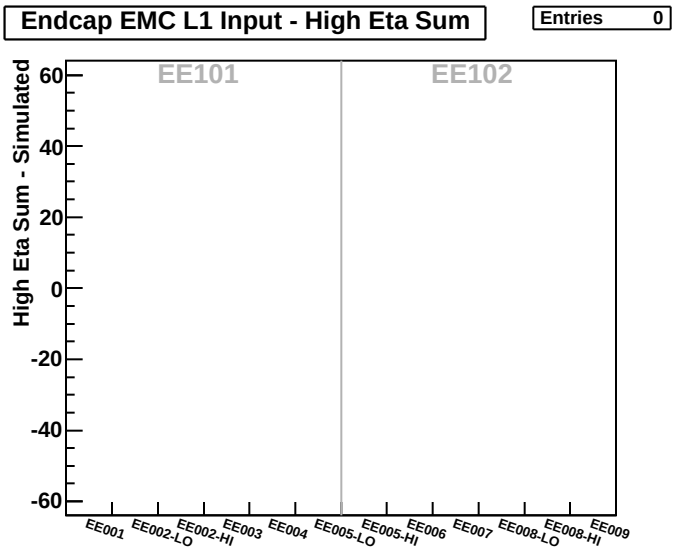

Barrel EMC L1 Input - High Tower Bits
Entries 4.724543e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

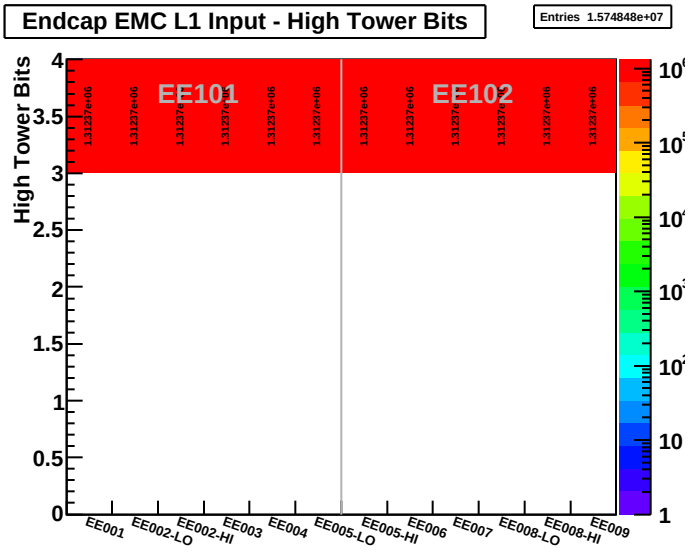

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

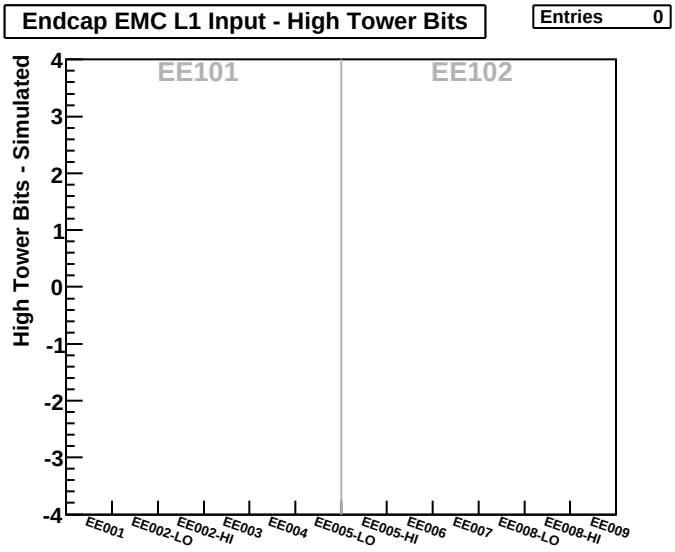

Endcap EMC L1 Input - Low Eta Sum
Entries 1.574848e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

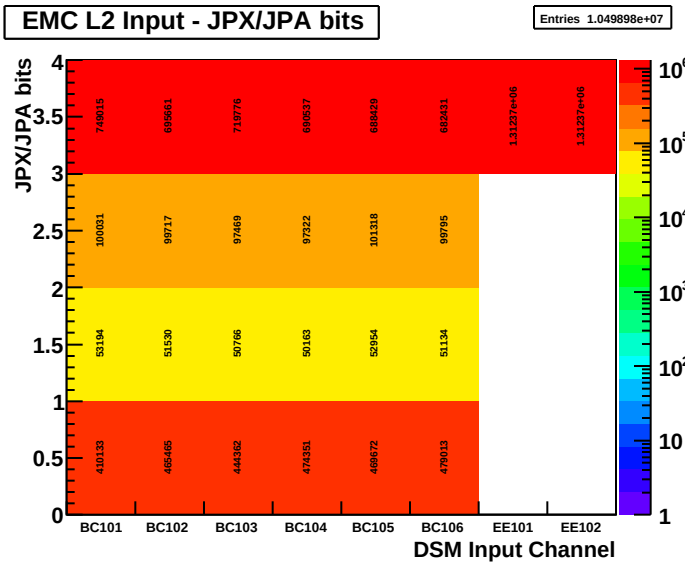

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

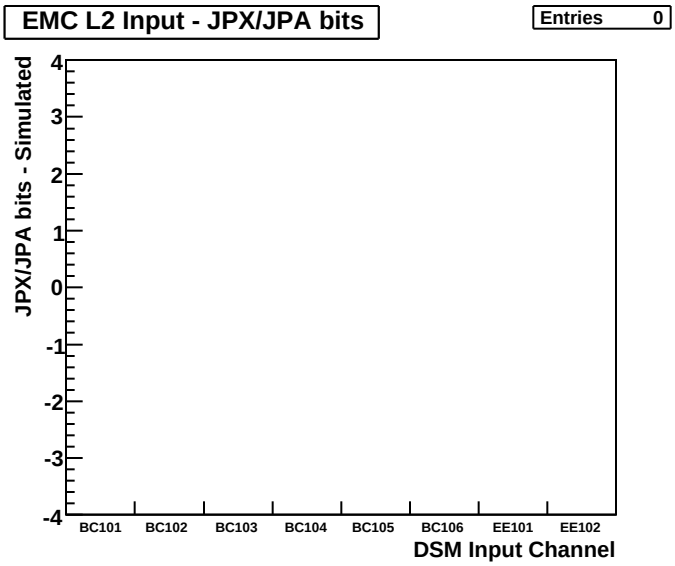

Endcap EMC L1 Input - High Eta Sum
Entries 1.574848e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

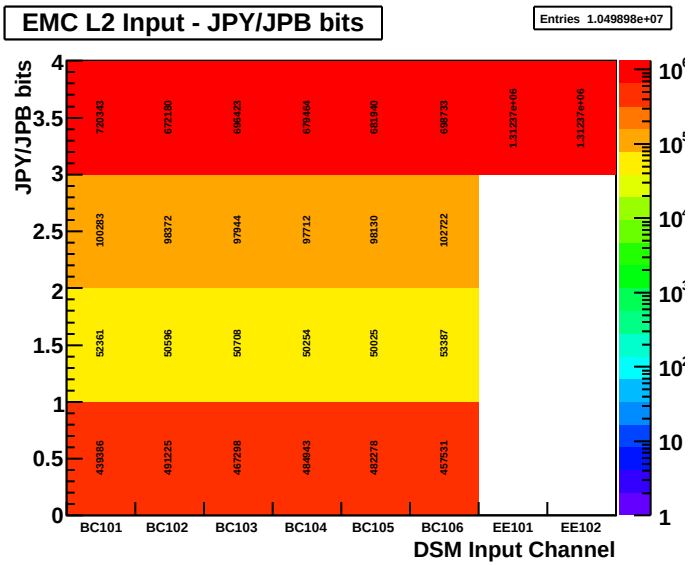

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

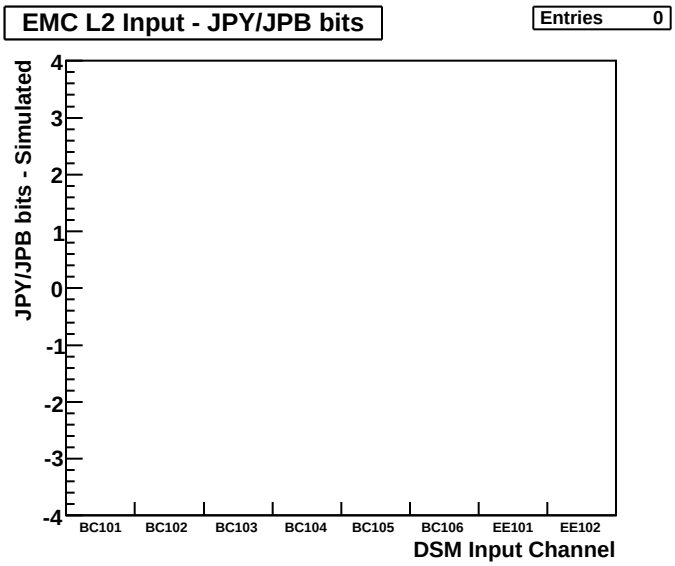

Endcap EMC L1 Input - High Tower Bits
Entries 1.574848e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

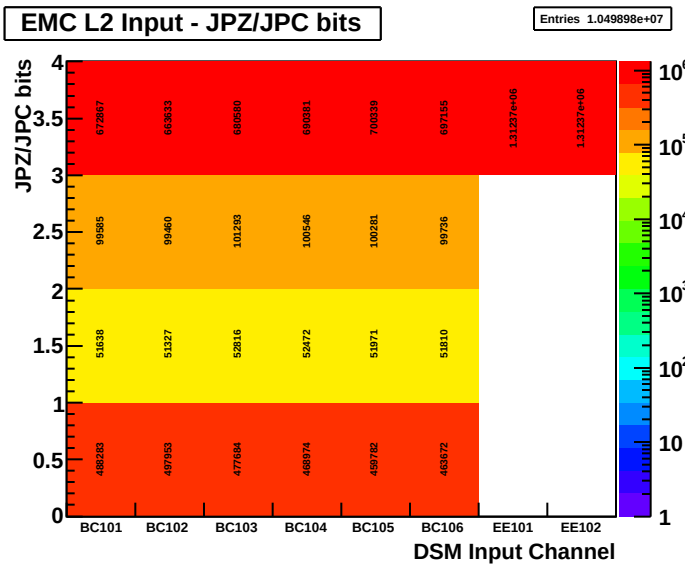

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

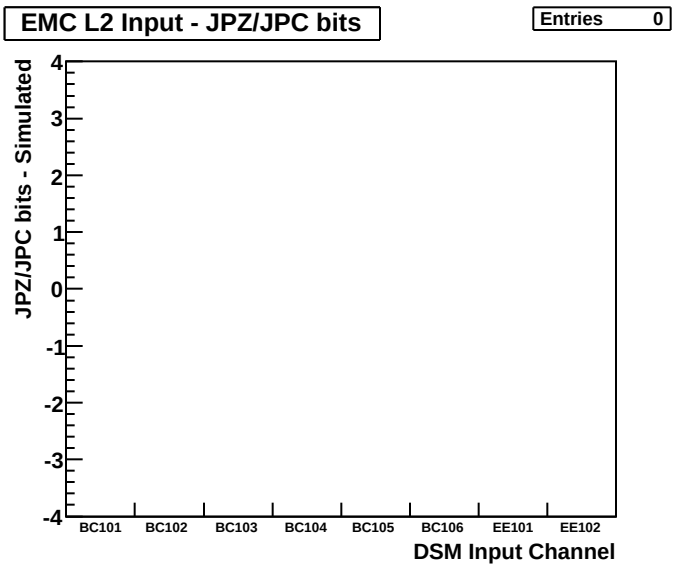

EMC L2 Input - JPX/JPA bits
Entries 1.049898e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
749015 695661 719776 690537 688429 682431 1.31237e+06 1.31237e+06
100031 99717 97469 97322 101318 99795
53194 51530 50766 50163 52954 51134
410133 465465 444362 474351 469672 479013

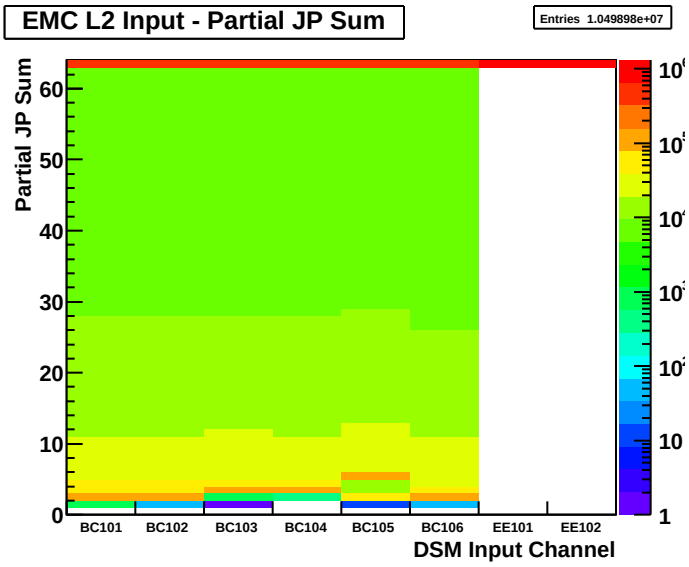

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

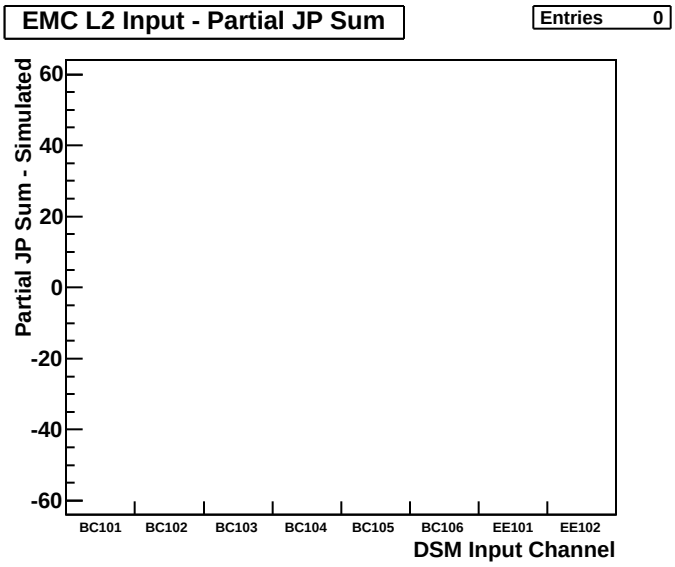

EMC L2 Input - JPY/JPB bits
Entries 1.049898e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
720343 672180 696423 679464 681940 698733 1.31237e+06 1.31237e+06
100283 98372 97944 97712 98130 102722
52361 50596 50708 50254 50025 53387
439386 491225 467298 484943 482278 457531

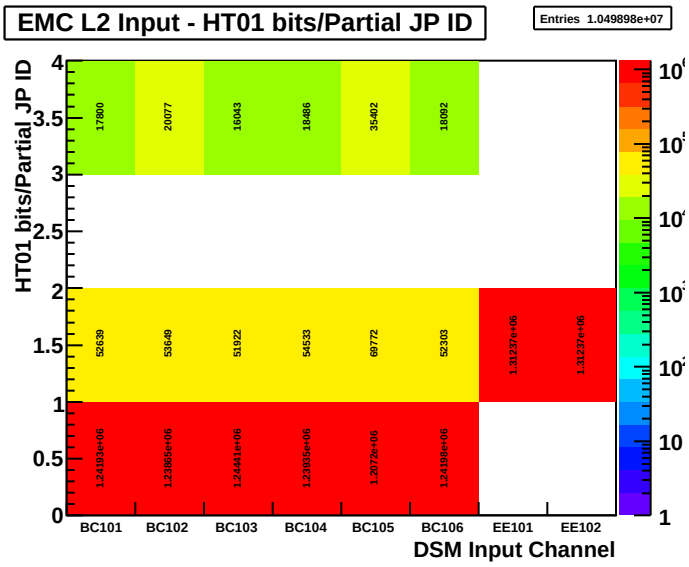

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

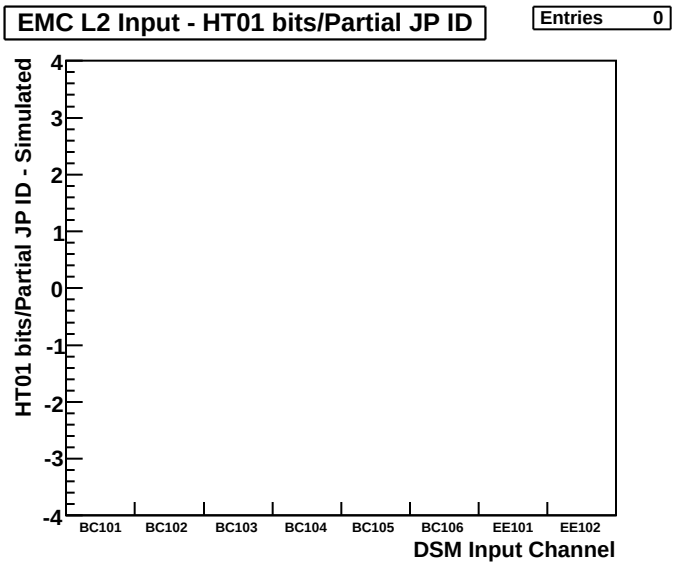

EMC L2 Input - JPZ/JPC bits
Entries 1.049898e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
672867 663633 680580 690381 700339 697155 1.31237e+06 1.31237e+06
99585 99460 101293 100546 100281 99736
51638 51327 52816 52472 51971 51810
488283 497953 477684 468974 459782 463672

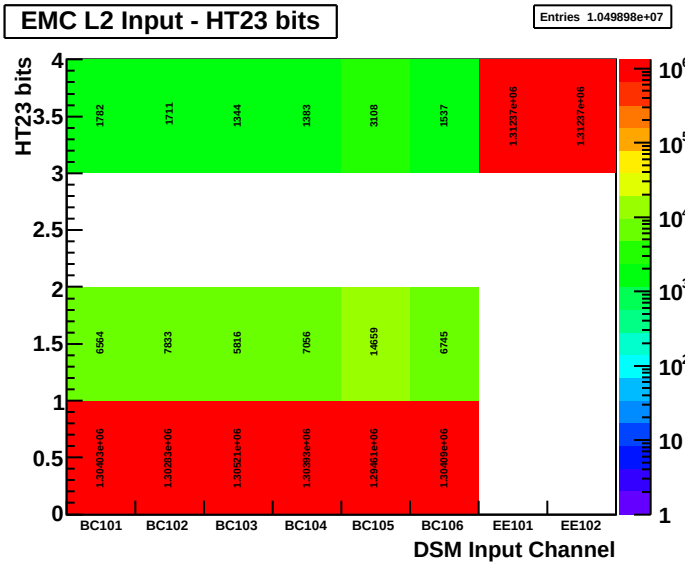

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

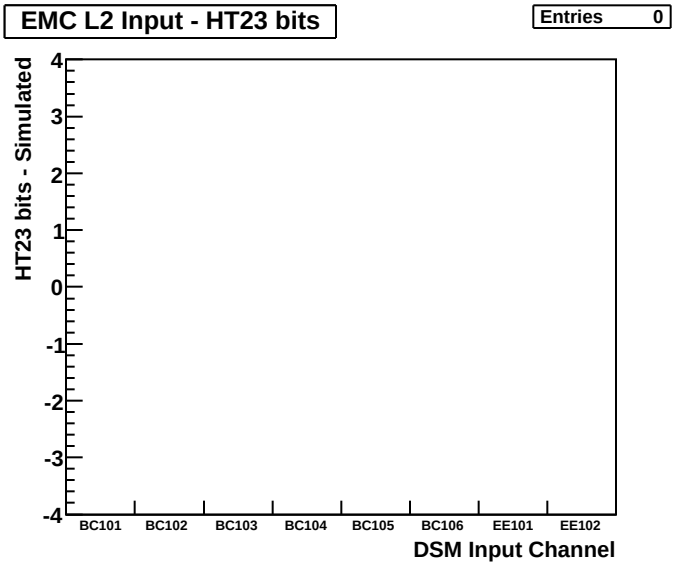

EMC L2 Input - Partial JP Sum
Entries 1.049898e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

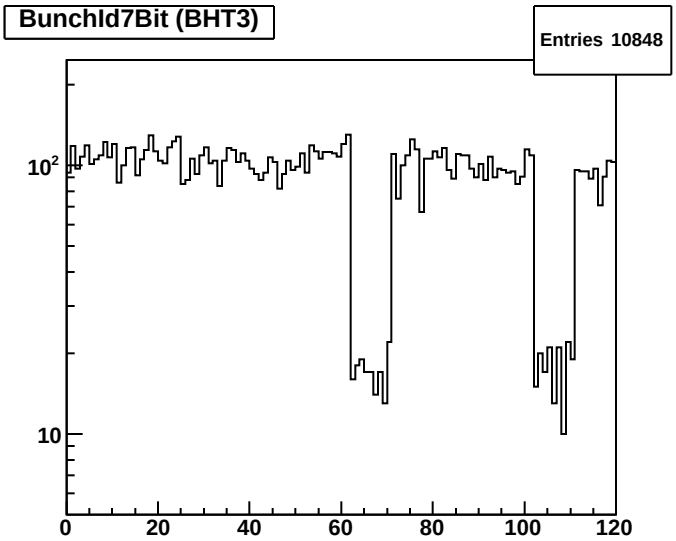

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

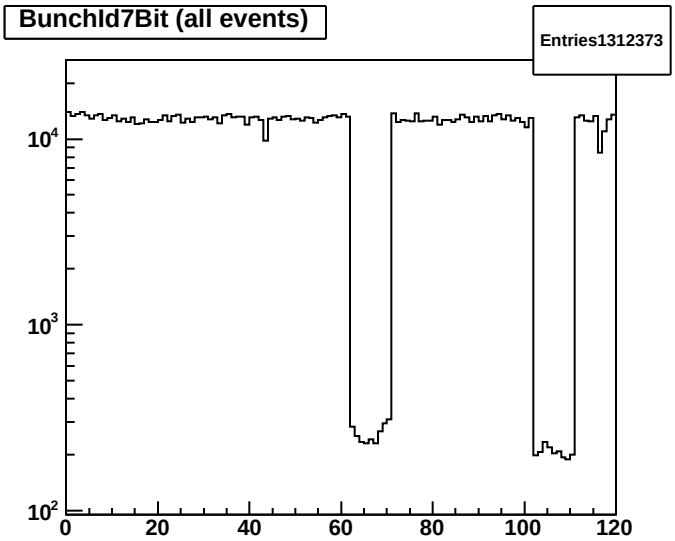

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.049898e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

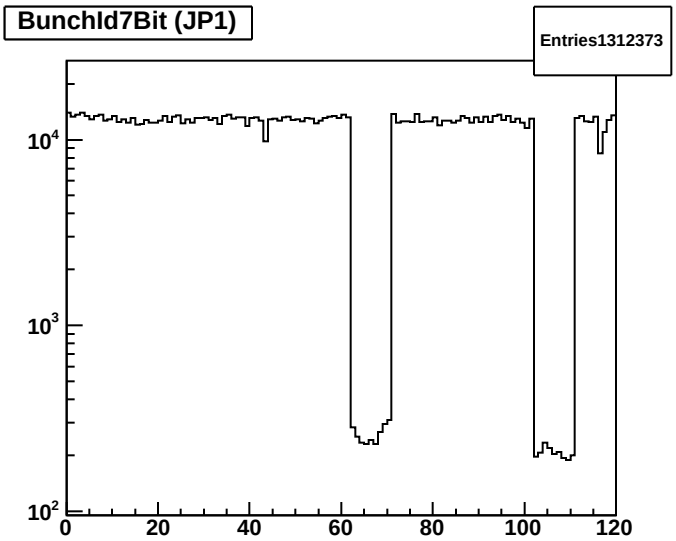

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

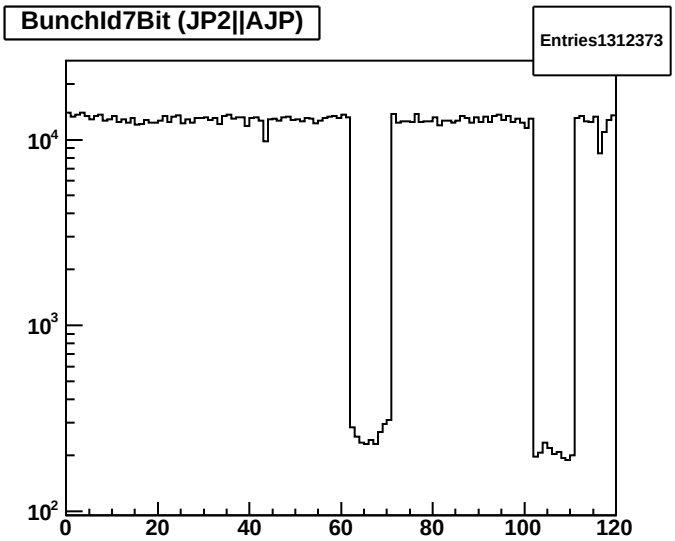

EMC L2 Input - HT23 bits
Entries 1.049898e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

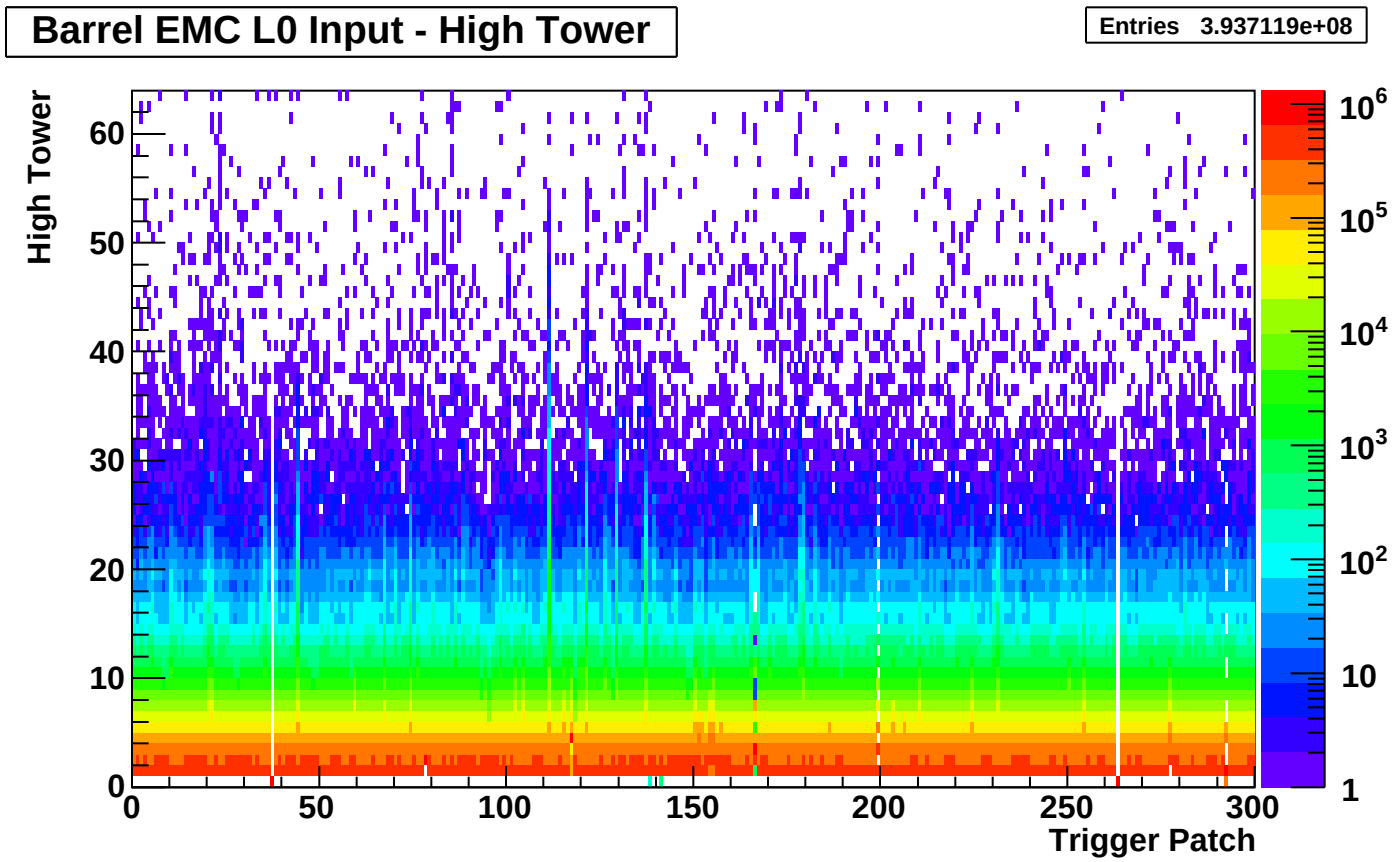

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

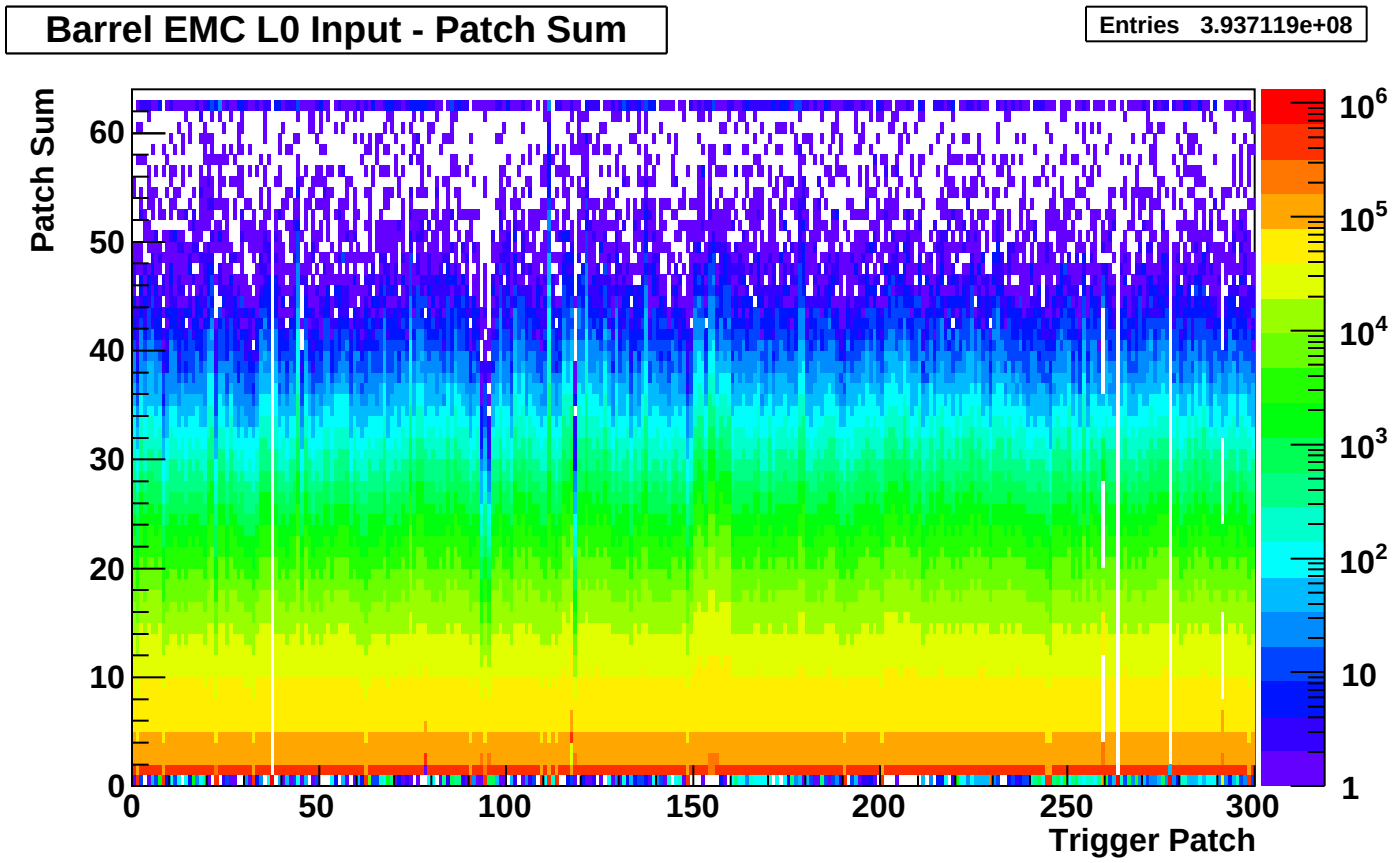

BunchId7Bit (BHT3)
Entries 10848
10^2
10
0
20
40
60
80
100
120

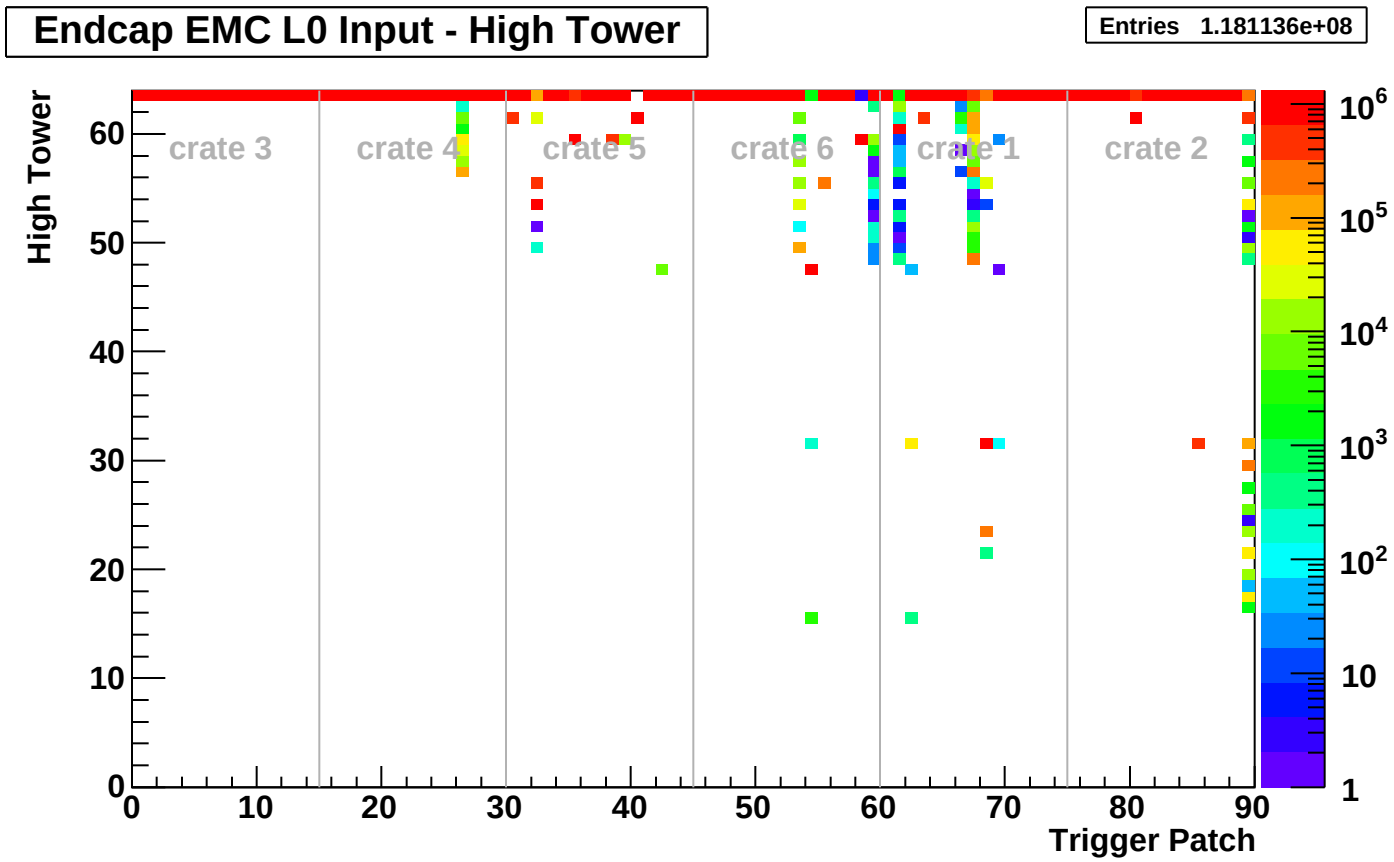

BunchId7Bit (all events)
Entries1312373
10^4
10^3
10^2
0
20
40
60
80
100
120

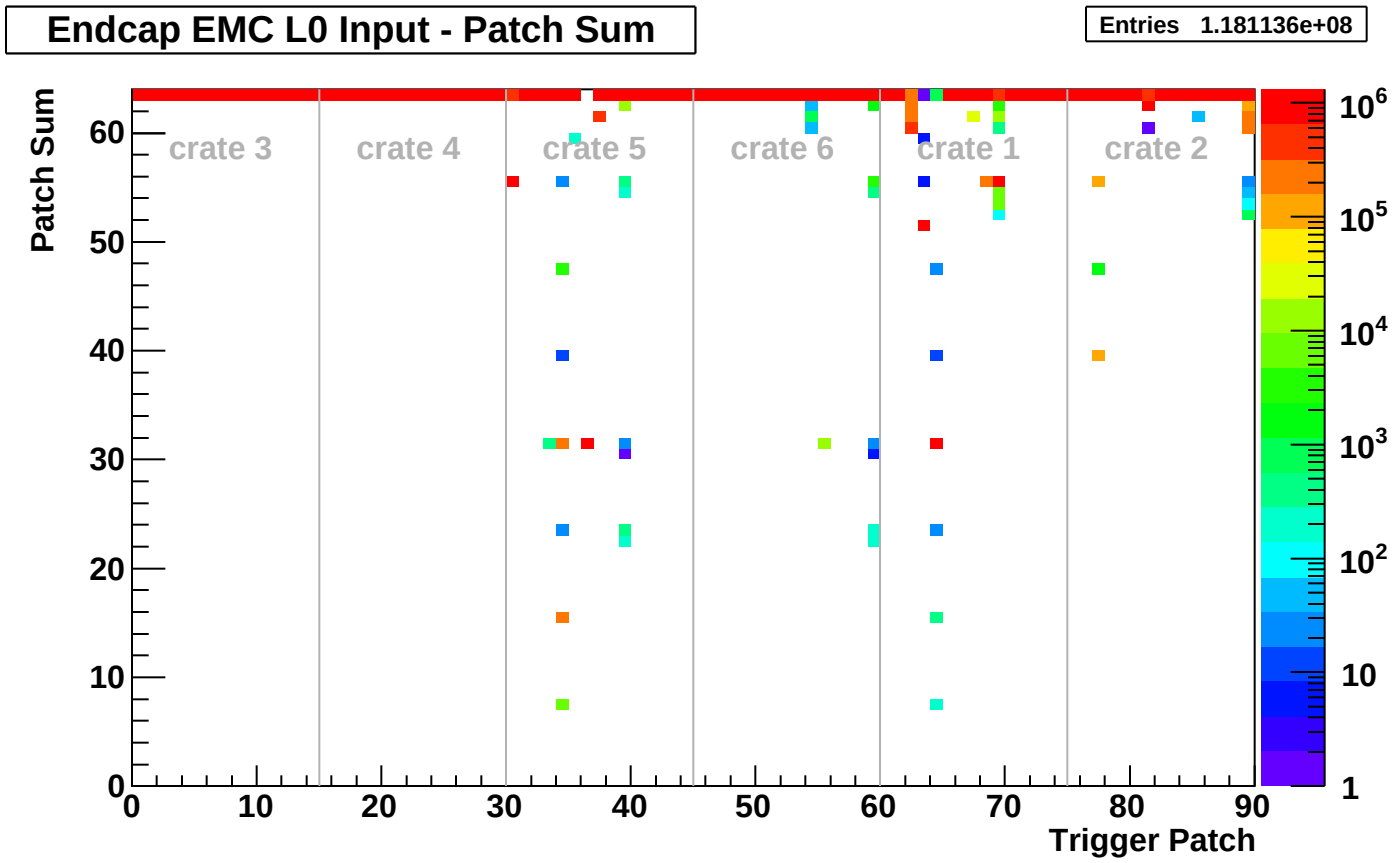

BunchId7Bit (JP1)
Entries1312373
10^4
10^3
10^2
0
20
40
60
80
100
120

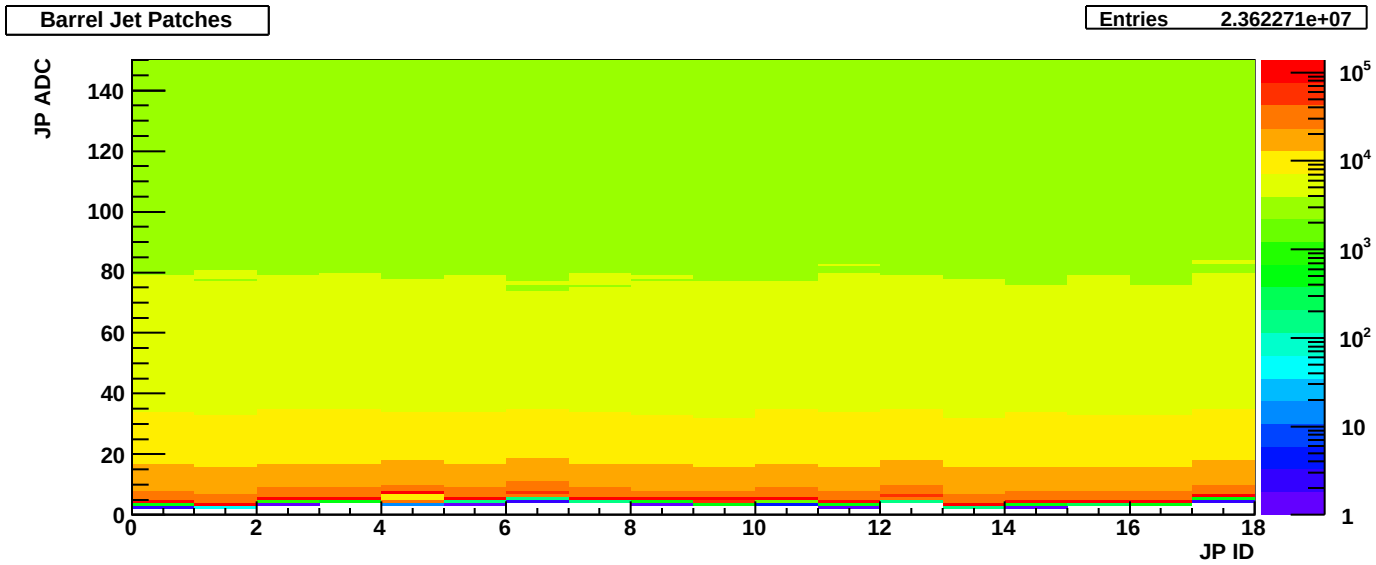

BunchId7Bit (JP2||AJP)
Entries1312373
10^4
10^3
10^2
0
20
40
60
80
100
120

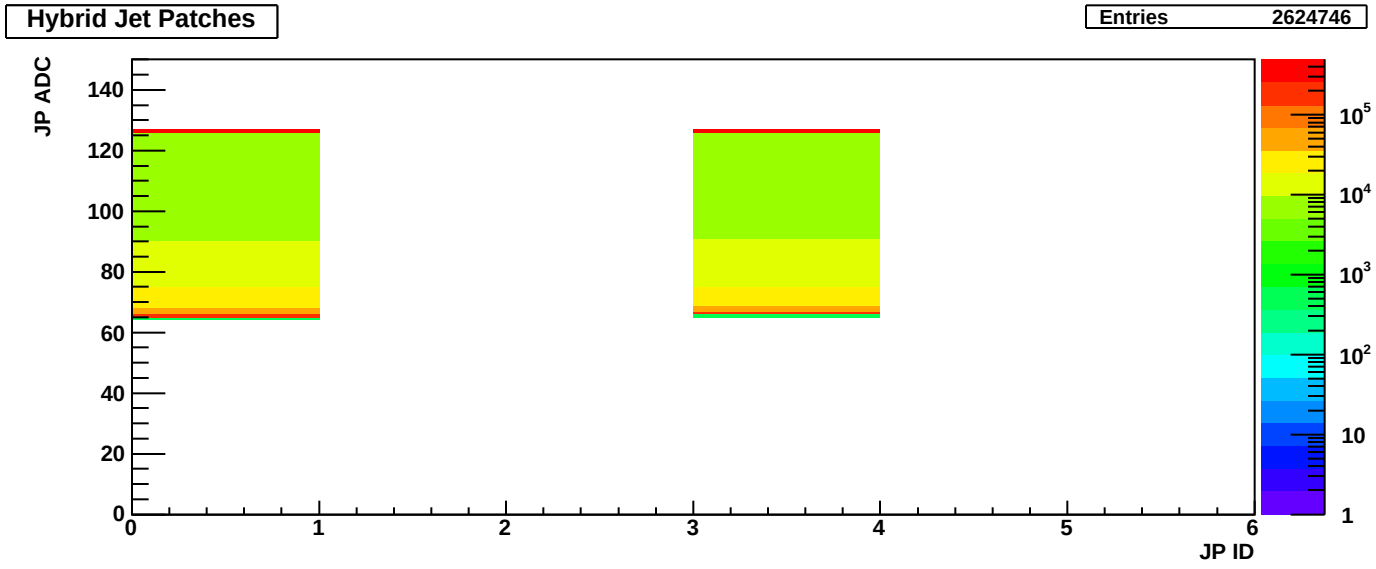

Barrel EMC L0 Input - High Tower
Entries 3.937119e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

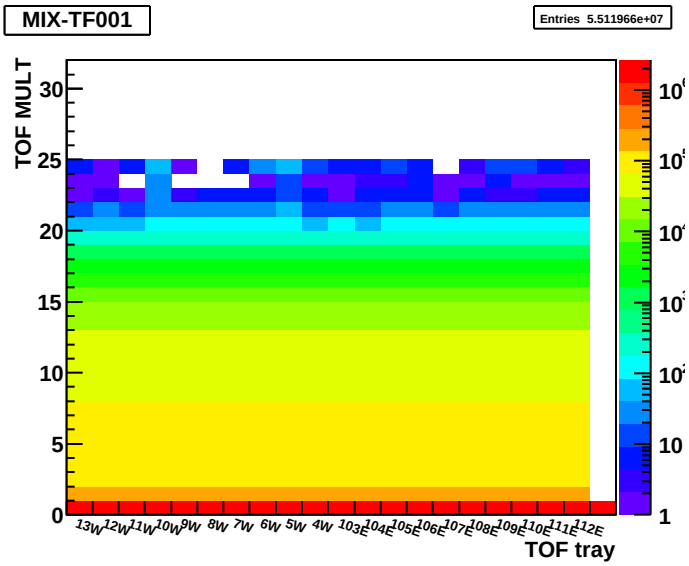

Barrel EMC L0 Input - Patch Sum
Entries 3.937119e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

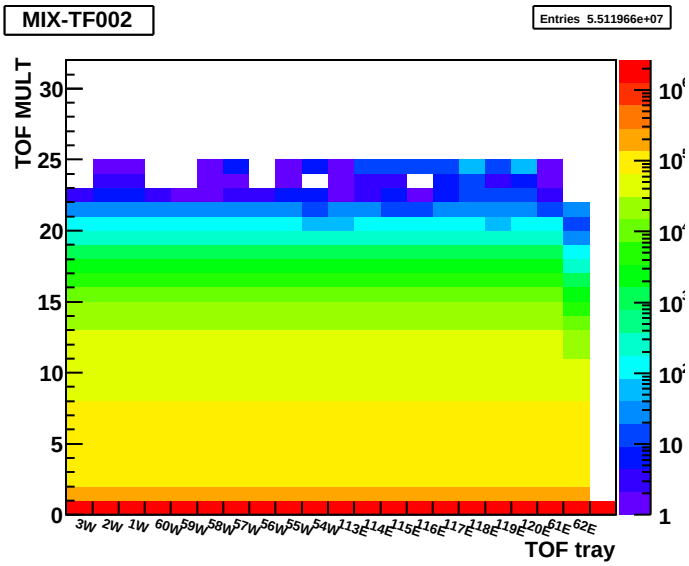

Endcap EMC L0 Input - High Tower
Entries 1.181136e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

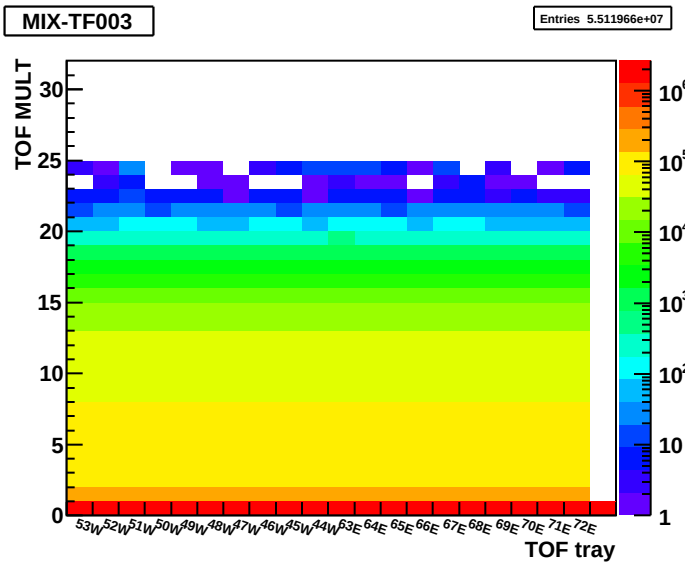

Endcap EMC L0 Input - Patch Sum
Entries 1.181136e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

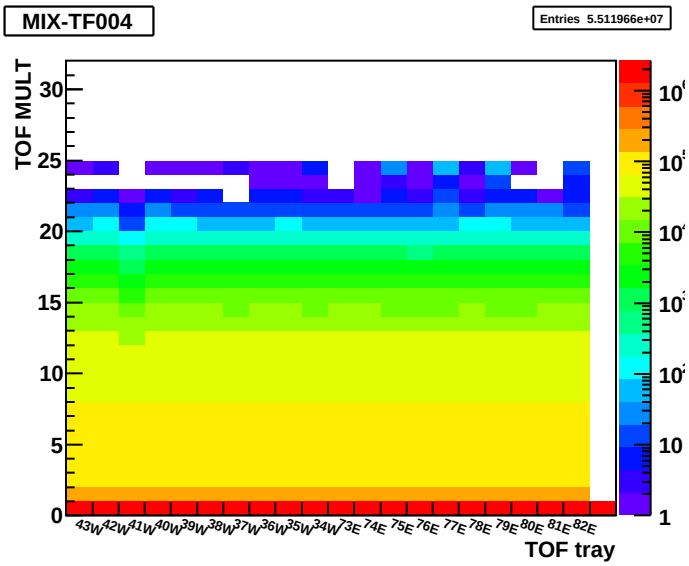

Barrel Jet Patches
Entries 2.362271e+07
JP ADC
JP ID

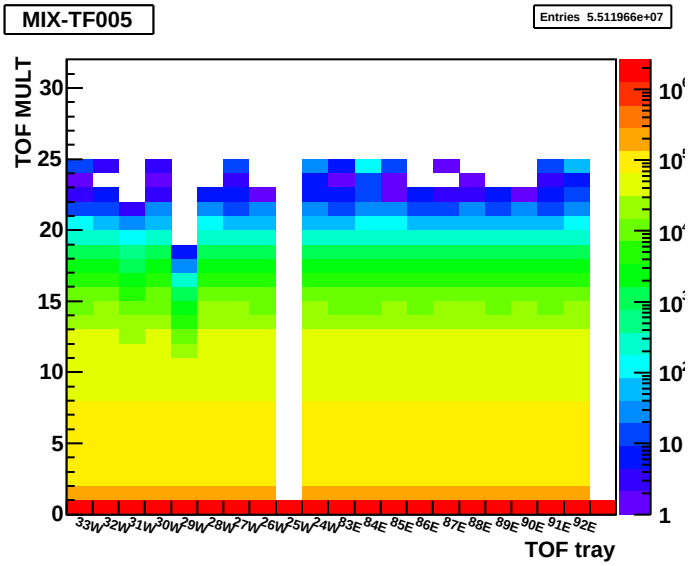

Hybrid Jet Patches
Entries 2624746
JP ADC
JP ID

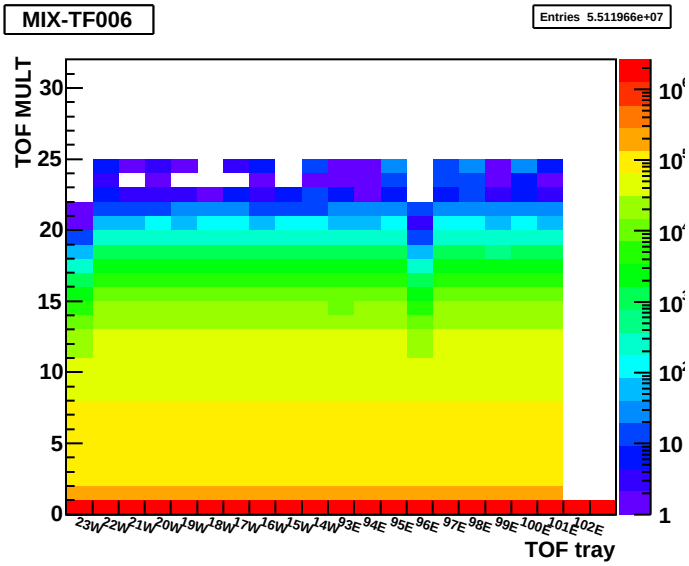

MIX-TF001
Entries 5.511966e+07
TOF MULT
TOF tray

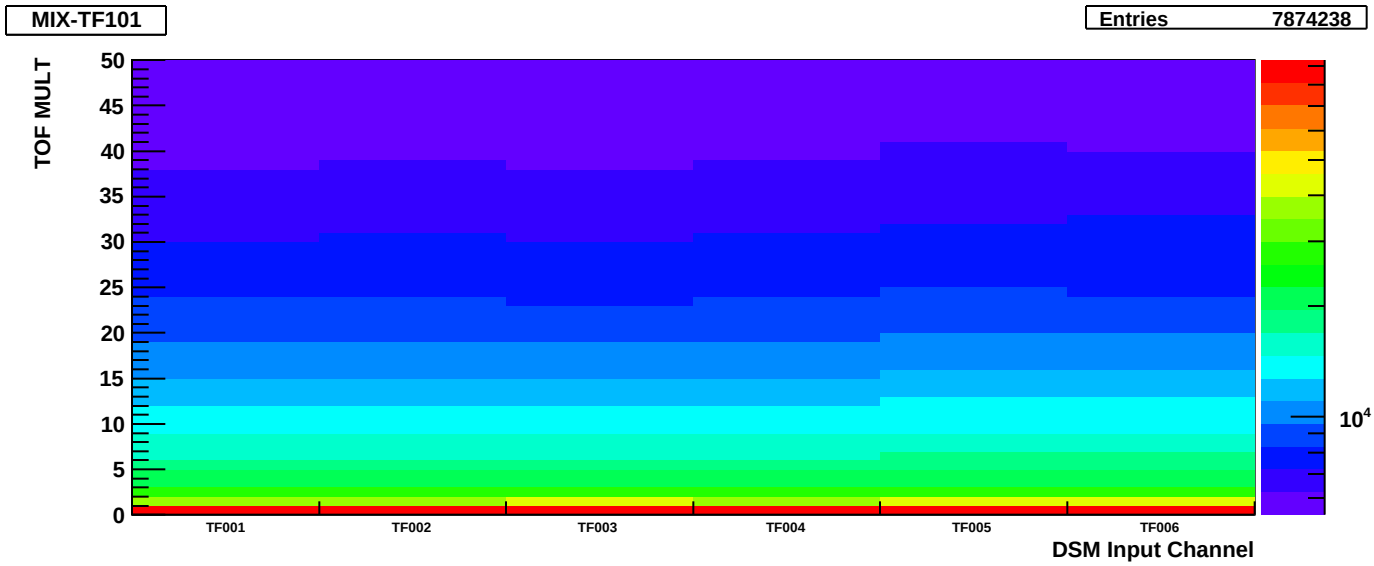

MIX-TF002
Entries 5.511966e+07
TOF MULT
TOF tray

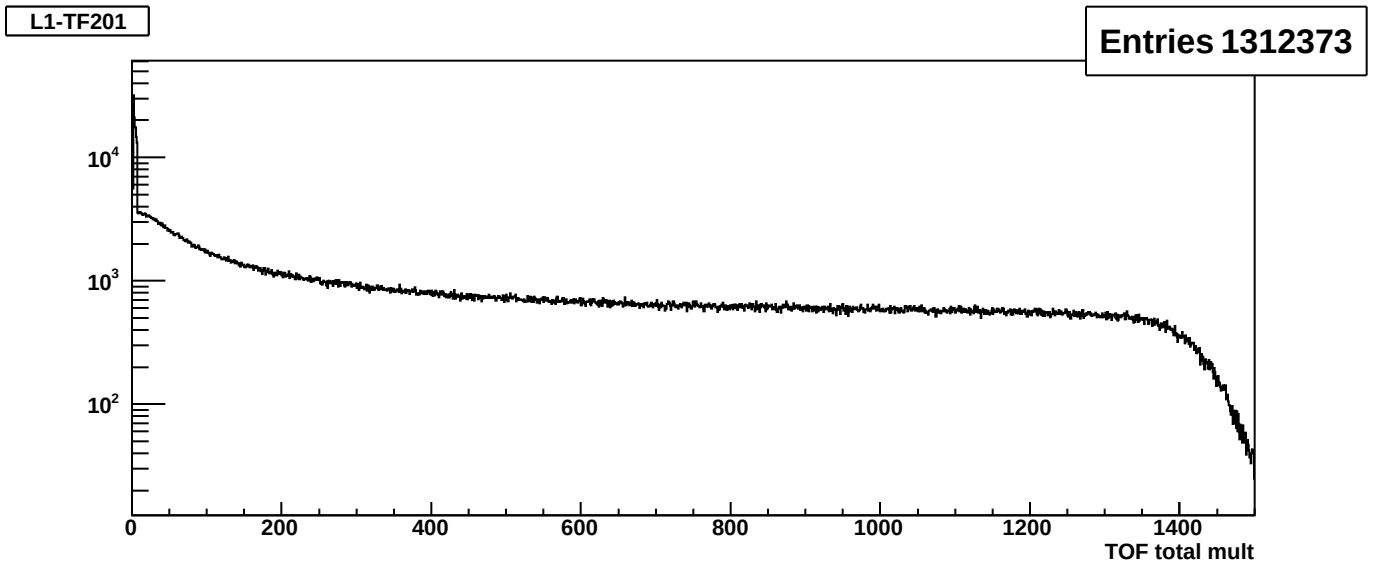

MIX-TF003
Entries 5.511966e+07
TOF MULT
TOF tray

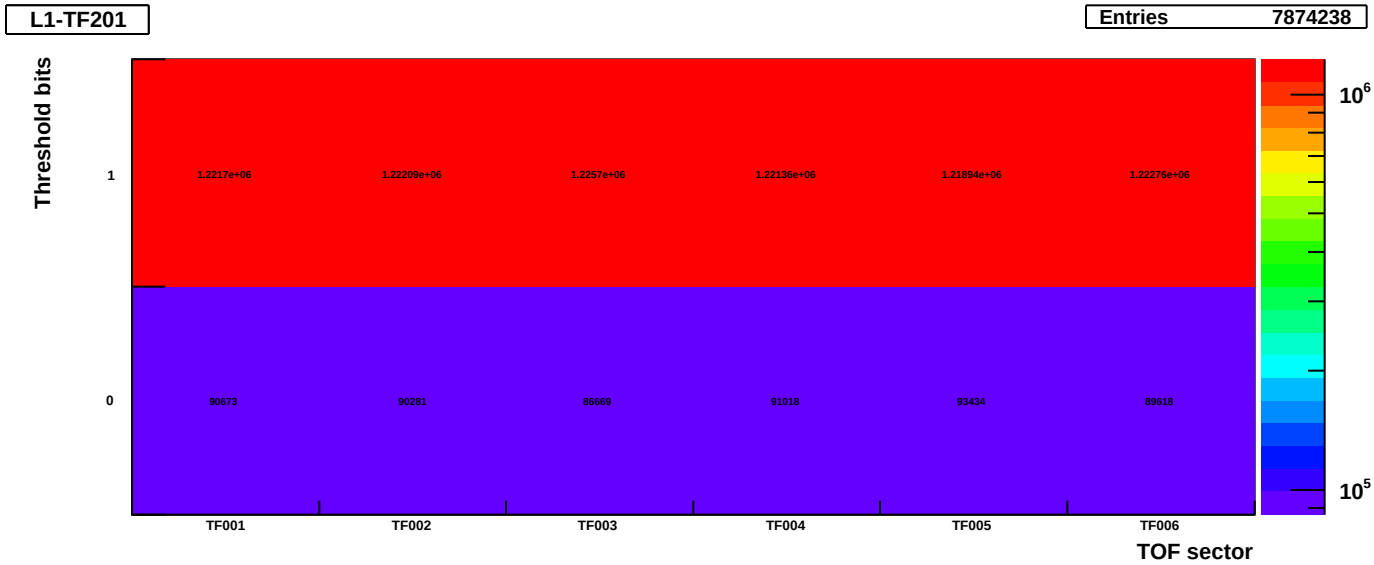

MIX-TF004
Entries 5.511966e+07
TOF MULT
TOF tray

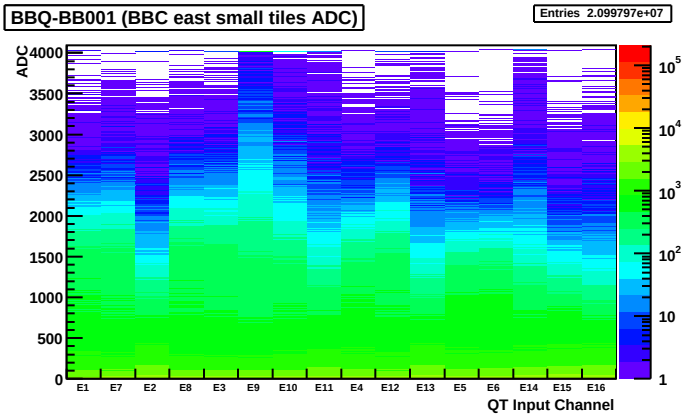

MIX-TF005
Entries 5.511966e+07
TOF MULT
TOF tray

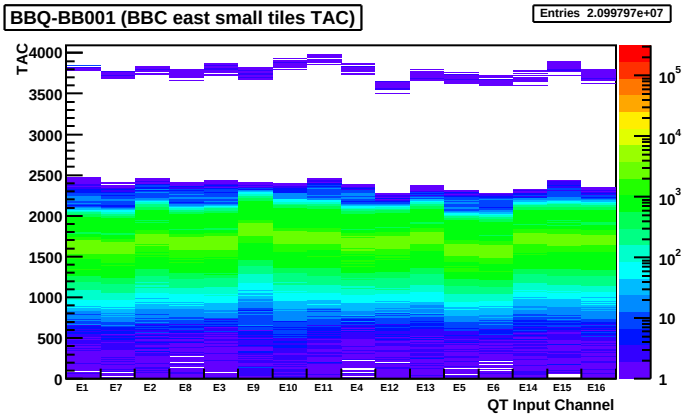

MIX-TF006
Entries 5.511966e+07
TOF MULT
TOF tray

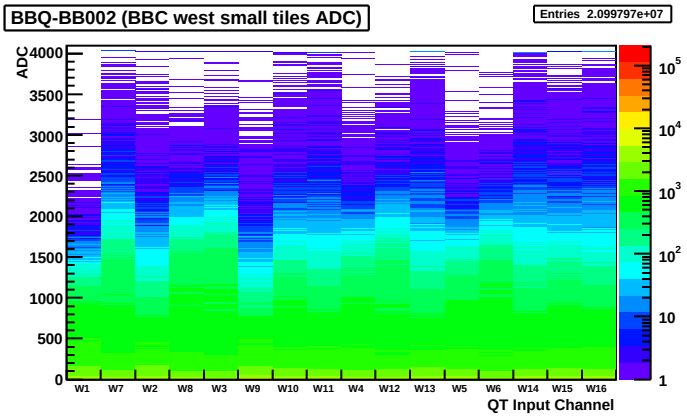

MIX-TF101
Entries 7874238
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

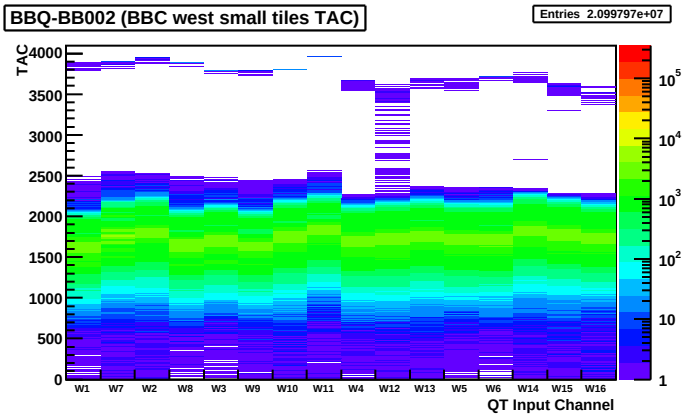

L1-TF201
Entries 1312373
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

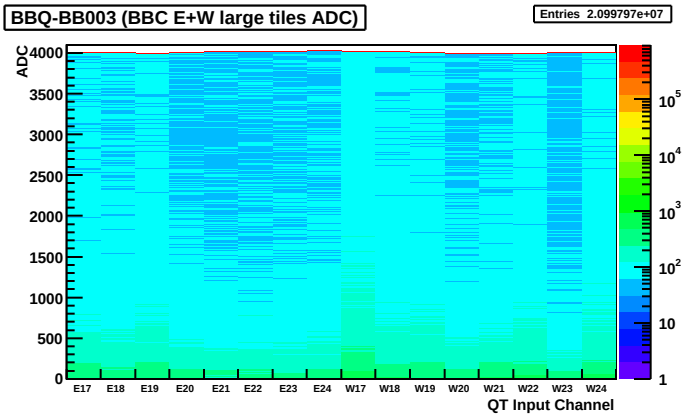

L1-TF201
Entries 7874238
Threshold bits
1
0
1.2217e+06
1.22209e+06
1.2257e+06
1.22136e+06
1.21894e+06
1.22276e+06
90673
90281
86669
91018
93434
89618
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

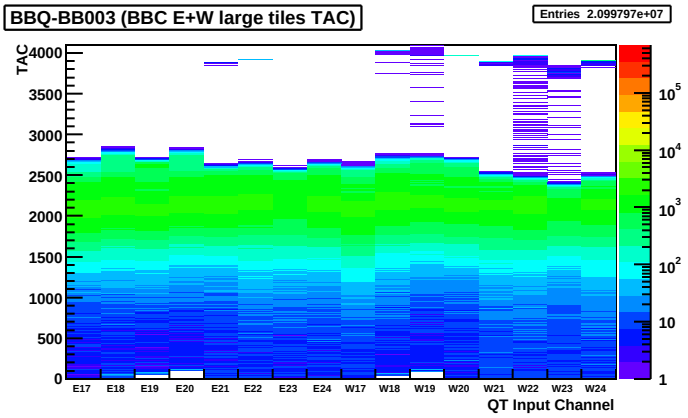

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.099797e+07
ADC
QT Input Channel

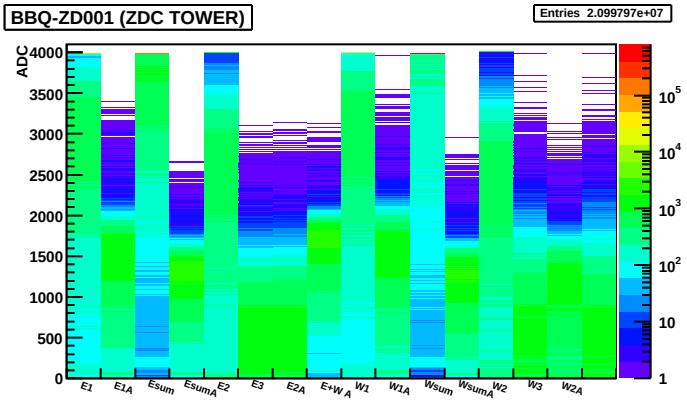

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.099797e+07
TAC
QT Input Channel

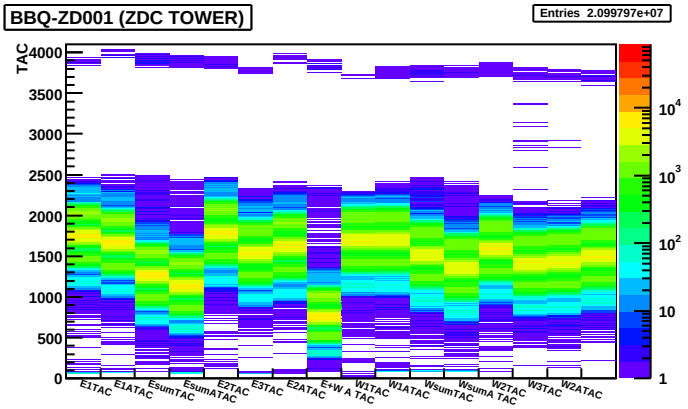

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.099797e+07
ADC
QT Input Channel

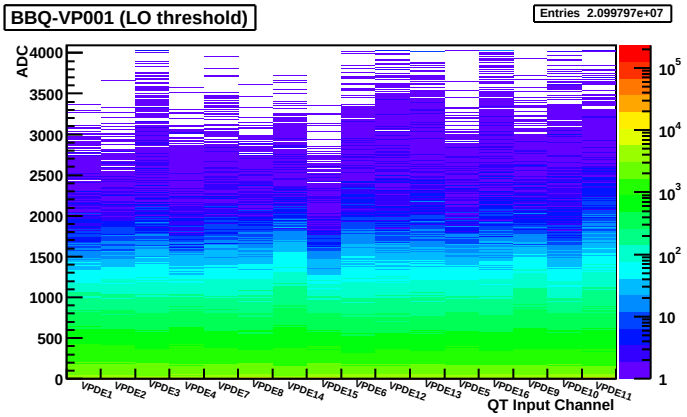

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.099797e+07
TAC
QT Input Channel

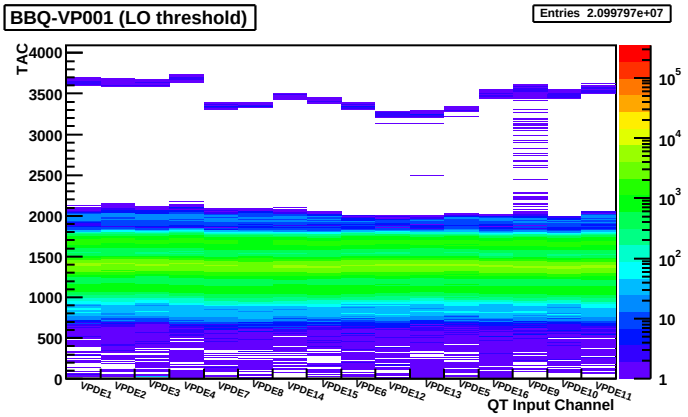

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.099797e+07
ADC
QT Input Channel

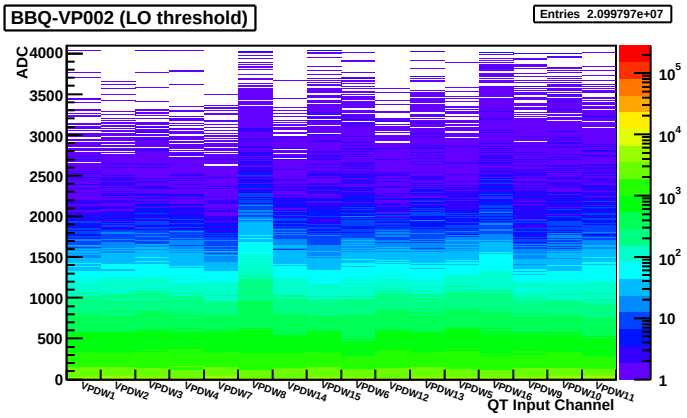

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.099797e+07
TAC
QT Input Channel

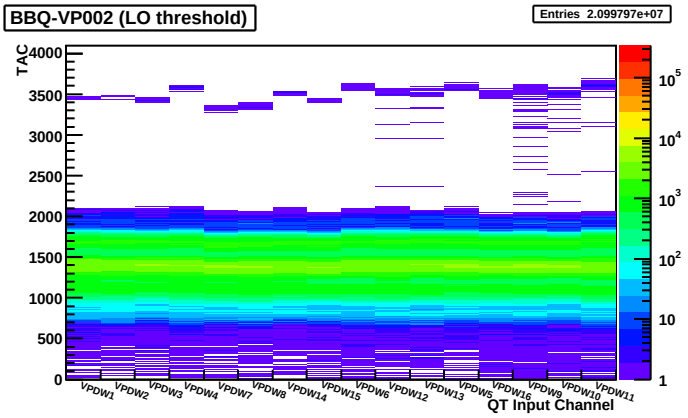

BBQ-ZD001 (ZDC TOWER)
Entries 2.099797e+07
ADC

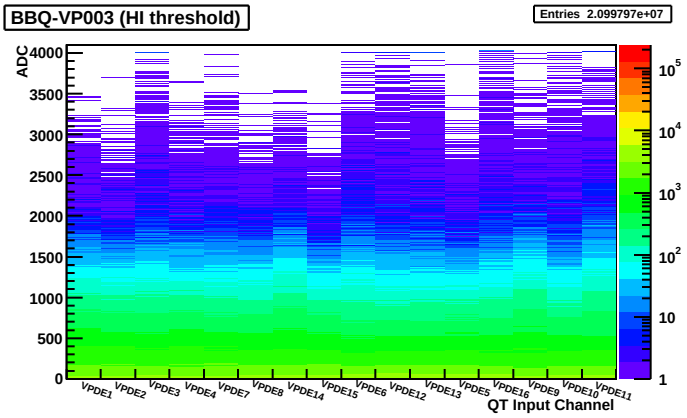

BBQ-ZD001 (ZDC TOWER)
Entries 2.099797e+07
TAC

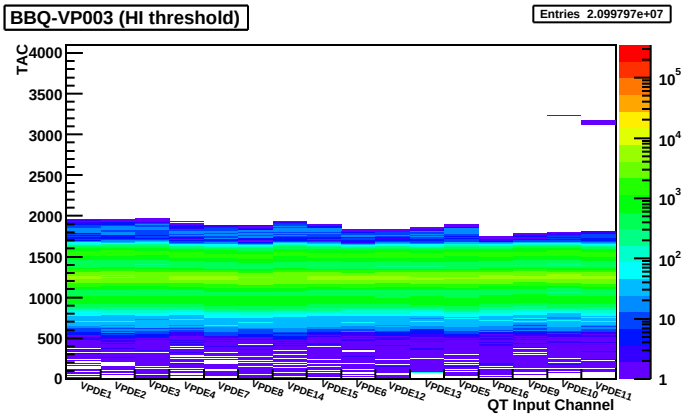

BBQ-VP001 (LO threshold)
Entries 2.099797e+07
ADC
QT Input Channel

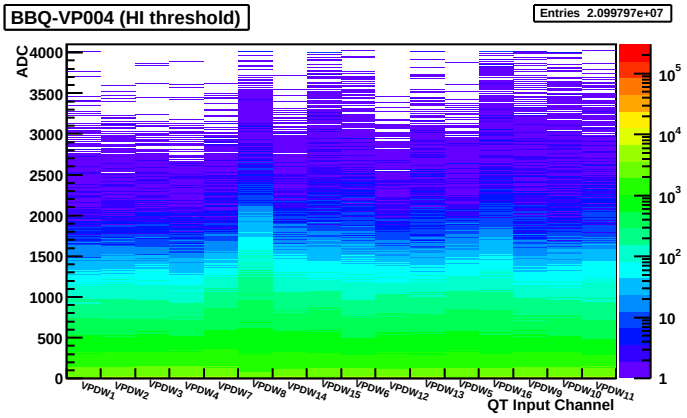

BBQ-VP001 (LO threshold)
Entries 2.099797e+07
TAC
QT Input Channel

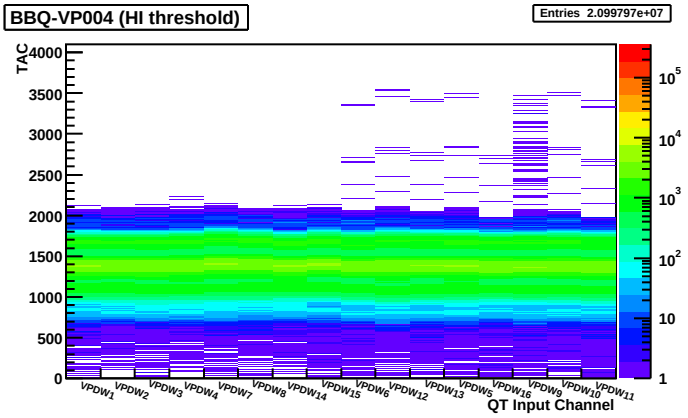

BBQ-VP002 (LO threshold)
Entries 2.099797e+07
ADC
QT Input Channel

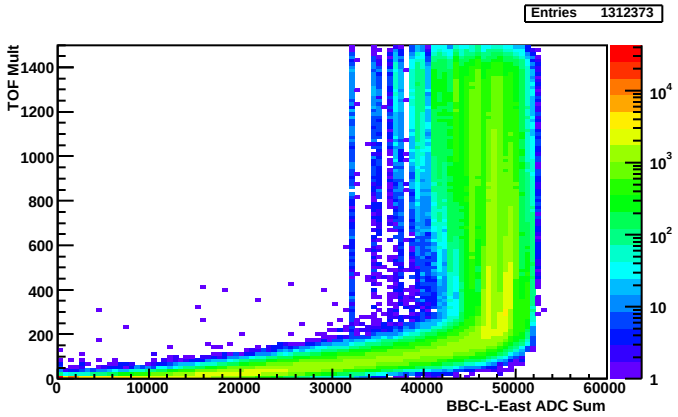

BBQ-VP002 (LO threshold)
Entries 2.099797e+07
TAC
QT Input Channel

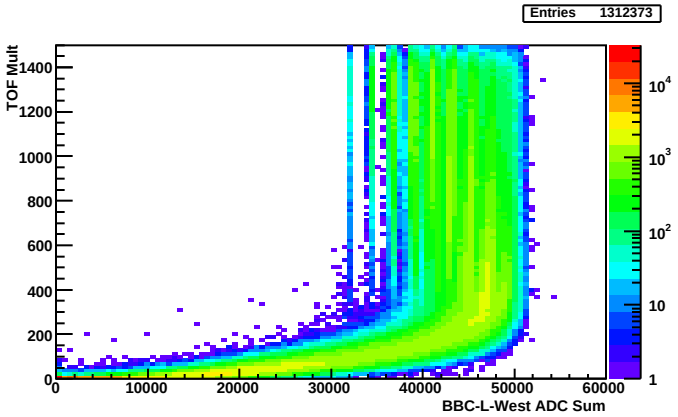

BBQ-VP003 (HI threshold)
Entries 2.099797e+07
ADC
QT Input Channel

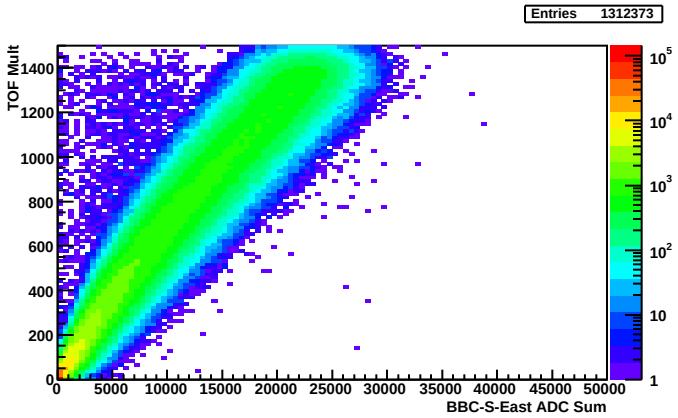

BBQ-VP003 (HI threshold)
Entries 2.099797e+07
TAC
QT Input Channel

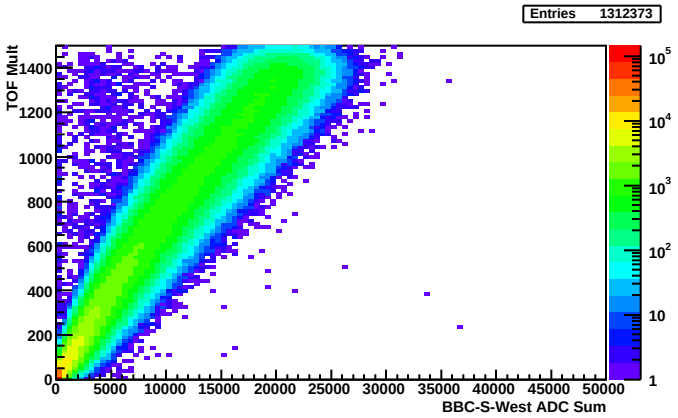

BBQ-VP004 (HI threshold)
Entries 2.099797e+07
ADC
QT Input Channel

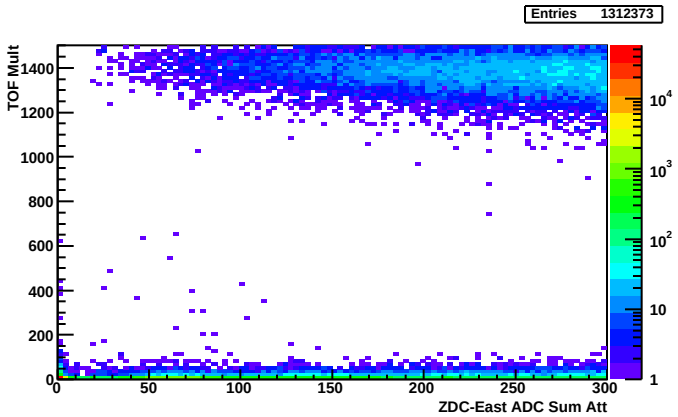

BBQ-VP004 (HI threshold)
Entries 2.099797e+07
TAC
QT Input Channel

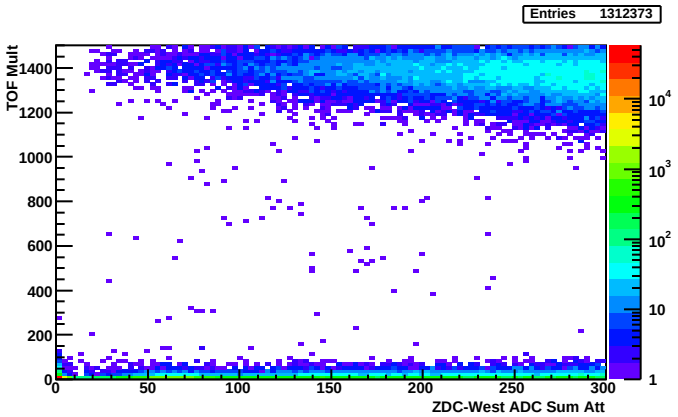

Entries 1312373
TOF Mult
BBC-L-East ADC Sum

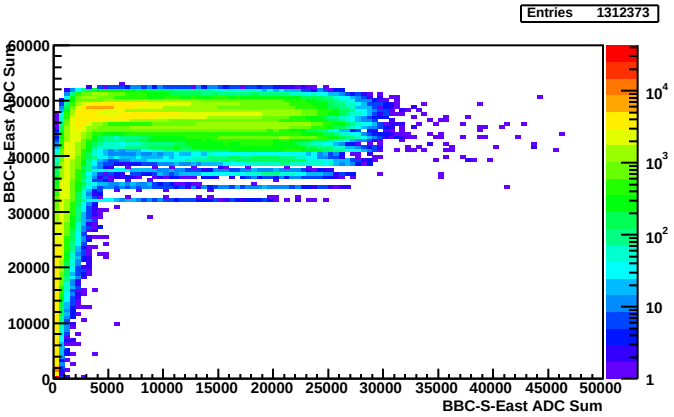

Entries 1312373
TOF Mult
BBC-L-West ADC Sum

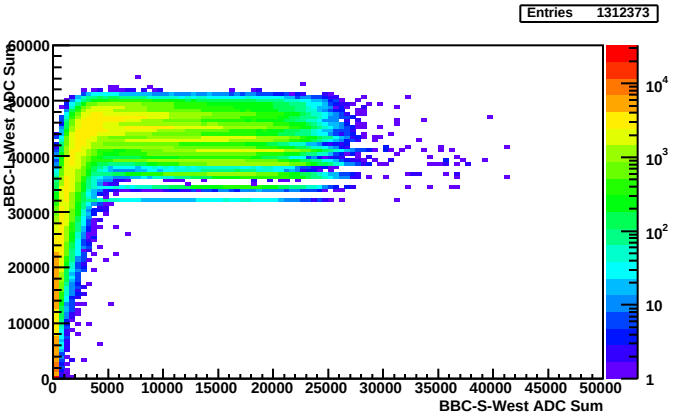

Entries 1312373
TOF Mult
BBC-S-East ADC Sum

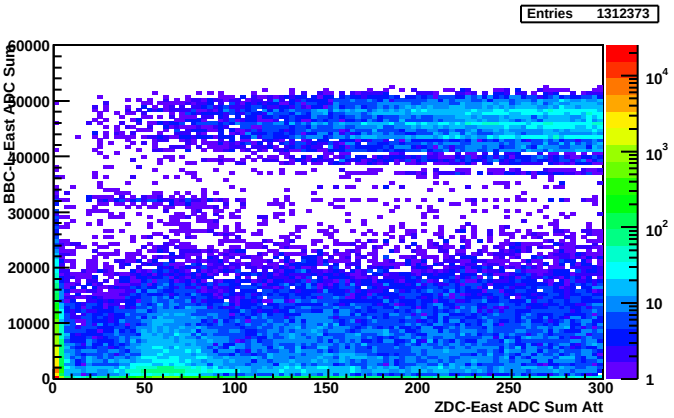

Entries 1312373
TOF Mult
BBC-S-West ADC Sum

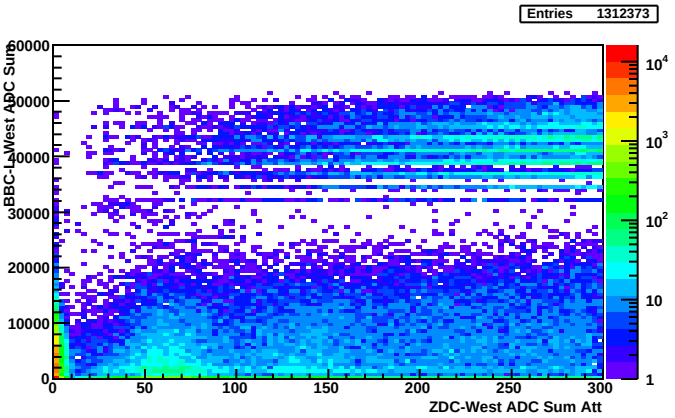

Entries 1312373
TOF Mult
ZDC-East ADC Sum Att

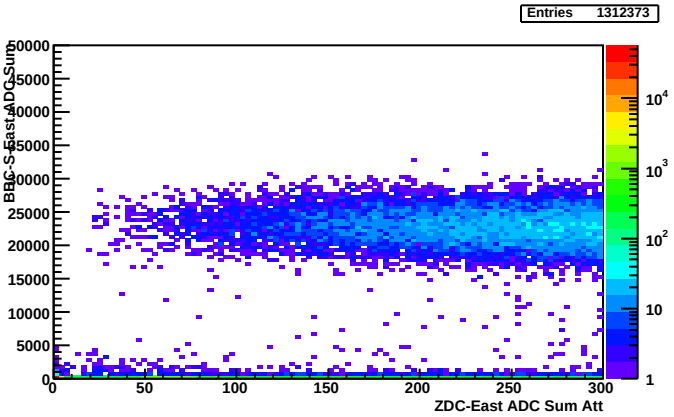

Entries 1312373
TOF Mult
ZDC-West ADC Sum Att

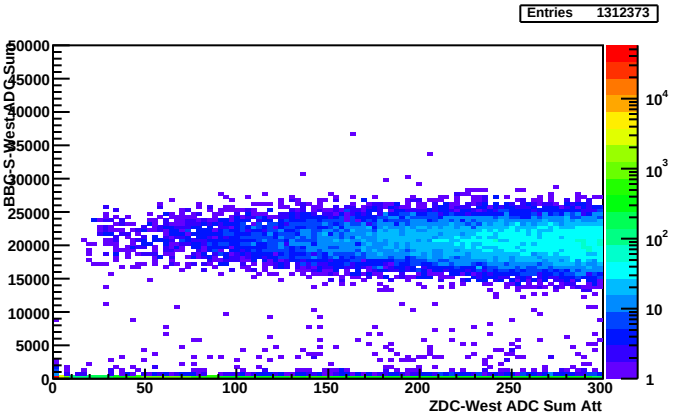

Entries 1312373
BBC-L-East ADC Sum
BBC-S-East ADC Sum

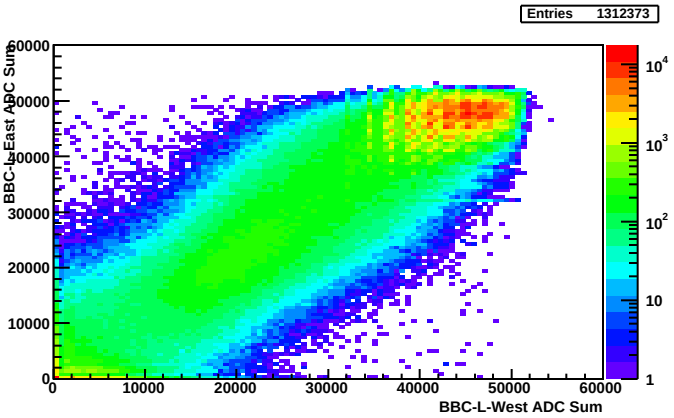

Entries 1312373
BBC-L-West ADC Sum
BBC-S-West ADC Sum

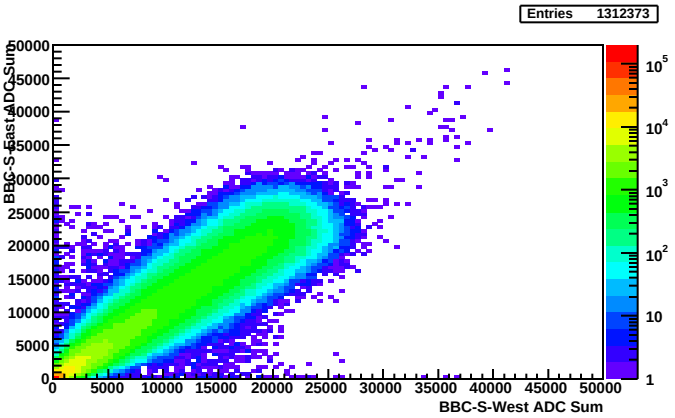

Entries 1312373
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

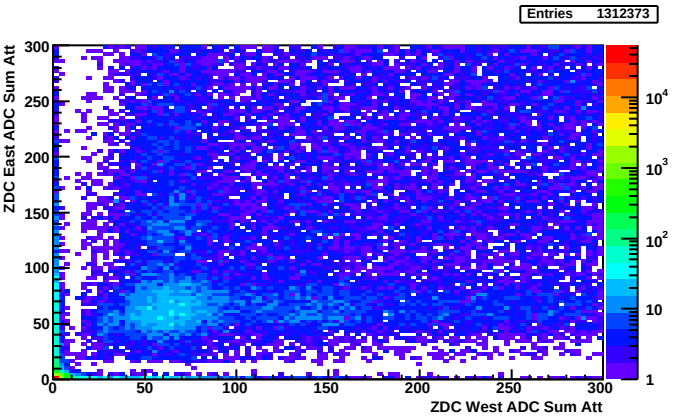

Entries 1312373
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

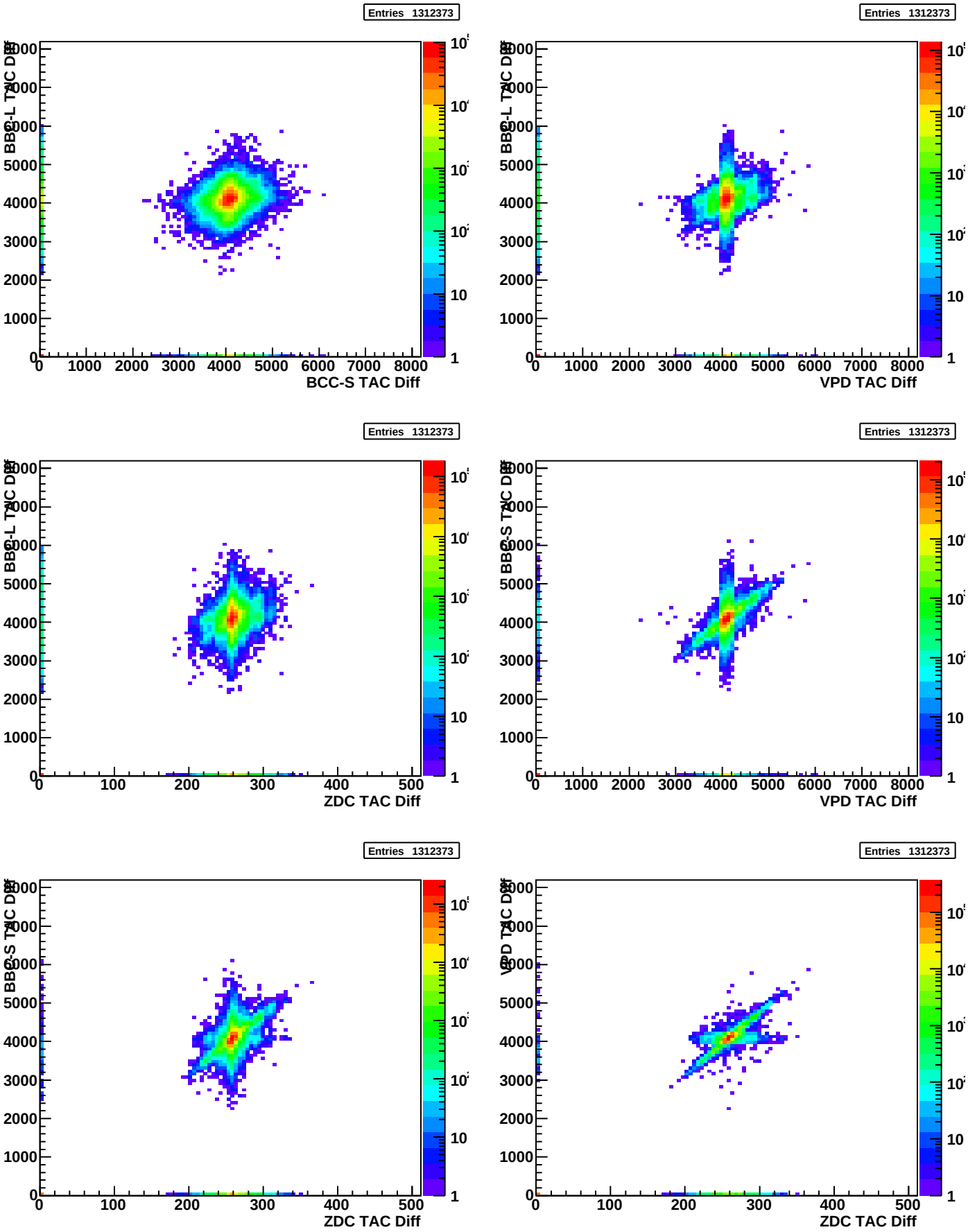

Entries 1312373
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

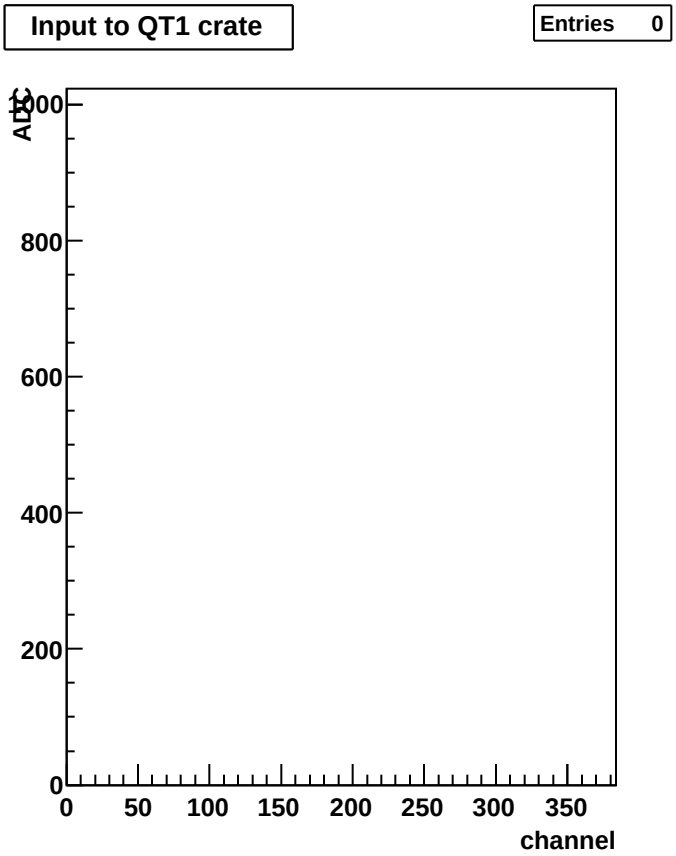

Entries 1312373
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

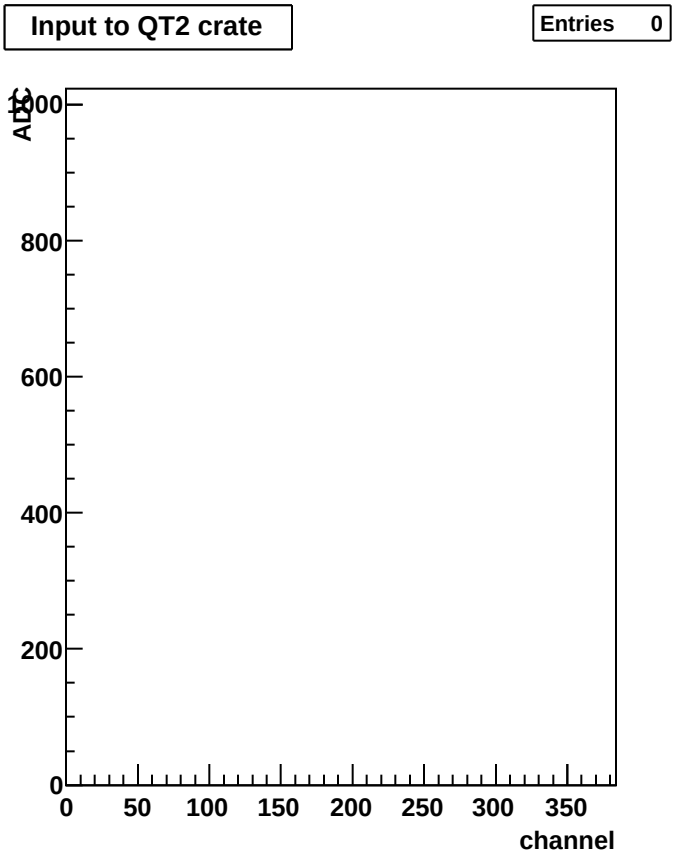

Entries 1312373
BBC-L-East ADC Sum
BBC-L-West ADC Sum

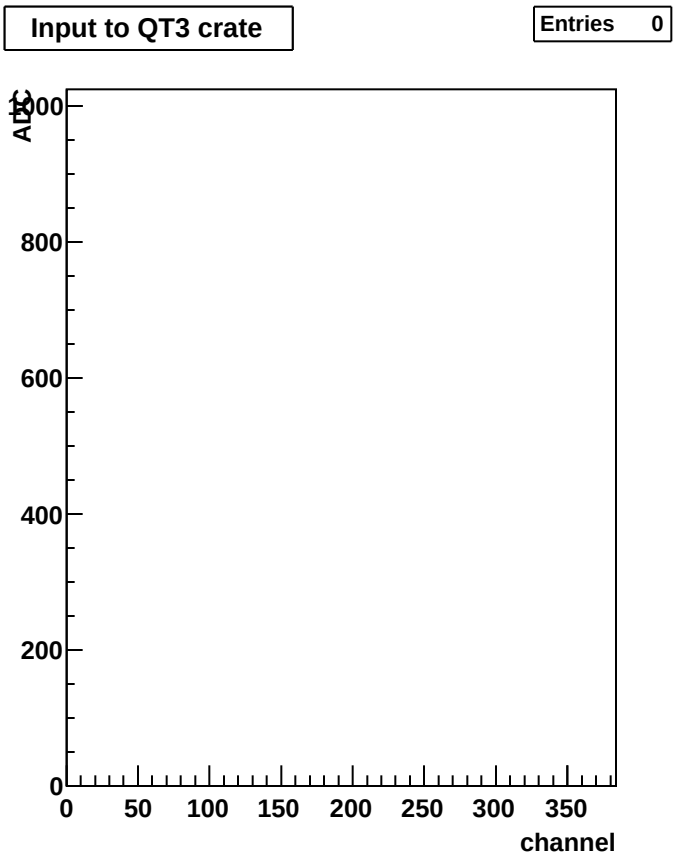

Entries 1312373
BBC-S-East ADC Sum
BBC-S-West ADC Sum

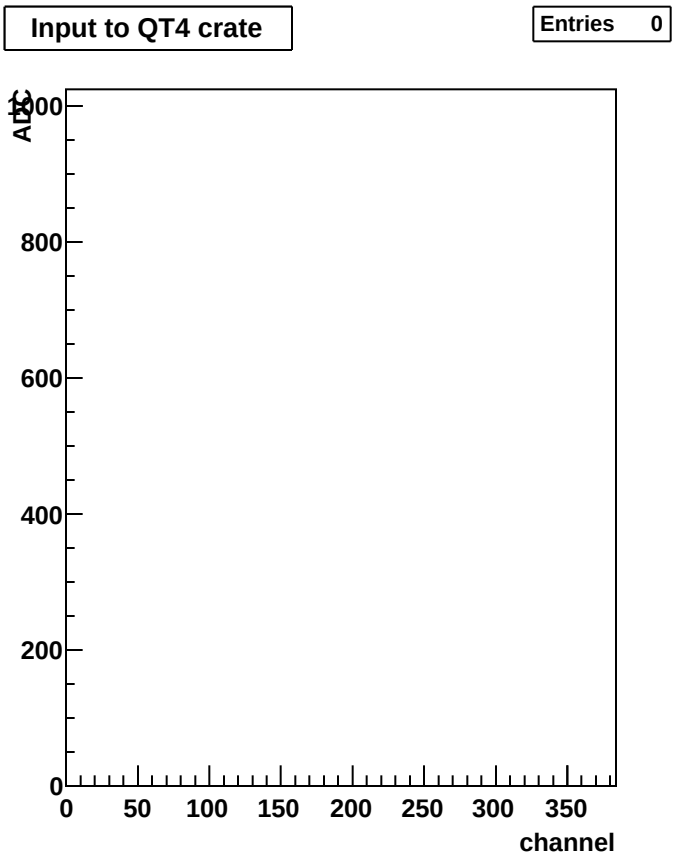

Entries 1312373
ZDC East ADC Sum Att
ZDC West ADC Sum Att

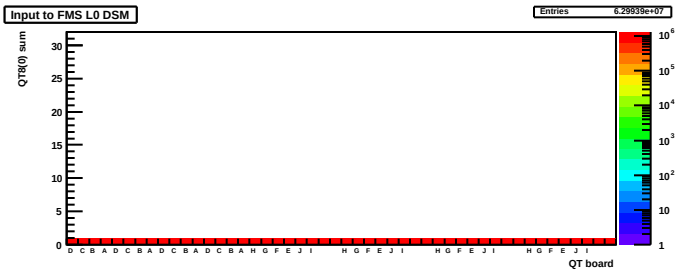

Entries 1312373
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1312373
BBC-L TAC Diff
VPD TAC Diff
Entries 1312373
BBC-L TAC Diff
ZDC TAC Diff
Entries 1312373
BBC-S TAC Diff
VPD TAC Diff
Entries 1312373
BBC-S TAC Diff
ZDC TAC Diff
Entries 1312373
VPD TAC Diff
ZDC TAC Diff

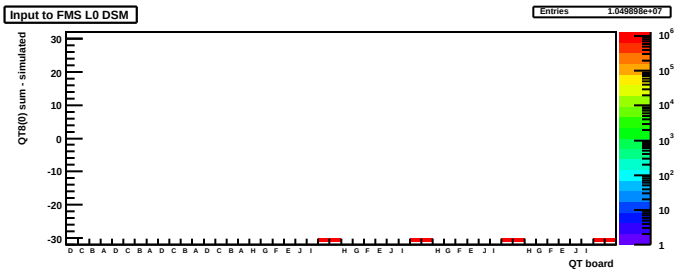

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

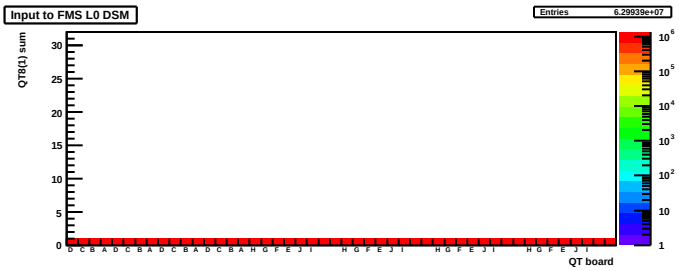

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

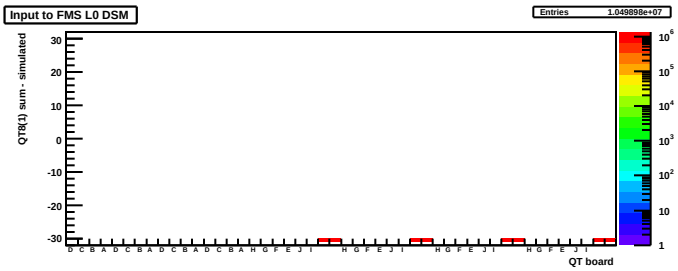

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

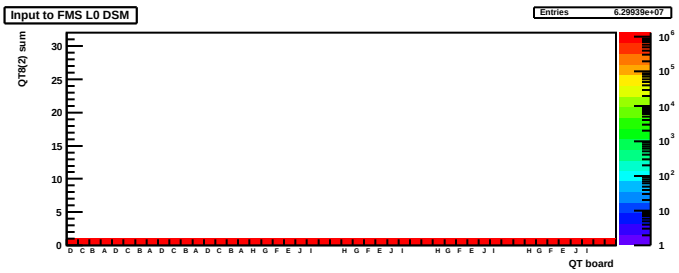

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

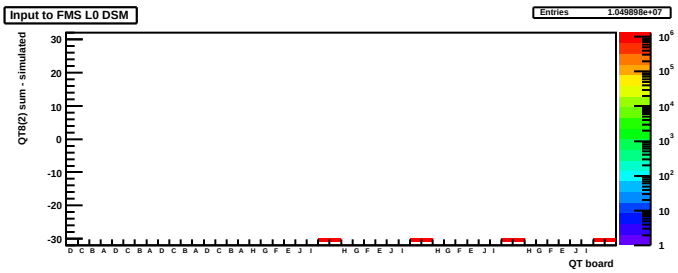

Input to FMS L0 DSM
Entries 6.29939e+07
QT8(0) sum
QT board

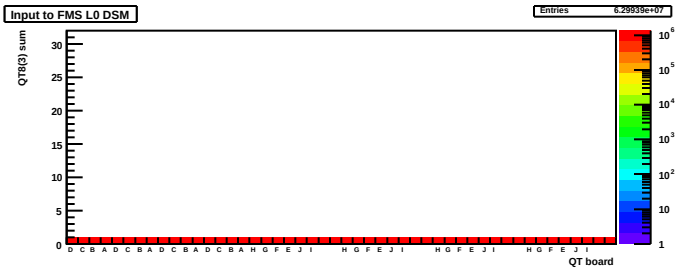

Input to FMS L0 DSM
Entries 1.049898e+07
QT8(0) sum - simulated
QT board

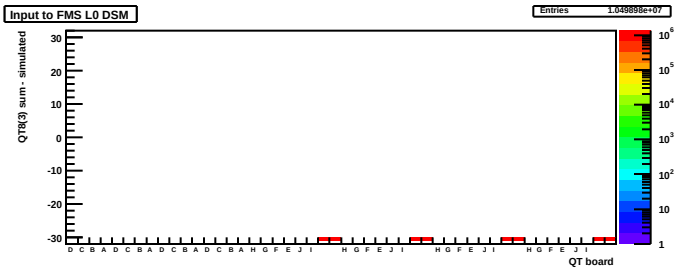

Input to FMS L0 DSM
Entries 6.29939e+07
QT8(1) sum
QT board

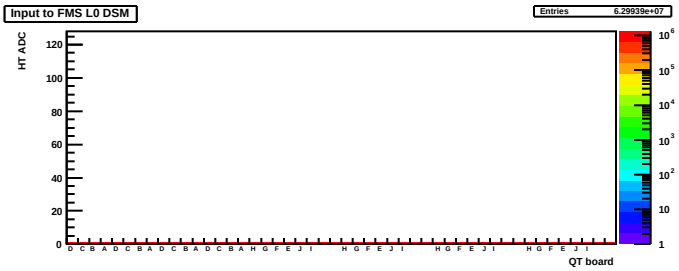

Input to FMS L0 DSM
Entries 1.049898e+07
QT8(1) sum - simulated
QT board

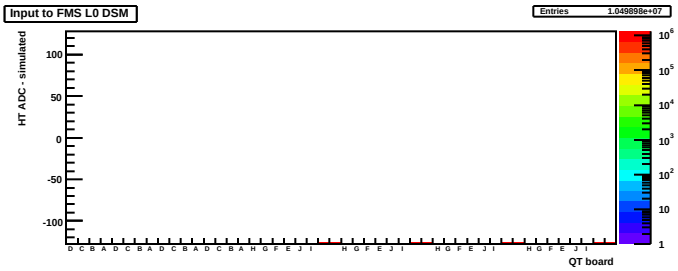

Input to FMS L0 DSM
Entries 6.29939e+07
QT8(2) sum
QT board

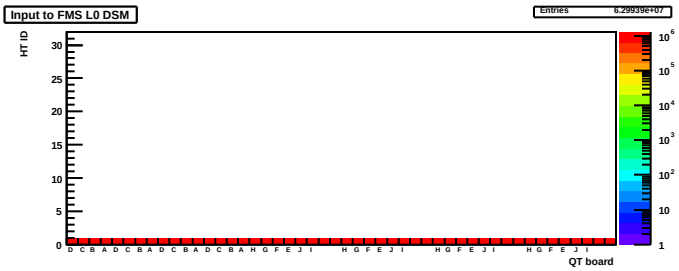

Input to FMS L0 DSM
Entries 1.049898e+07
QT8(2) sum - simulated
QT board

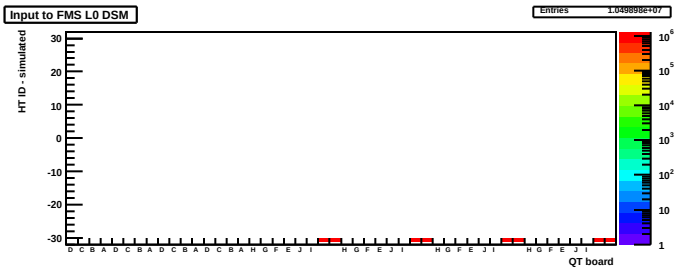

Input to FMS L0 DSM
Entries 6.29939e+07
QT8(3) sum
QT board

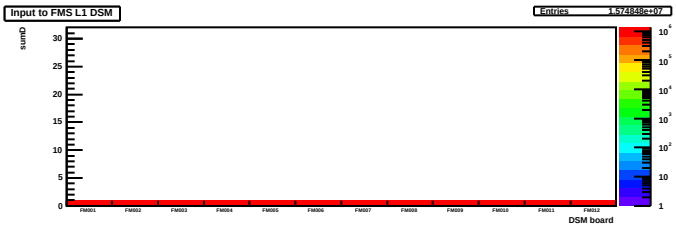

Input to FMS L0 DSM
Entries 1.049898e+07
QT8(3) sum - simulated
QT board

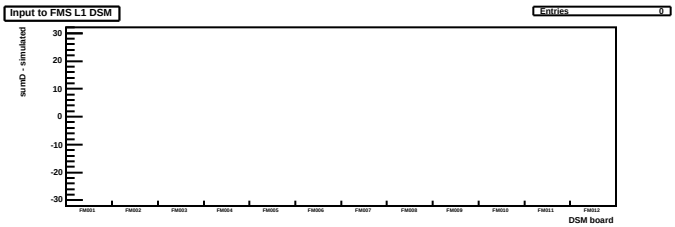

Input to FMS L0 DSM
Entries 6.29939e+07
HT ADC
QT board

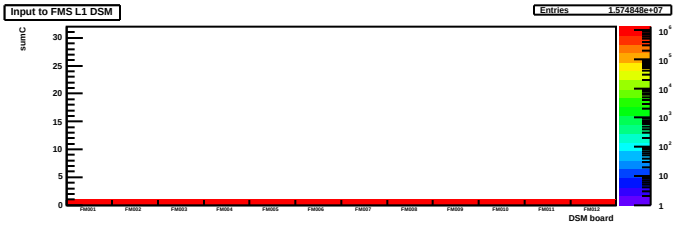

Input to FMS L0 DSM
Entries 1.049898e+07
HT ADC - simulated
QT board

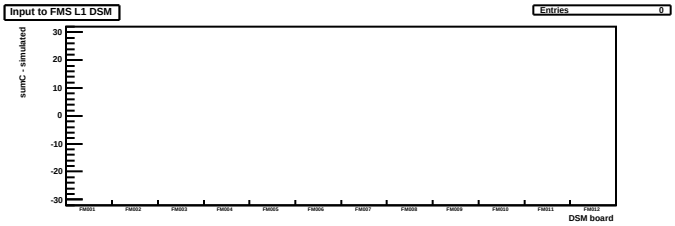

Input to FMS L0 DSM
Entries 6.29939e+07
HT ID
QT board

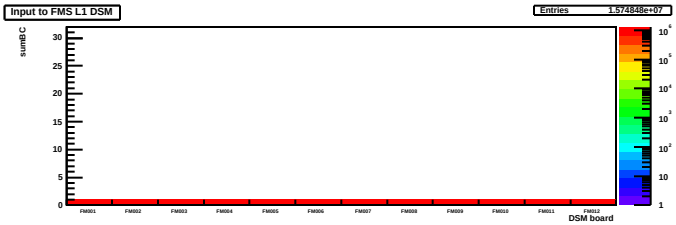

Input to FMS L0 DSM
Entries 1.049898e+07
HT ID - simulated
QT board

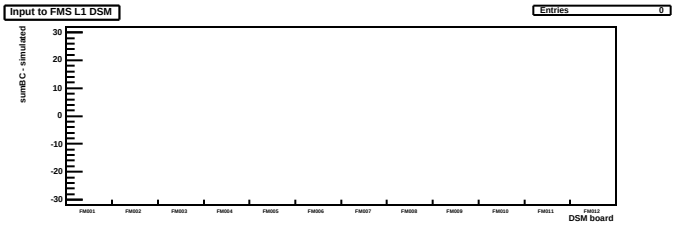

Input to FMS L1 DSM
Entries 1.574848e+07
sumD
DSM board

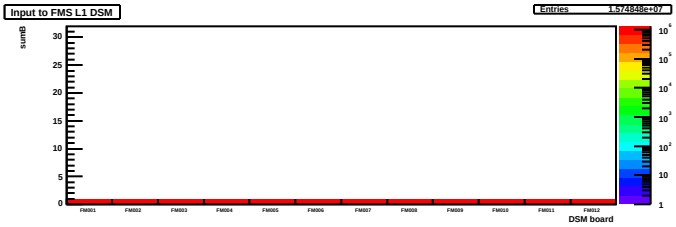

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

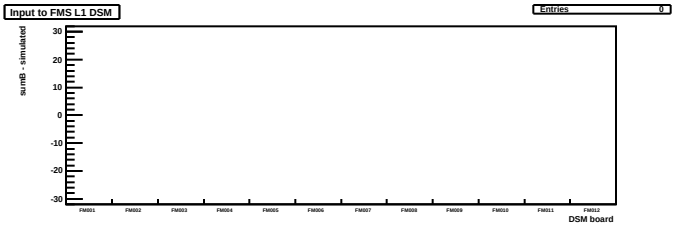

Input to FMS L1 DSM
Entries 1.574848e+07
sumC
DSM board

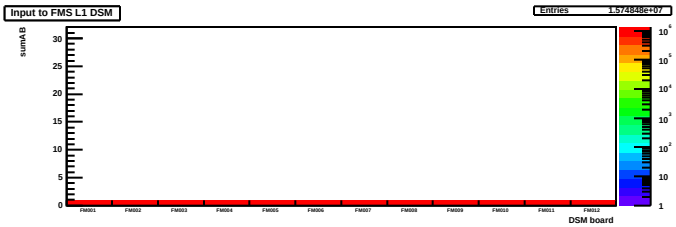

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

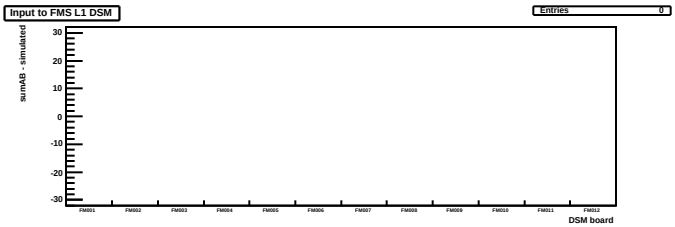

Input to FMS L1 DSM
Entries 1.574848e+07
sumBC
DSM board

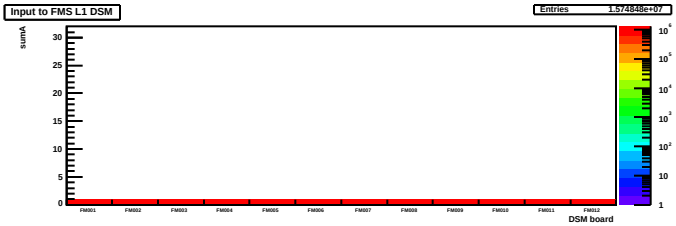

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

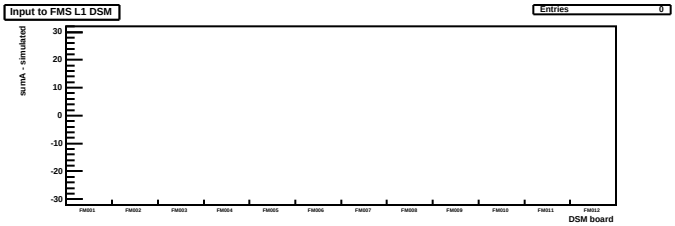

Input to FMS L1 DSM
Entries 1.574848e+07
sumB
DSM board

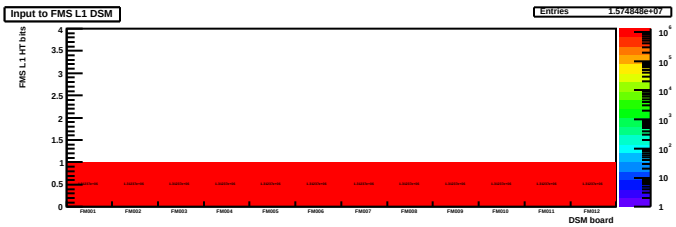

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

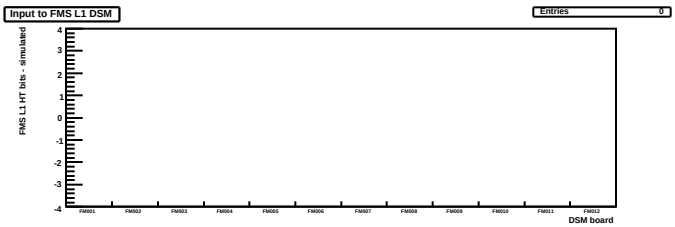

Input to FMS L1 DSM
Entries 1.574848e+07
sumAB
DSM board

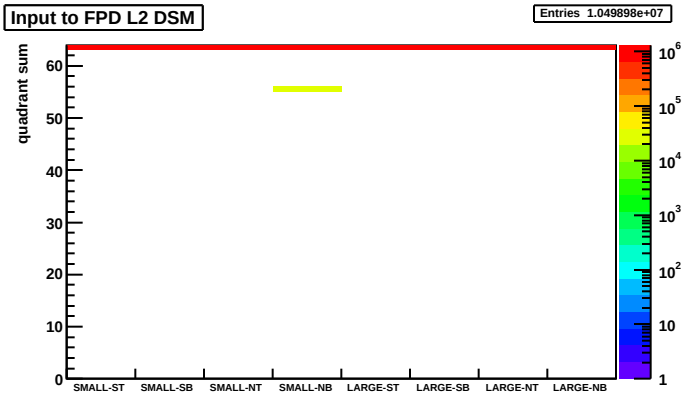

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

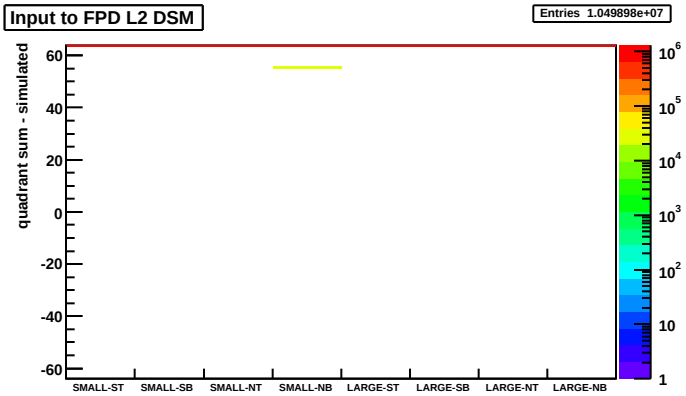

Input to FMS L1 DSM
Entries 1.574848e+07
sumA
DSM board

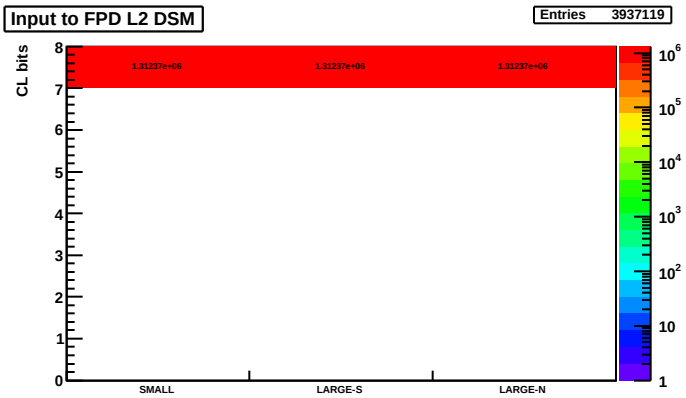

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

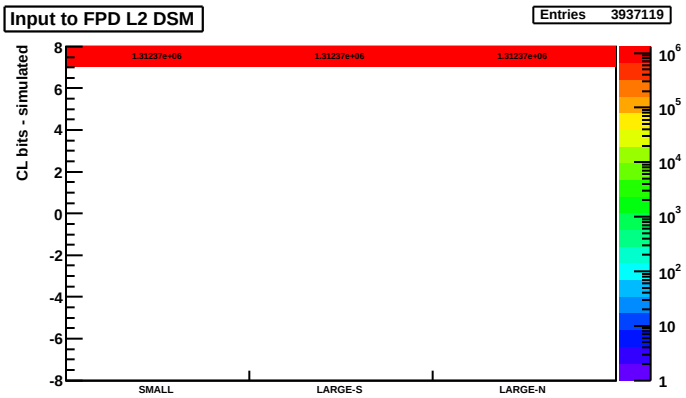

Input to FMS L1 DSM
Entries 1.574848e+07
FMS L1 HT bits
DSM board

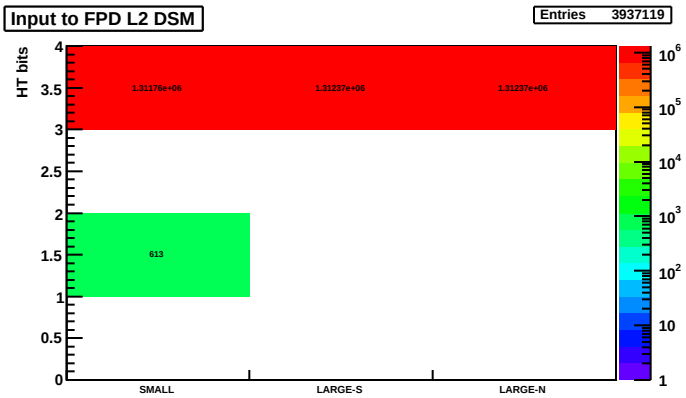

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

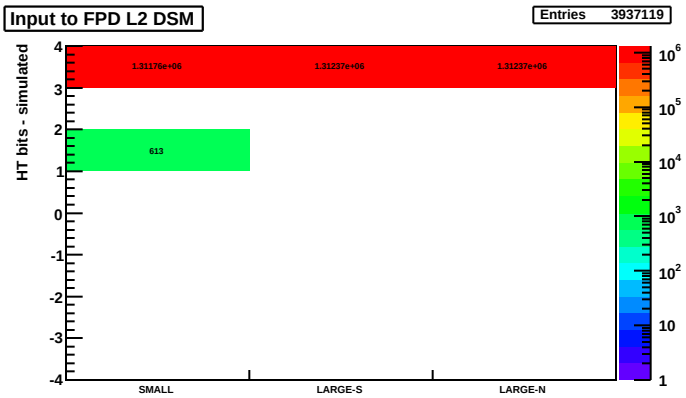

Input to FPD L2 DSM
Entries 1.049898e+07
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

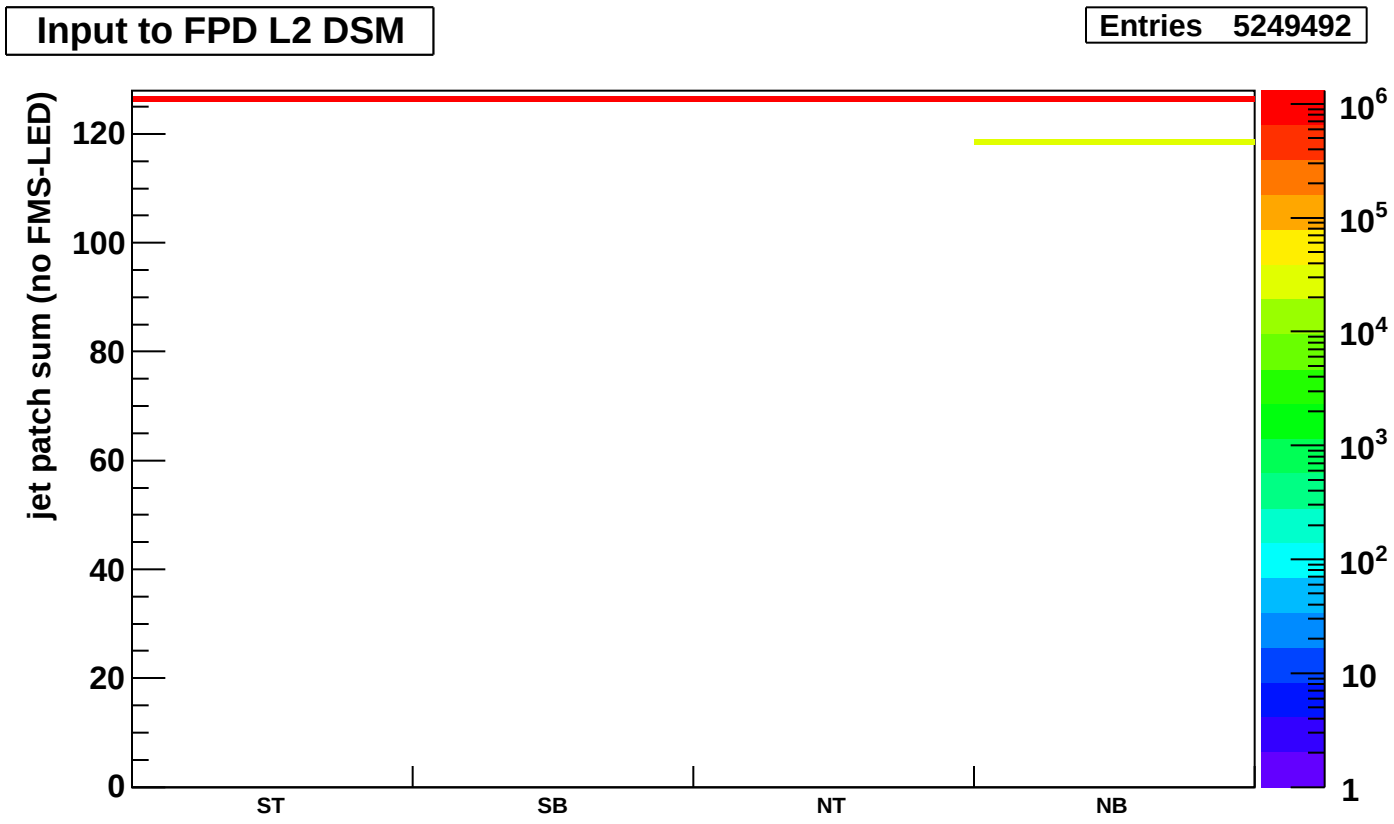

Input to FPD L2 DSM
Entries 1.049898e+07
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

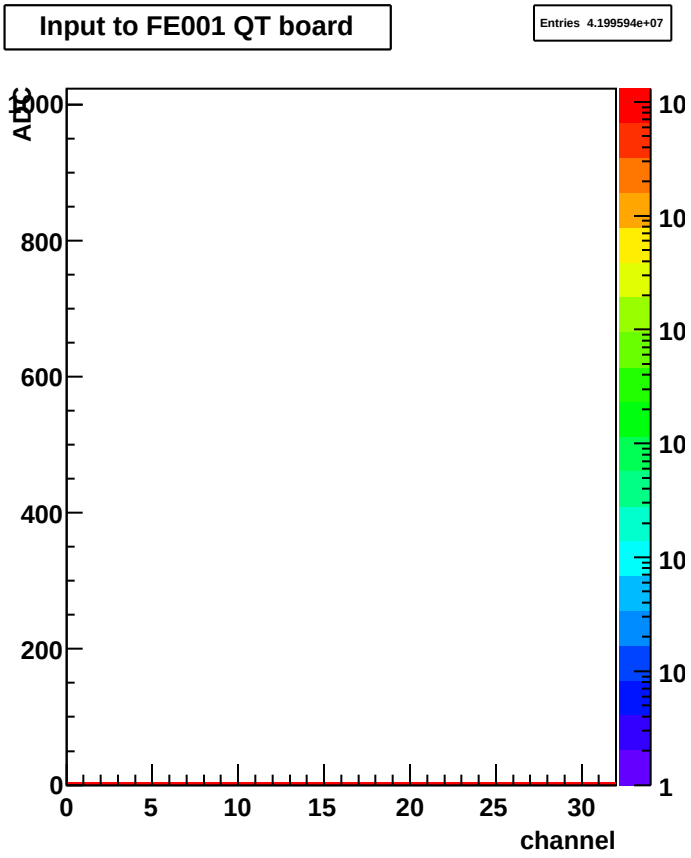

Input to FPD L2 DSM
Entries 3937119
CL bits
1.31237e+06
1.31237e+06
1.31237e+06
SMALL
LARGE-S
LARGE-N

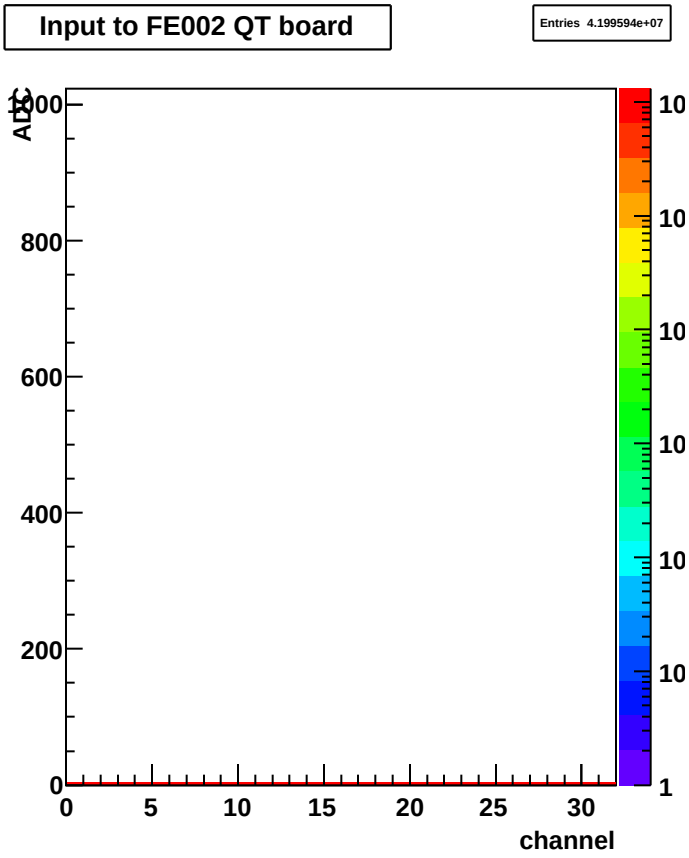

Input to FPD L2 DSM
Entries 3937119
CL bits - simulated
1.31237e+06
1.31237e+06
1.31237e+06
SMALL
LARGE-S
LARGE-N

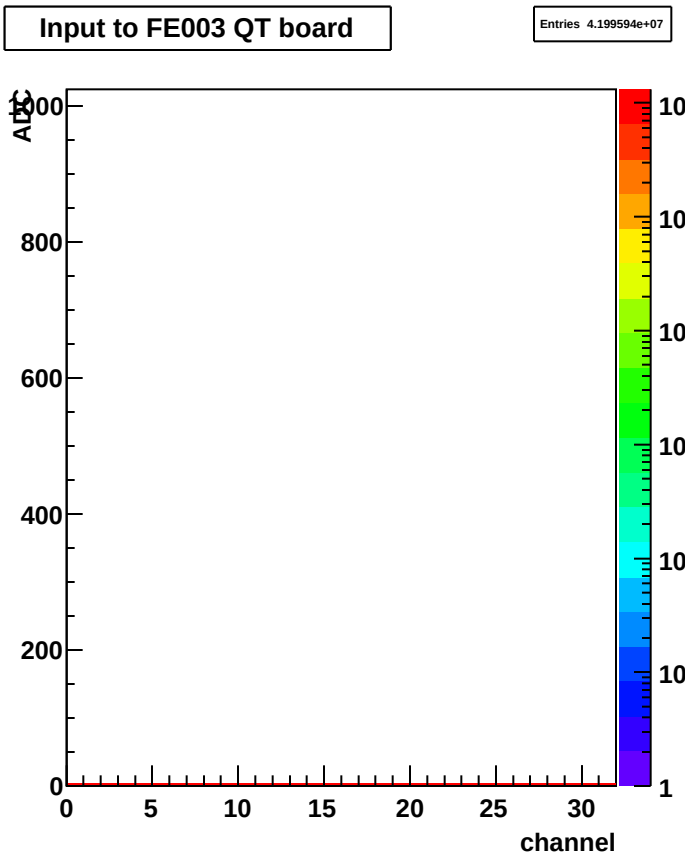

Input to FPD L2 DSM
Entries 3937119
HT bits
1.31176e+06
1.31237e+06
1.31237e+06
613
SMALL
LARGE-S
LARGE-N

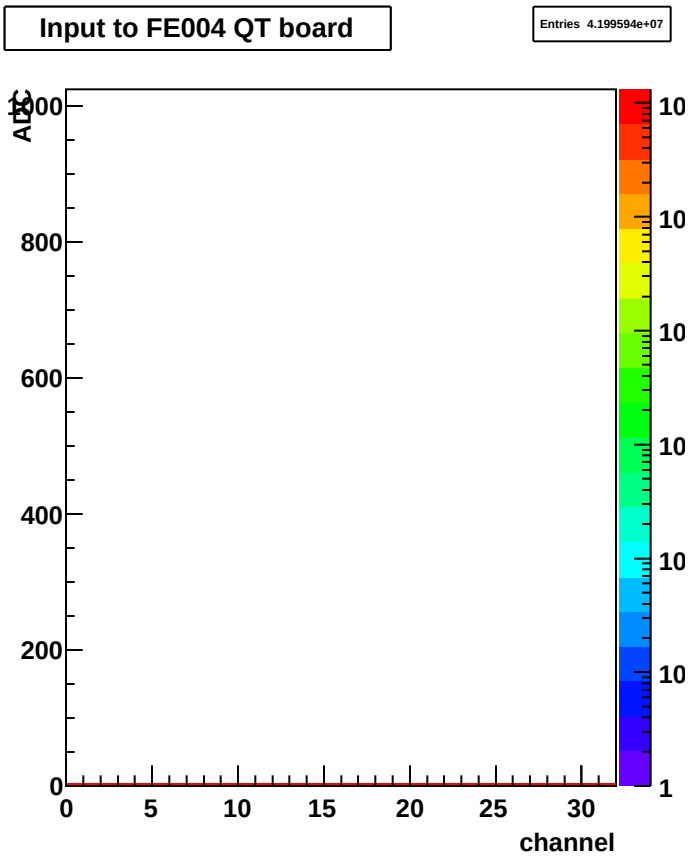

Input to FPD L2 DSM
Entries 3937119
HT bits - simulated
1.31176e+06
1.31237e+06
1.31237e+06
613
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 5249492
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

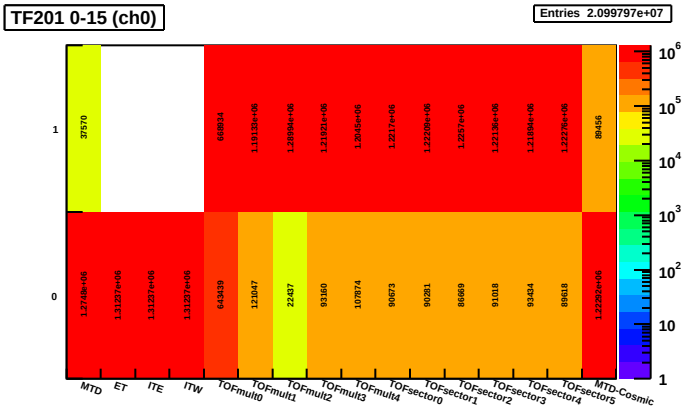

Input to FE001 QT board
Entries 4.199594e+07
ADC
channel


Input to FE002 QT board
Entries 4.199594e+07
ADC
channel

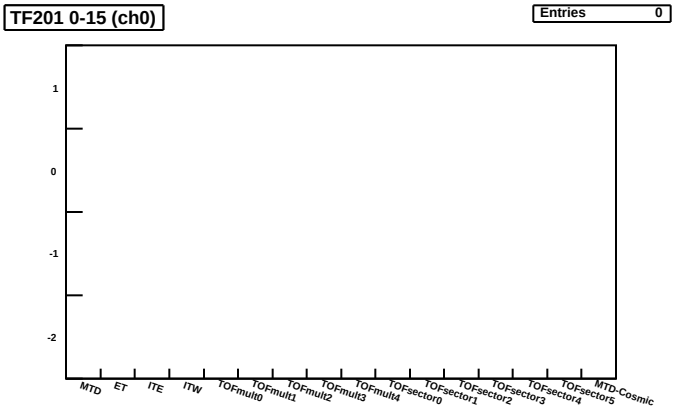

Input to FE003 QT board
Entries 4.199594e+07
ADC
channel


Input to FE004 QT board
Entries 4.199594e+07
ADC
channel

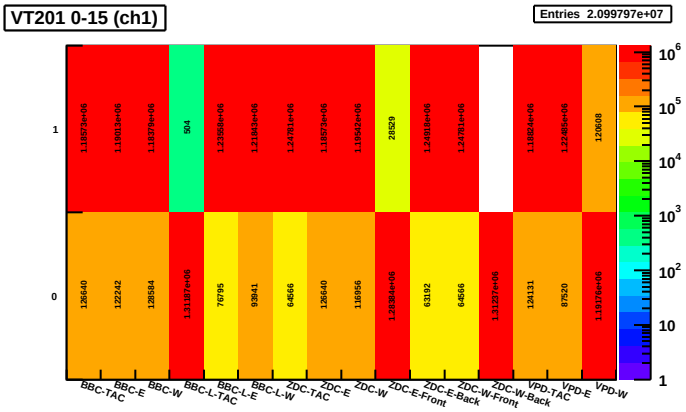
TF201 0-15 (ch0)



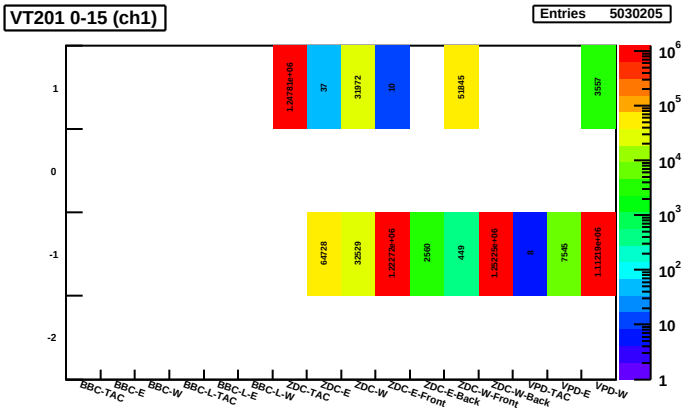
TF201 0-15 (ch0)



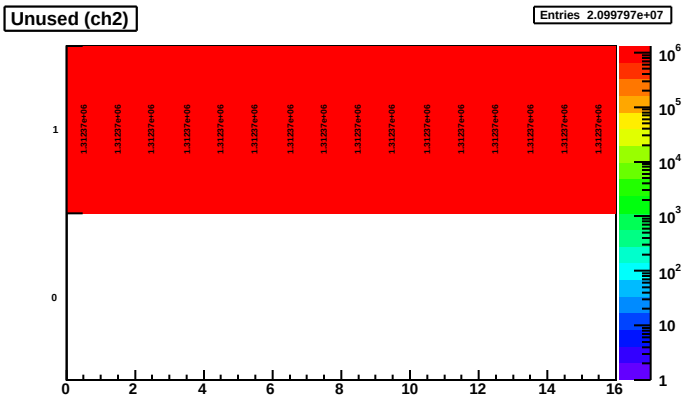
VT201 0-15 (ch1)

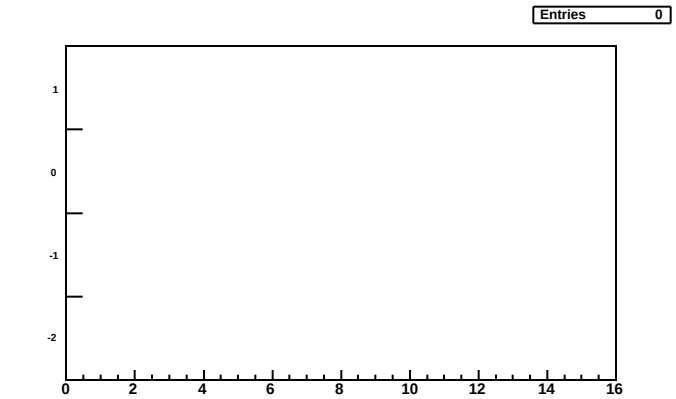


VT201 0-15 (ch1)

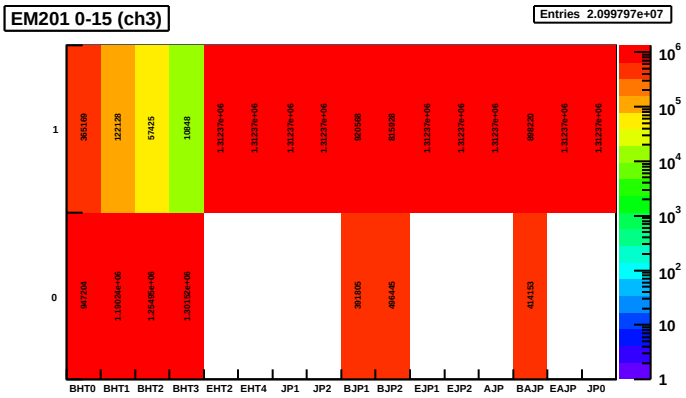


Unused (ch2)

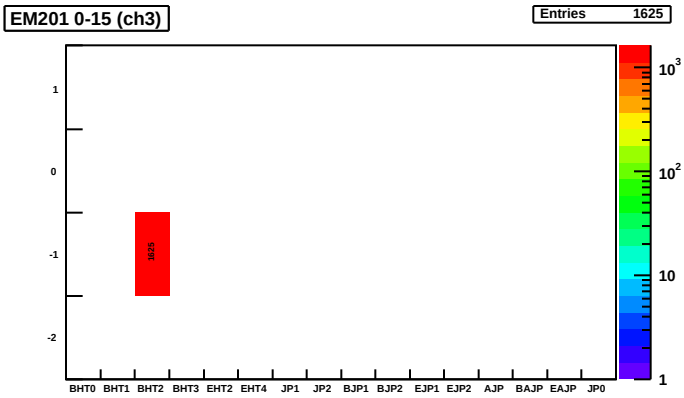


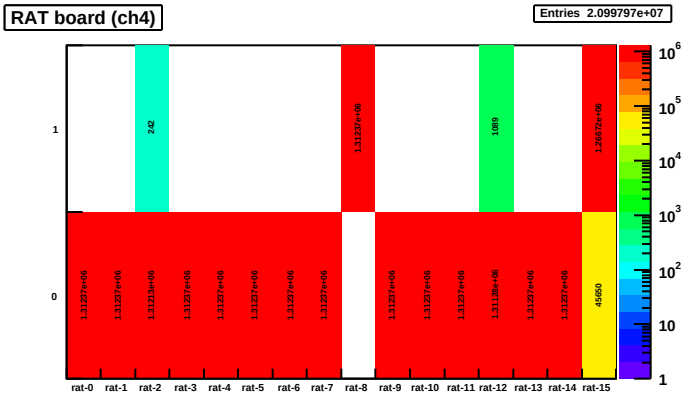


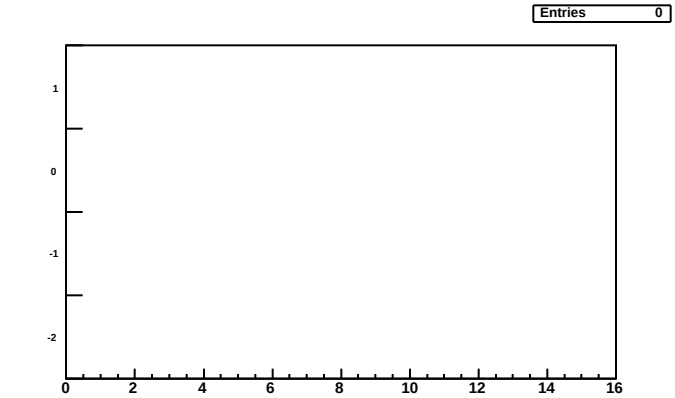
EM201 0-15 (ch3)

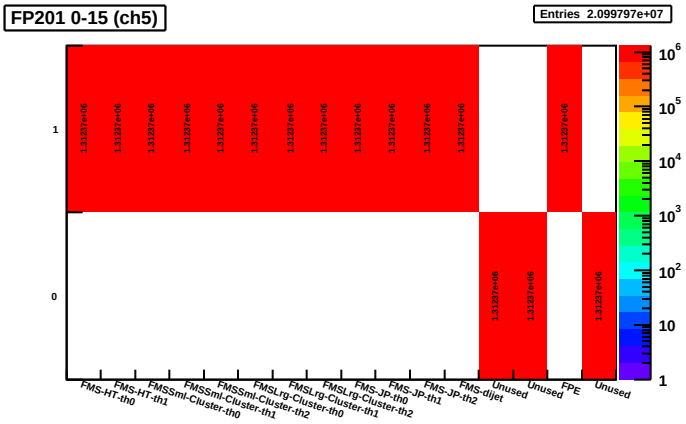


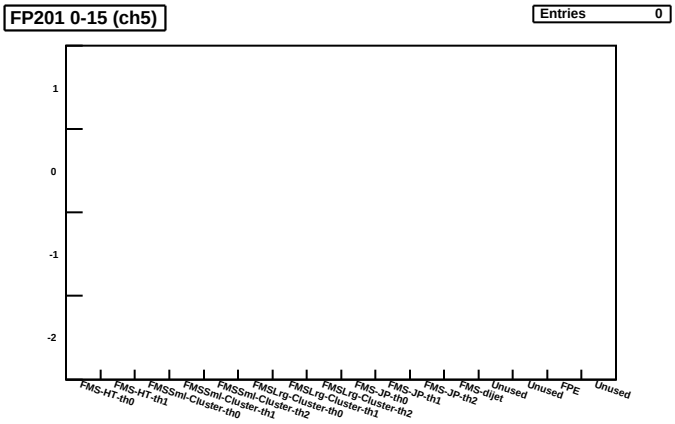
EM201 0-15 (ch3)

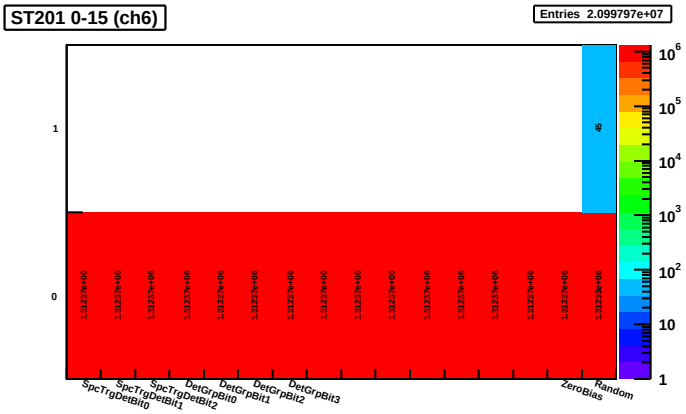


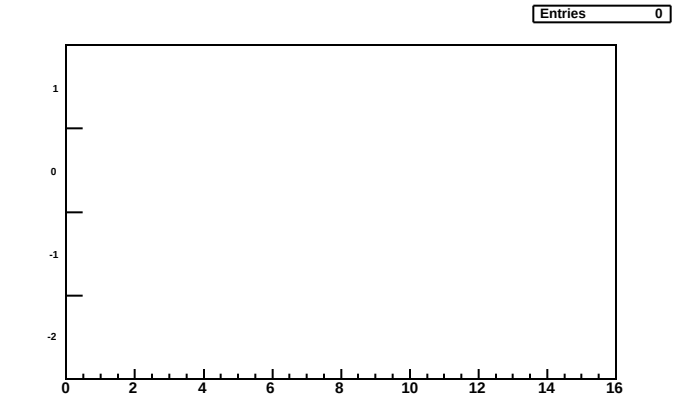

RAT board (ch4)
Entries 2.099797e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

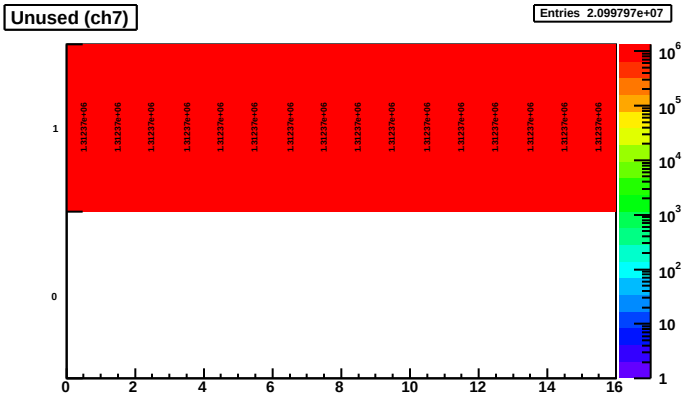

Entries 0

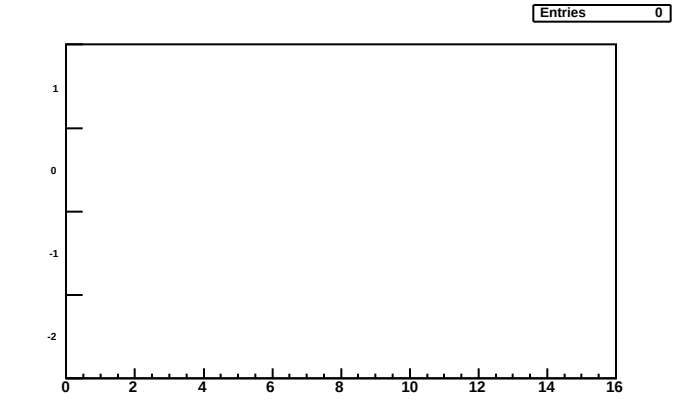

FP201 0-15 (ch5)
Entries 2.099797e+07
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-dijet
Unused
Unused
FPE
Unused


FP201 0-15 (ch5)
Entries 0
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-dijet
Unused
Unused
FPE
Unused


ST201 0-15 (ch6)
Entries 2.099797e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random


Entries 0


Unused (ch7)
Entries 2.099797e+07


Entries 0