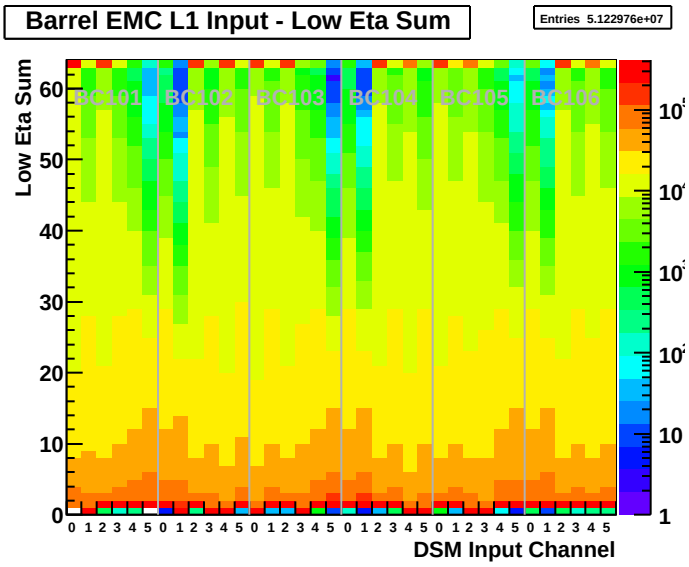
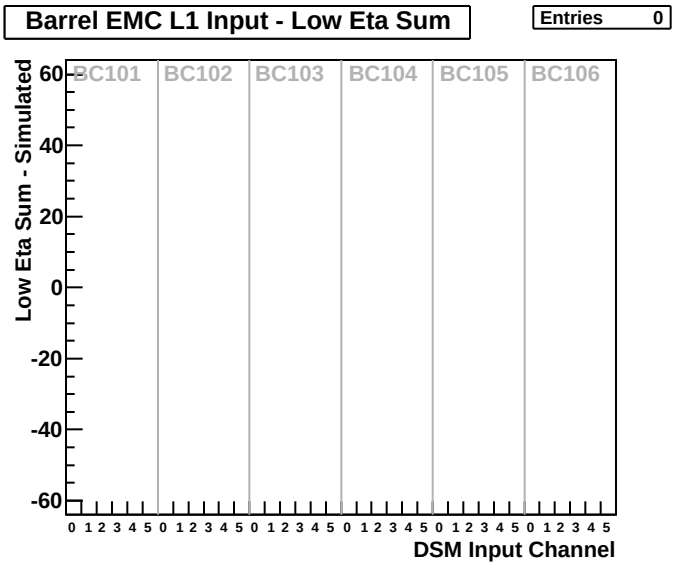
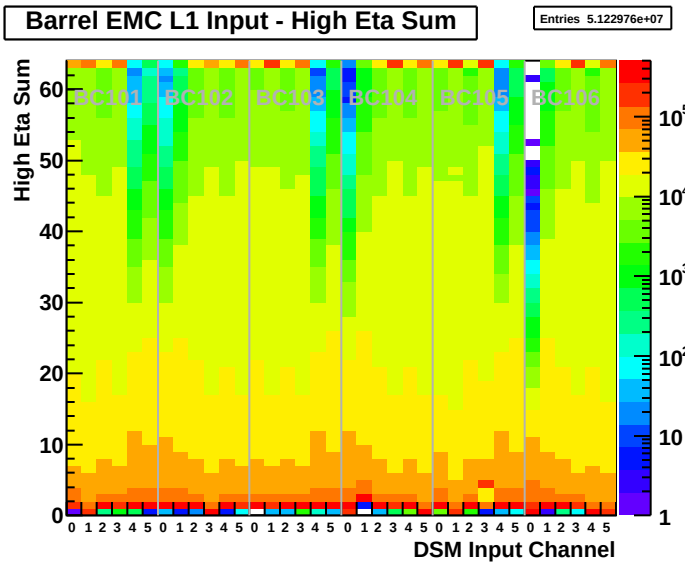
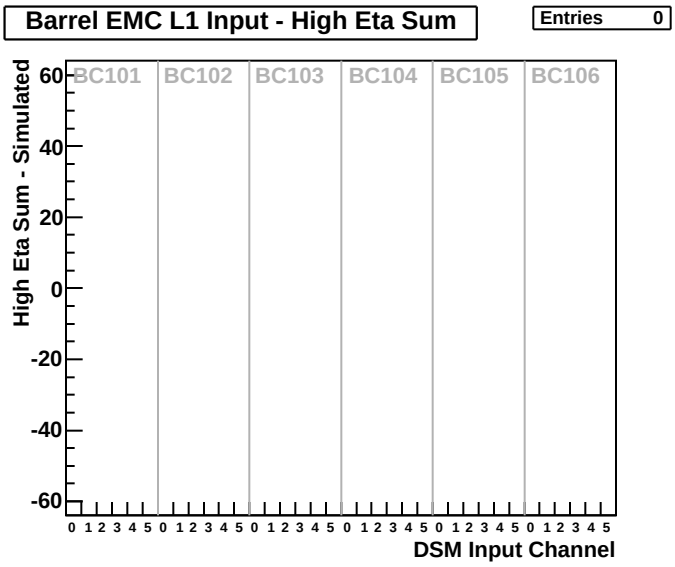
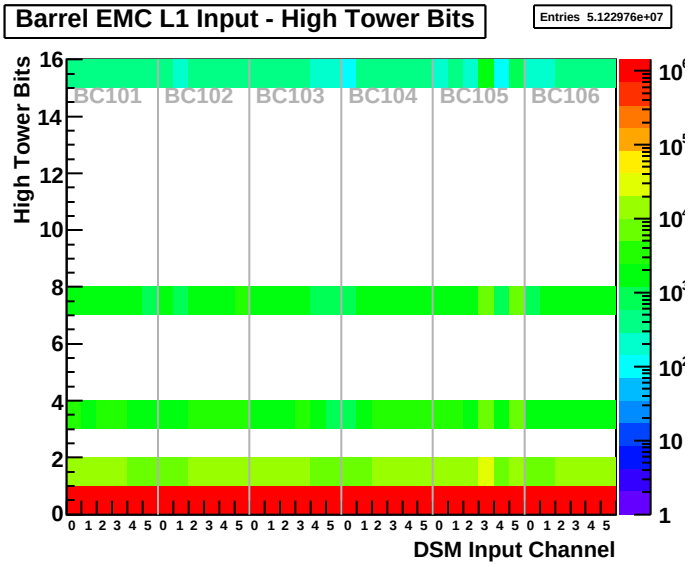
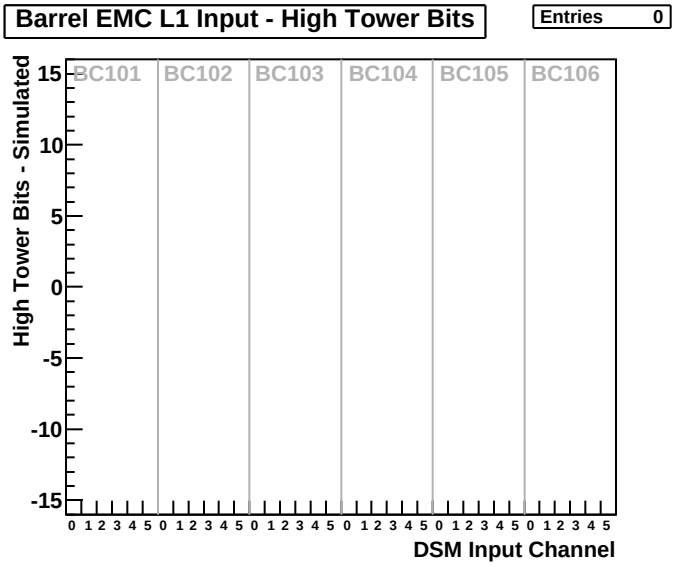

Barrel EMC L1 Input - Low Eta Sum
Entries 5.122976e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

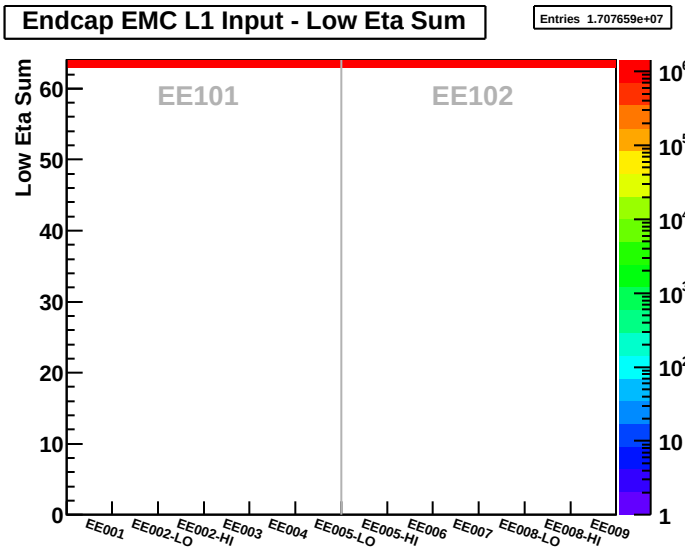

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

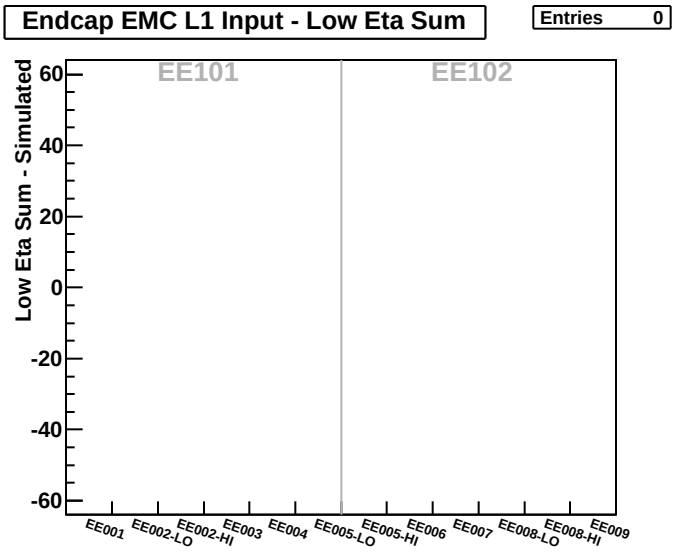

Barrel EMC L1 Input - High Eta Sum
Entries 5.122976e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

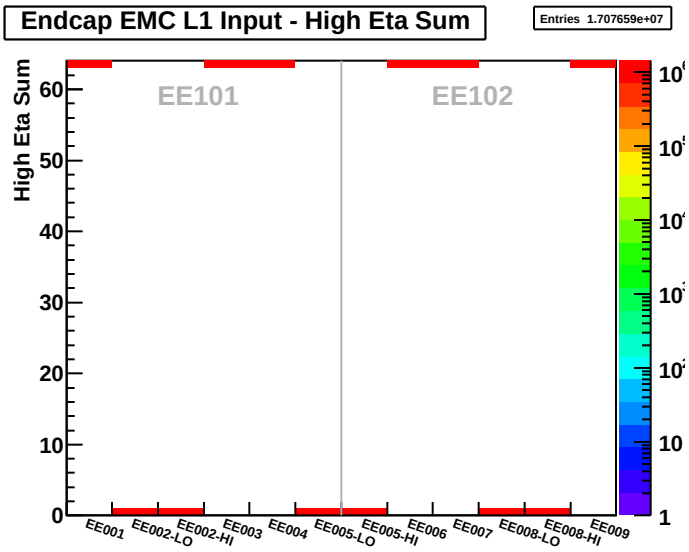

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

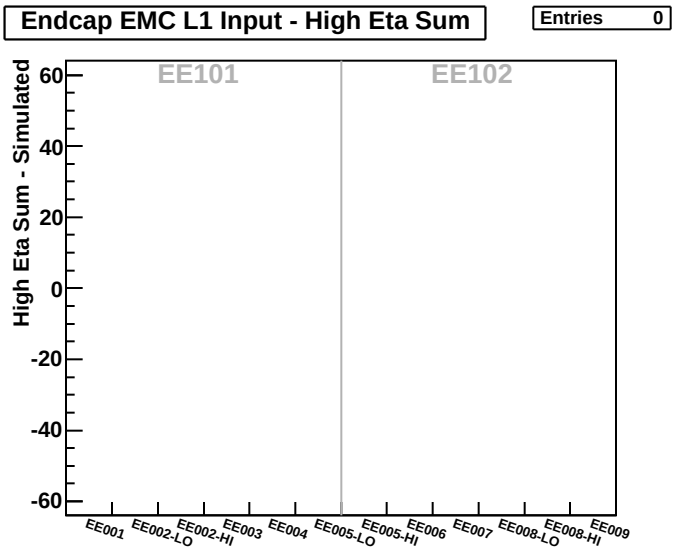

Barrel EMC L1 Input - High Tower Bits
Entries 5.122976e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

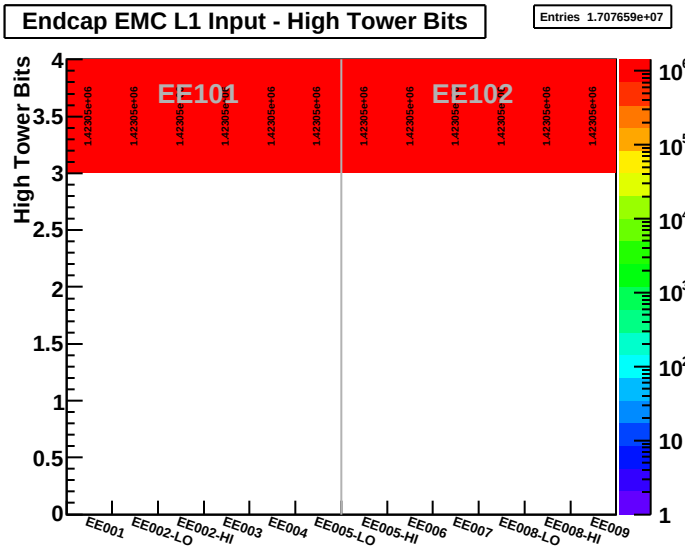

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

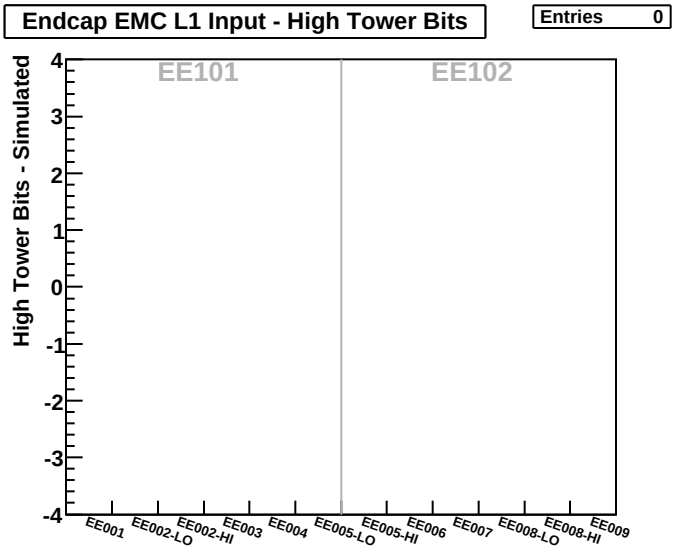

Endcap EMC L1 Input - Low Eta Sum
Entries 1.707659e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

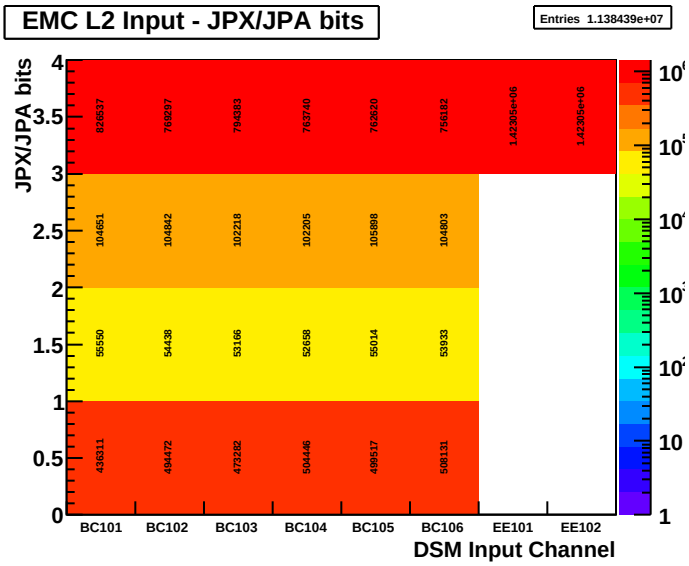

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

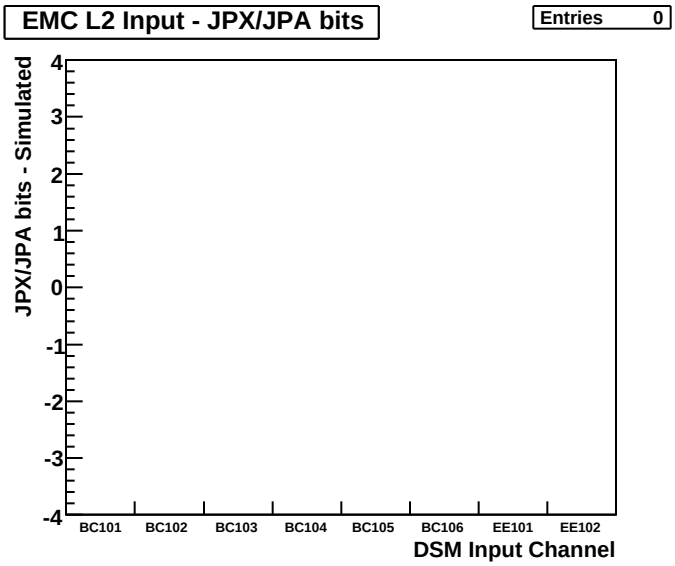

Endcap EMC L1 Input - High Eta Sum
Entries 1.707659e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

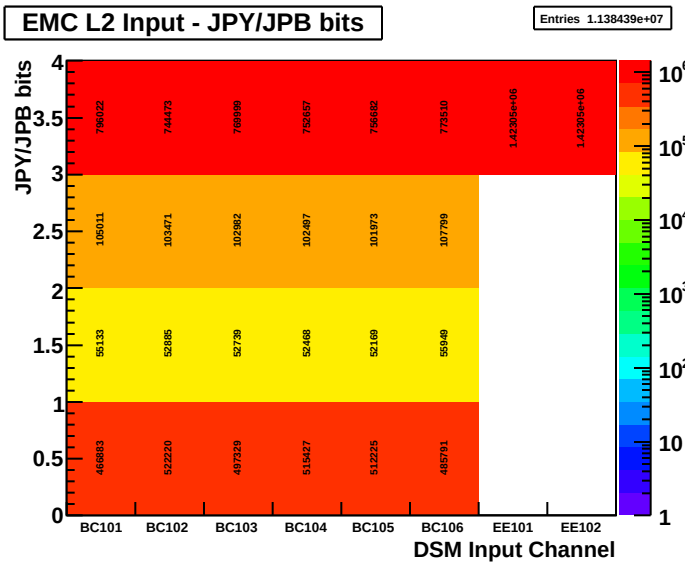

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

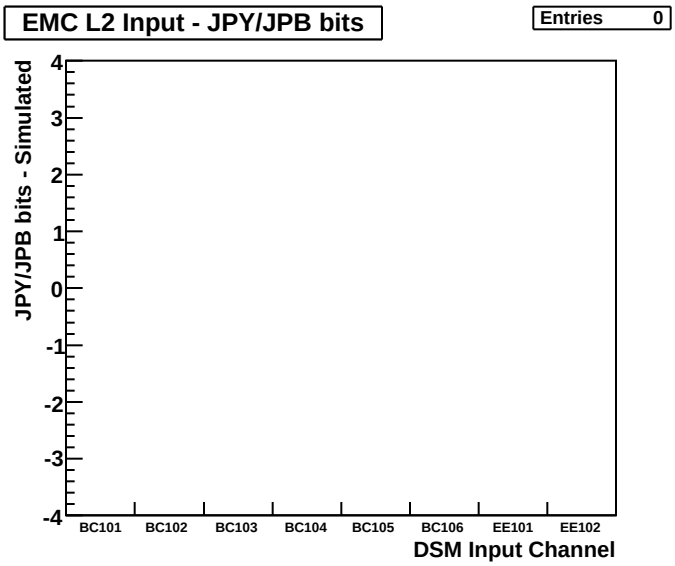

Endcap EMC L1 Input - High Tower Bits
Entries 1.707659e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

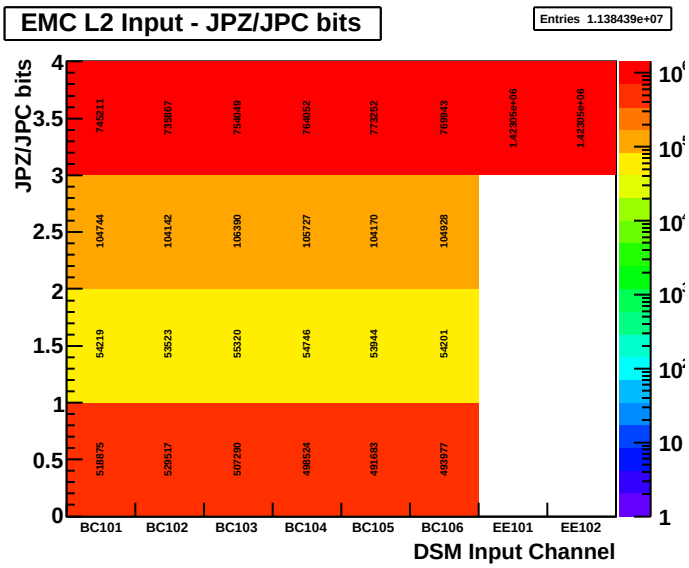

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

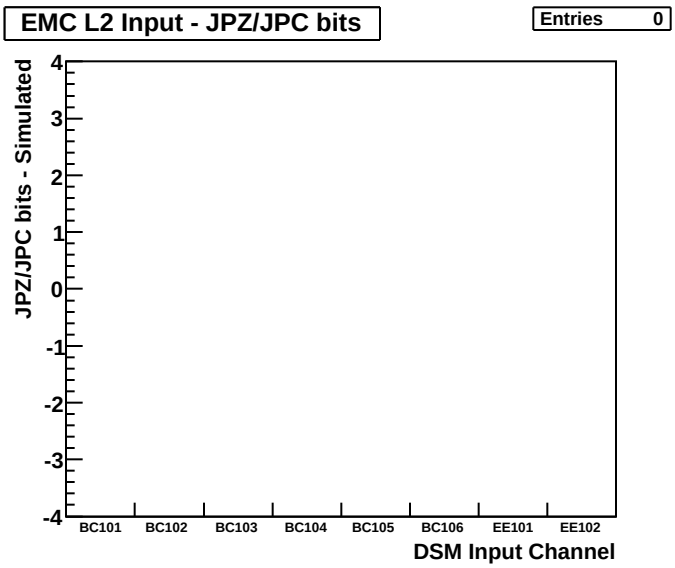

EMC L2 Input - JPX/JPA bits
Entries 1.138439e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
826537 769297 794383 783740 762820 756182 1.42305e+06 1.42305e+06
104651 104942 102218 102205 105898 104803
55550 54438 53166 52658 55014 53933
436311 494472 473282 504446 499517 508131

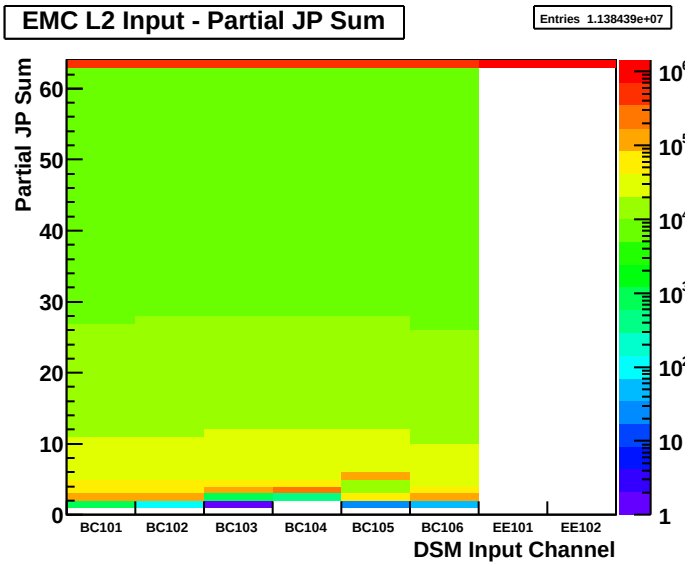

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

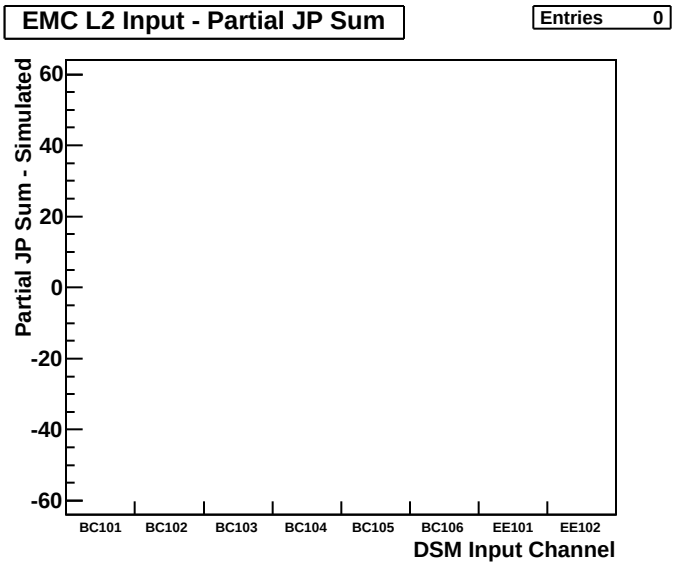

EMC L2 Input - JPY/JPB bits
Entries 1.138439e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
796022 744473 769999 752657 755682 773510 1.42305e+06 1.42305e+06
105011 103471 102982 102497 101973 107799
55133 52885 52739 52468 52169 55949
466883 522220 497329 515427 512225 485791

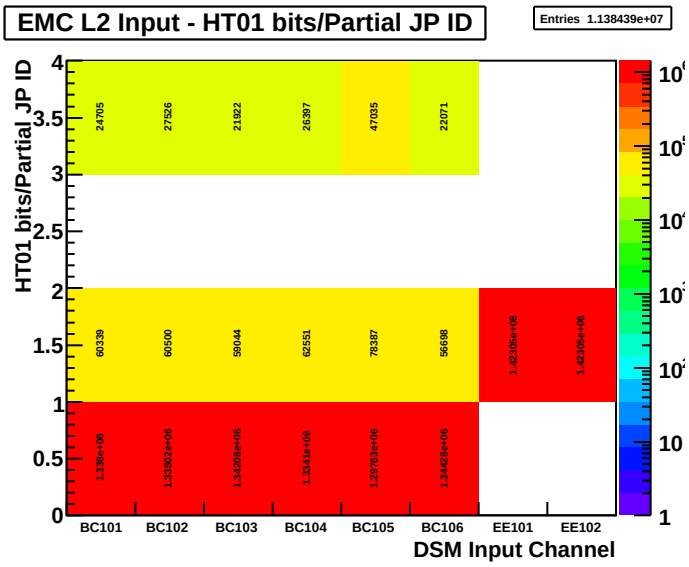

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

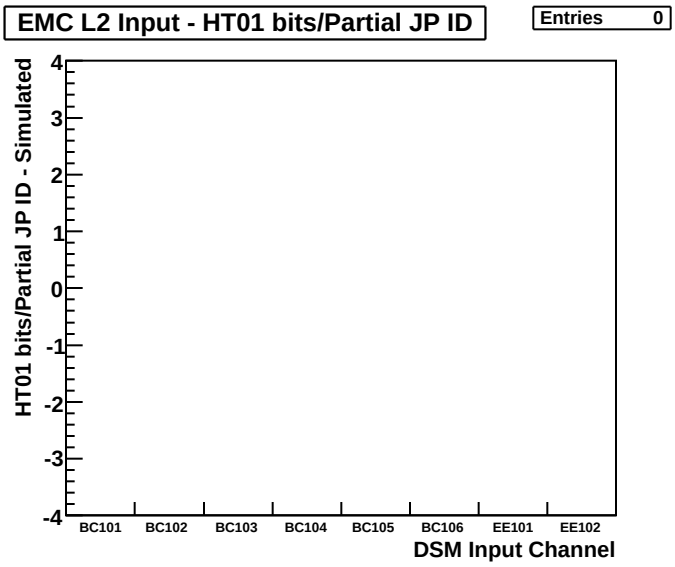

EMC L2 Input - JPZ/JPC bits
Entries 1.138439e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
745211 738567 754049 764052 772952 769943 1.42305e+06 1.42305e+06
104744 104142 106390 105727 104170 104928
54219 53623 55320 54746 53944 54201
518875 529517 507290 498524 491683 493977

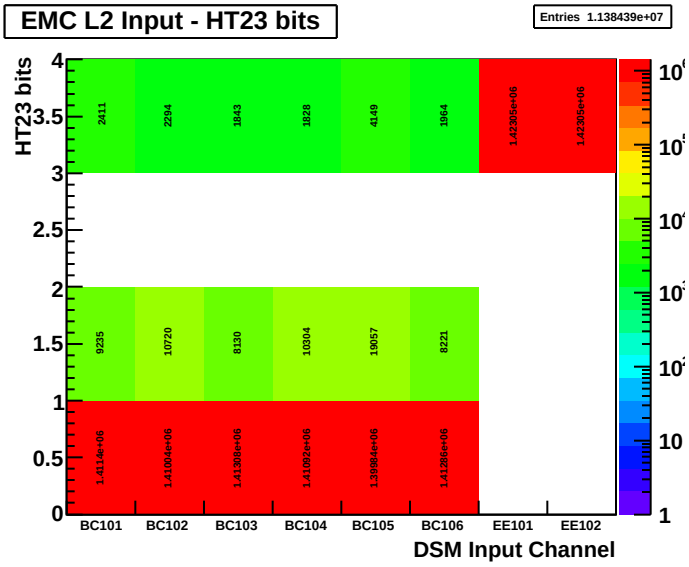

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

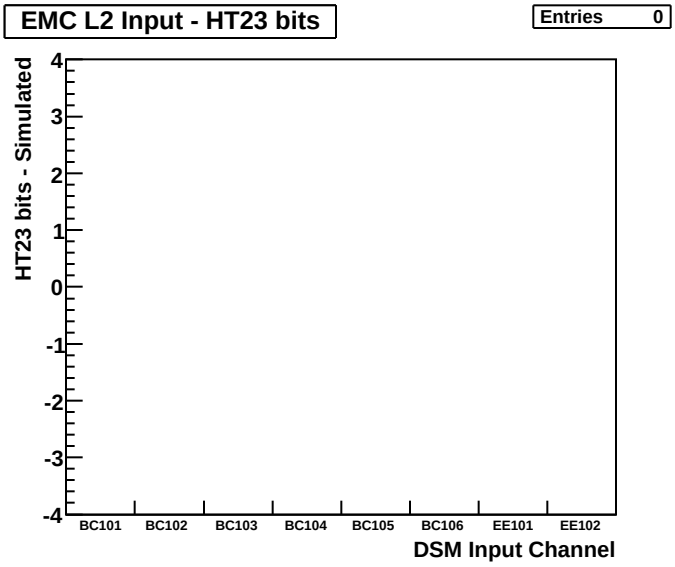

EMC L2 Input - Partial JP Sum
Entries 1.138439e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

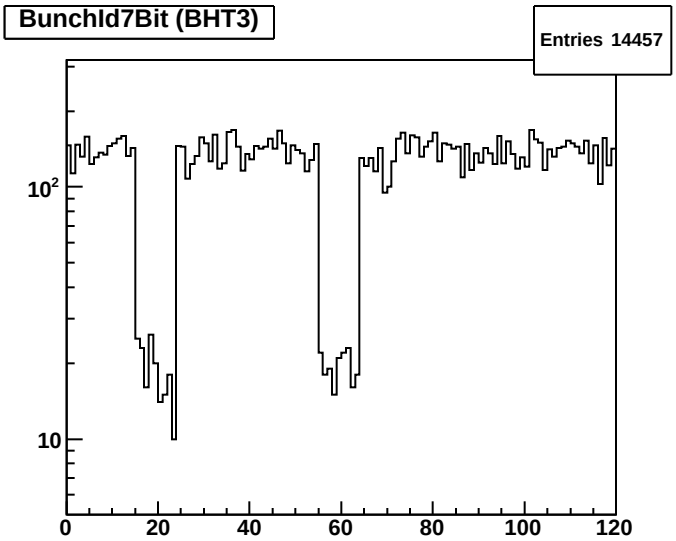

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

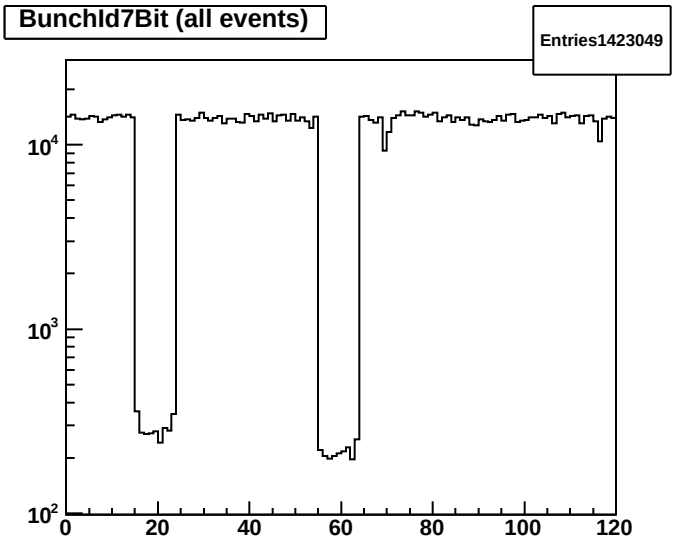

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.138439e+07
HT01 bits/Partial JP ID
24705 27526 21922 26397 47035 22071
60339 60500 59044 62551 76387 56698 1.42305e+06 1.42305e+06
1.338e+06 1.33502e+06 1.34208e+06 1.3341e+06 1.29783e+06 1.34428e+06
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

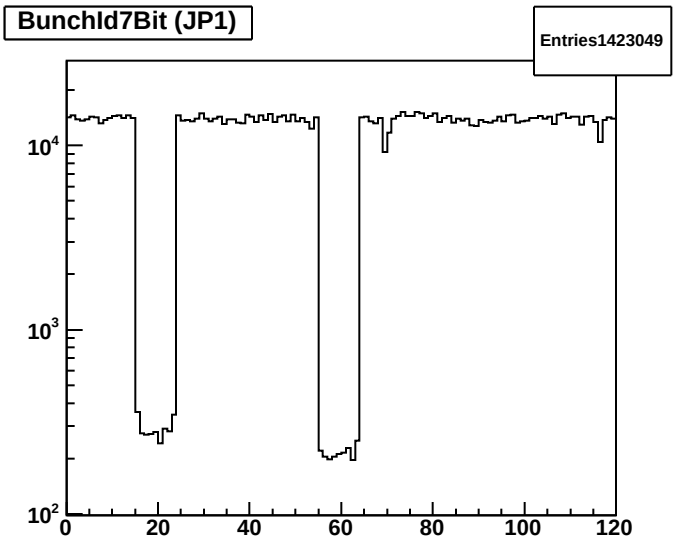

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

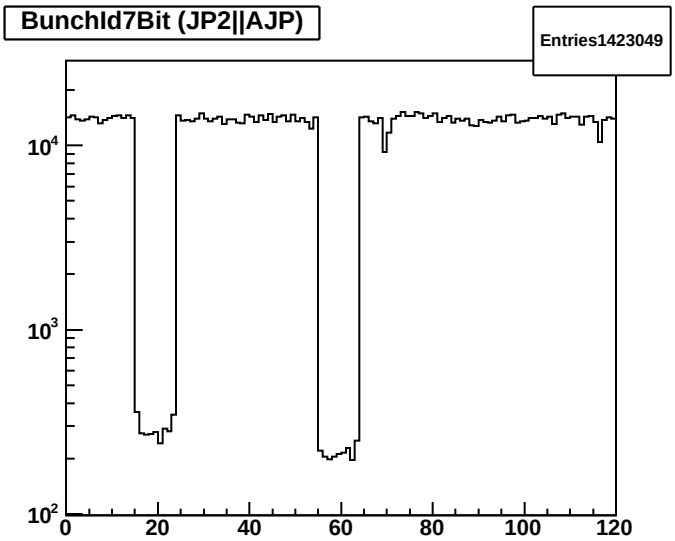

EMC L2 Input - HT23 bits
Entries 1.138439e+07
HT23 bits
2411 2294 1943 1928 4149 1964 1.42305e+06 1.42305e+06
9235 10720 8130 10304 19057 8221
1.4114e+06 1.41004e+06 1.41308e+06 1.41092e+06 1.39984e+06 1.41286e+06
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

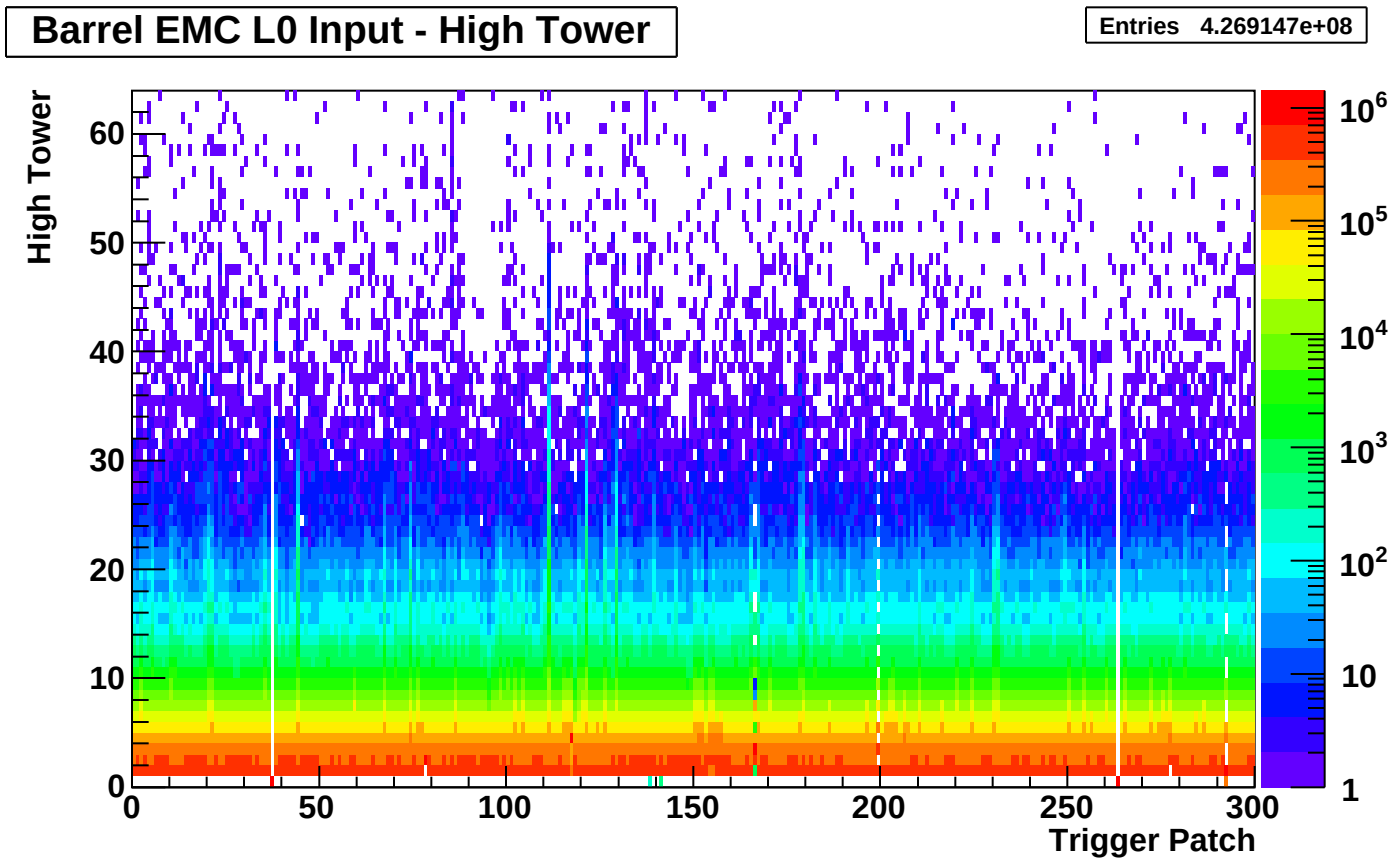

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

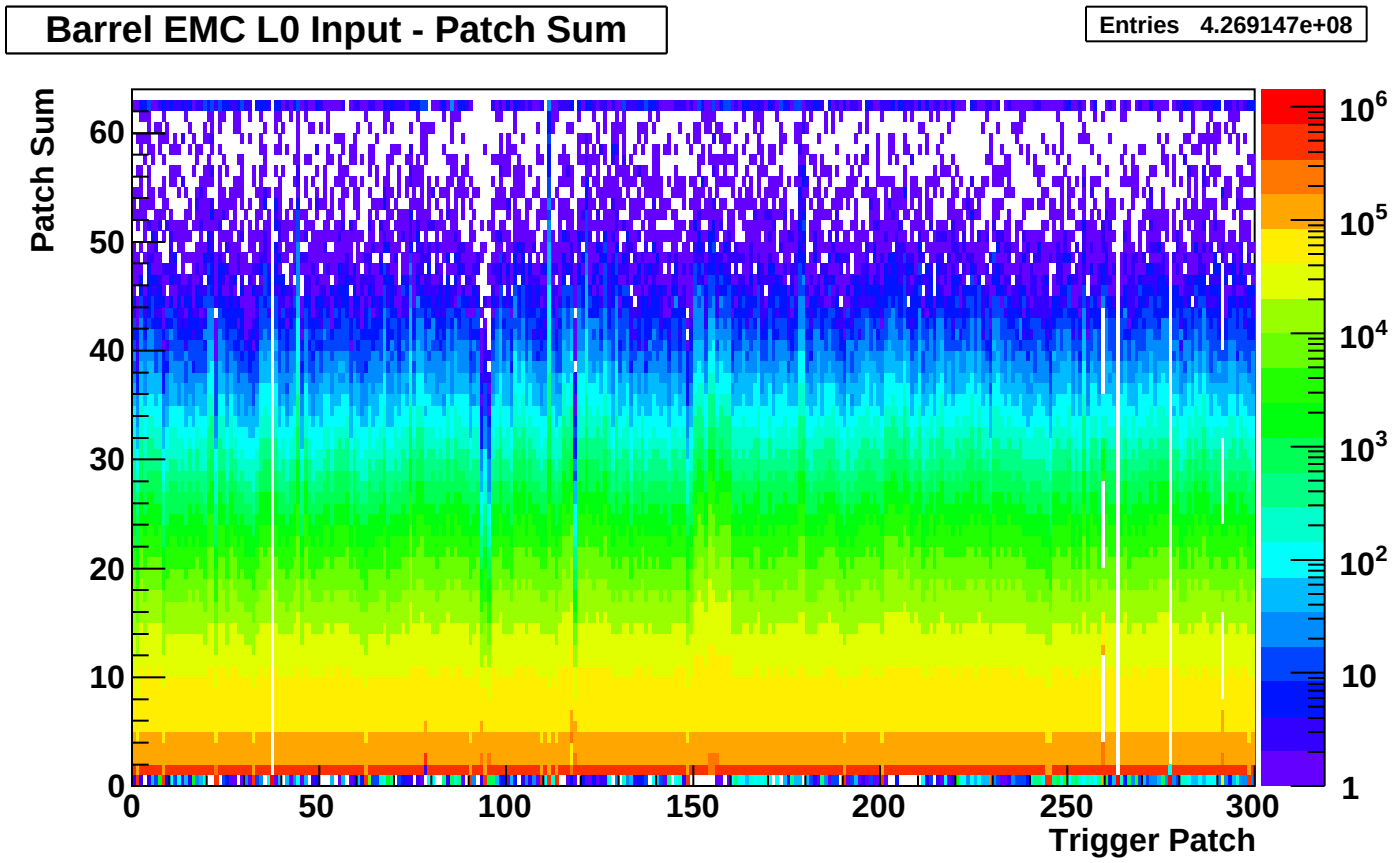

BunchId7Bit (BHT3)
Entries 14457
10^2
10
0
20
40
60
80
100
120

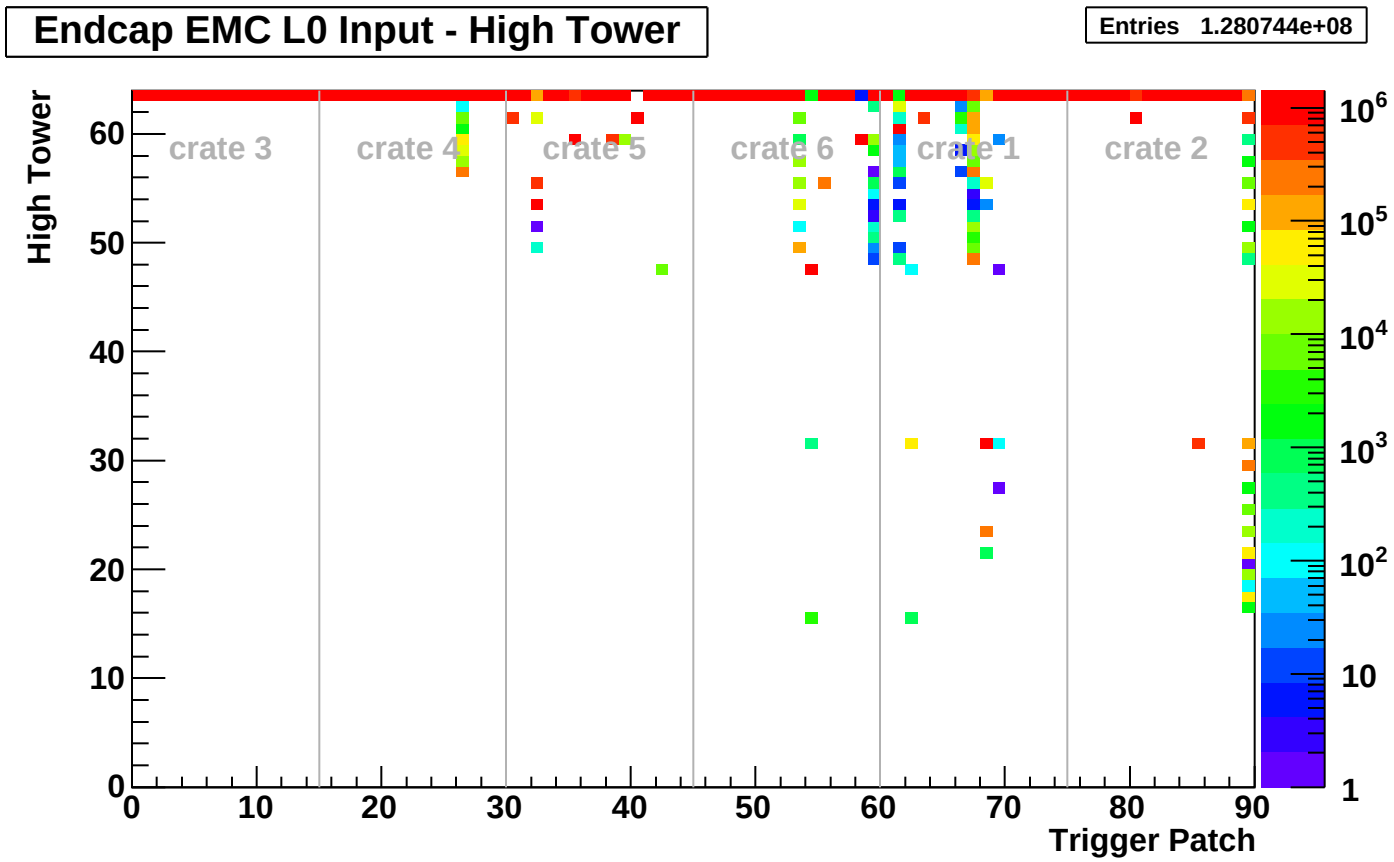

BunchId7Bit (all events)
Entries1423049
10^4
10^3
10^2
0
20
40
60
80
100
120

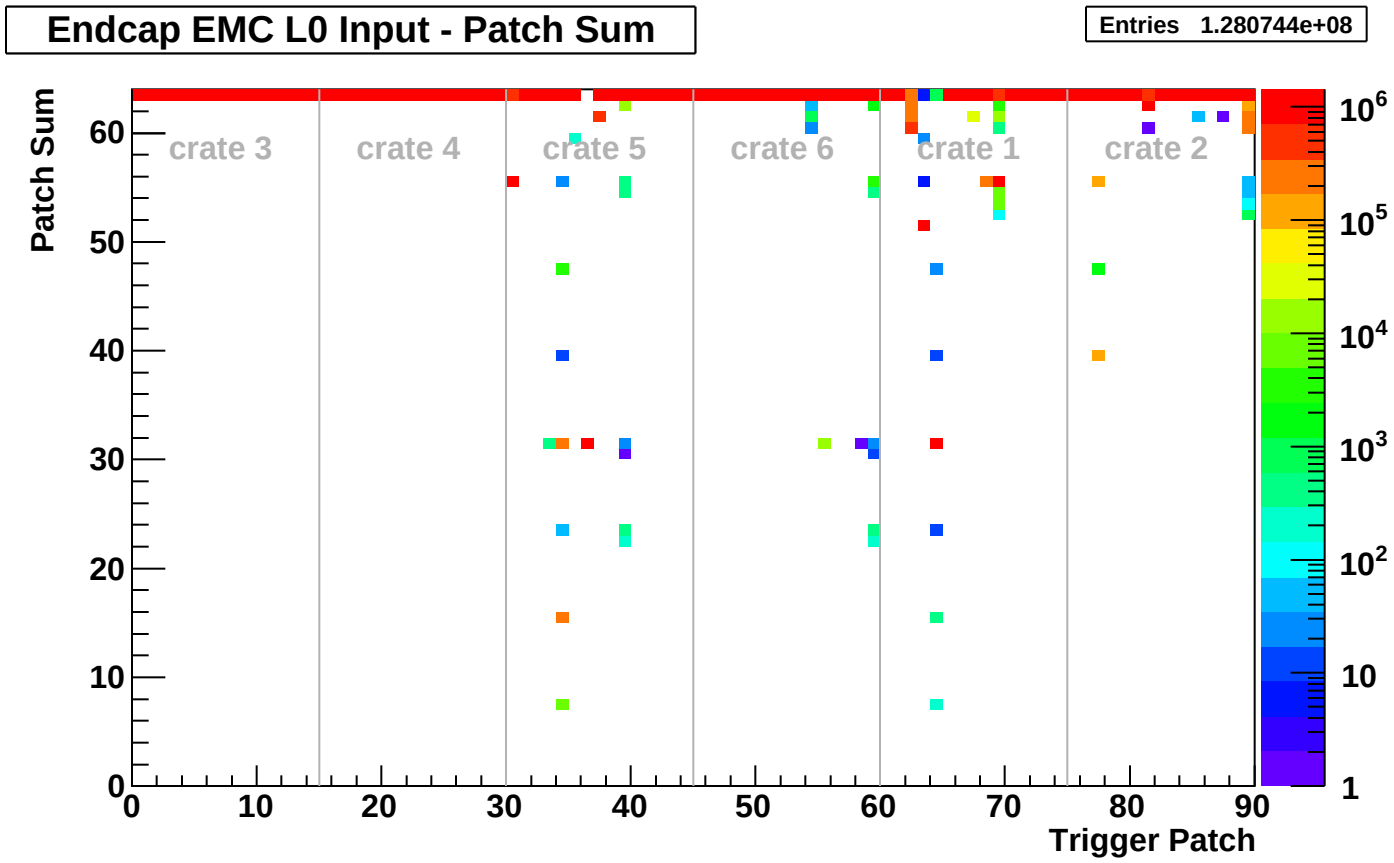

BunchId7Bit (JP1)
Entries1423049
10^4
10^3
10^2
0
20
40
60
80
100
120

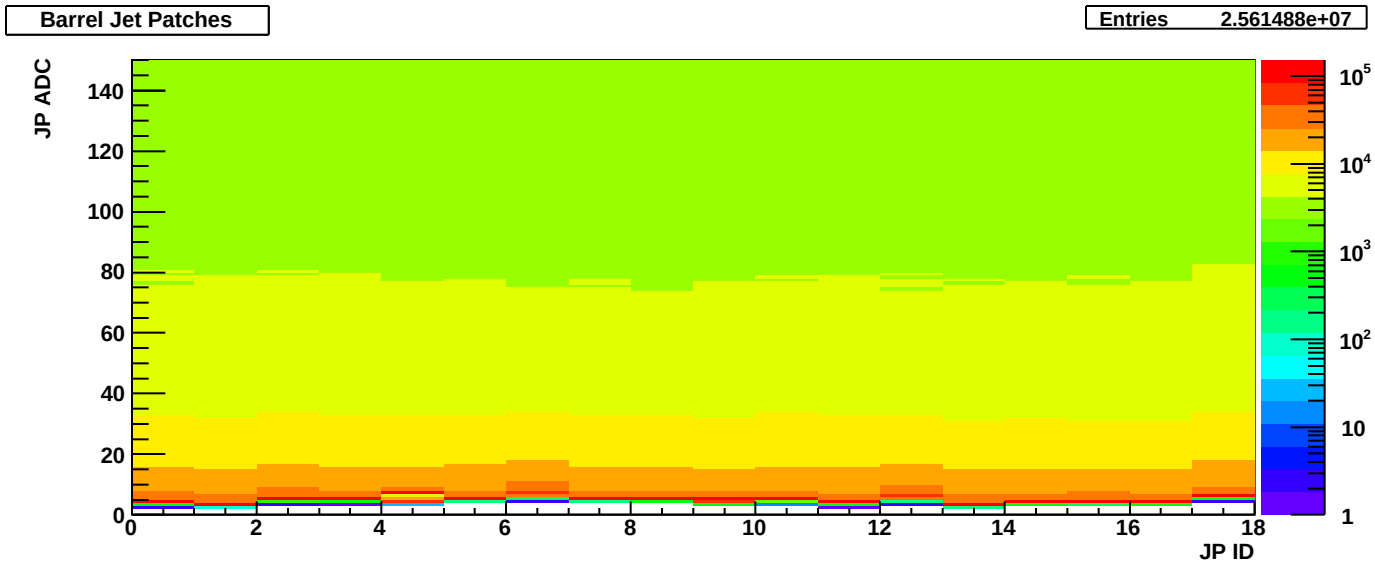

BunchId7Bit (JP2||AJP)
Entries1423049
10^4
10^3
10^2
0
20
40
60
80
100
120

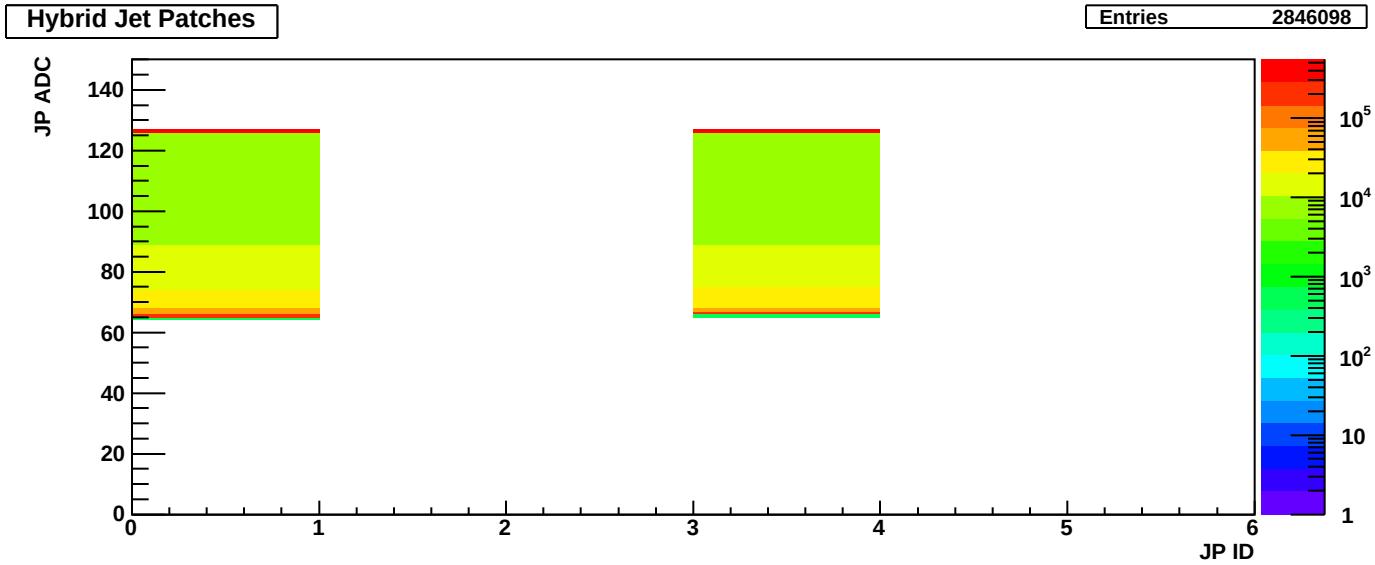

Barrel EMC L0 Input - High Tower
Entries 4.269147e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

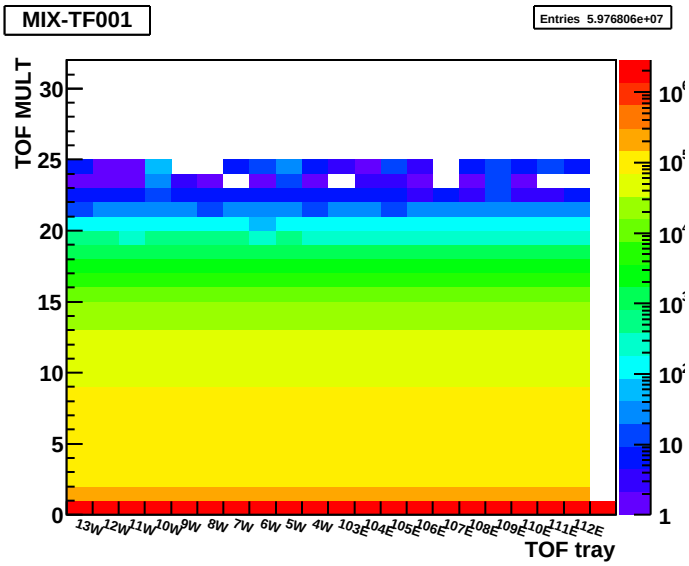

Barrel EMC L0 Input - Patch Sum
Entries 4.269147e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

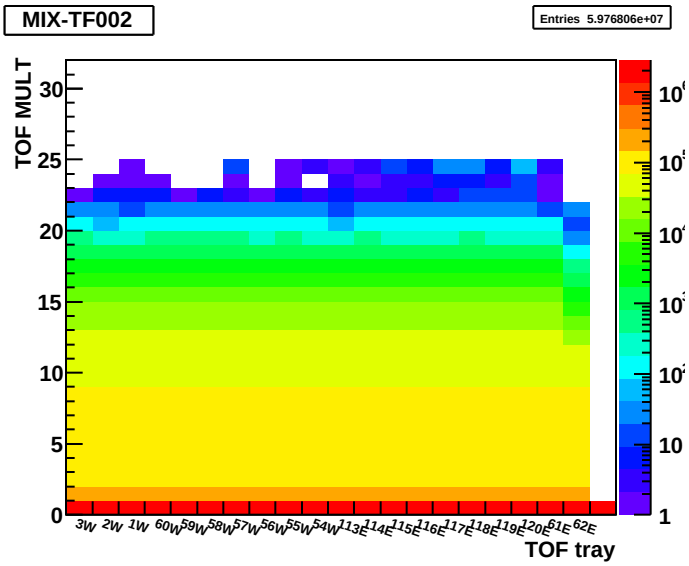

Endcap EMC L0 Input - High Tower
Entries 1.280744e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

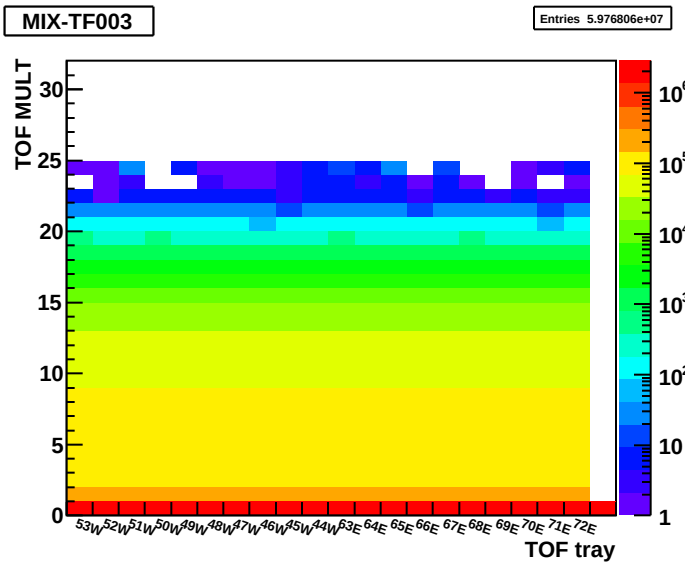

Endcap EMC L0 Input - Patch Sum
Entries 1.280744e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

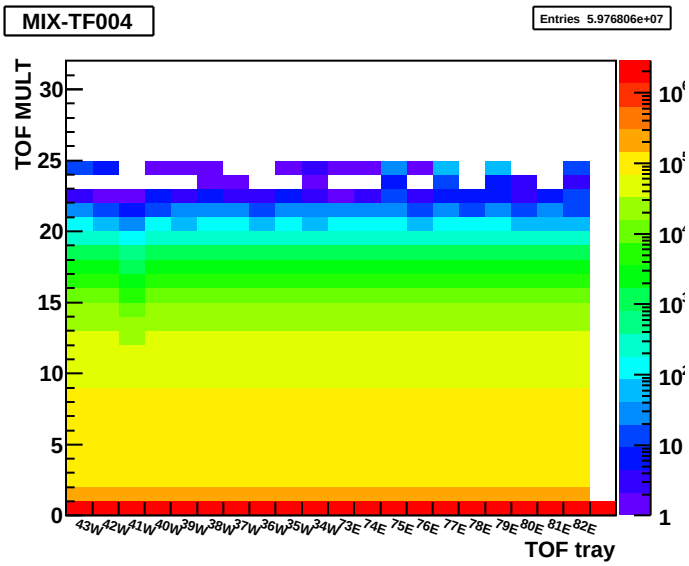

Barrel Jet Patches
Entries 2.561488e+07
JP ADC
JP ID

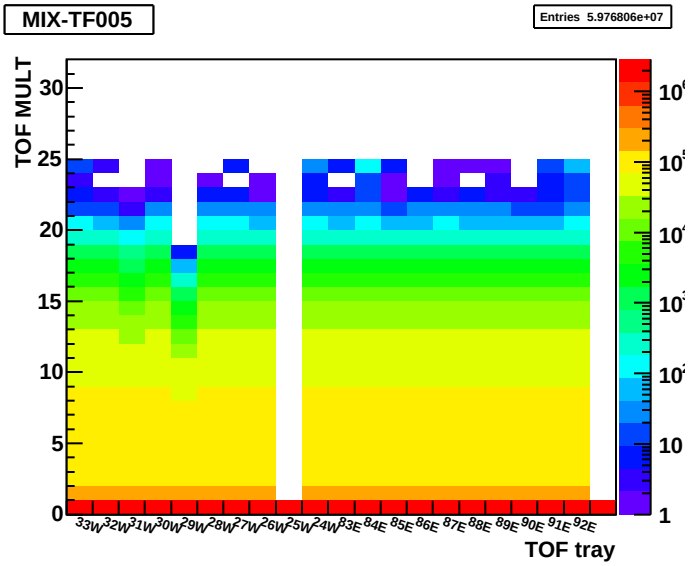

Hybrid Jet Patches
Entries 2846098
JP ADC
JP ID

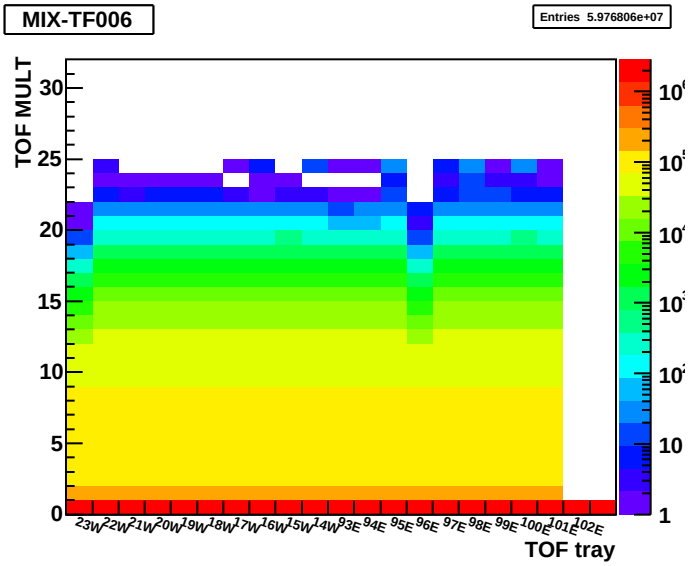

MIX-TF001
Entries 5.976806e+07
TOF MULT
TOF tray

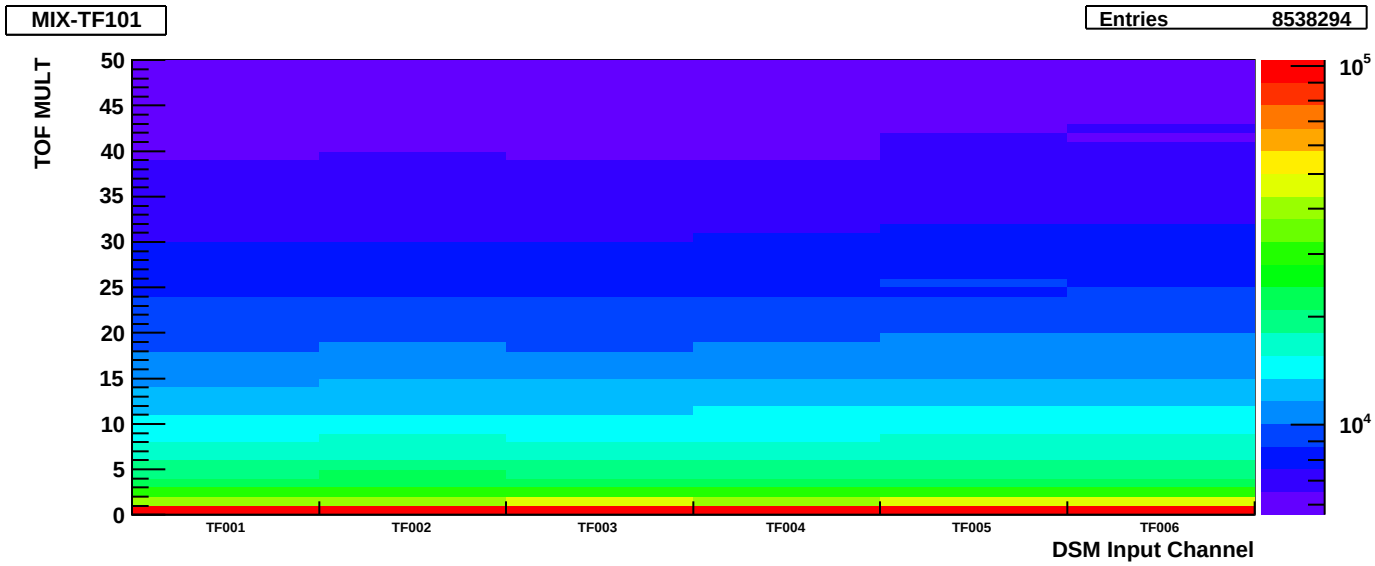

MIX-TF002
Entries 5.976806e+07
TOF MULT
TOF tray

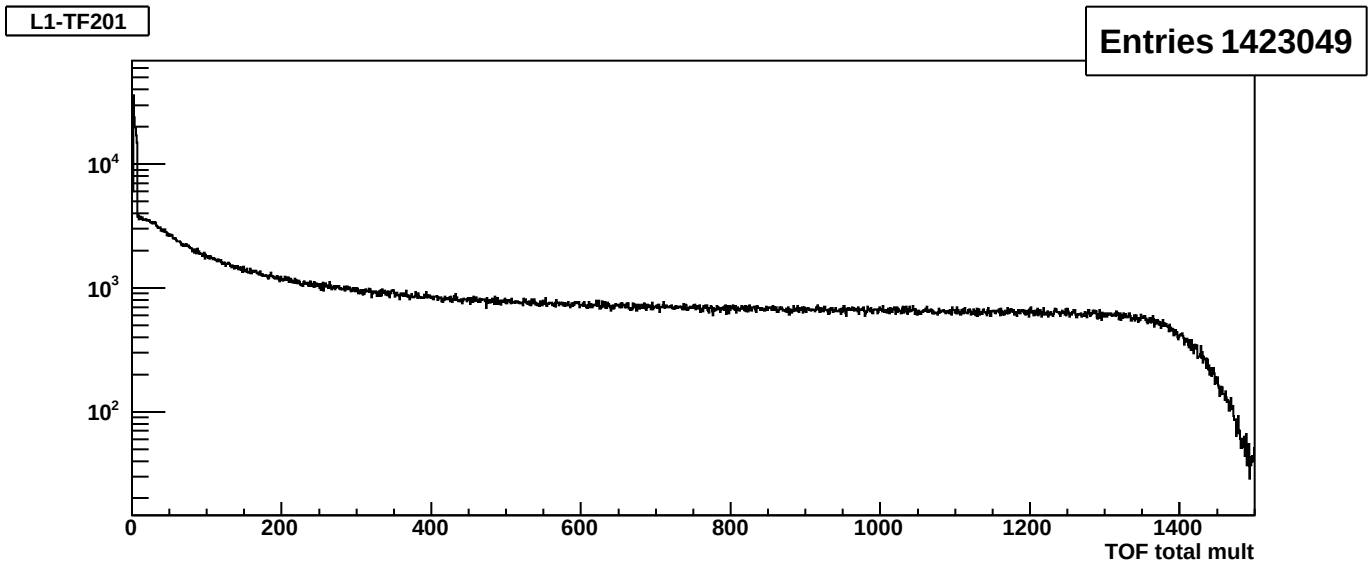

MIX-TF003
Entries 5.976806e+07
TOF MULT
TOF tray

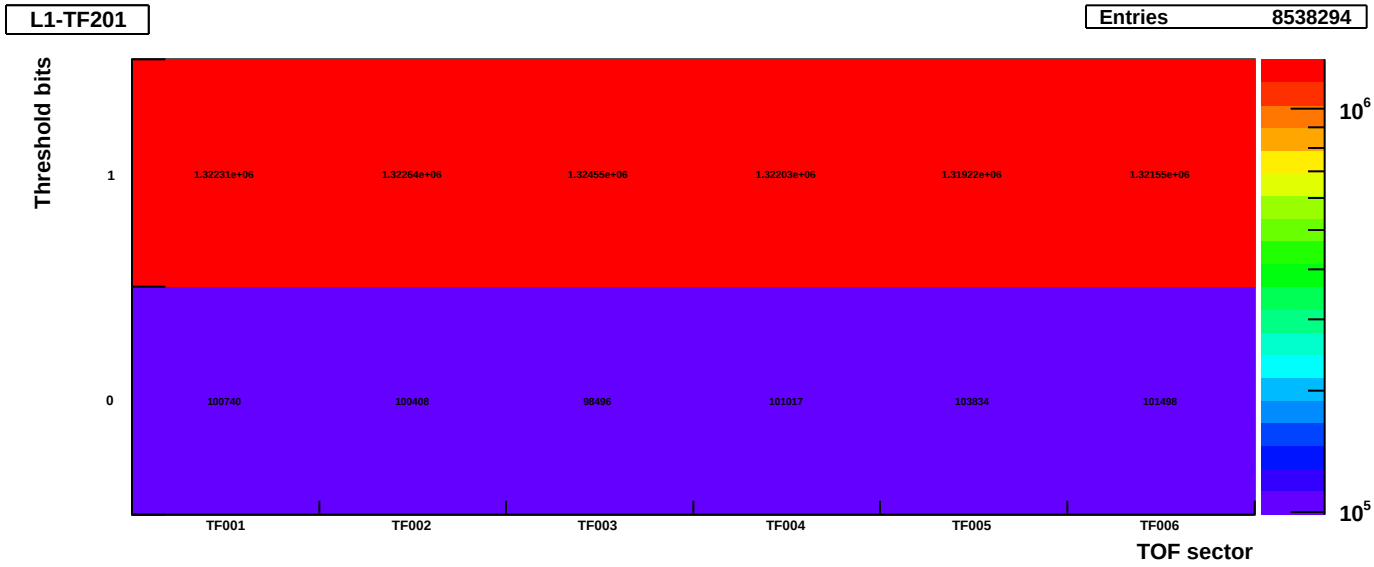

MIX-TF004
Entries 5.976806e+07
TOF MULT
TOF tray

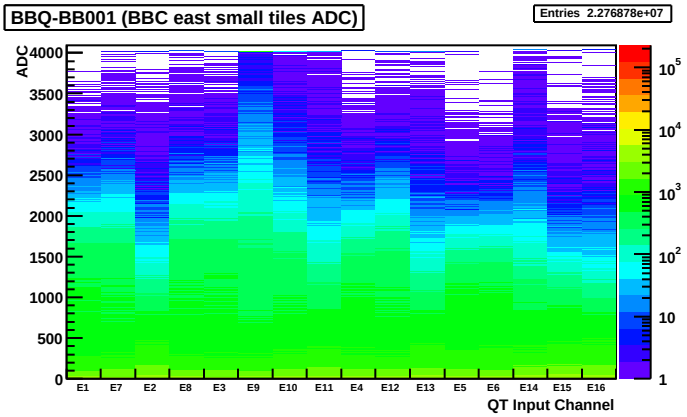

MIX-TF005
Entries 5.976806e+07
TOF MULT
TOF tray

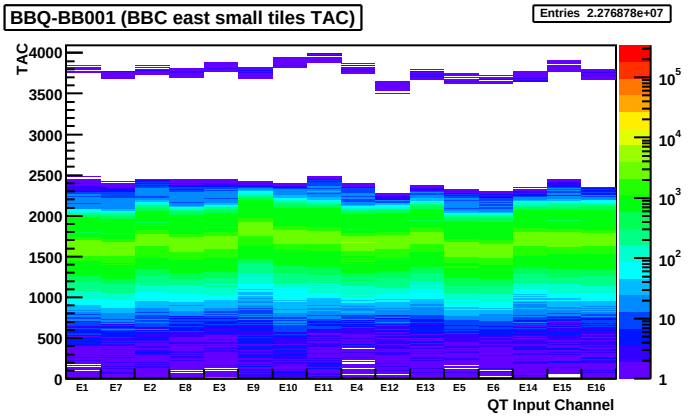

MIX-TF006
Entries 5.976806e+07
TOF MULT
TOF tray

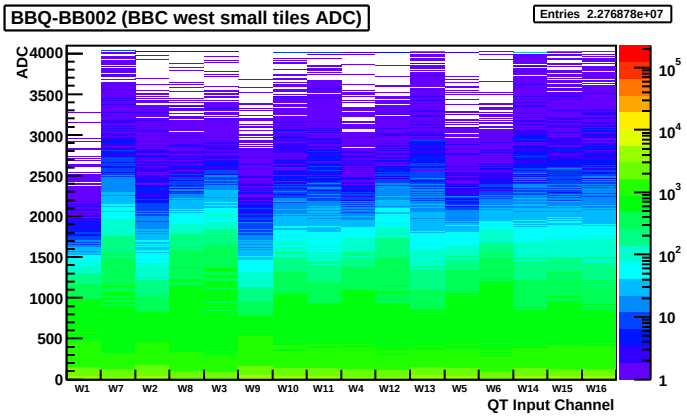

MIX-TF101
Entries 8538294
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4

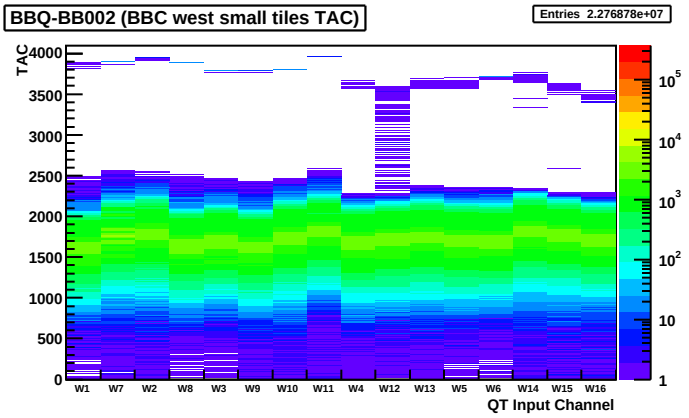

L1-TF201
Entries 1423049
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

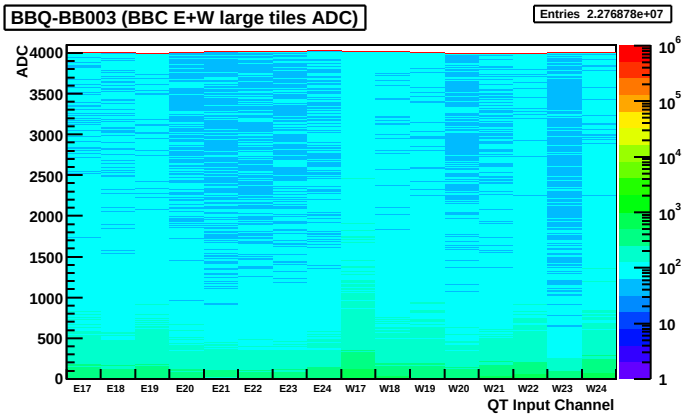

L1-TF201
Entries 8538294
Threshold bits
1
0
1.32231e+06
1.32264e+06
1.32455e+06
1.32203e+06
1.31922e+06
1.32155e+06
100740
100408
98496
101017
103834
101498
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

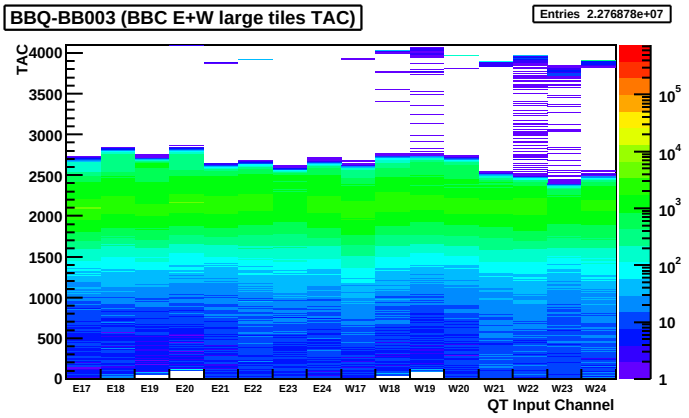

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.276878e+07
ADC
QT Input Channel

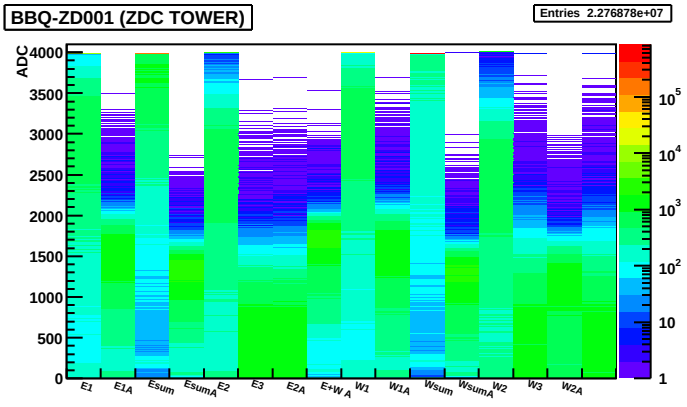

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.276878e+07
TAC
QT Input Channel

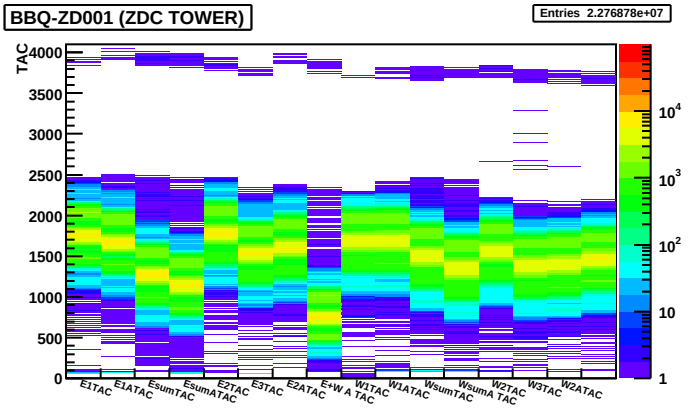

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.276878e+07
ADC
QT Input Channel

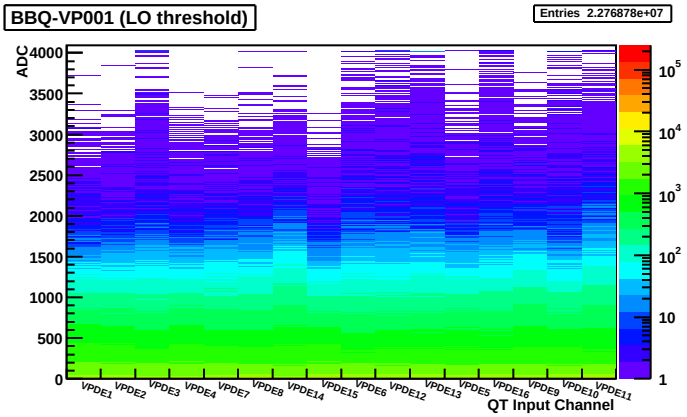

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.276878e+07
TAC
QT Input Channel

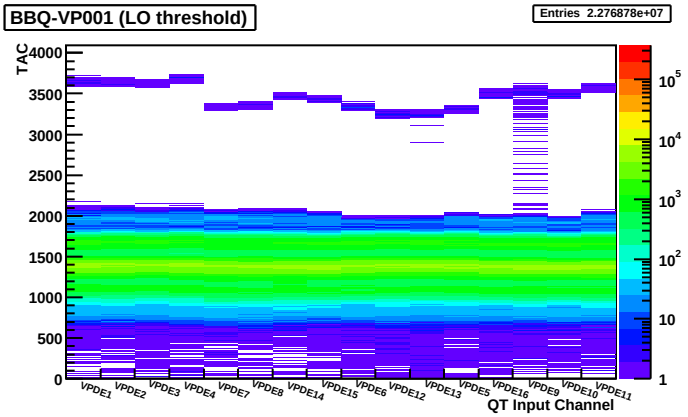

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.276878e+07
ADC
QT Input Channel

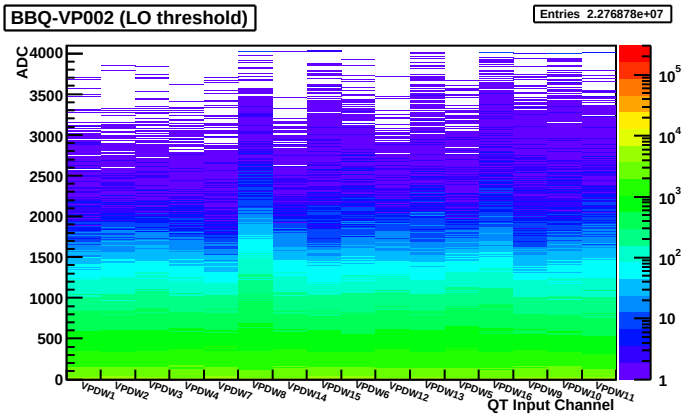

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.276878e+07
TAC
QT Input Channel

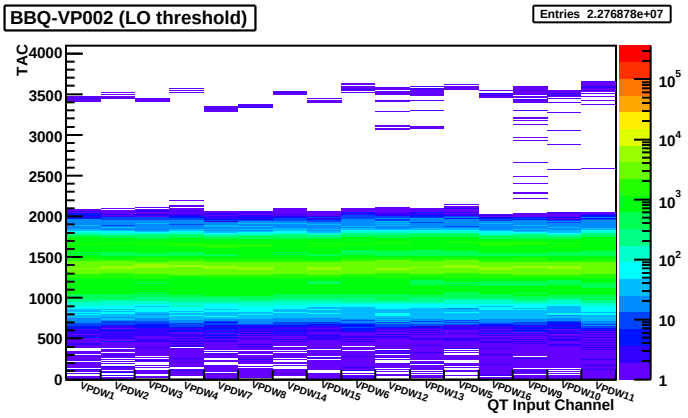

BBQ-ZD001 (ZDC TOWER)
Entries 2.276878e+07
ADC

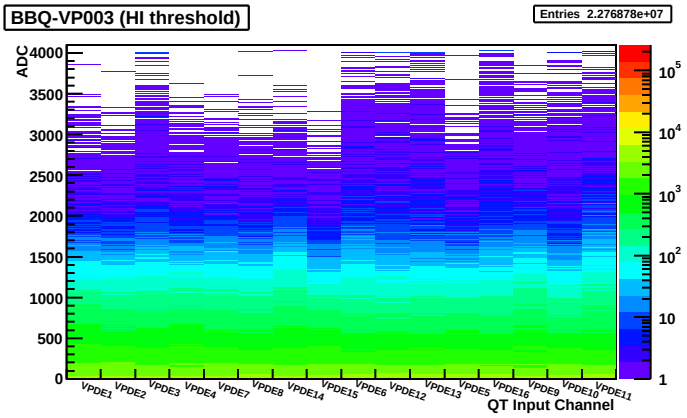

BBQ-ZD001 (ZDC TOWER)
Entries 2.276878e+07
TAC

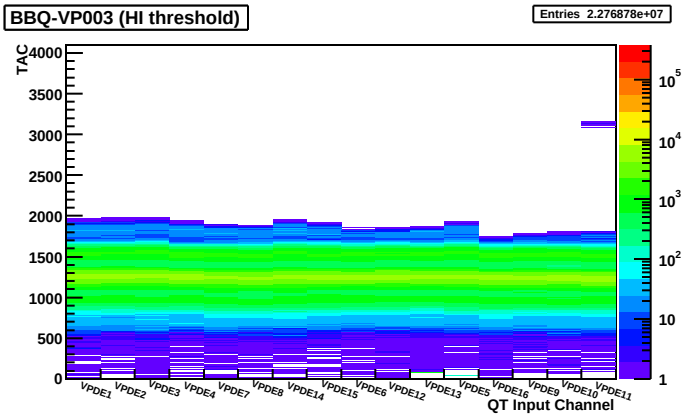

BBQ-VP001 (LO threshold)
Entries 2.276878e+07
ADC
QT Input Channel

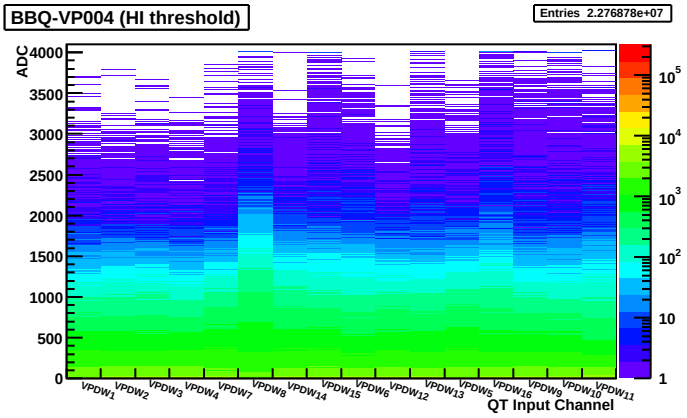

BBQ-VP001 (LO threshold)
Entries 2.276878e+07
TAC
QT Input Channel

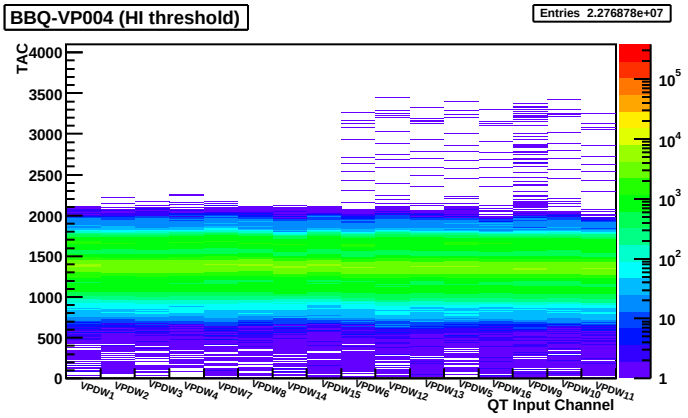

BBQ-VP002 (LO threshold)
Entries 2.276878e+07
ADC
QT Input Channel

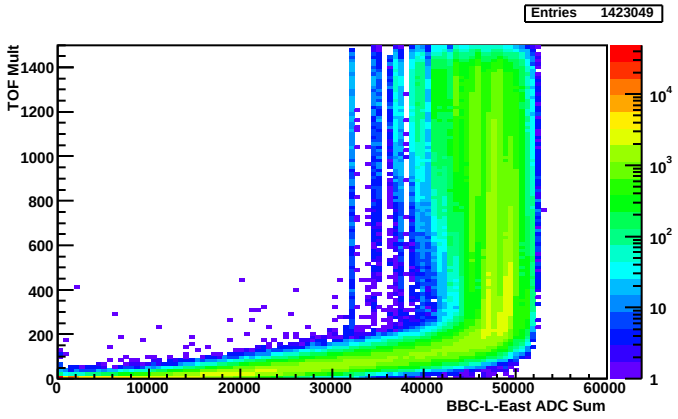

BBQ-VP002 (LO threshold)
Entries 2.276878e+07
TAC
QT Input Channel

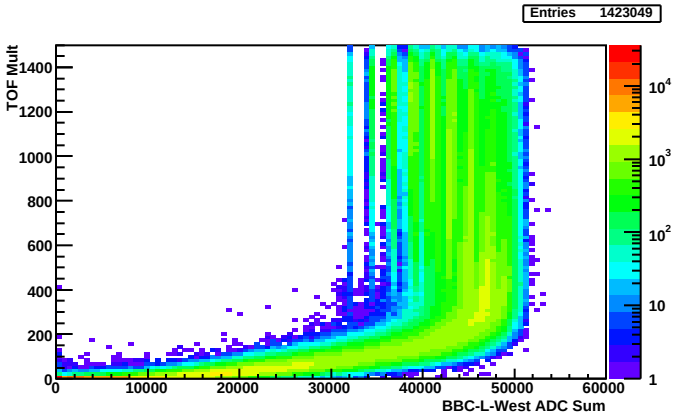

BBQ-VP003 (HI threshold)
Entries 2.276878e+07
ADC
QT Input Channel

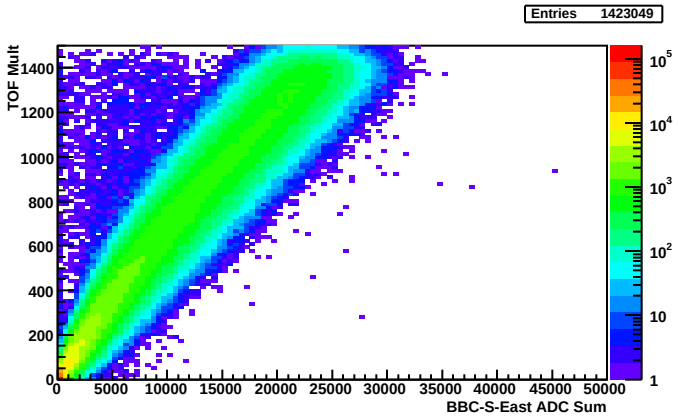

BBQ-VP003 (HI threshold)
Entries 2.276878e+07
TAC
QT Input Channel

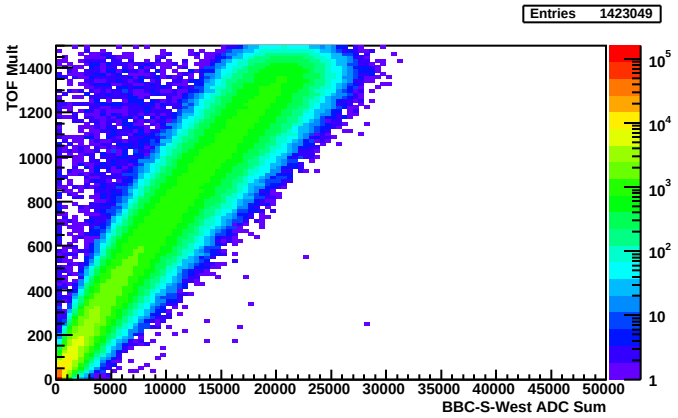

BBQ-VP004 (HI threshold)
Entries 2.276878e+07
ADC
QT Input Channel

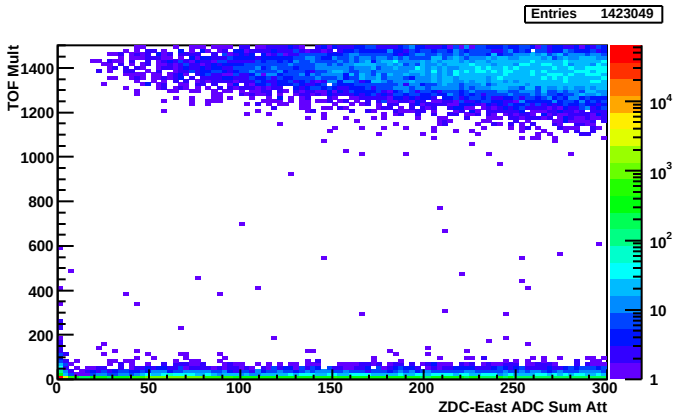

BBQ-VP004 (HI threshold)
Entries 2.276878e+07
TAC
QT Input Channel

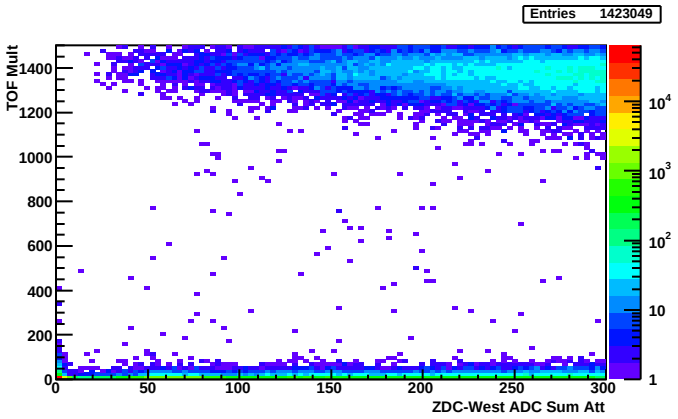

Entries 1423049
TOF Mult
BBC-L-East ADC Sum

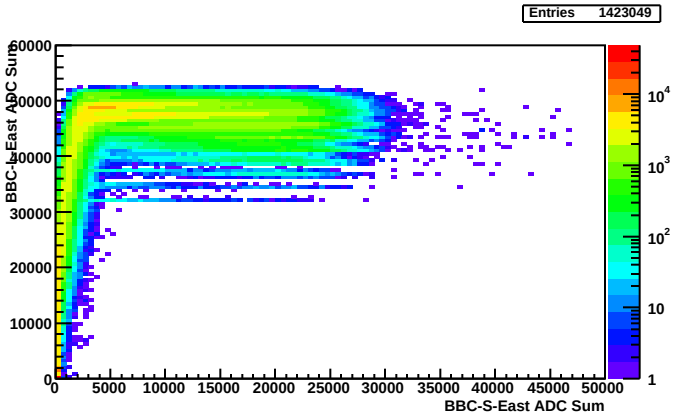

Entries 1423049
TOF Mult
BBC-L-West ADC Sum

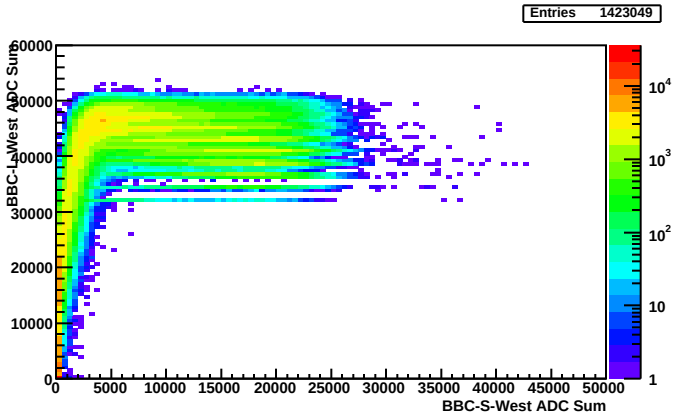

Entries 1423049
TOF Mult
BBC-S-East ADC Sum

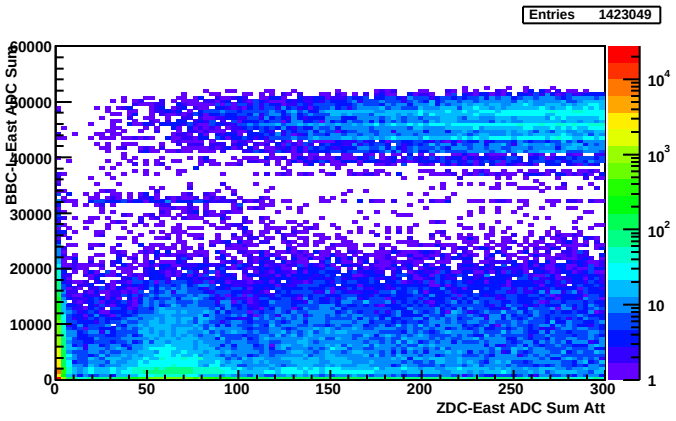

Entries 1423049
TOF Mult
BBC-S-West ADC Sum

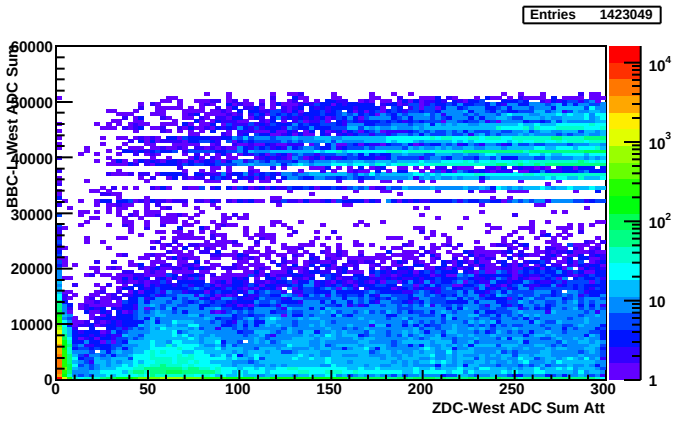

Entries 1423049
TOF Mult
ZDC-East ADC Sum Att


Entries 1423049
TOF Mult
ZDC-West ADC Sum Att


Entries 1423049
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1423049
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1423049
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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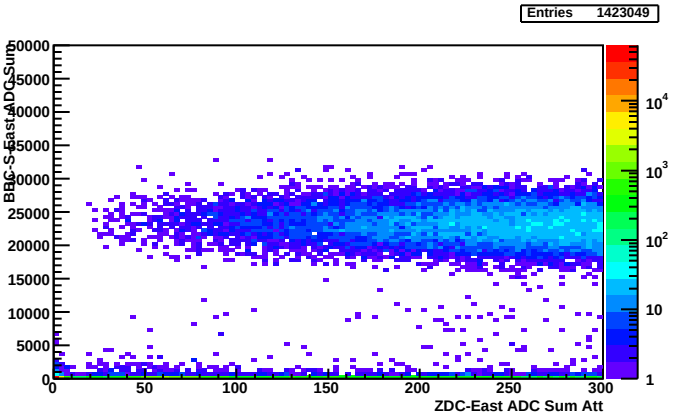
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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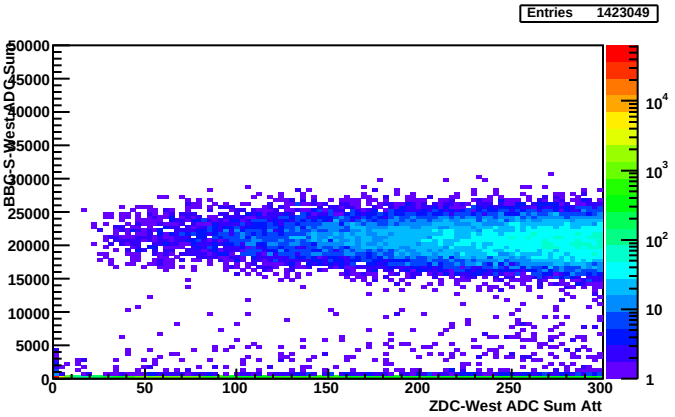
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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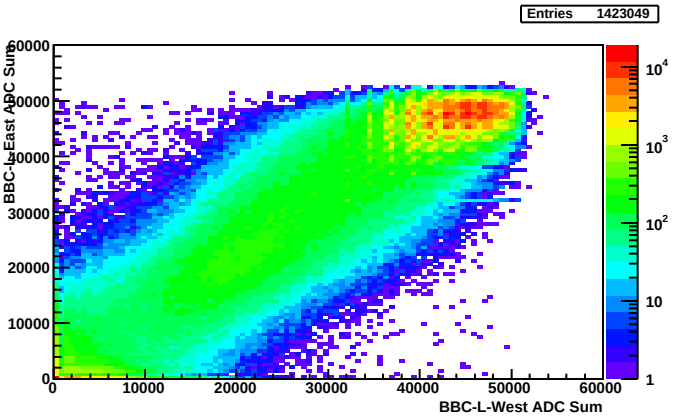
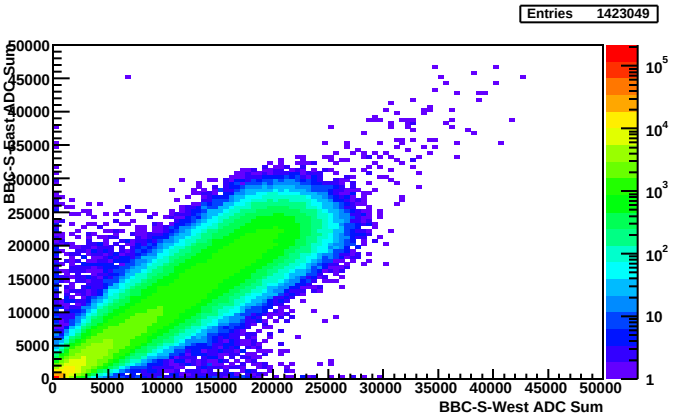
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1423049
BBC-L-East ADC Sum
BBC-L-West ADC Sum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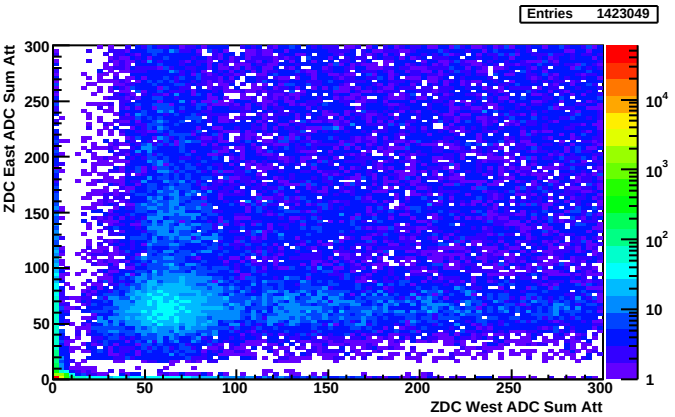
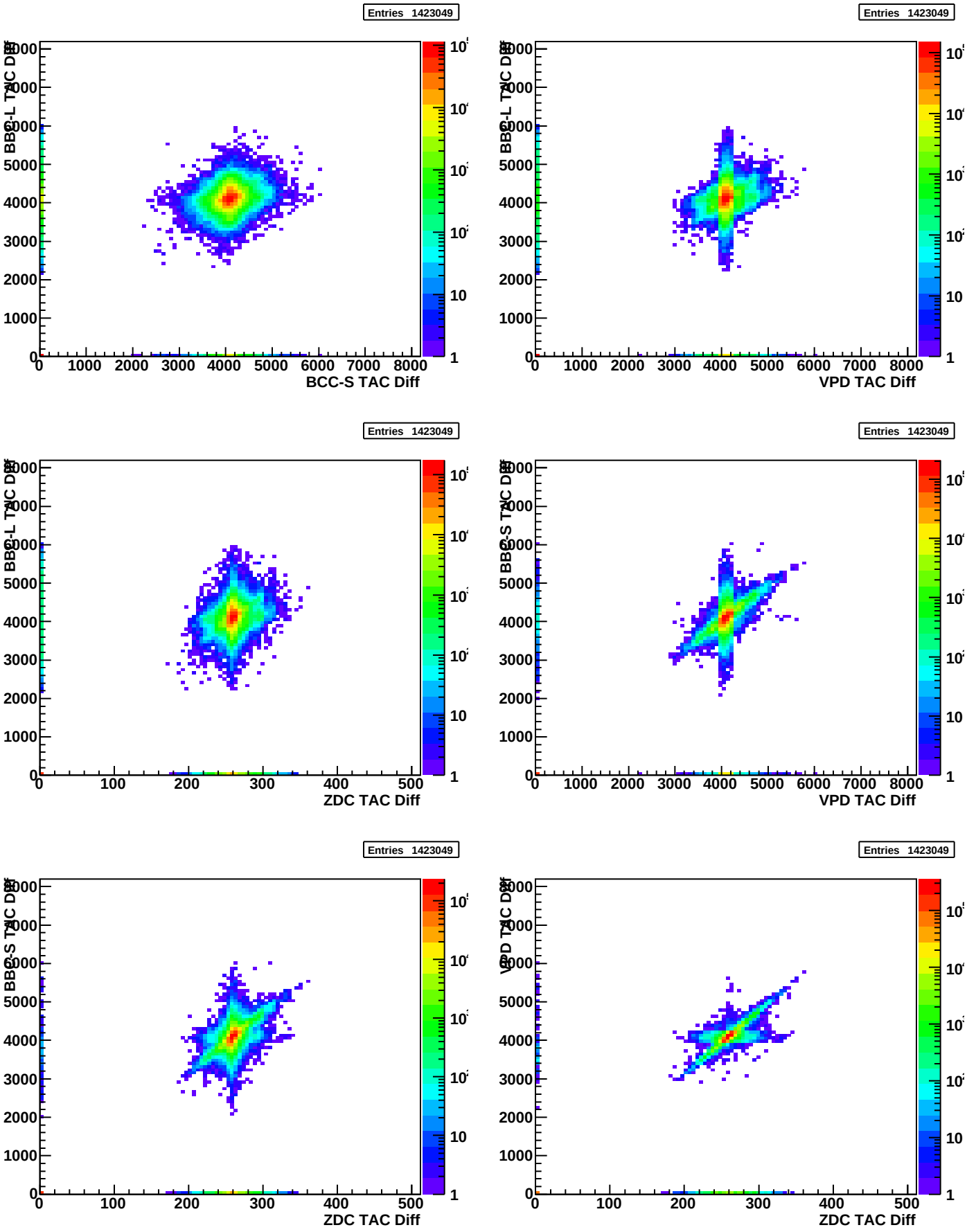
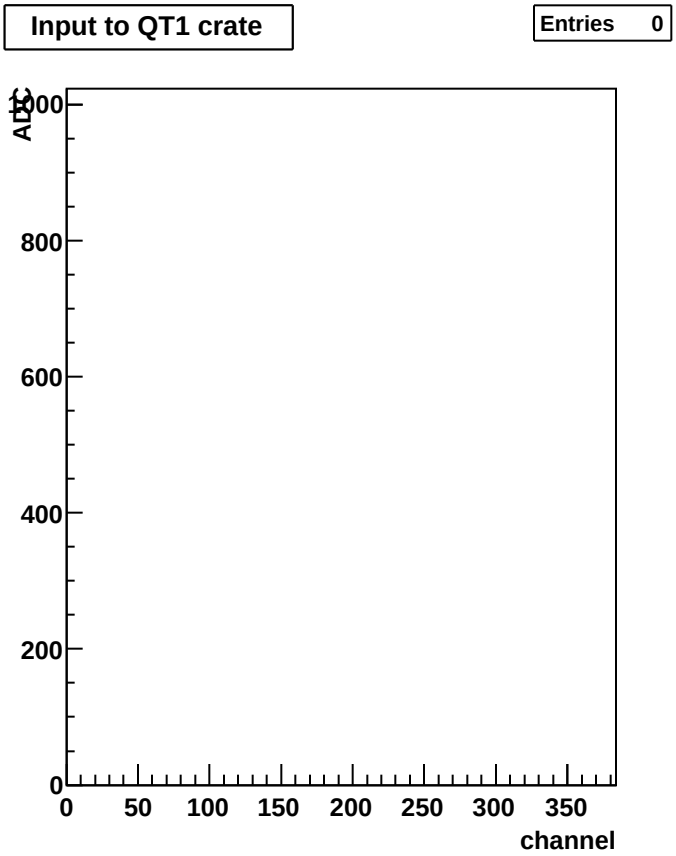
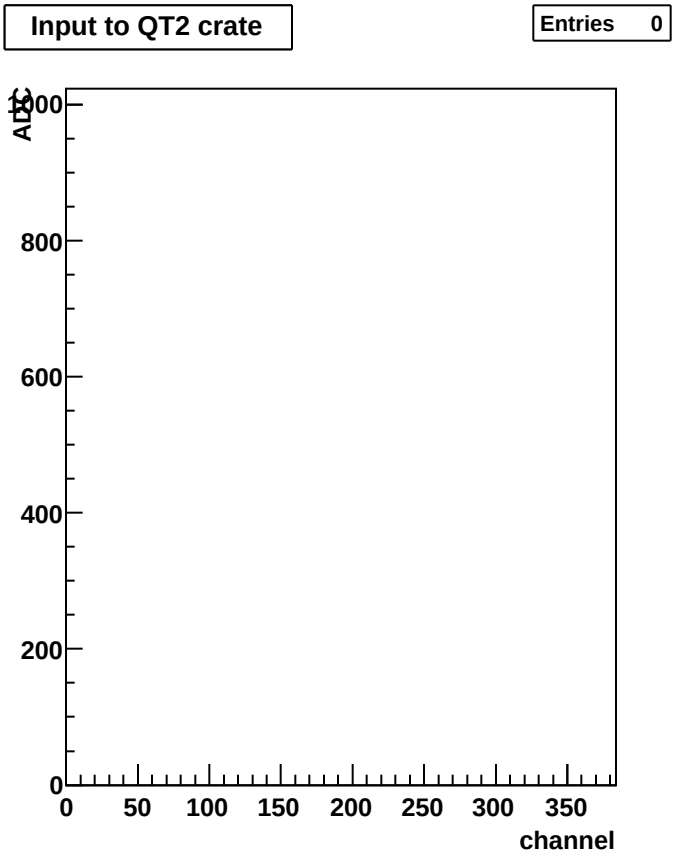
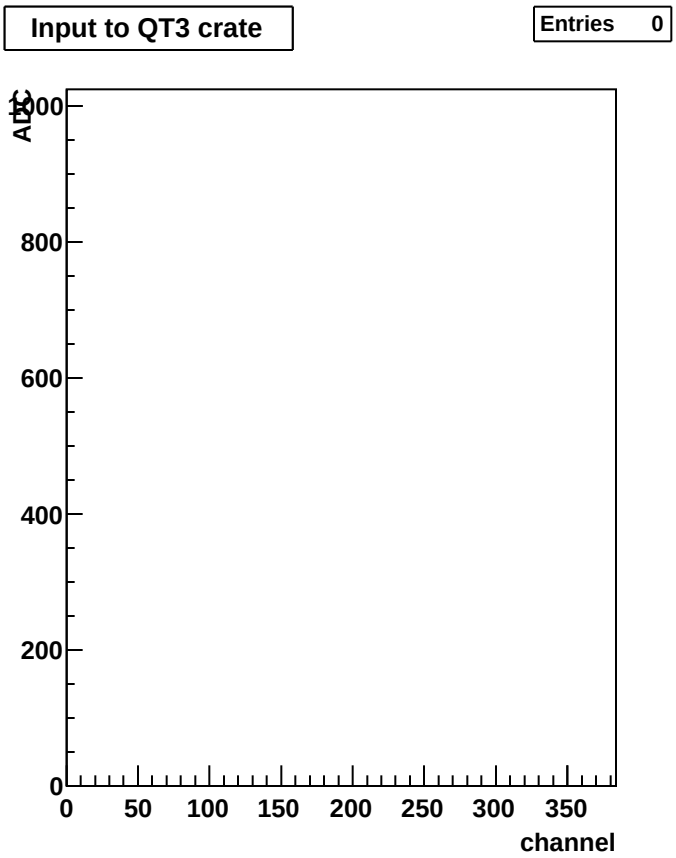
BBC-S-East ADC Sum
BBC-S-West ADC Sum

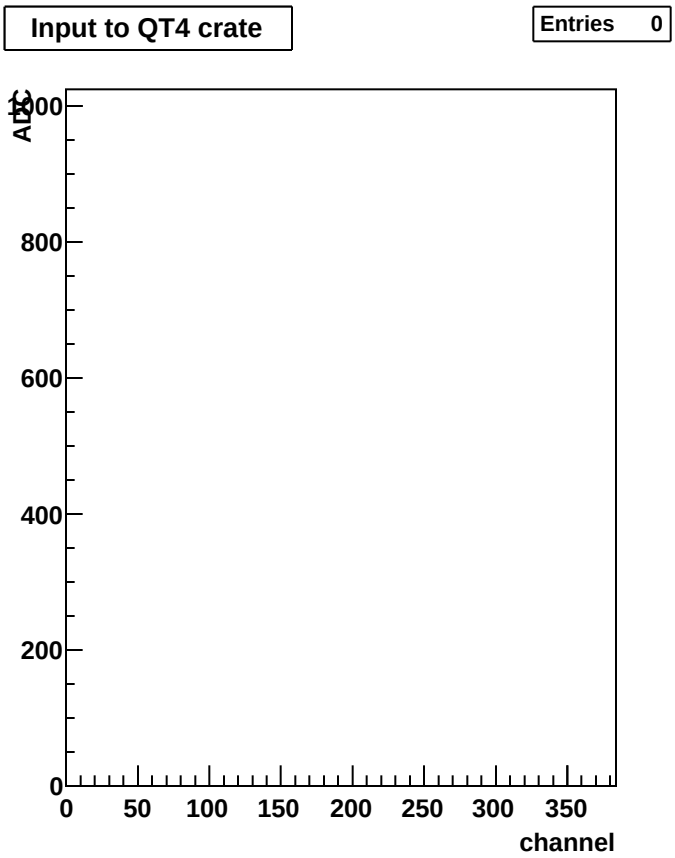

Entries 1423049
ZDC East ADC Sum Att
ZDC West ADC Sum Att

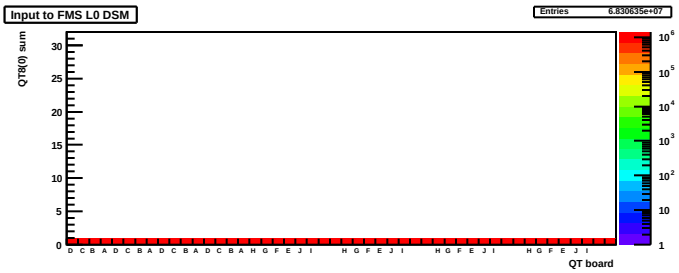

Entries 1423049
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1423049
BBC-L TAC Diff
VPD TAC Diff
Entries 1423049
BBC-L TAC Diff
ZDC TAC Diff
Entries 1423049
BBC-S TAC Diff
VPD TAC Diff
Entries 1423049
BBC-S TAC Diff
ZDC TAC Diff
Entries 1423049
VPD TAC Diff
ZDC TAC Diff

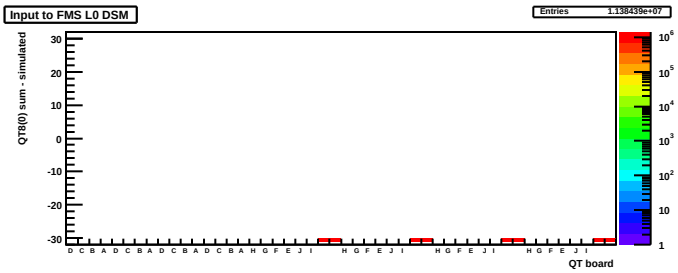

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

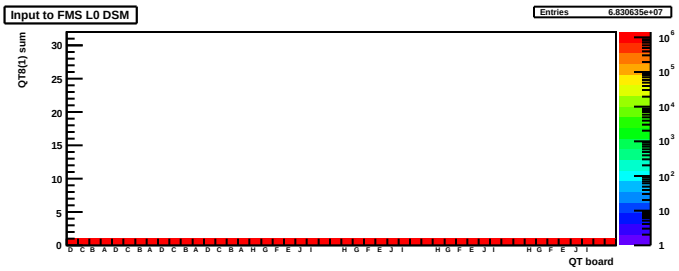

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

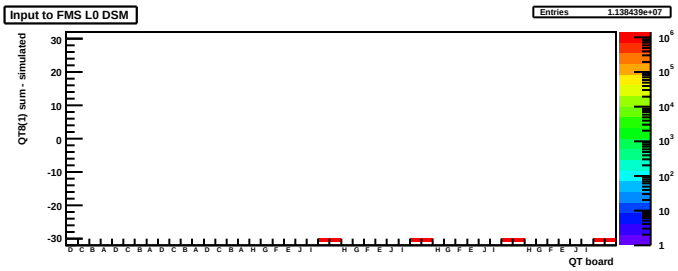

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

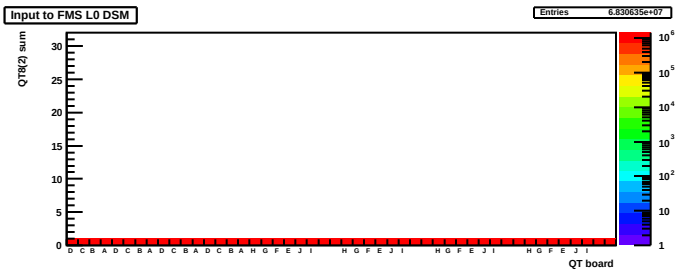

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

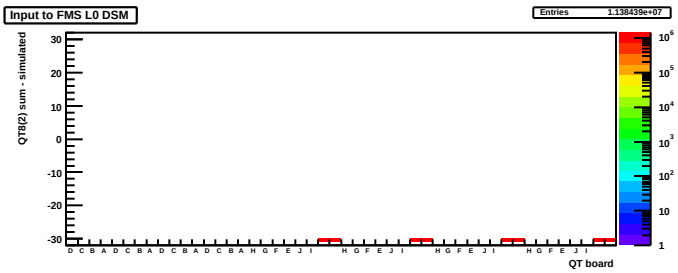

Input to FMS L0 DSM
Entries 6.830635e+07
QT8(0) sum
QT board

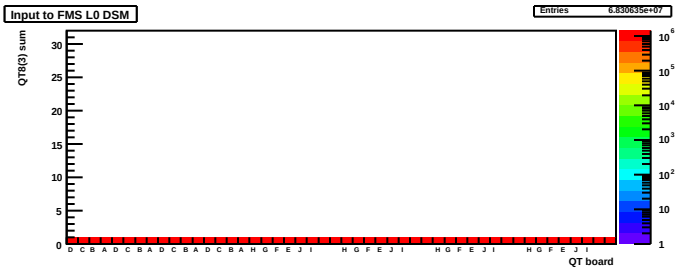

Input to FMS L0 DSM
Entries 1.138439e+07
QT8(0) sum - simulated
QT board

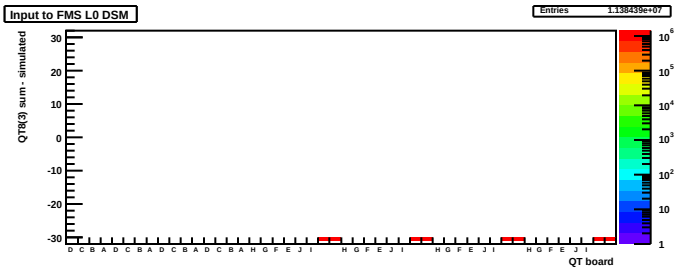

Input to FMS L0 DSM
Entries 6.830635e+07
QT8(1) sum
QT board

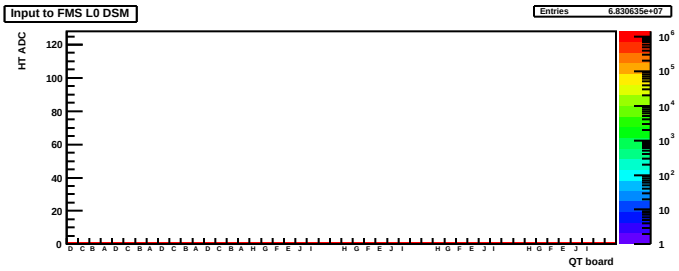

Input to FMS L0 DSM
Entries 1.138439e+07
QT8(1) sum - simulated
QT board

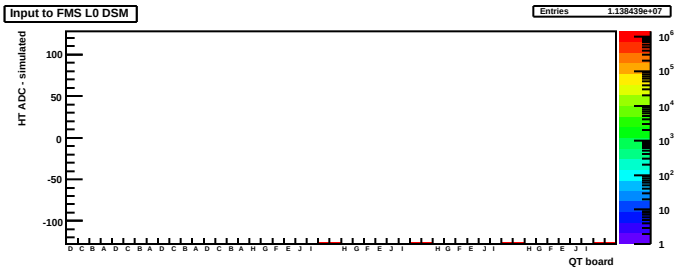

Input to FMS L0 DSM
Entries 6.830635e+07
QT8(2) sum
QT board

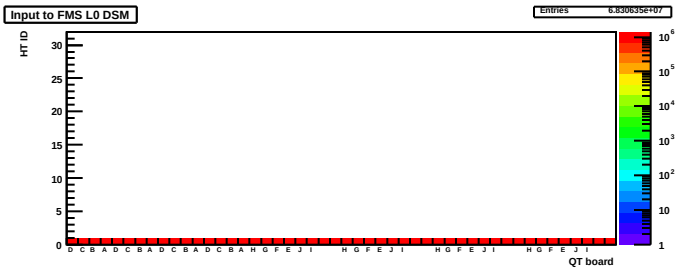

Input to FMS L0 DSM
Entries 1.138439e+07
QT8(2) sum - simulated
QT board

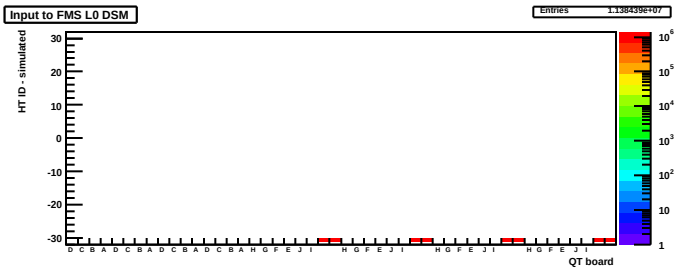

Input to FMS L0 DSM
Entries 6.830635e+07
QT8(3) sum
QT board

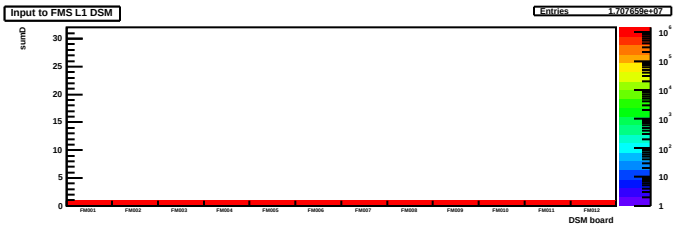

Input to FMS L0 DSM
Entries 1.138439e+07
QT8(3) sum - simulated
QT board

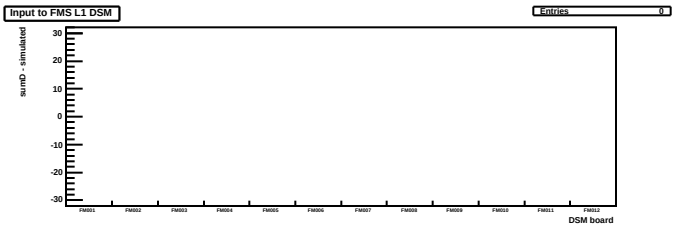

Input to FMS L0 DSM
Entries 6.830635e+07
HT ADC
QT board

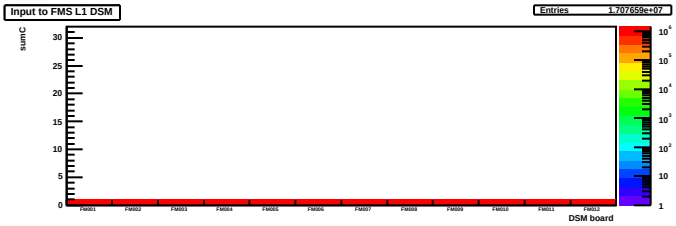

Input to FMS L0 DSM
Entries 1.138439e+07
HT ADC - simulated
QT board

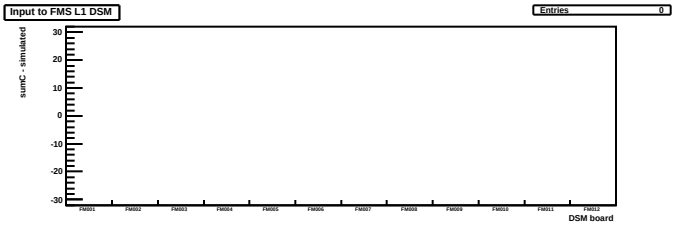

Input to FMS L0 DSM
Entries 6.830635e+07
HT ID
QT board

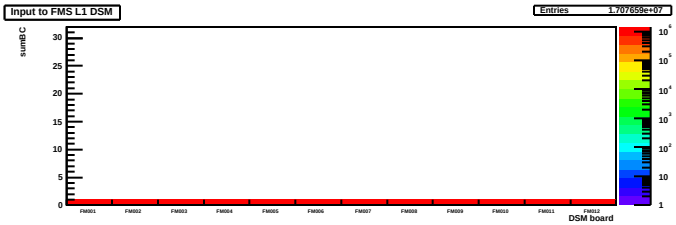

Input to FMS L0 DSM
Entries 1.138439e+07
HT ID - simulated
QT board

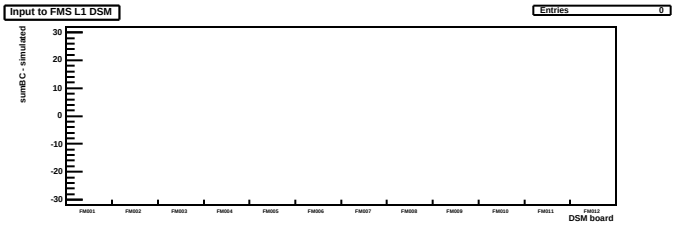

Input to FMS L1 DSM
Entries 1.707659e+07
sumD
DSM board

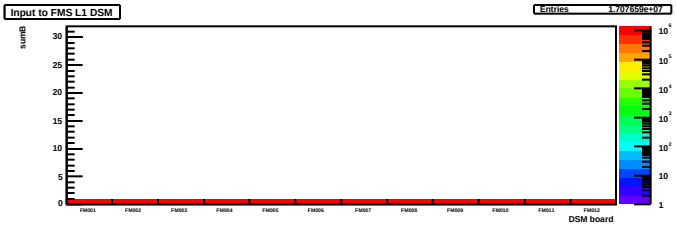

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

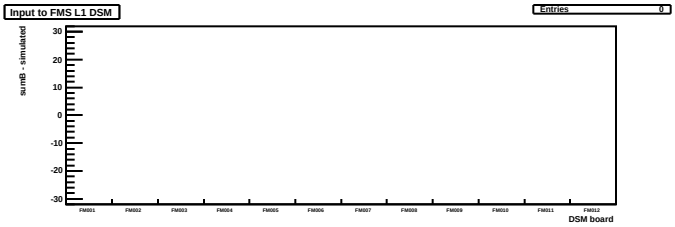

Input to FMS L1 DSM
Entries 1.707659e+07
sumC
DSM board

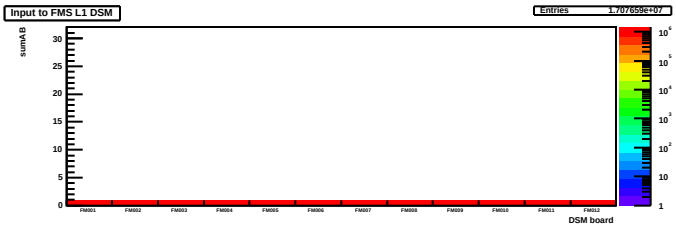

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

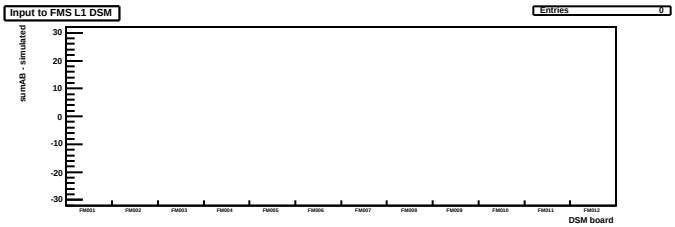

Input to FMS L1 DSM
Entries 1.707659e+07
sumBC
DSM board

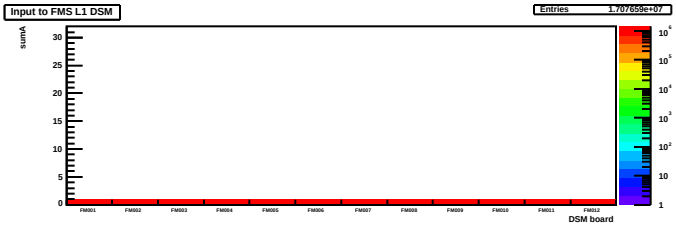

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

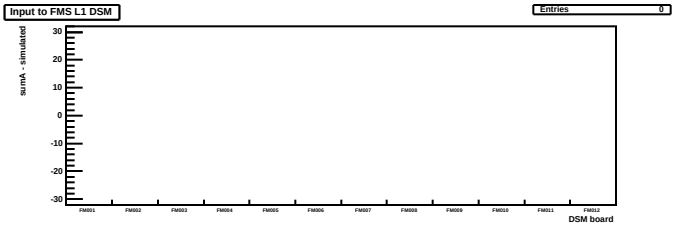

Input to FMS L1 DSM
Entries 1.707659e+07
sumB
DSM board

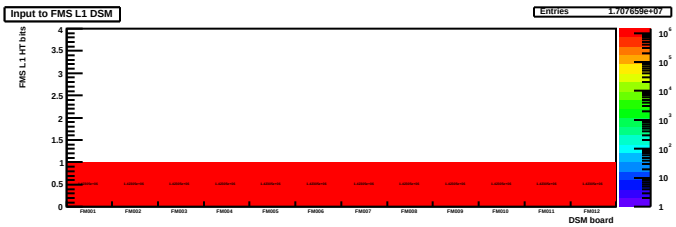

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

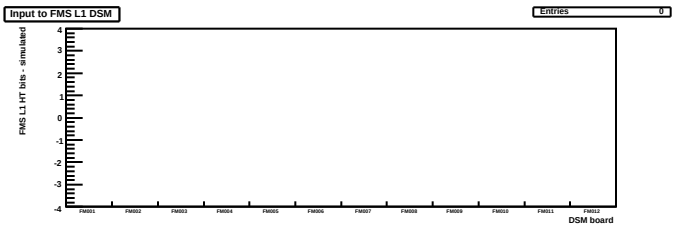

Input to FMS L1 DSM
Entries 1.707659e+07
sumAB
DSM board

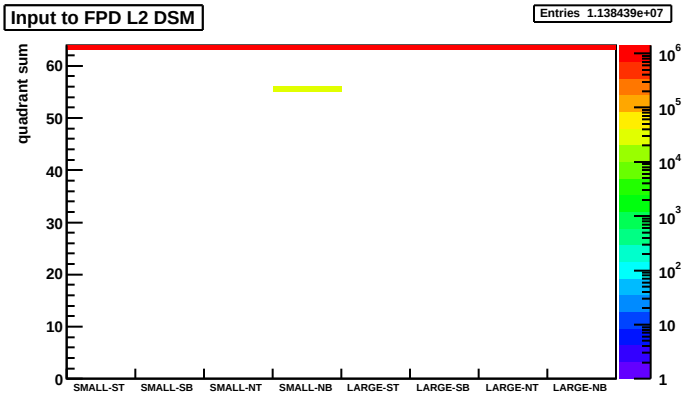

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

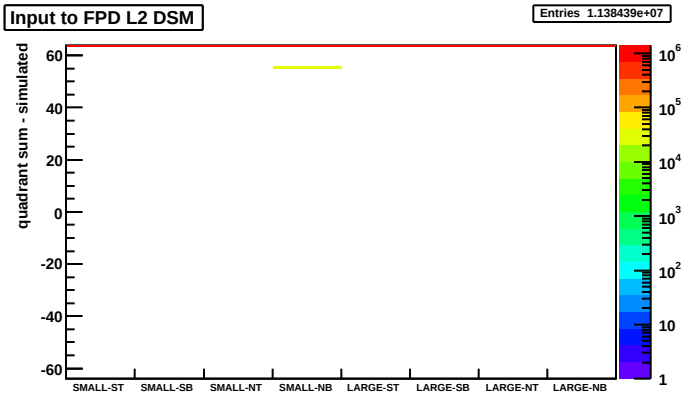

Input to FMS L1 DSM
Entries 1.707659e+07
sumA
DSM board

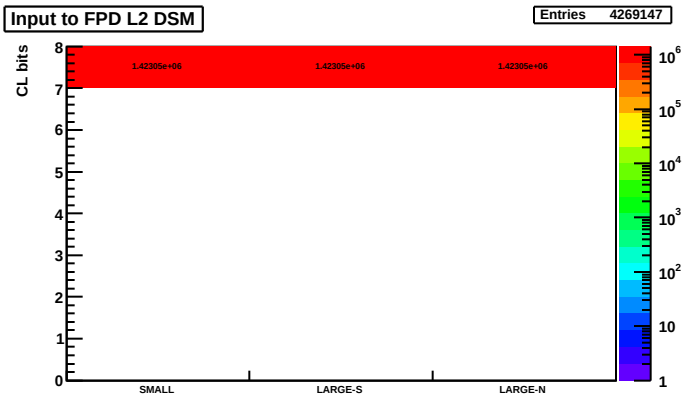

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

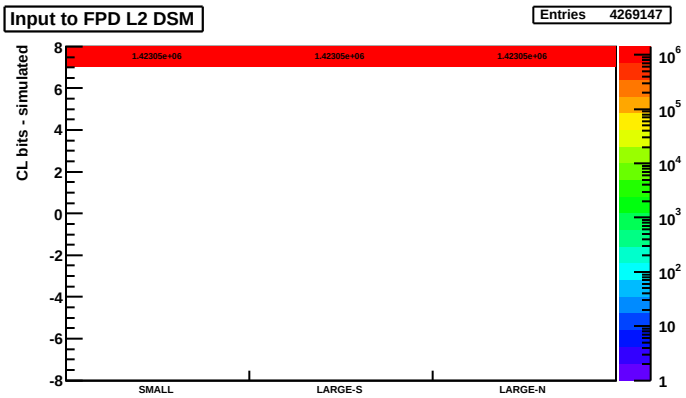

Input to FMS L1 DSM
Entries 1.707659e+07
FMS L1 HT bits
DSM board

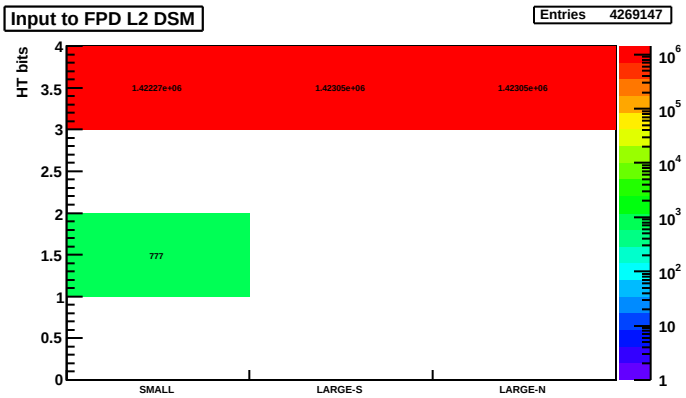

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

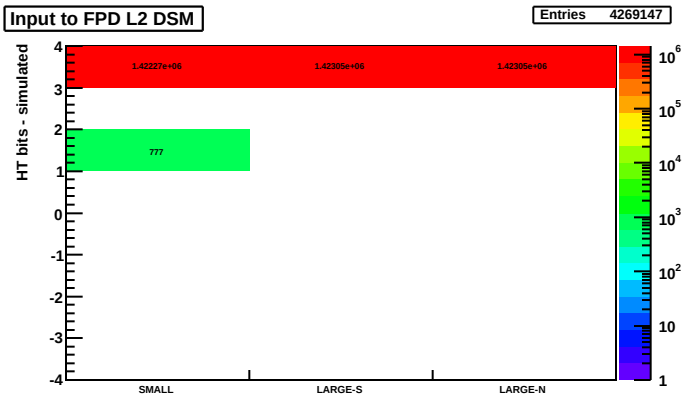

Input to FPD L2 DSM
Entries 1.138439e+07
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

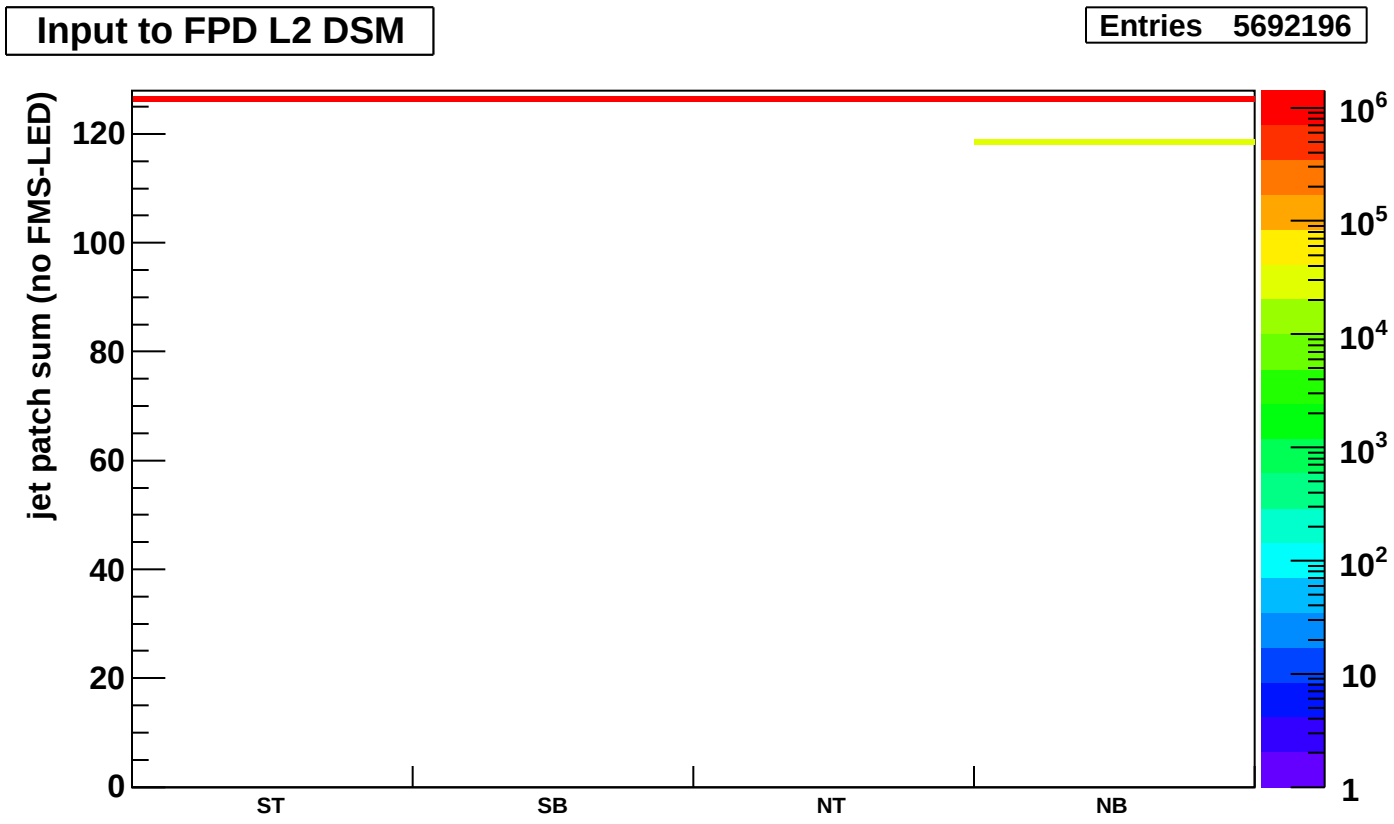

Input to FPD L2 DSM
Entries 1.138439e+07
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

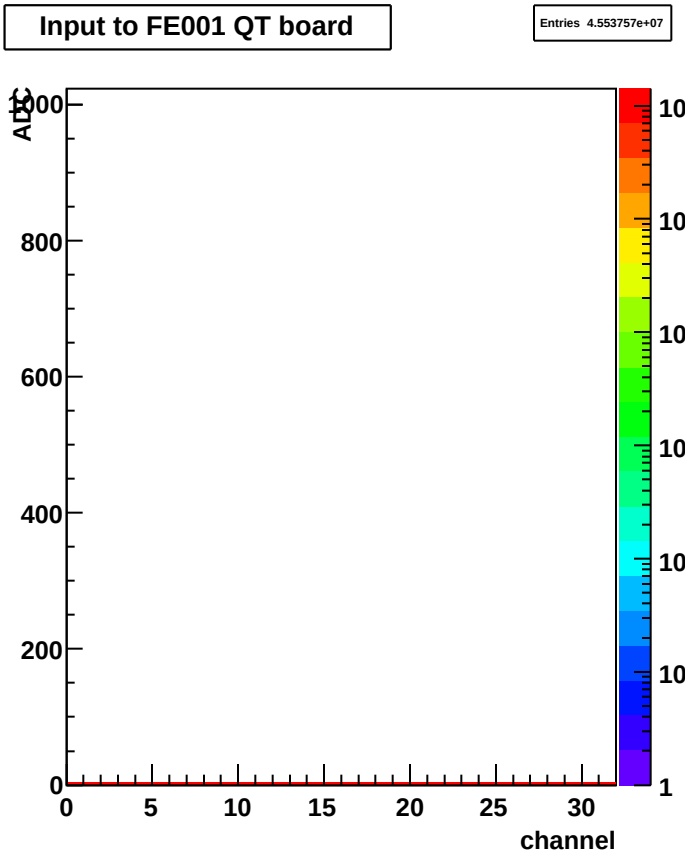

Input to FPD L2 DSM
Entries 4269147
CL bits
1.42305e+06
1.42305e+06
1.42305e+06
SMALL
LARGE-S
LARGE-N

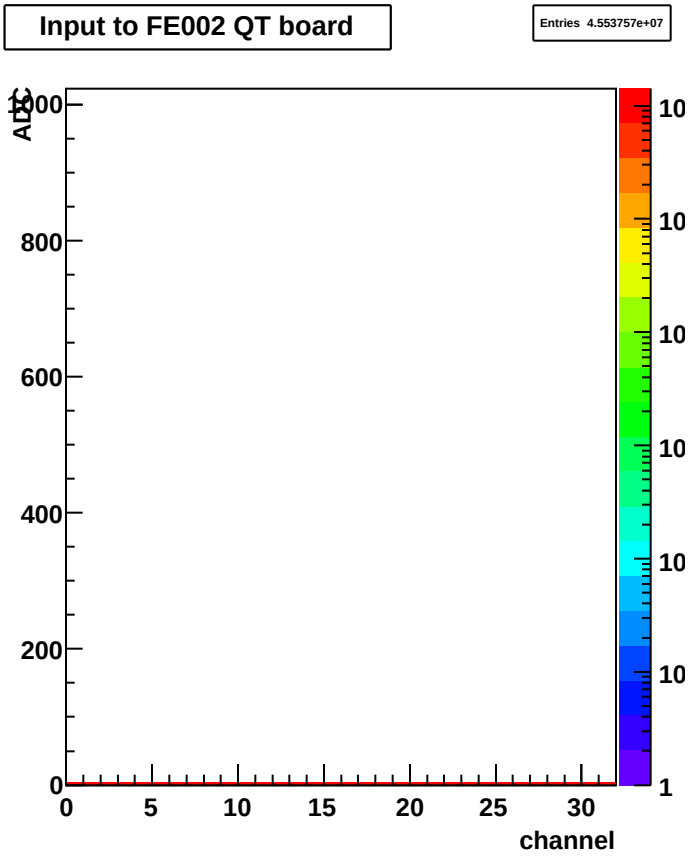

Input to FPD L2 DSM
Entries 4269147
CL bits - simulated
1.42305e+06
1.42305e+06
1.42305e+06
SMALL
LARGE-S
LARGE-N

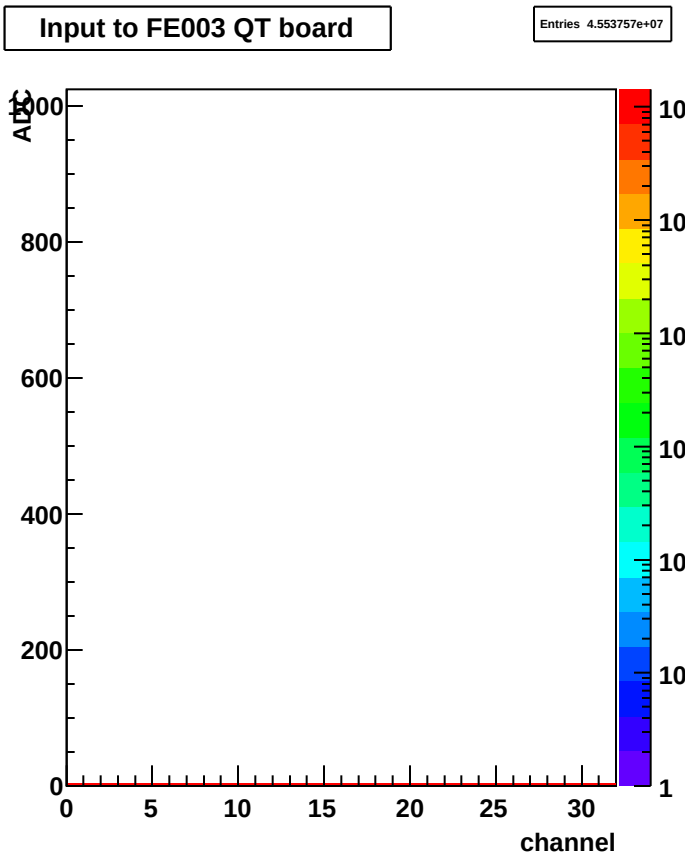

Input to FPD L2 DSM
Entries 4269147
HT bits
1.42227e+06
1.42305e+06
1.42305e+06
777
SMALL
LARGE-S
LARGE-N

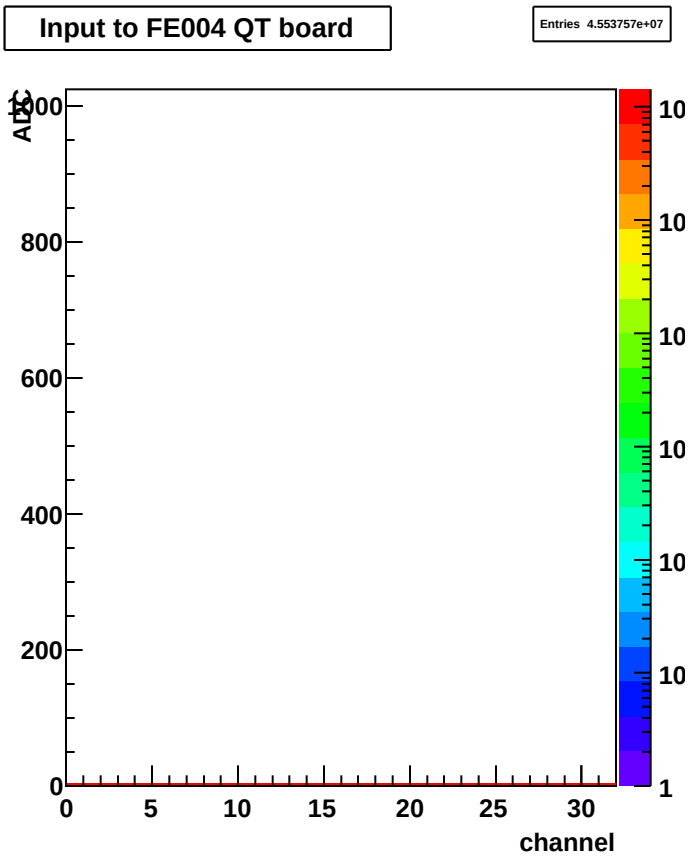

Input to FPD L2 DSM
Entries 4269147
HT bits - simulated
1.42227e+06
1.42305e+06
1.42305e+06
777
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 5692196
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

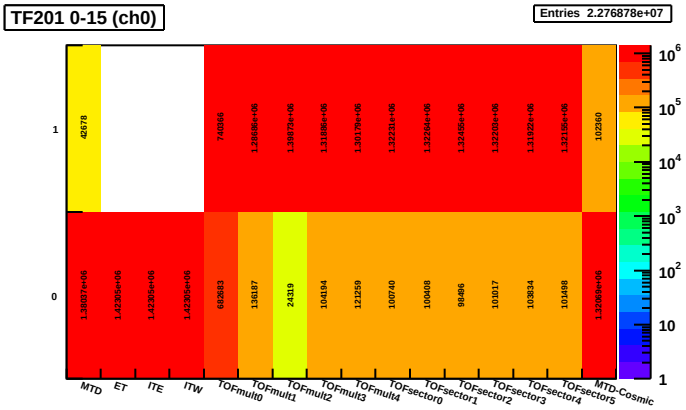

Input to FE001 QT board
Entries 4.553757e+07
ADC
channel


Input to FE002 QT board
Entries 4.553757e+07
ADC
channel

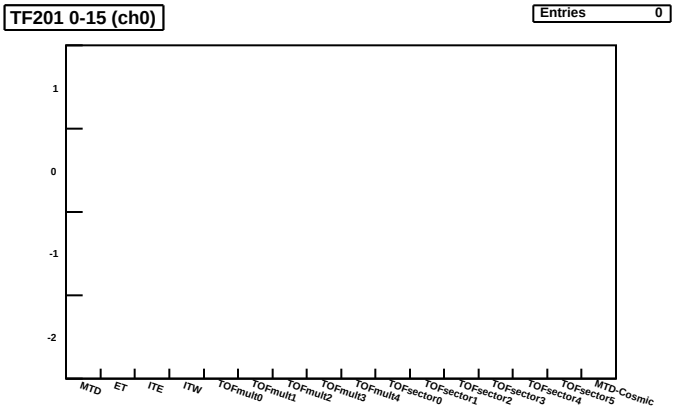

Input to FE003 QT board
Entries 4.553757e+07
ADC
channel


Input to FE004 QT board
Entries 4.553757e+07
ADC
channel

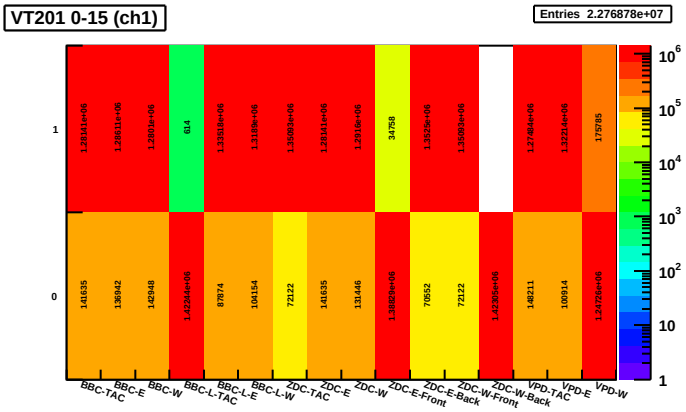
TF201 0-15 (ch0)



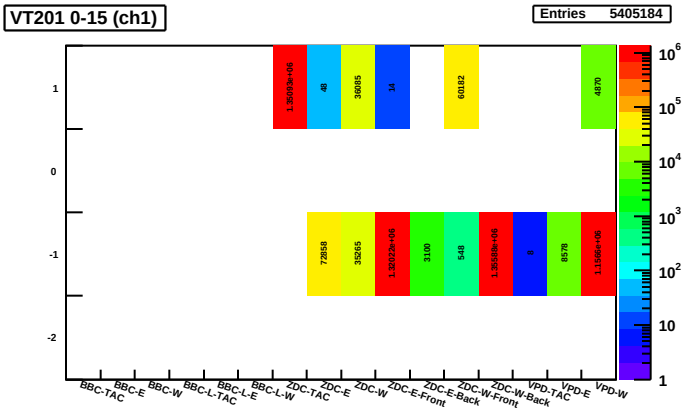
TF201 0-15 (ch0)



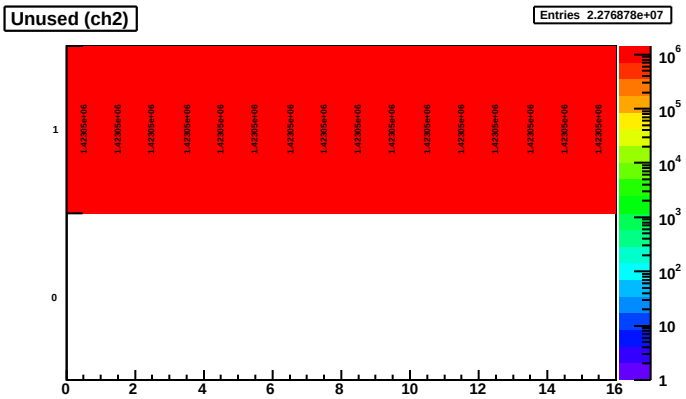
VT201 0-15 (ch1)

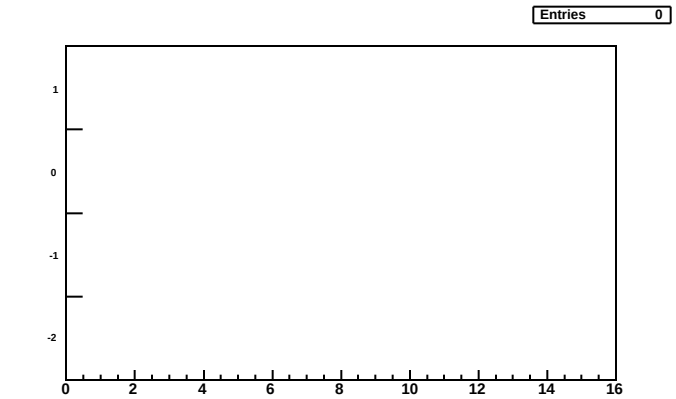


VT201 0-15 (ch1)

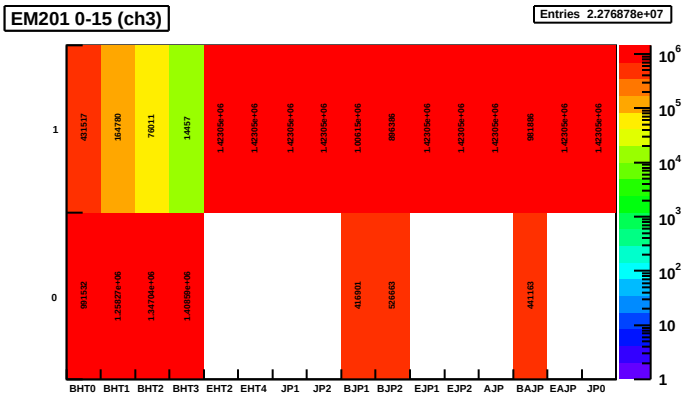


Unused (ch2)

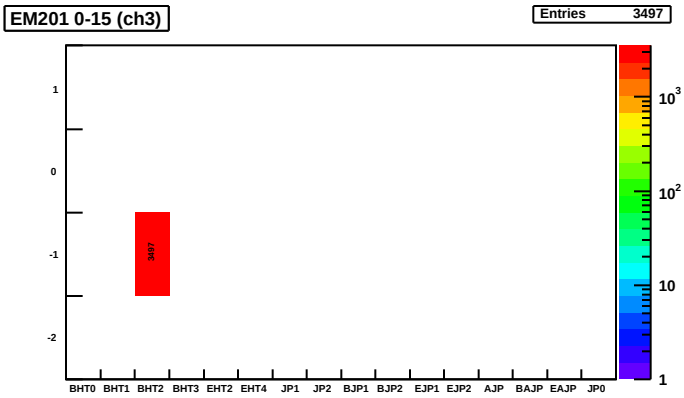




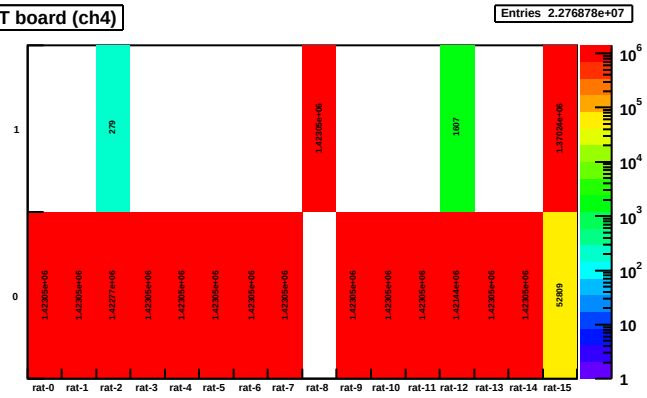
EM201 0-15 (ch3)

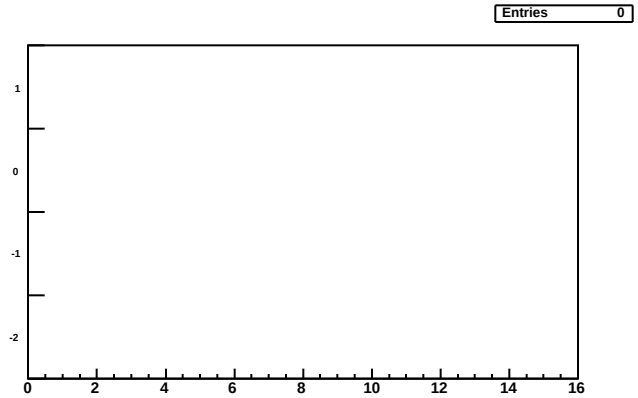


EM201 0-15 (ch3)

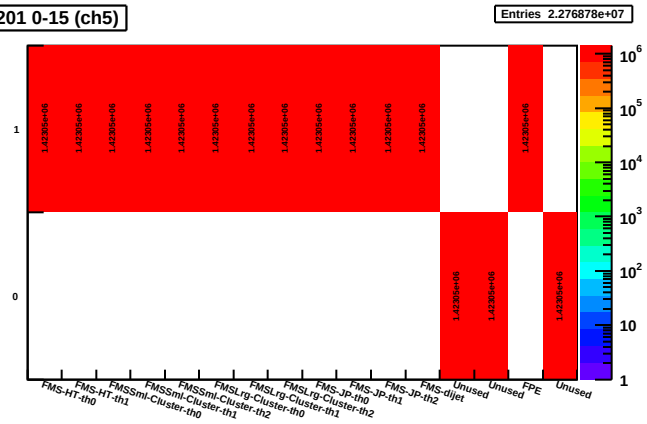


RAT board (ch4)

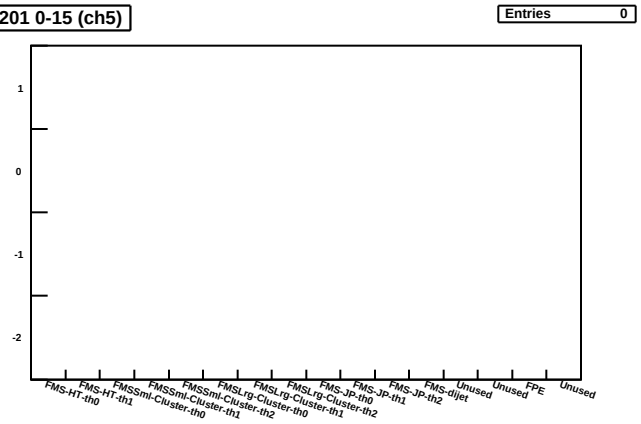




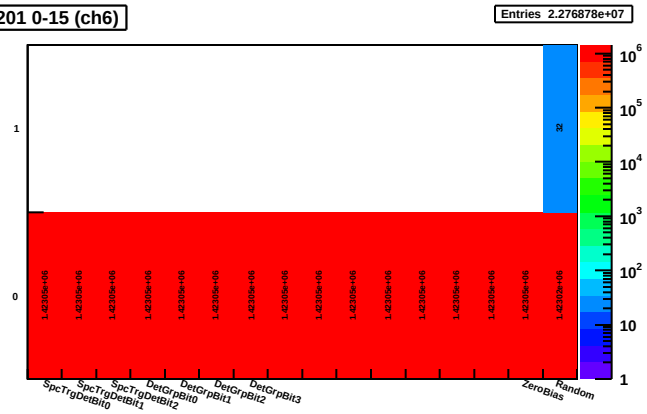
FP201 0-15 (ch5)

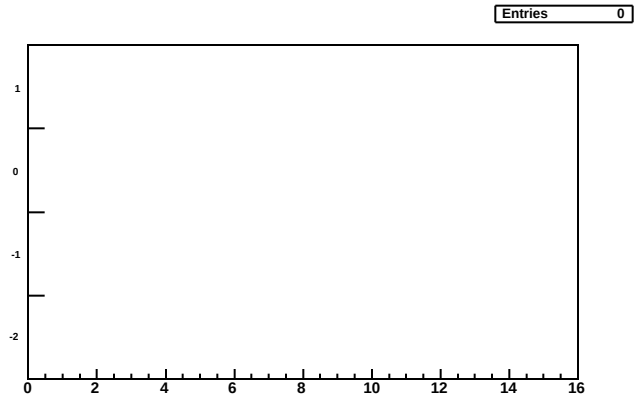
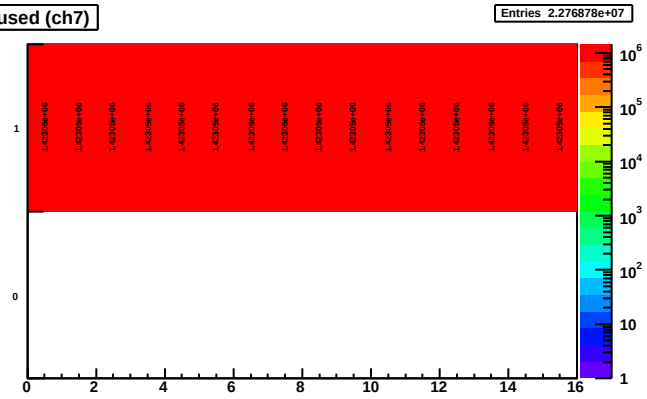
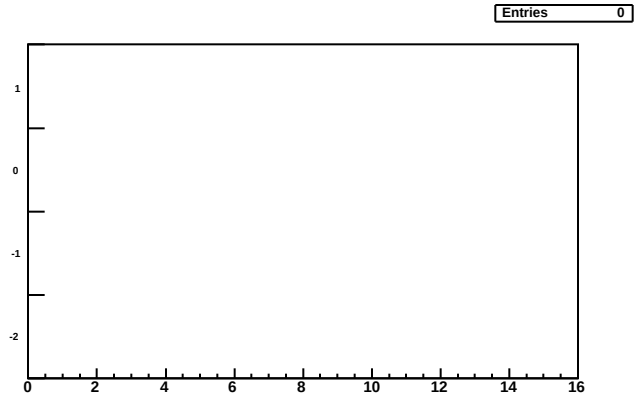


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)