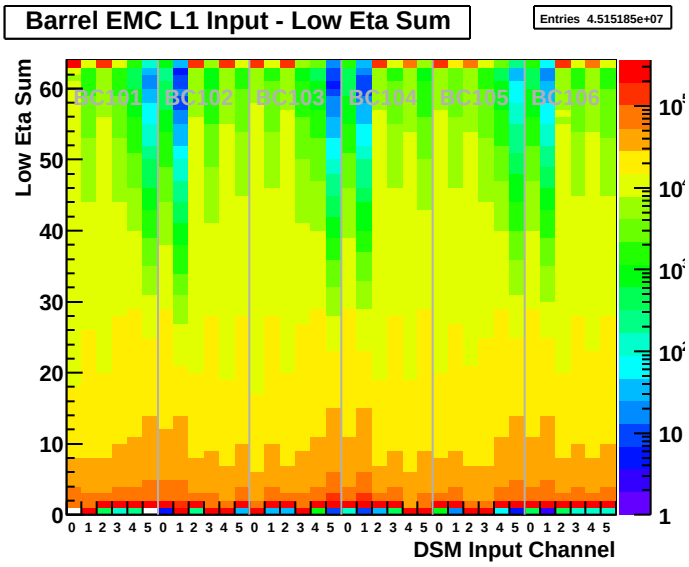
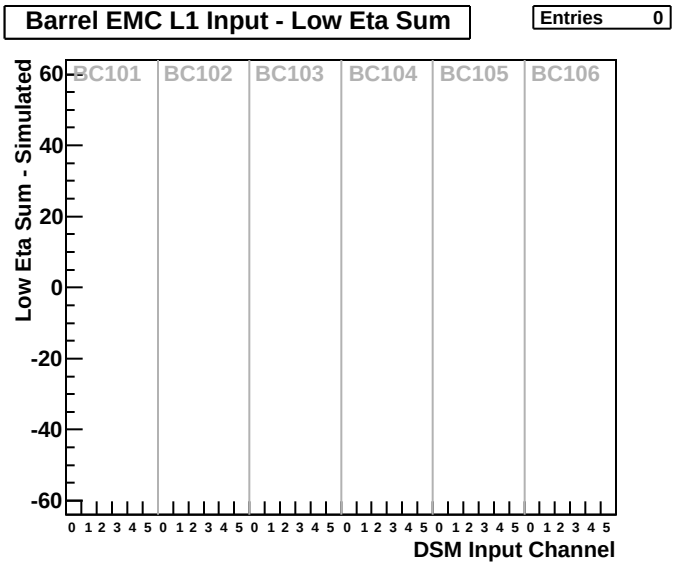
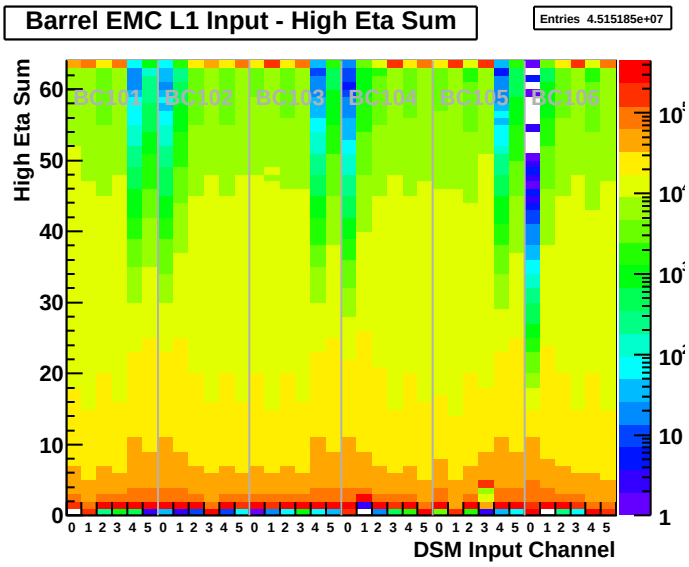
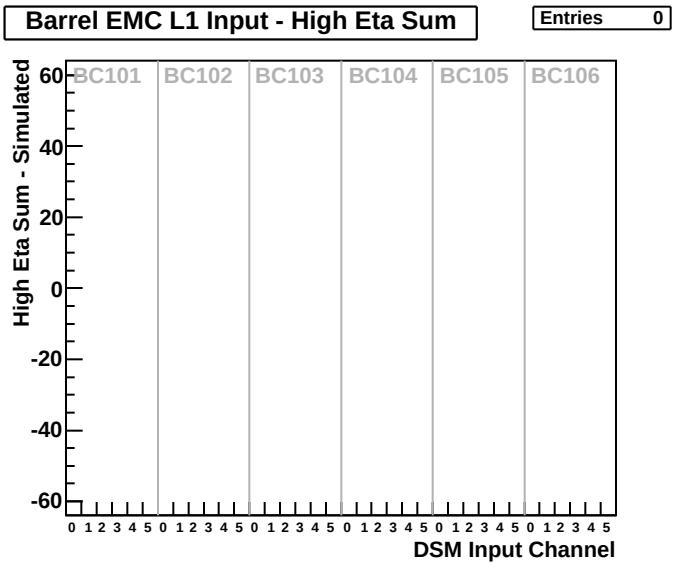
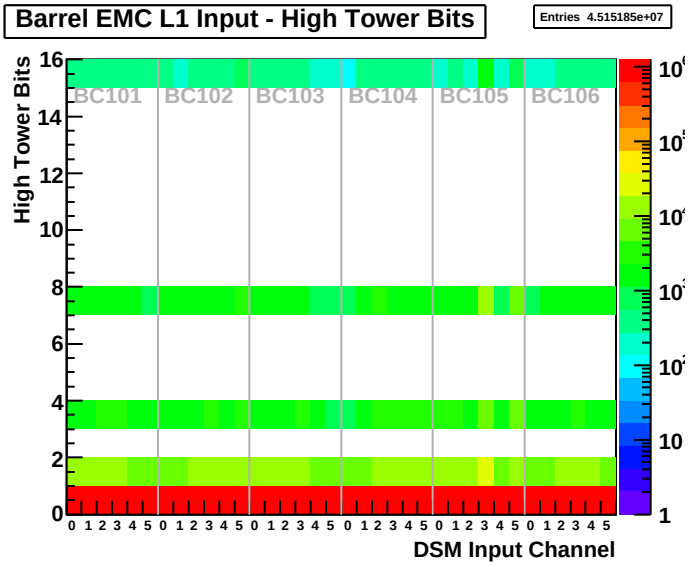
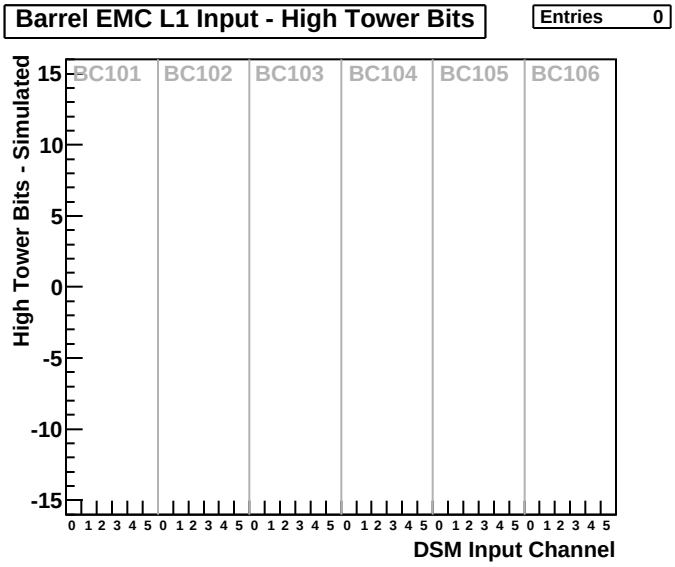

Barrel EMC L1 Input - Low Eta Sum
Entries 4.515185e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

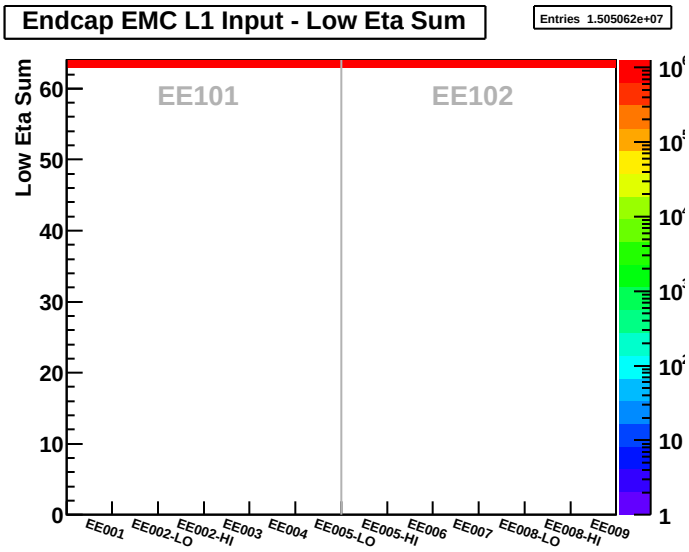

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

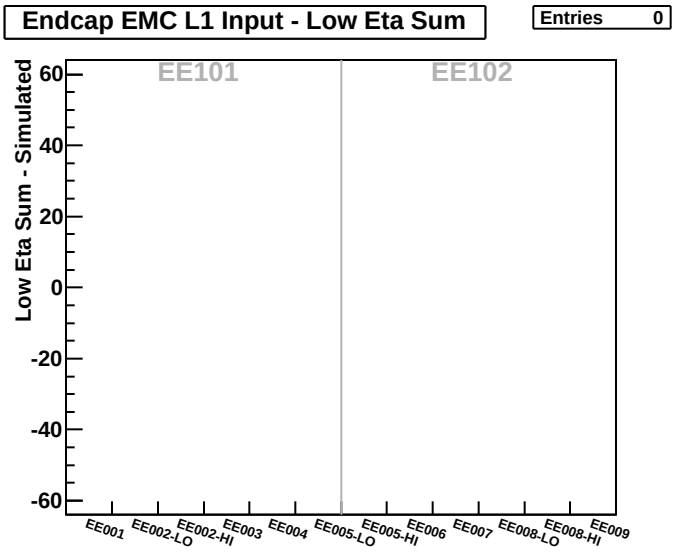

Barrel EMC L1 Input - High Eta Sum
Entries 4.515185e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

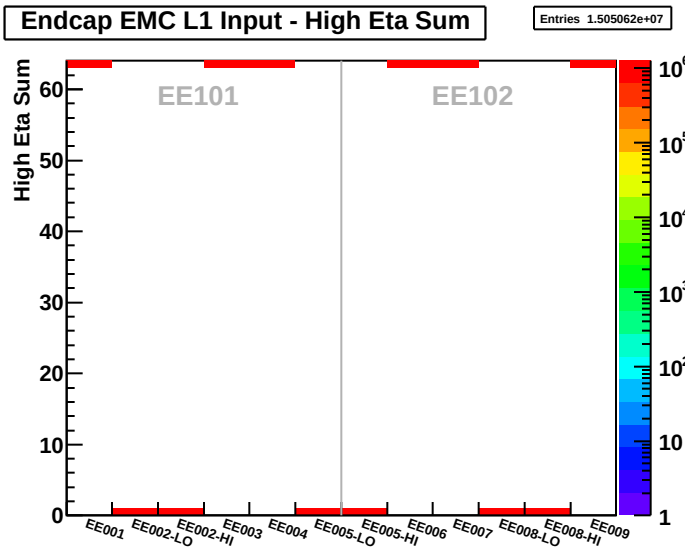

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

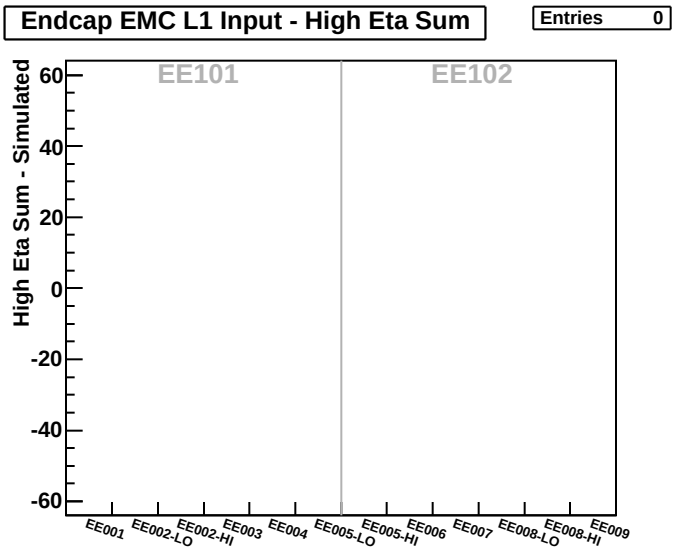

Barrel EMC L1 Input - High Tower Bits
Entries 4.515185e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

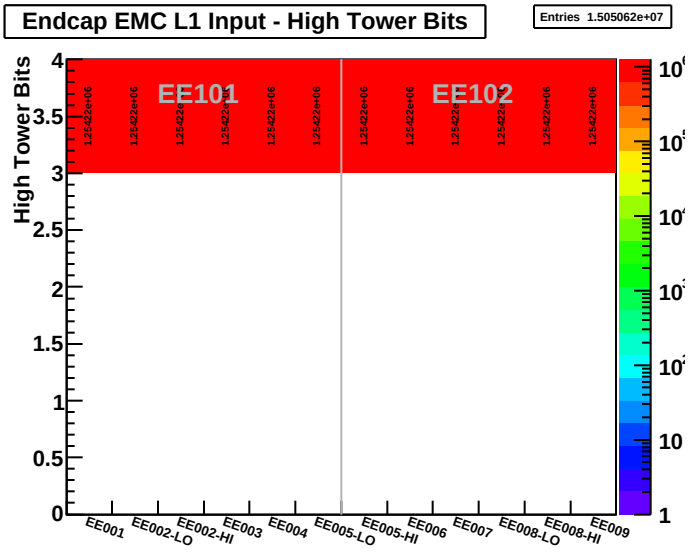

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

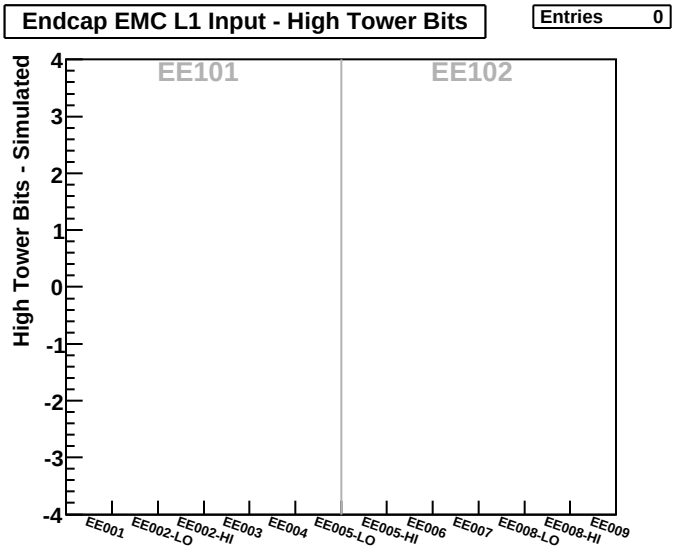

Endcap EMC L1 Input - Low Eta Sum
Entries 1.505062e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

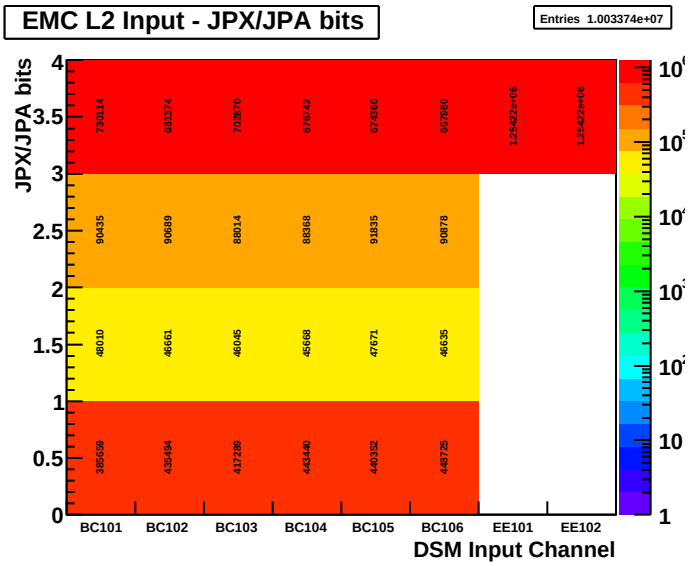

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

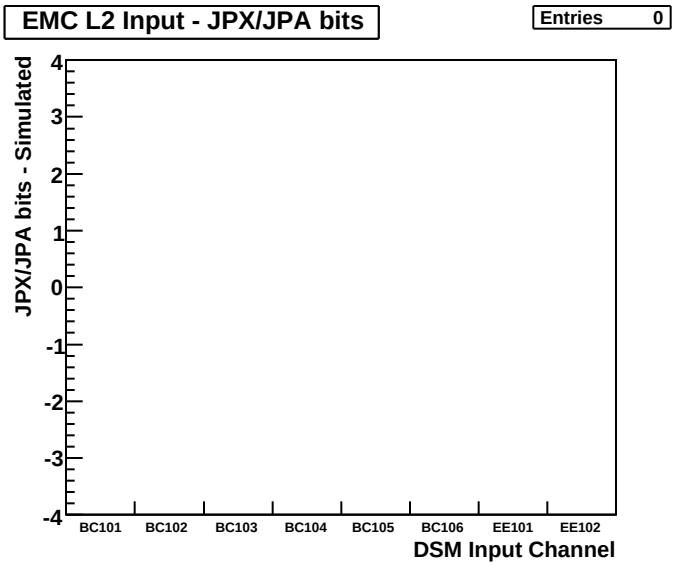

Endcap EMC L1 Input - High Eta Sum
Entries 1.505062e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

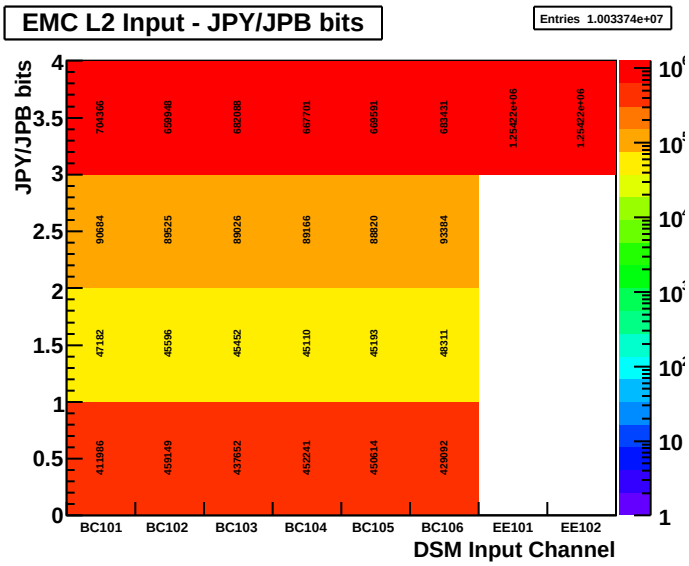

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

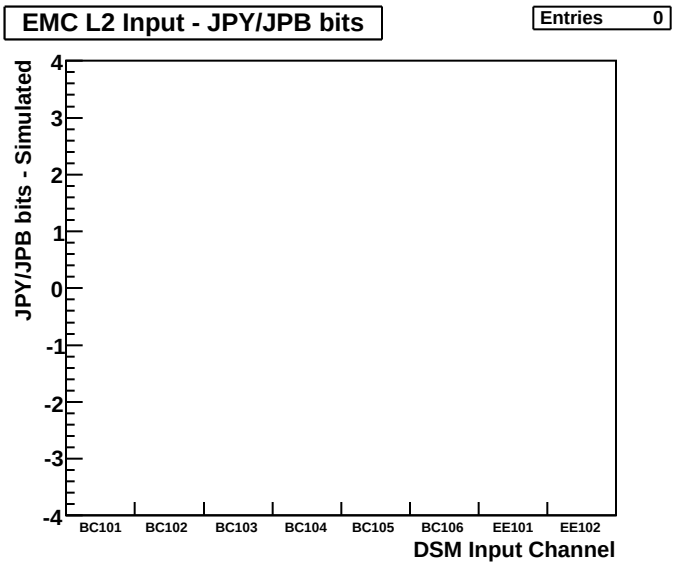

Endcap EMC L1 Input - High Tower Bits
Entries 1.505062e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

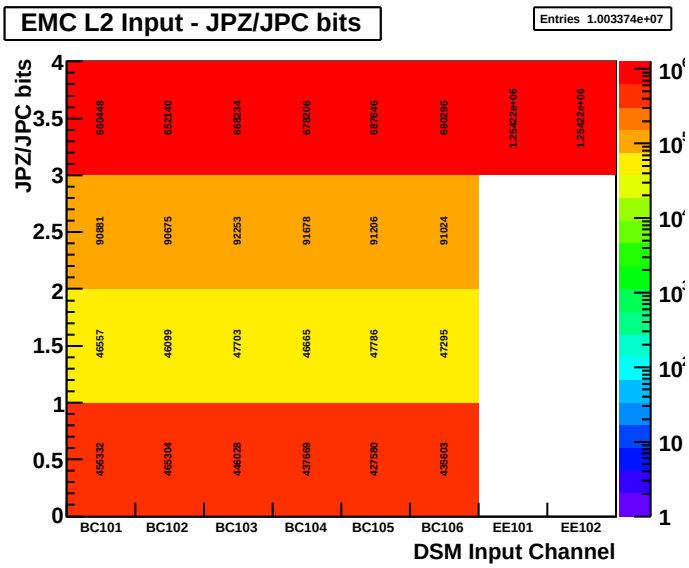

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

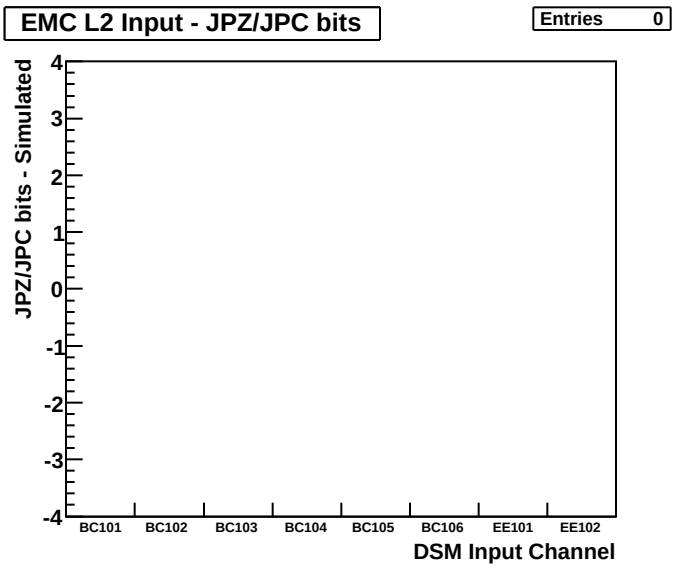

EMC L2 Input - JPX/JPA bits
Entries 1.003374e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
730114 681374 702870 676742 674360 667980 1.25422e+06 1.25422e+06
90435 90689 88014 88368 91835 90878
48010 46661 46045 45668 47671 46635
385659 435494 417289 443440 440352 448725

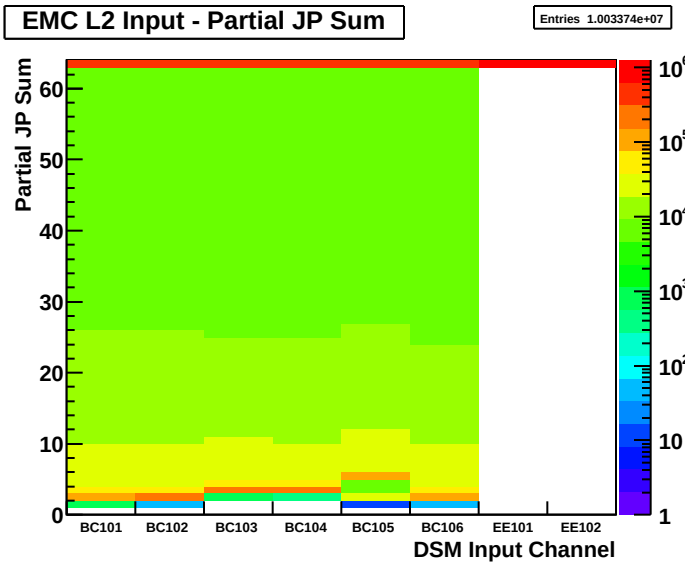

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

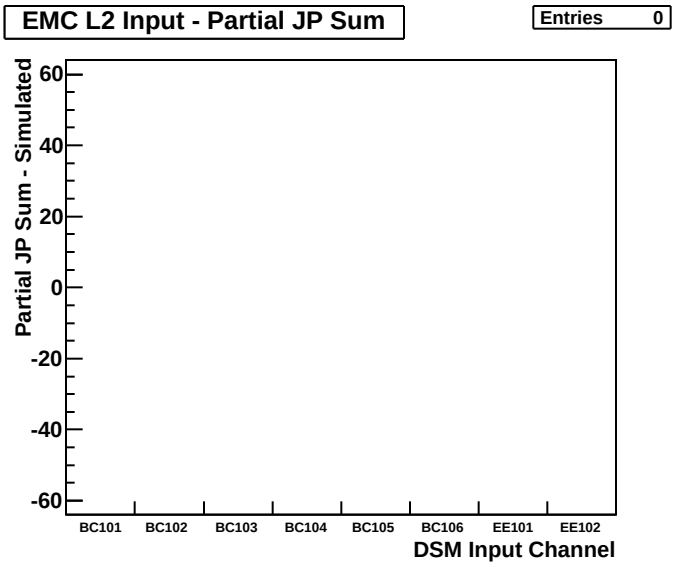

EMC L2 Input - JPY/JPB bits
Entries 1.003374e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
704365 659948 682088 667701 669591 683431 1.25422e+06 1.25422e+06
90684 89525 89026 89166 88820 93384
47182 45596 45452 45110 45193 48311
411986 459149 437652 452241 450614 429092

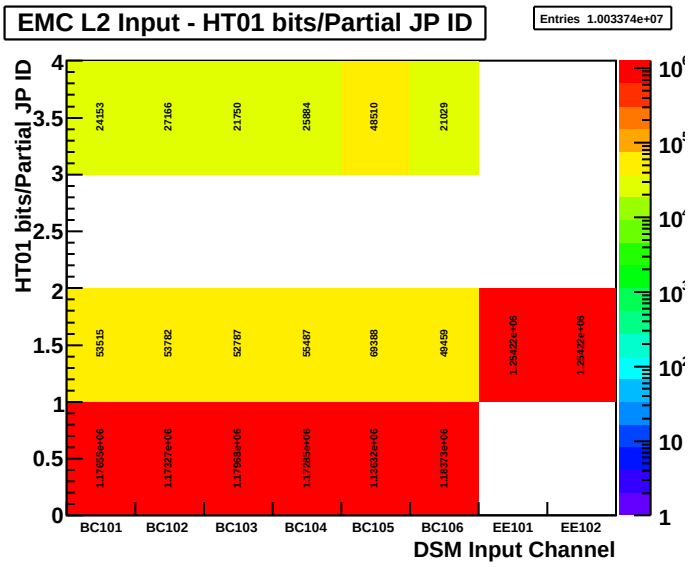

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

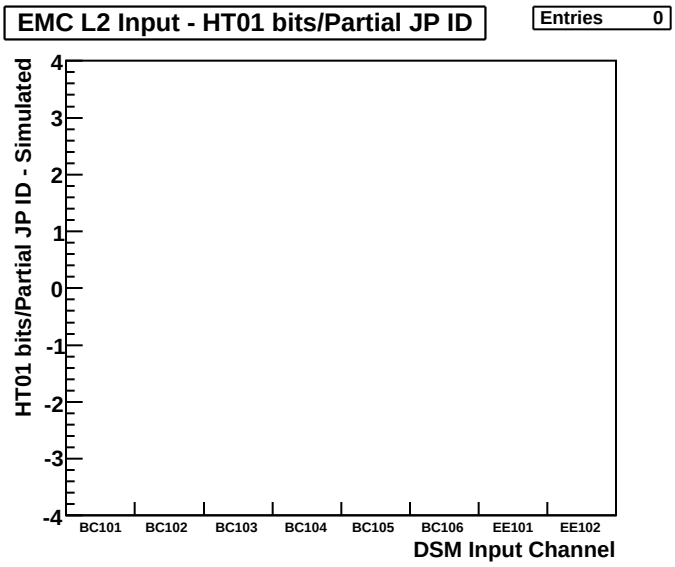

EMC L2 Input - JPZ/JPC bits
Entries 1.003374e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
660448 652140 668234 678206 687646 680296 1.25422e+06 1.25422e+06
90881 90675 92253 91678 91206 91024
46557 46099 47703 46665 47786 47295
456332 465304 446028 437669 427580 435603

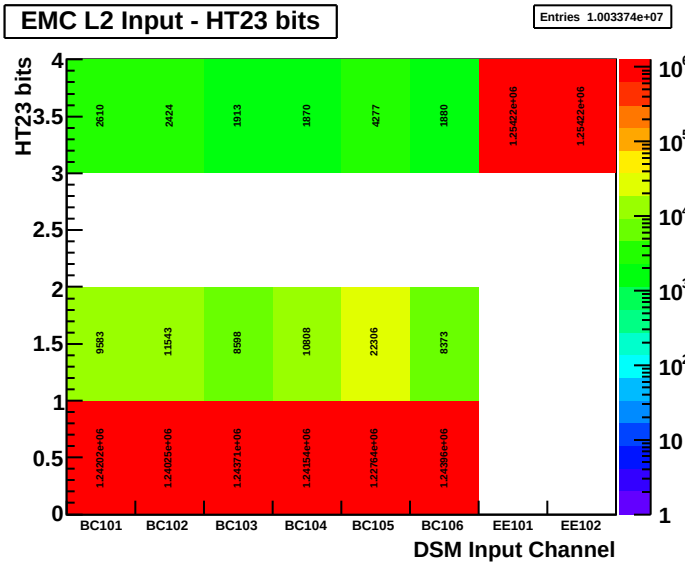

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

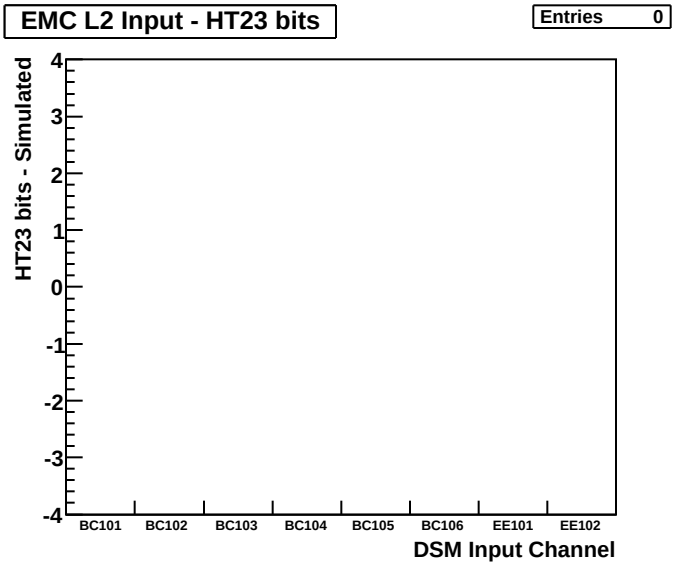

EMC L2 Input - Partial JP Sum
Entries 1.003374e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

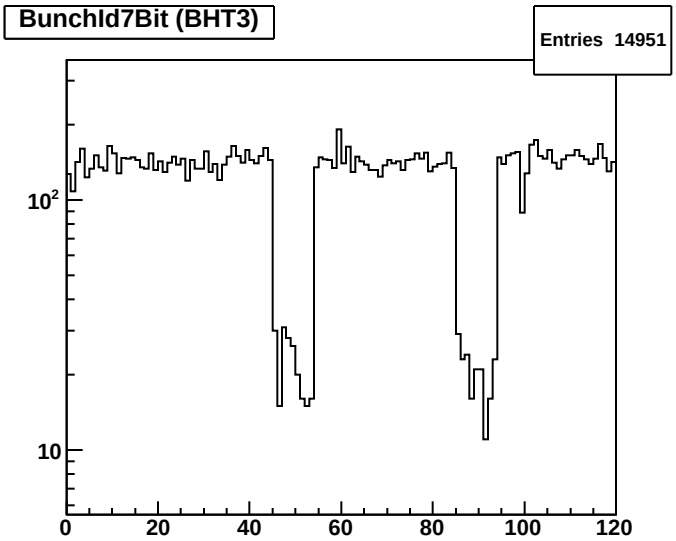

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

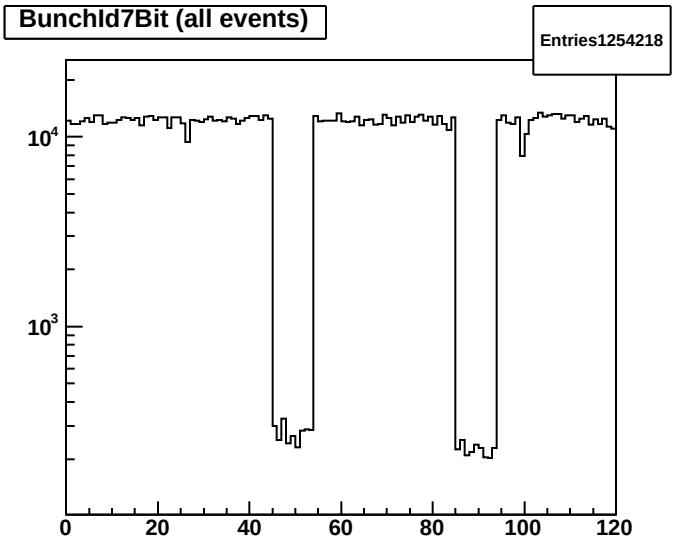

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.003374e+07
HT01 bits/Partial JP ID
24153 27166 21750 25884 48510 21029
53515 53782 52787 55487 69388 49459 1.25422e+06 1.25422e+06
1.17655e+06 1.17327e+06 1.17968e+06 1.17285e+06 1.13632e+06 1.18373e+06
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

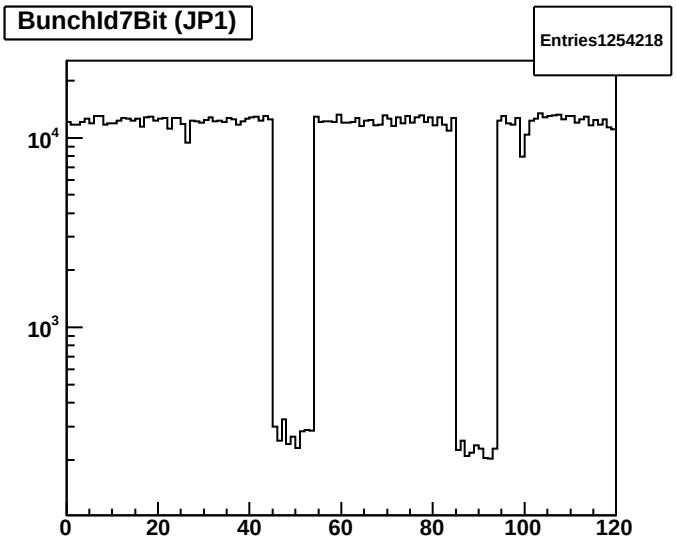

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

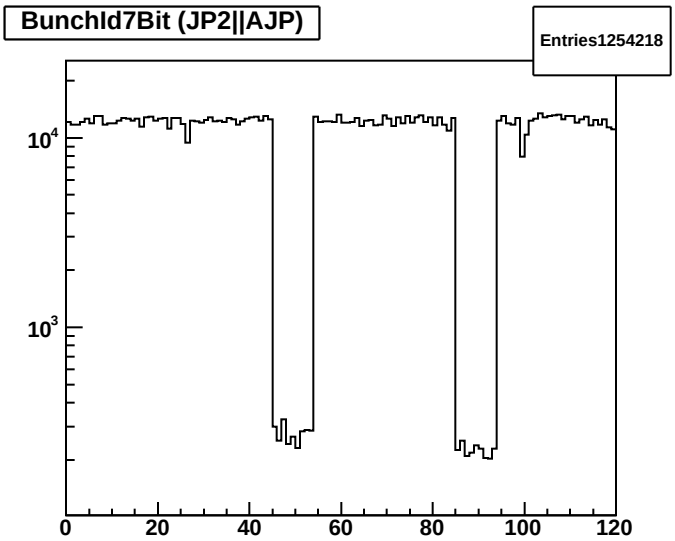

EMC L2 Input - HT23 bits
Entries 1.003374e+07
HT23 bits
2610 2424 1913 1970 4277 1880 1.25422e+06 1.25422e+06
9583 11543 8598 10808 22306 8373
1.24202e+06 1.24025e+06 1.24371e+06 1.24154e+06 1.22764e+06 1.24396e+06
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

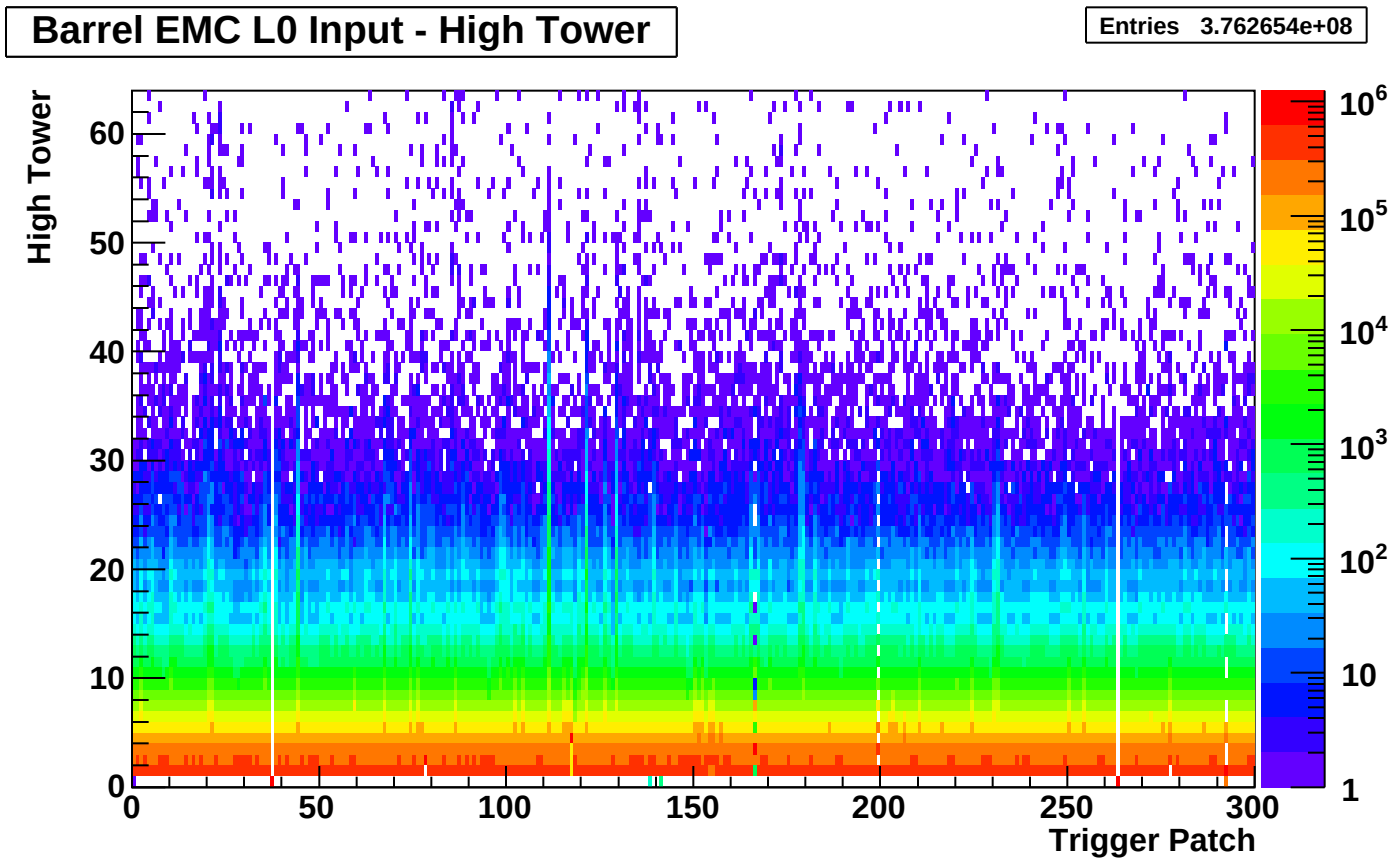

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

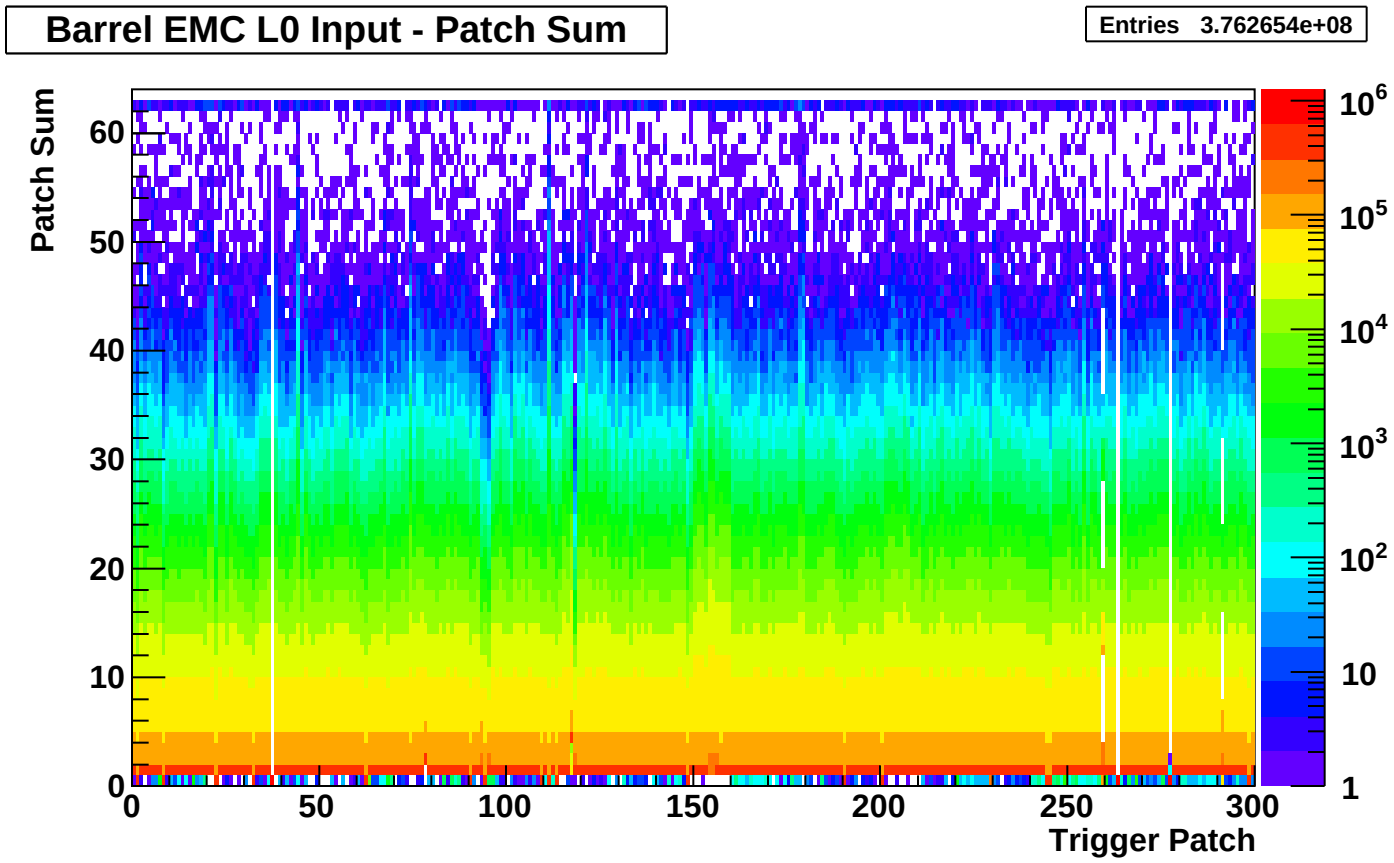

BunchId7Bit (BHT3)
Entries 14951
10^2
10
0
20
40
60
80
100
120

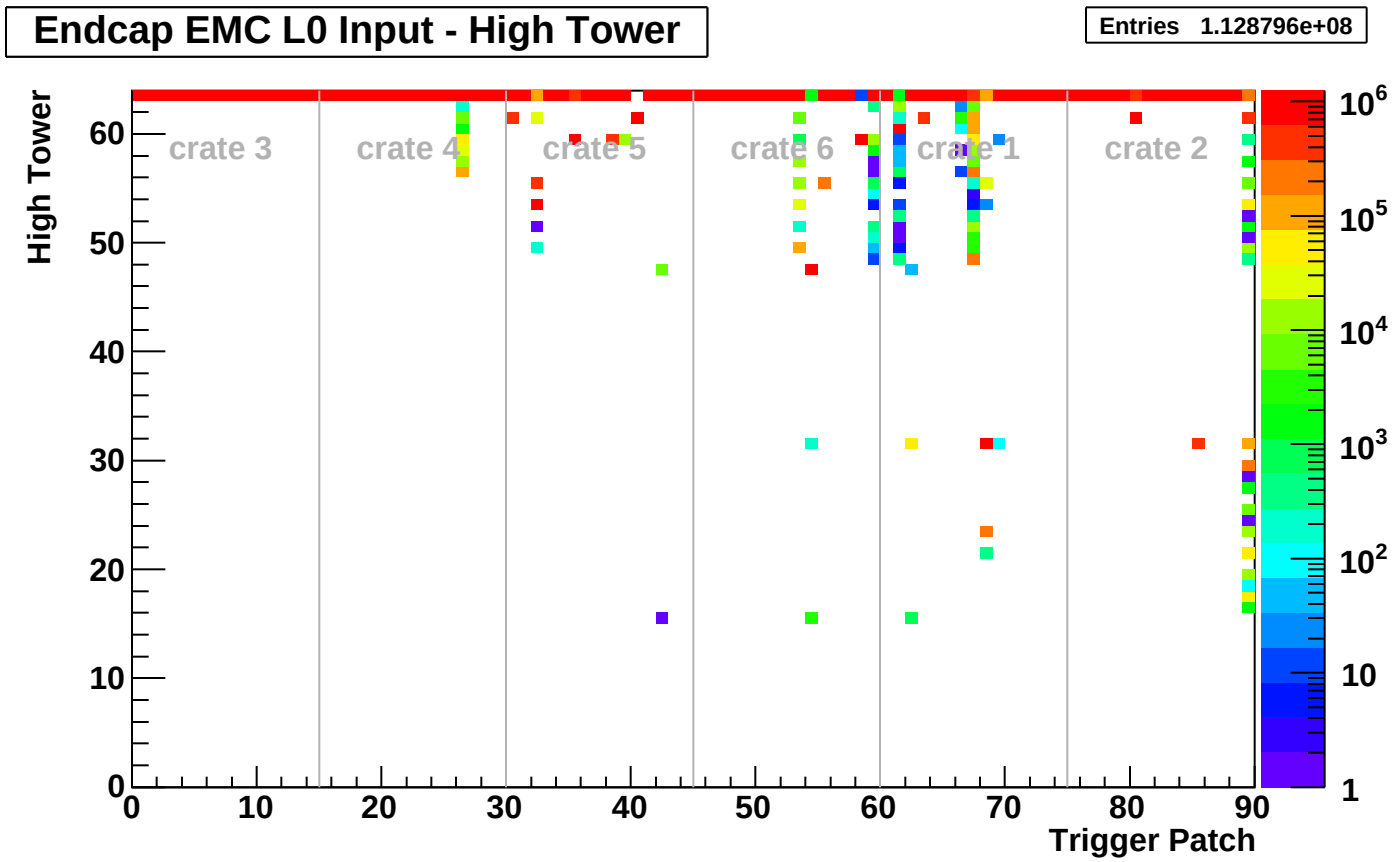

BunchId7Bit (all events)
Entries1254218
10^4
10^3
0
20
40
60
80
100
120

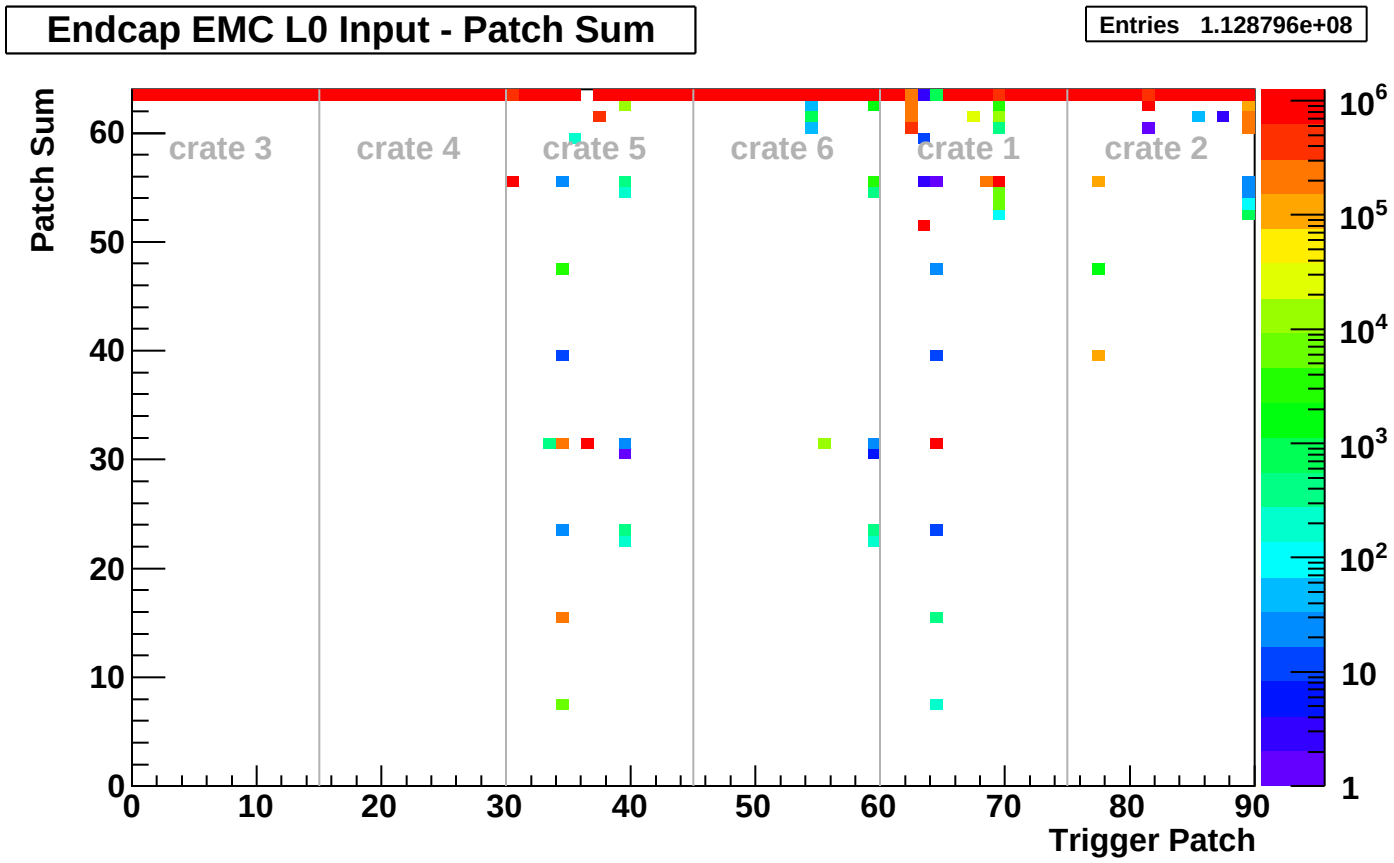

BunchId7Bit (JP1)
Entries1254218
10^4
10^3
0
20
40
60
80
100
120

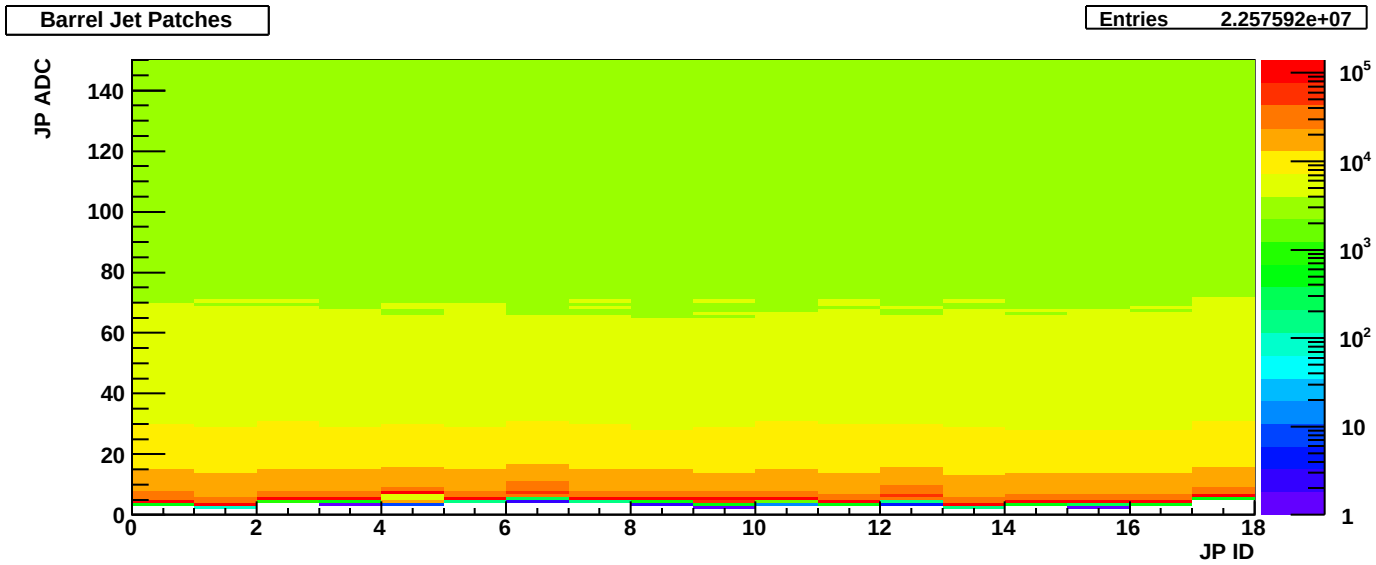

BunchId7Bit (JP2||AJP)
Entries1254218
10^4
10^3
0
20
40
60
80
100
120

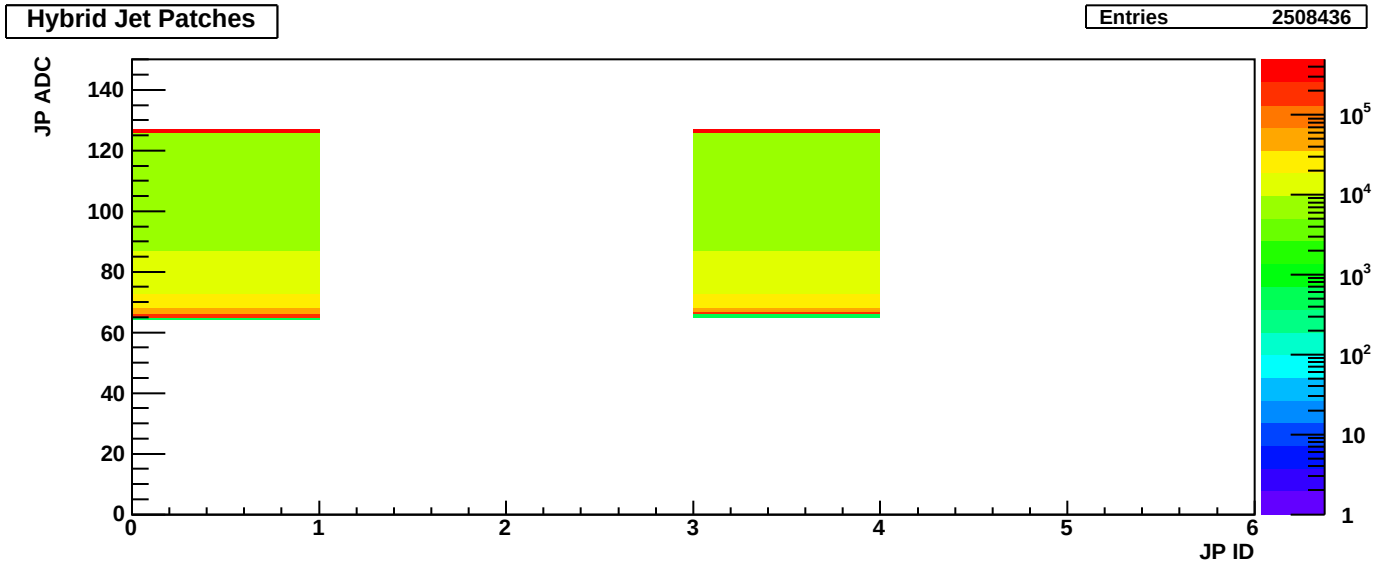

Barrel EMC L0 Input - High Tower
Entries 3.762654e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

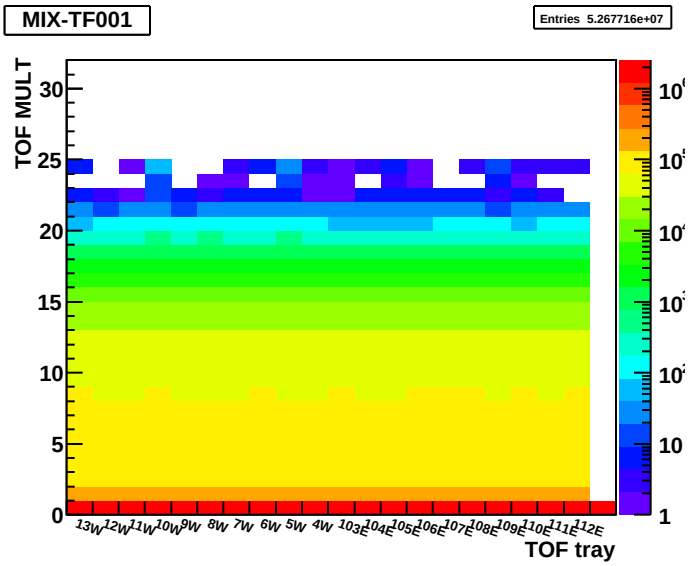

Barrel EMC L0 Input - Patch Sum
Entries 3.762654e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

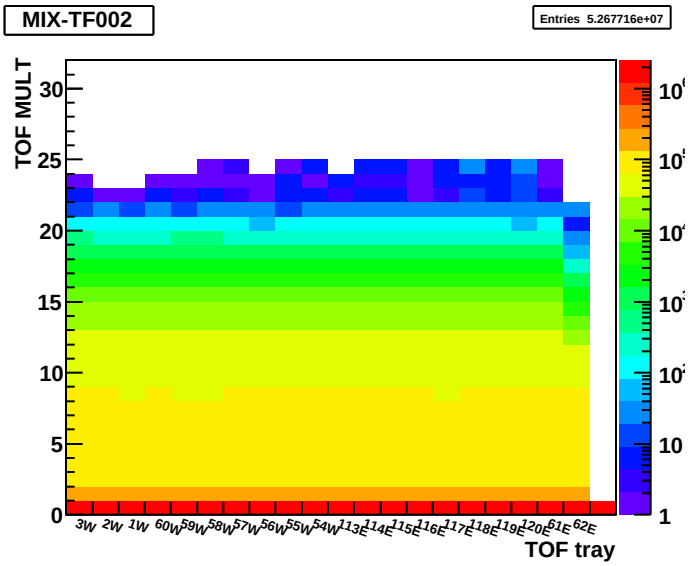

Endcap EMC L0 Input - High Tower
Entries 1.128796e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

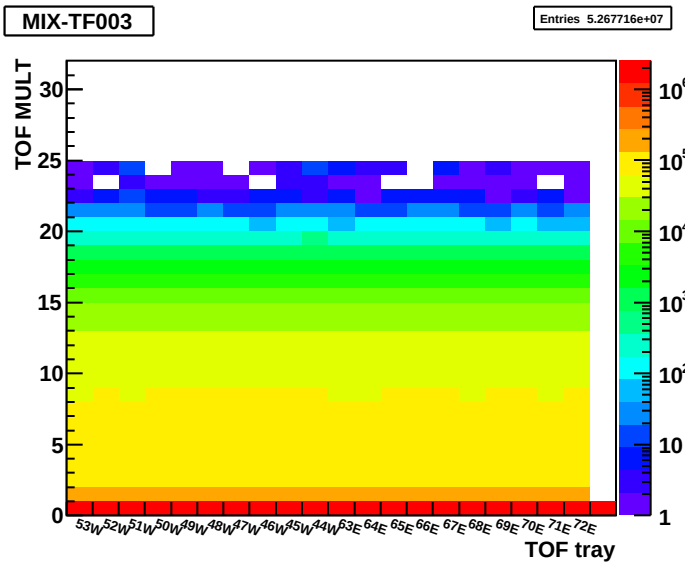

Endcap EMC L0 Input - Patch Sum
Entries 1.128796e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

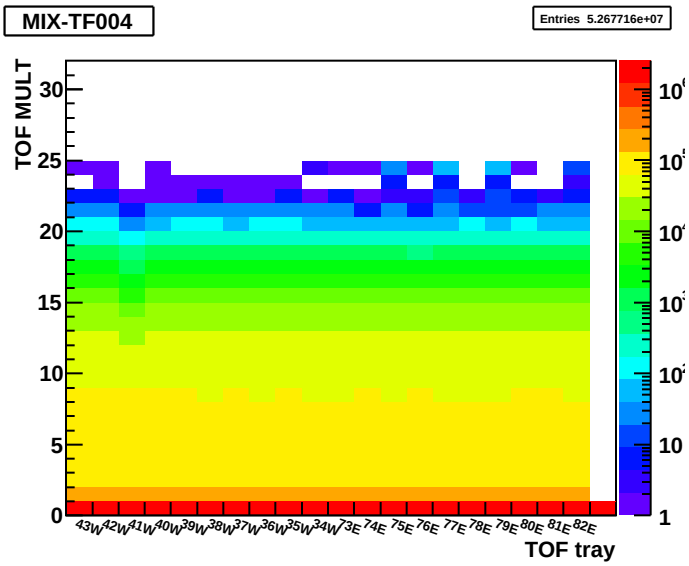

Barrel Jet Patches
Entries 2.257592e+07
JP ADC
JP ID

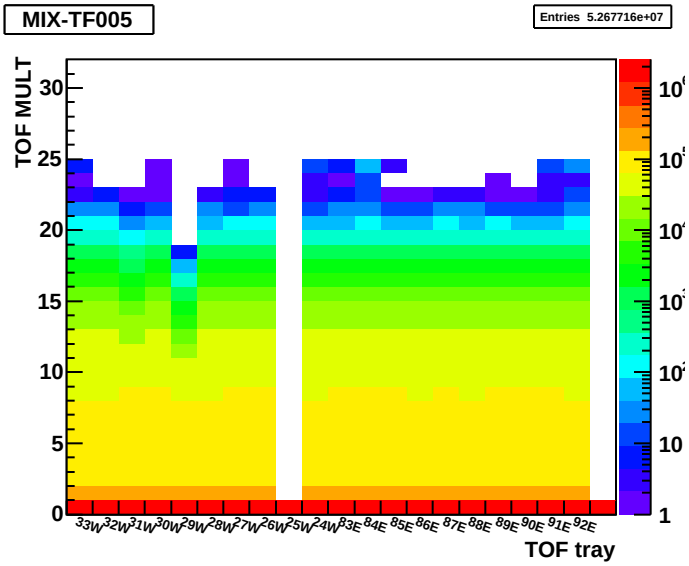

Hybrid Jet Patches
Entries 2508436
JP ADC
JP ID

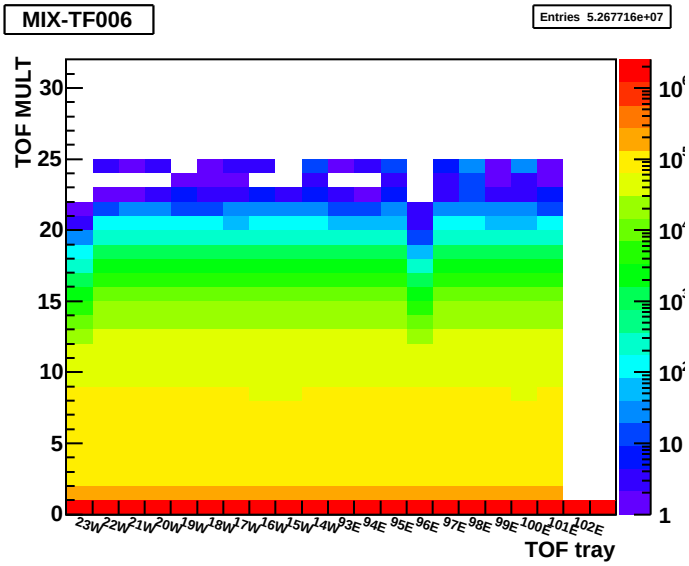

MIX-TF001
Entries 5.267716e+07
TOF MULT
TOF tray

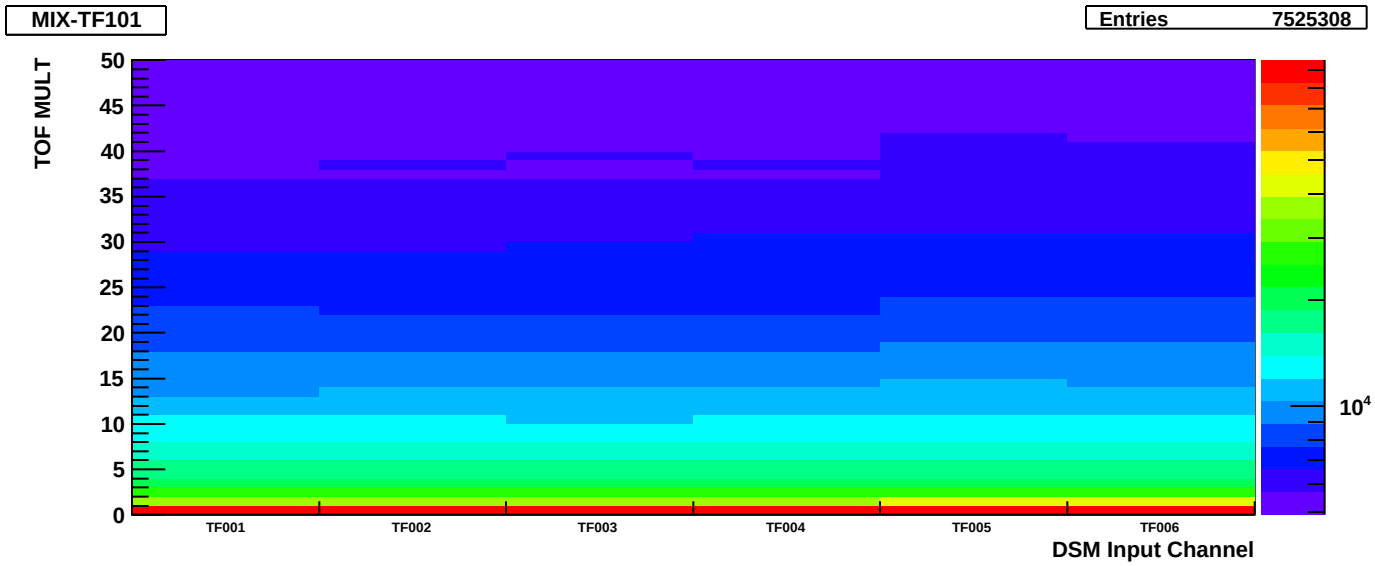

MIX-TF002
Entries 5.267716e+07
TOF MULT
TOF tray

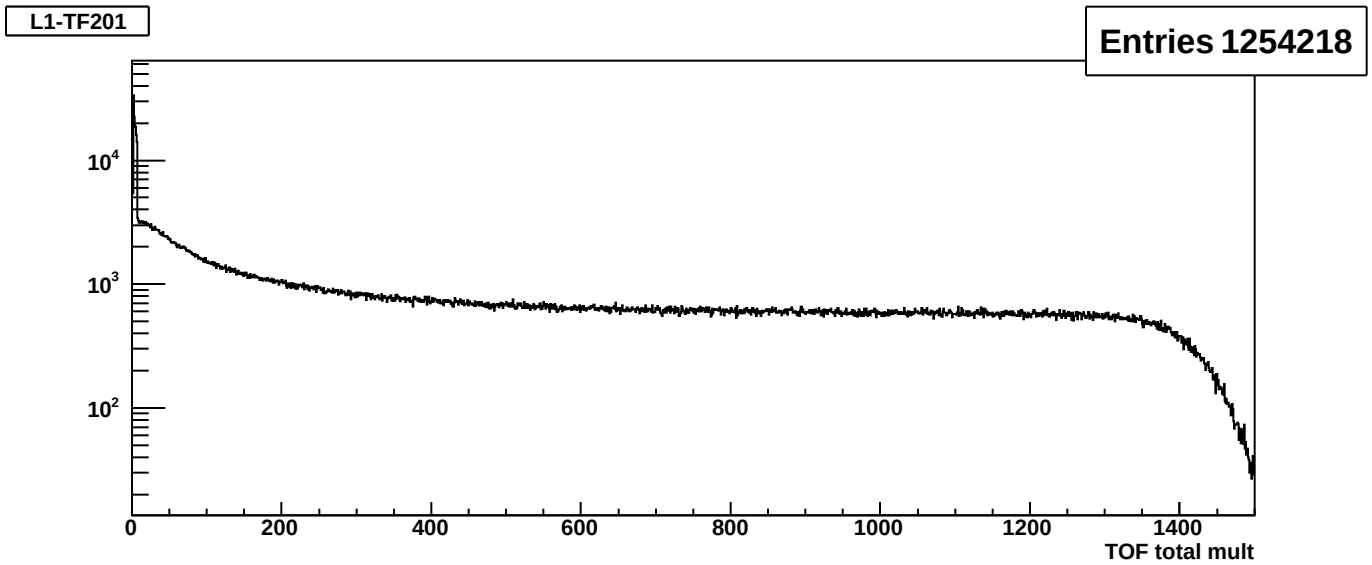

MIX-TF003
Entries 5.267716e+07
TOF MULT
TOF tray

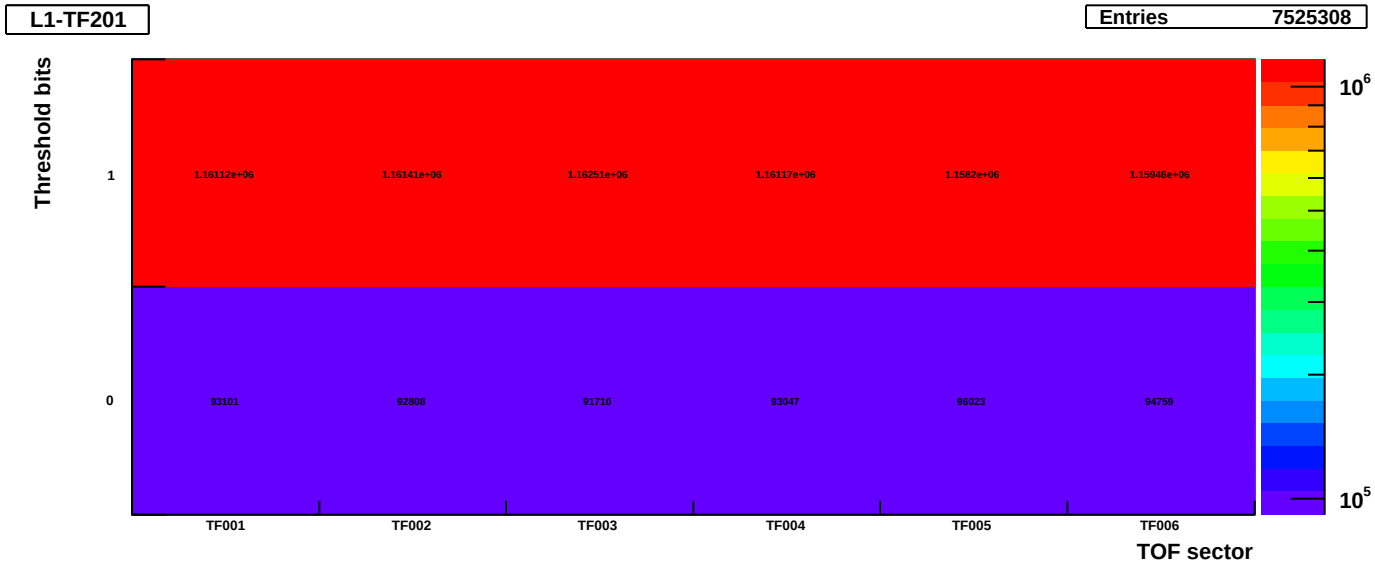

MIX-TF004
Entries 5.267716e+07
TOF MULT
TOF tray

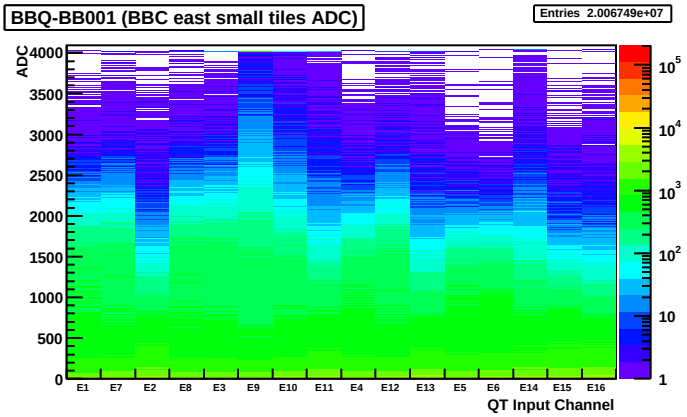

MIX-TF005
Entries 5.267716e+07
TOF MULT
TOF tray

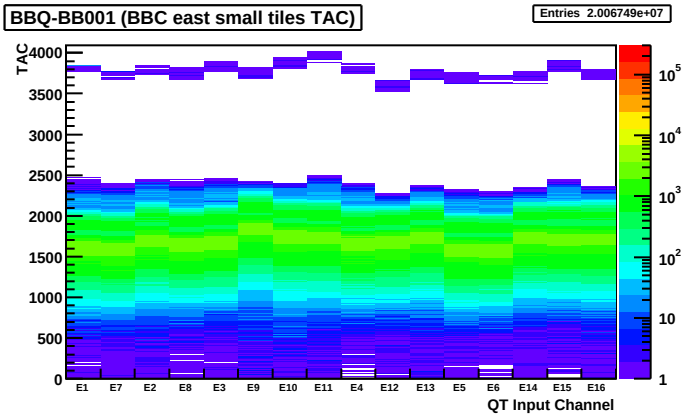

MIX-TF006
Entries 5.267716e+07
TOF MULT
TOF tray

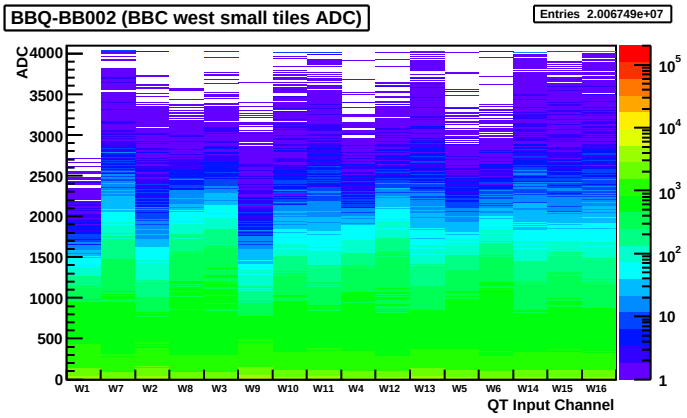

MIX-TF101
Entries 7525308
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

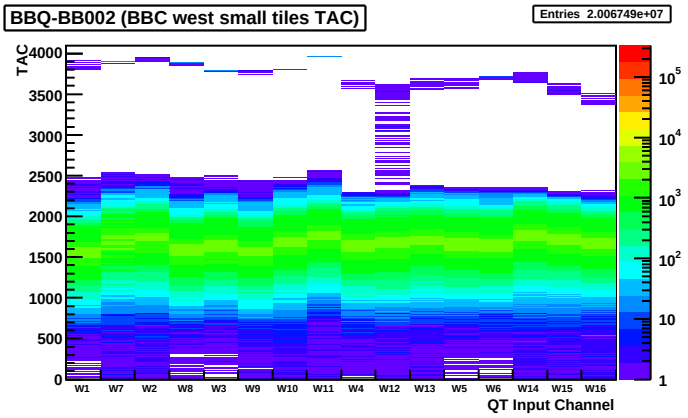

L1-TF201
Entries 1254218
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

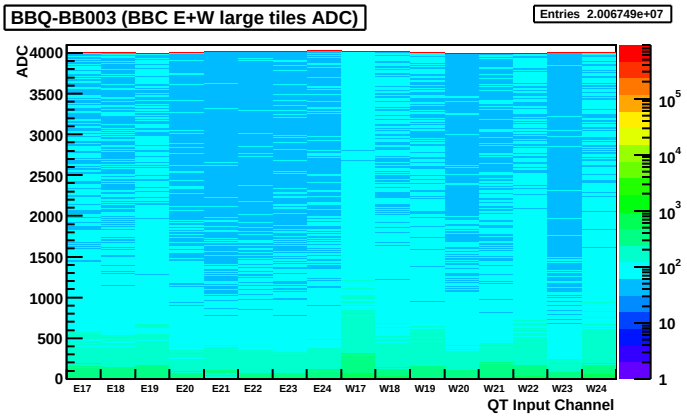

L1-TF201
Entries 7525308
Threshold bits
1
0
1.16112e+06
1.16141e+06
1.16251e+06
1.16117e+06
1.1582e+06
1.15946e+06
93101
92808
91710
93047
96023
94759
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

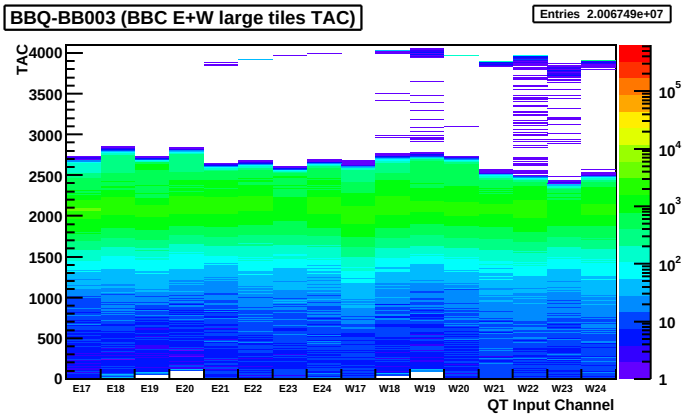

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.006749e+07
ADC
QT Input Channel

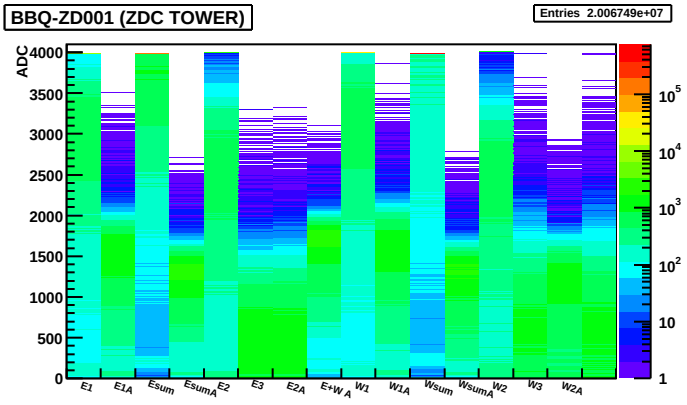

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.006749e+07
TAC
QT Input Channel

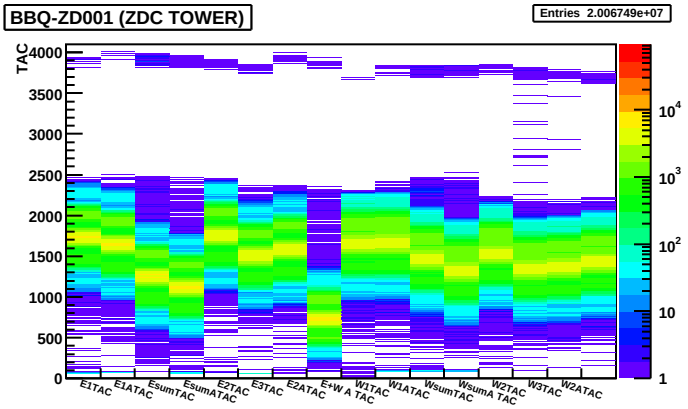

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.006749e+07
ADC
QT Input Channel

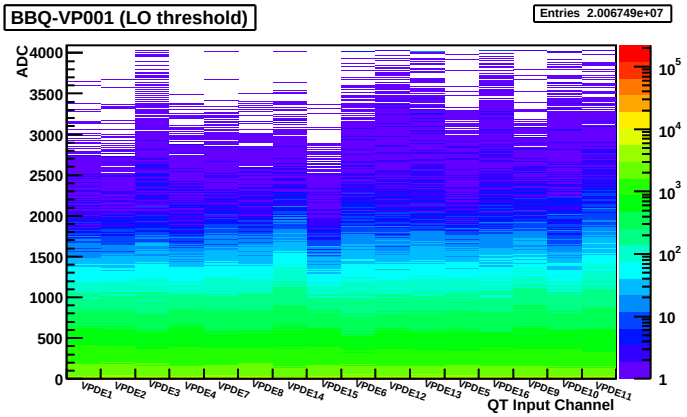

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.006749e+07
TAC
QT Input Channel

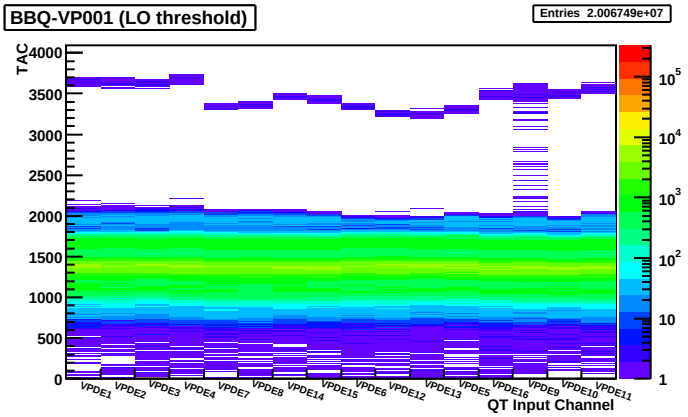

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.006749e+07
ADC
QT Input Channel

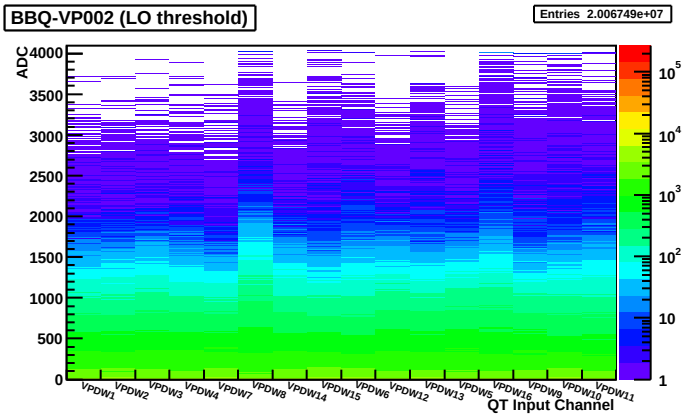

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.006749e+07
TAC
QT Input Channel

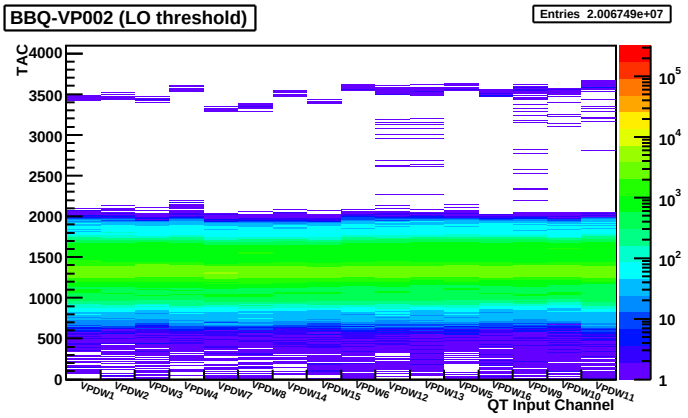

BBQ-ZD001 (ZDC TOWER)
Entries 2.006749e+07
ADC

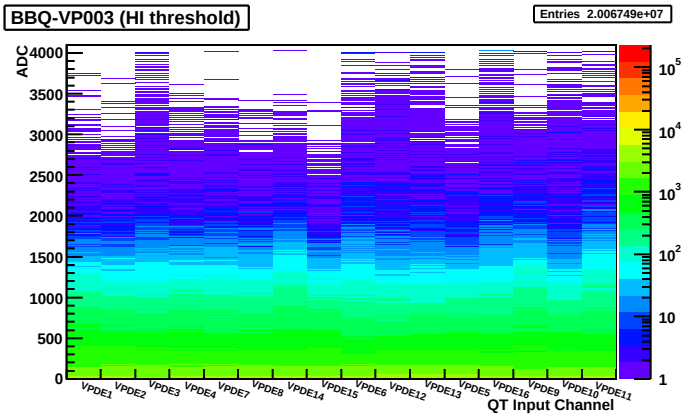

BBQ-ZD001 (ZDC TOWER)
Entries 2.006749e+07
TAC

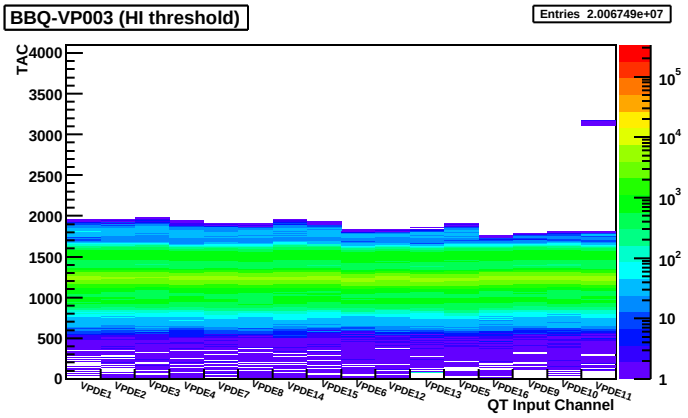

BBQ-VP001 (LO threshold)
Entries 2.006749e+07
ADC
QT Input Channel

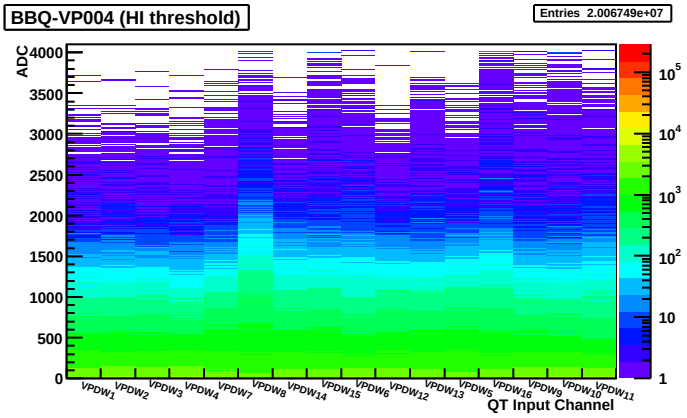

BBQ-VP001 (LO threshold)
Entries 2.006749e+07
TAC
QT Input Channel

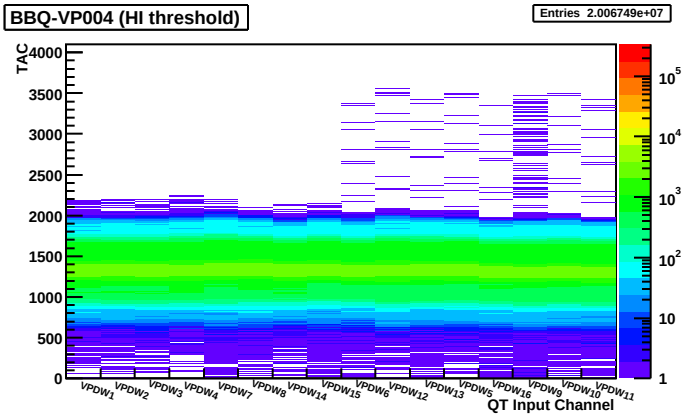

BBQ-VP002 (LO threshold)
Entries 2.006749e+07
ADC
QT Input Channel

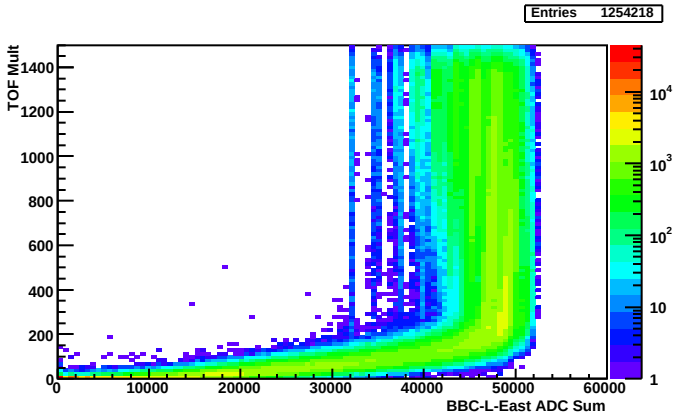

BBQ-VP002 (LO threshold)
Entries 2.006749e+07
TAC
QT Input Channel

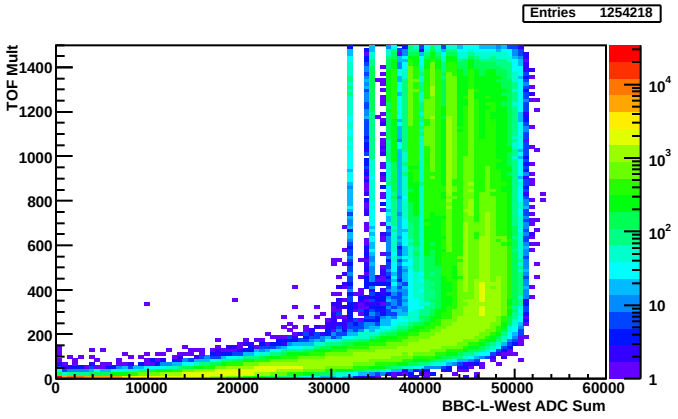

BBQ-VP003 (HI threshold)
Entries 2.006749e+07
ADC
QT Input Channel

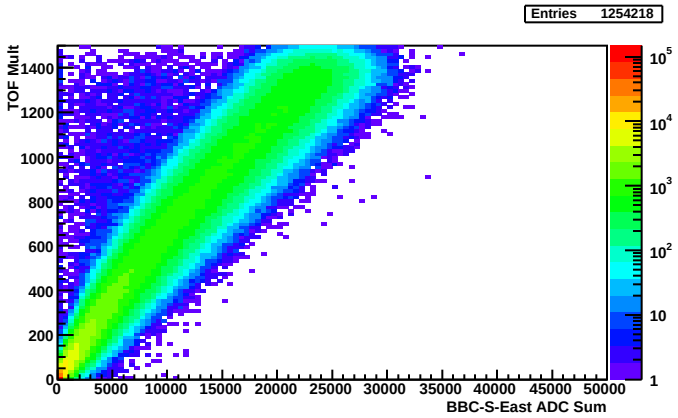

BBQ-VP003 (HI threshold)
Entries 2.006749e+07
TAC
QT Input Channel

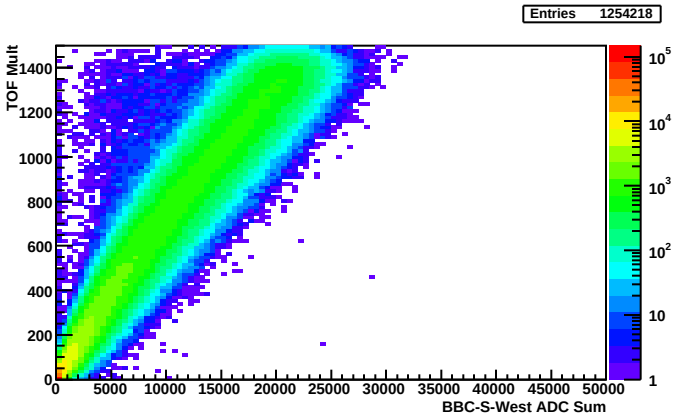

BBQ-VP004 (HI threshold)
Entries 2.006749e+07
ADC
QT Input Channel

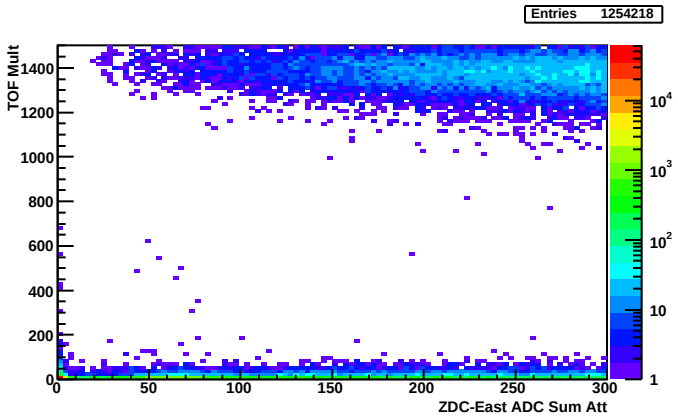

BBQ-VP004 (HI threshold)
Entries 2.006749e+07
TAC
QT Input Channel

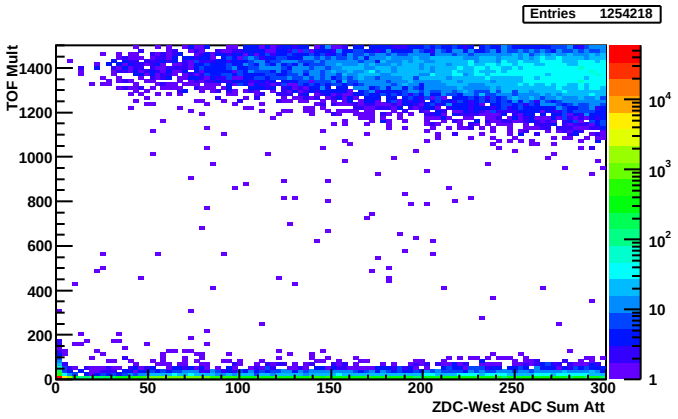

Entries 1254218
TOF Mult
BBC-L-East ADC Sum

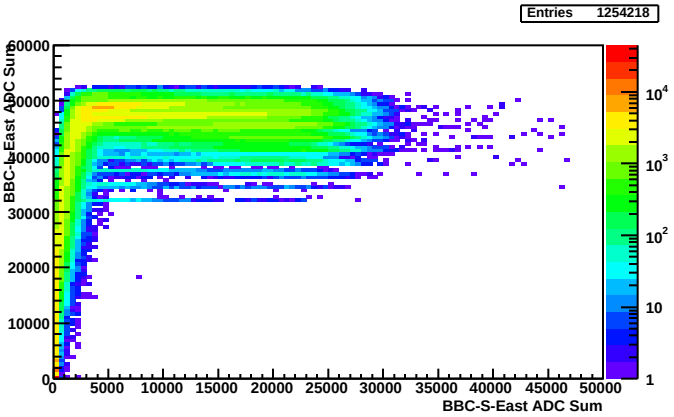

Entries 1254218
TOF Mult
BBC-L-West ADC Sum

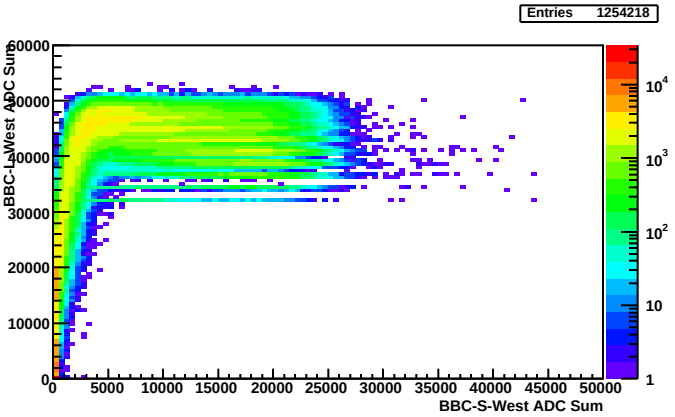

Entries 1254218
TOF Mult
BBC-S-East ADC Sum

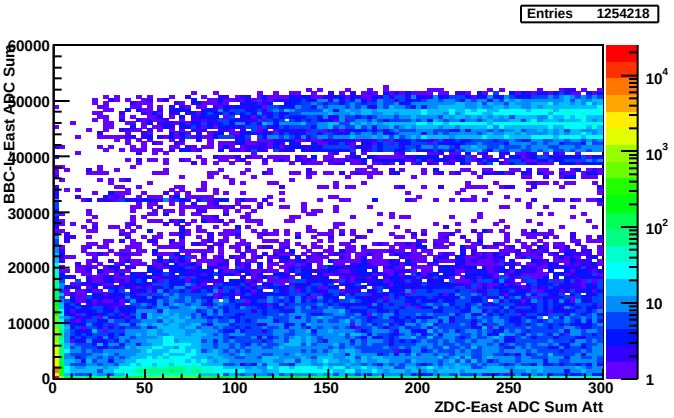

Entries 1254218
TOF Mult
BBC-S-West ADC Sum

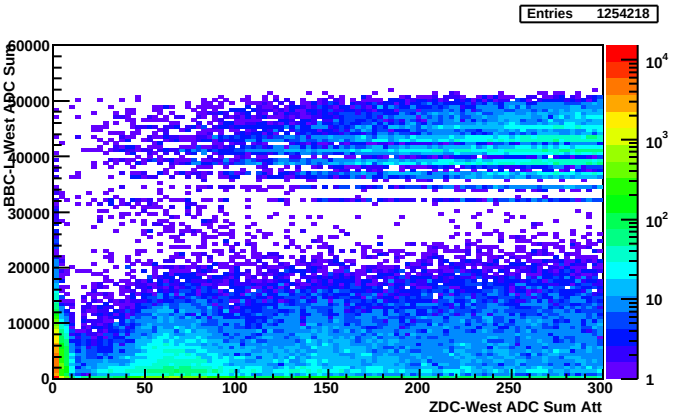

Entries 1254218
TOF Mult
ZDC-East ADC Sum Att


Entries 1254218
TOF Mult
ZDC-West ADC Sum Att


Entries 1254218
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1254218
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1254218
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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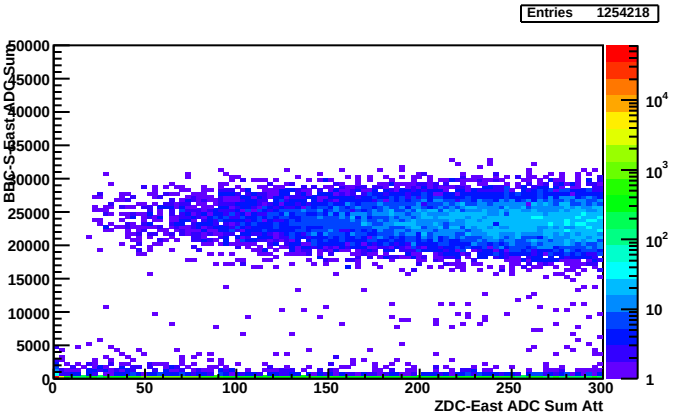
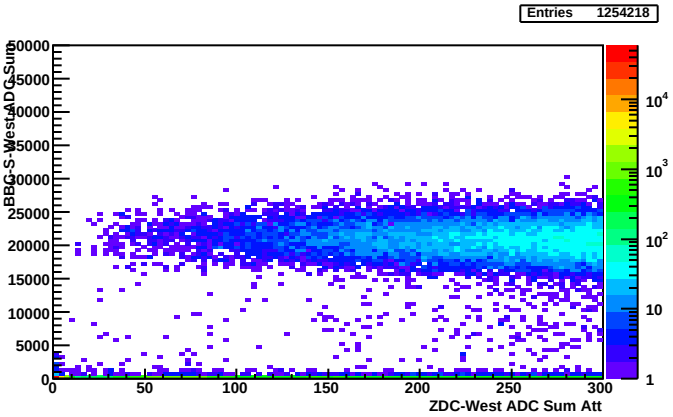
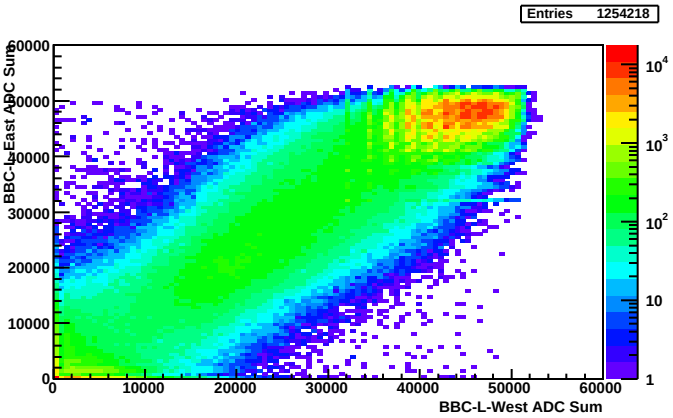
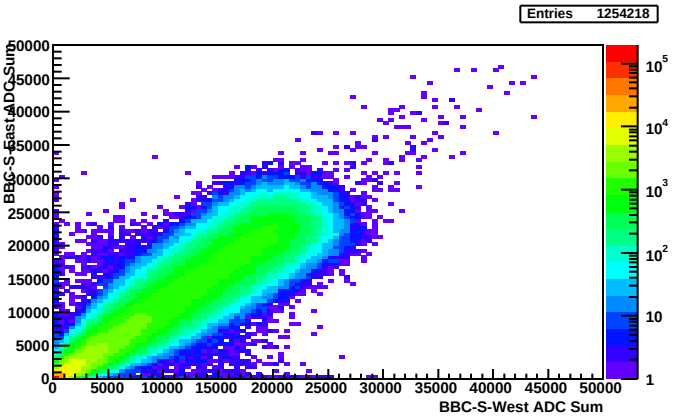
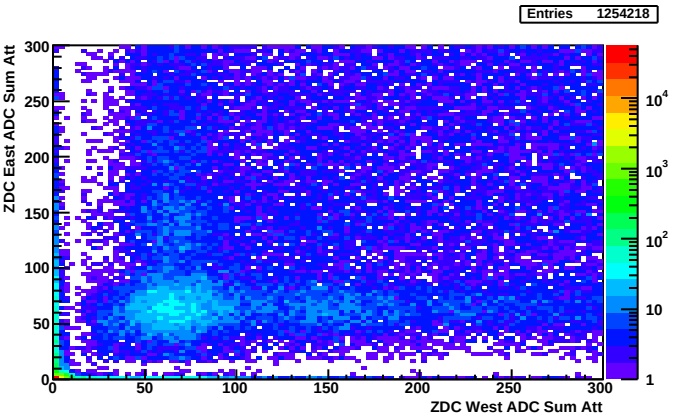
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

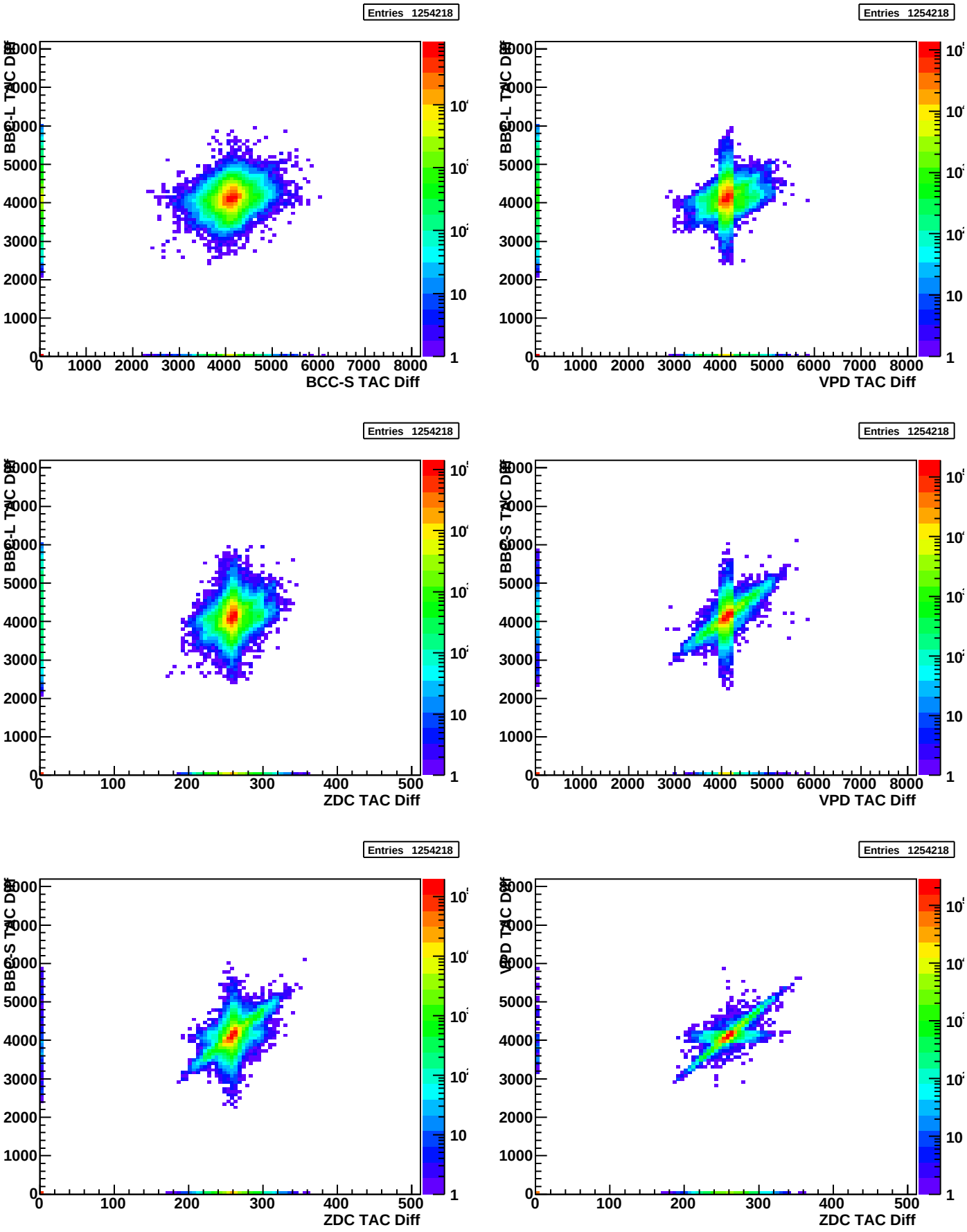

Entries 1254218
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

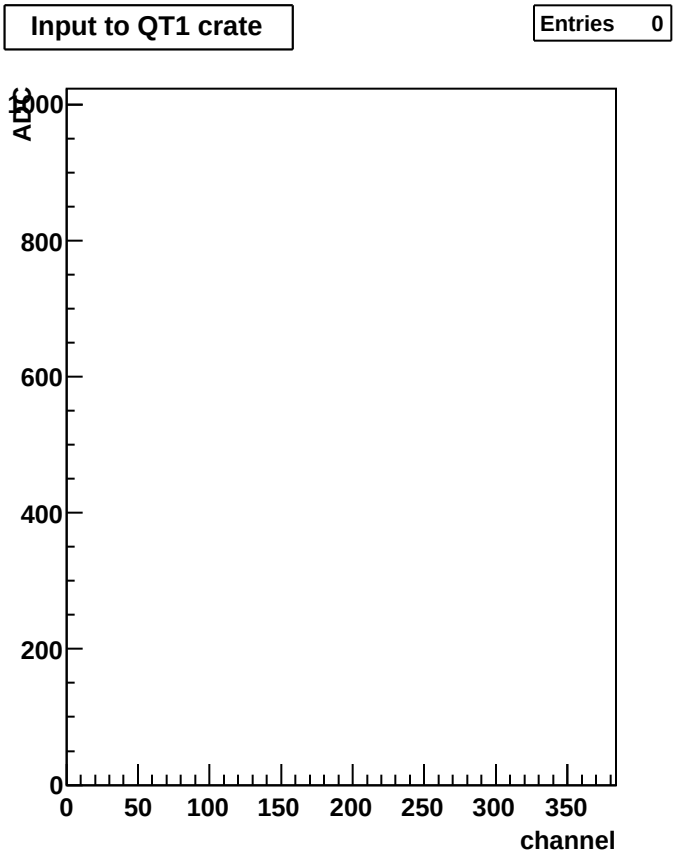

Entries 1254218
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

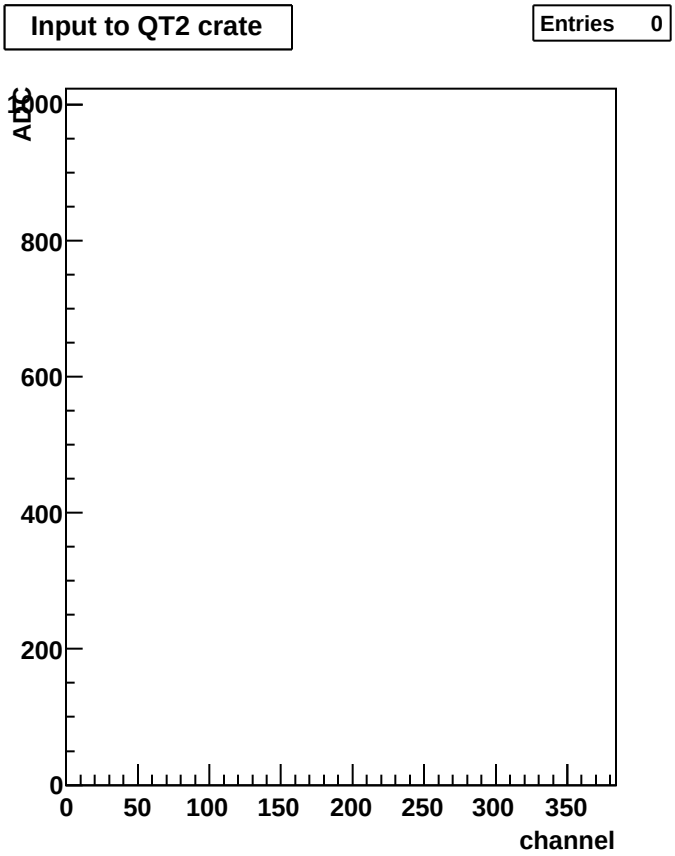

Entries 1254218
BBC-L-East ADC Sum
BBC-L-West ADC Sum

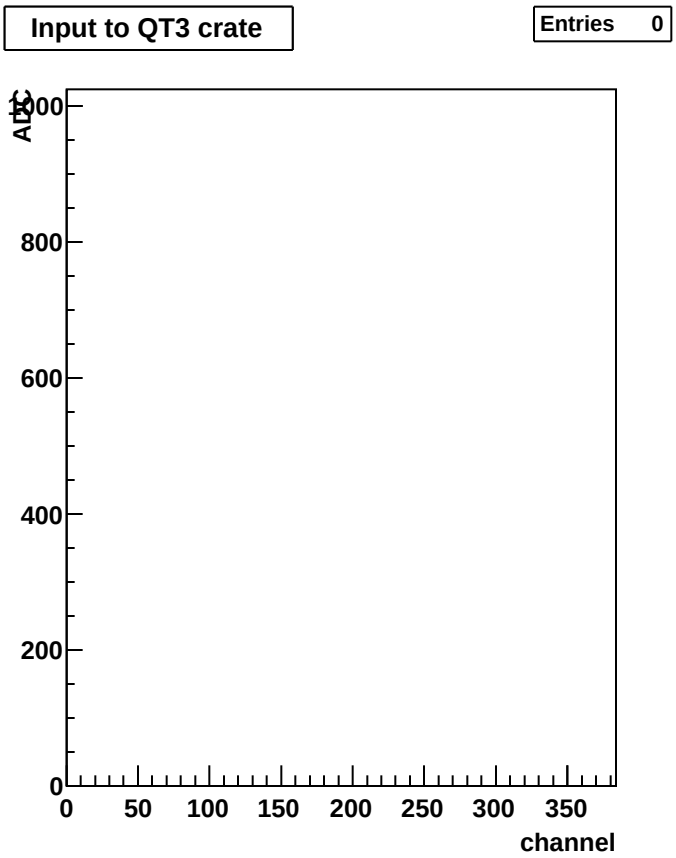

Entries 1254218
BBC-S-East ADC Sum
BBC-S-West ADC Sum

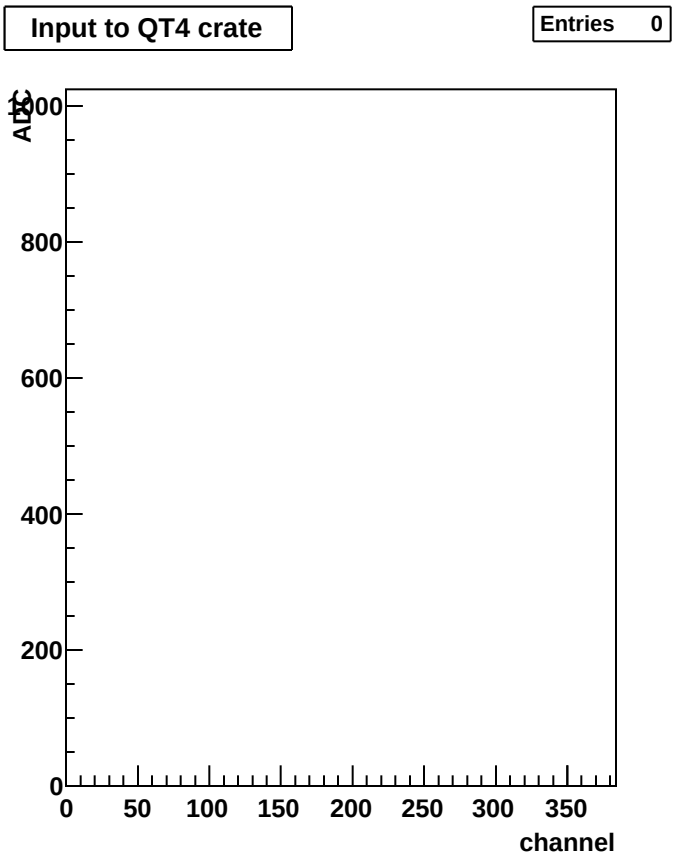

Entries 1254218
ZDC East ADC Sum Att
ZDC West ADC Sum Att

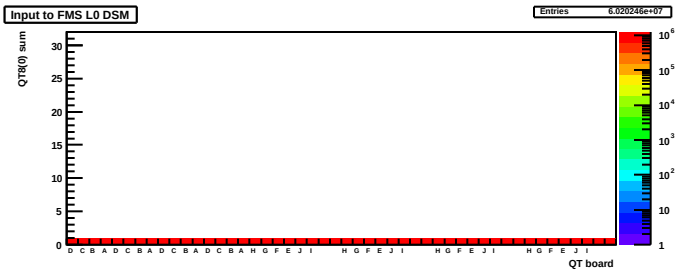

Entries 1254218
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1254218
BBC-L TAC Diff
VPD TAC Diff
Entries 1254218
BBC-L TAC Diff
ZDC TAC Diff
Entries 1254218
BBC-S TAC Diff
VPD TAC Diff
Entries 1254218
BBC-S TAC Diff
ZDC TAC Diff
Entries 1254218
VPD TAC Diff
ZDC TAC Diff

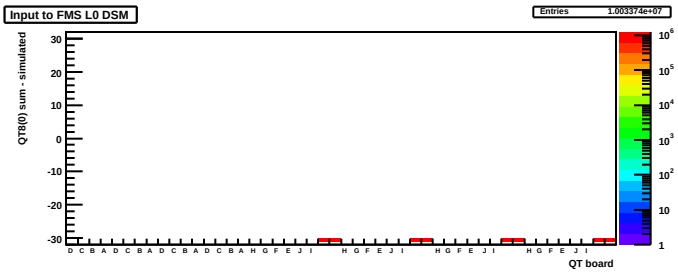

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

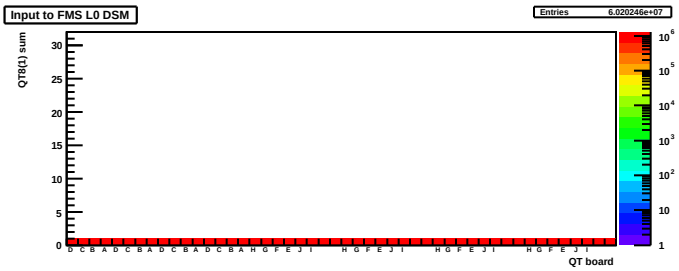

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

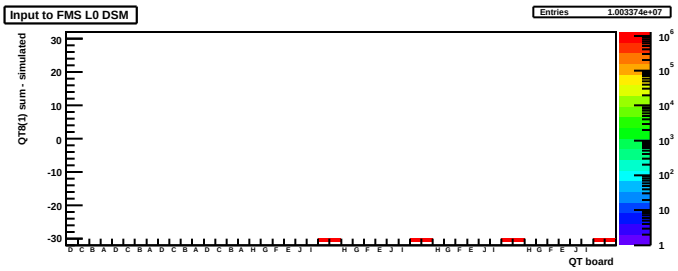

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

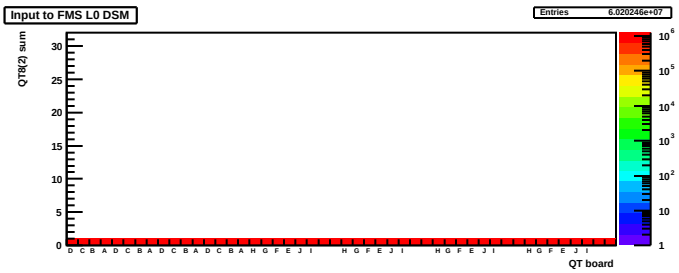

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

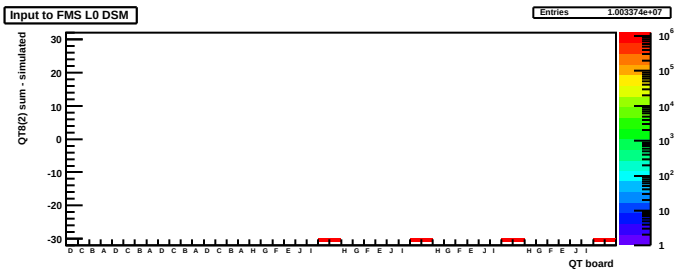

Input to FMS L0 DSM
Entries 6.020246e+07
QT8(0) sum
QT board

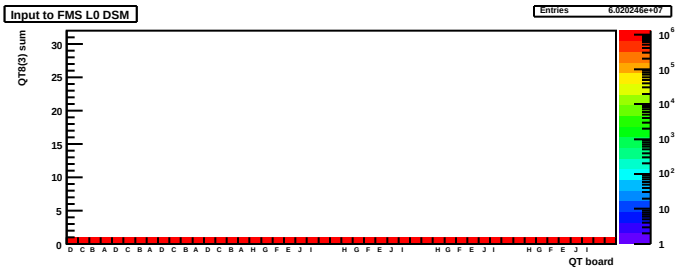

Input to FMS L0 DSM
Entries 1.003374e+07
QT8(0) sum - simulated
QT board

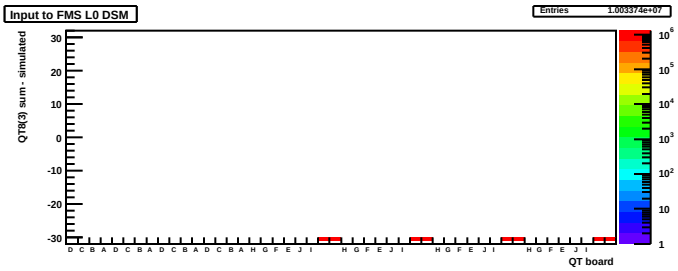

Input to FMS L0 DSM
Entries 6.020246e+07
QT8(1) sum
QT board

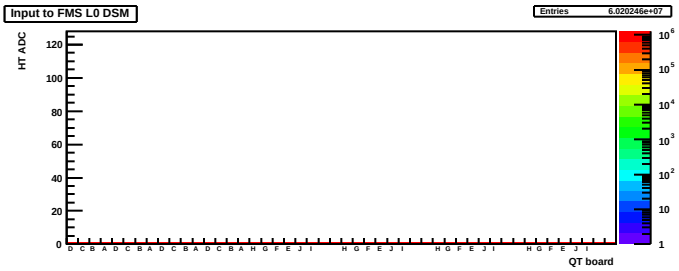

Input to FMS L0 DSM
Entries 1.003374e+07
QT8(1) sum - simulated
QT board

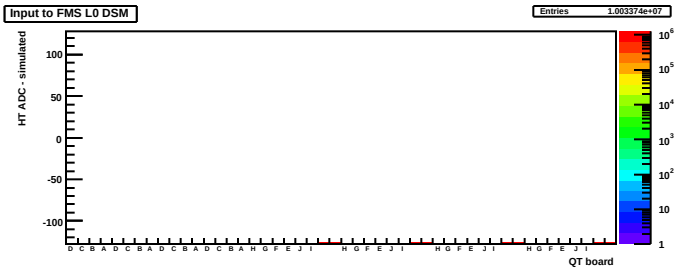

Input to FMS L0 DSM
Entries 6.020246e+07
QT8(2) sum
QT board

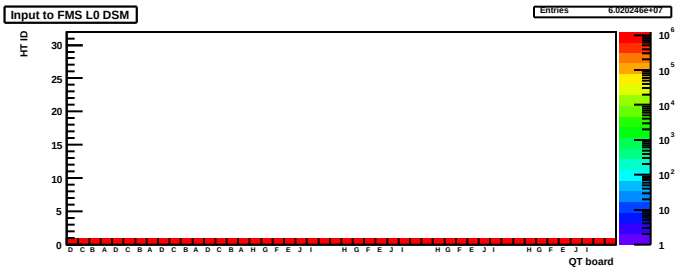

Input to FMS L0 DSM
Entries 1.003374e+07
QT8(2) sum - simulated
QT board

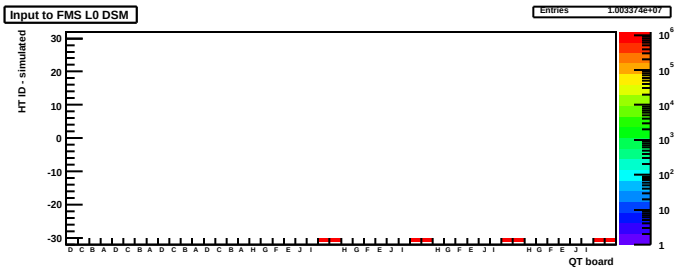

Input to FMS L0 DSM
Entries 6.020246e+07
QT8(3) sum
QT board

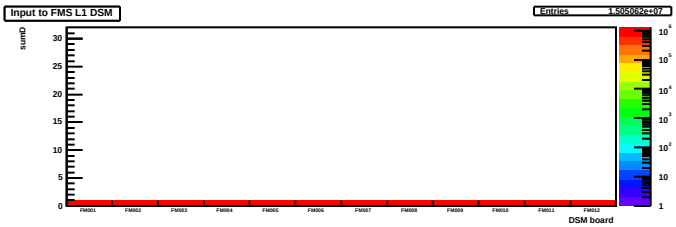

Input to FMS L0 DSM
Entries 1.003374e+07
QT8(3) sum - simulated
QT board

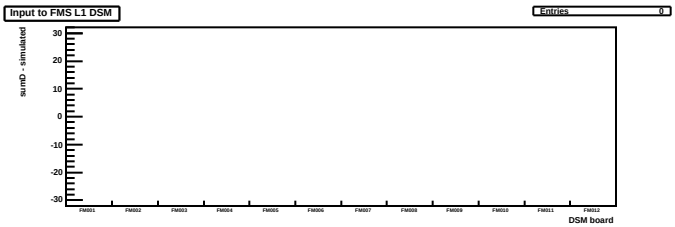

Input to FMS L0 DSM
Entries 6.020246e+07
HT ADC
QT board

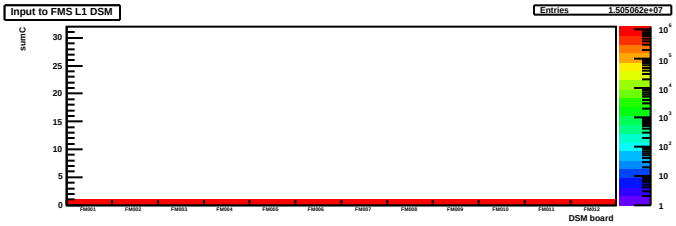

Input to FMS L0 DSM
Entries 1.003374e+07
HT ADC - simulated
QT board

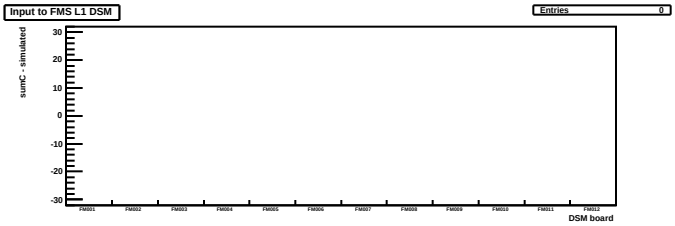

Input to FMS L0 DSM
Entries 6.020246e+07
HT ID
QT board

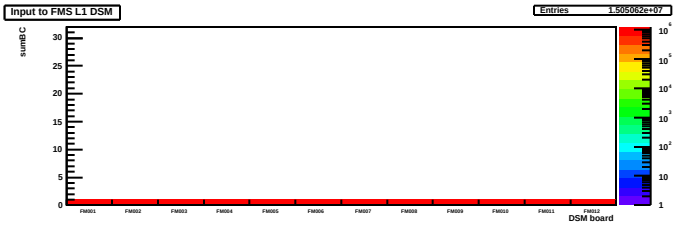

Input to FMS L0 DSM
Entries 1.003374e+07
HT ID - simulated
QT board

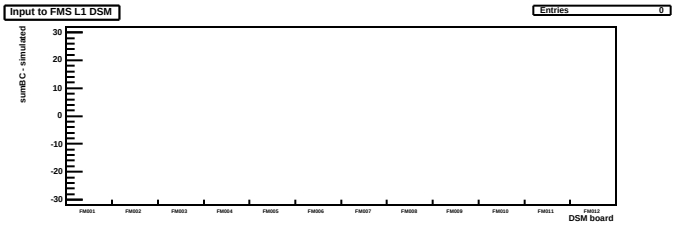

Input to FMS L1 DSM
Entries 1.505062e+07
sumD
DSM board

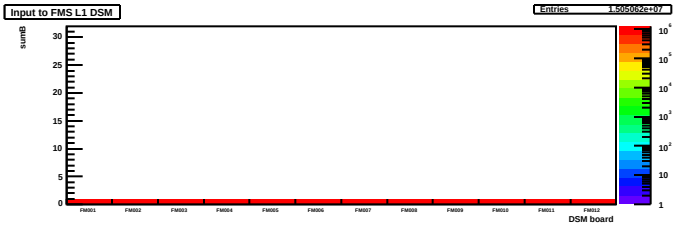

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

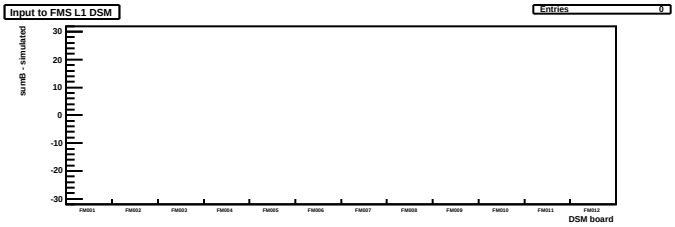

Input to FMS L1 DSM
Entries 1.505062e+07
sumC
DSM board

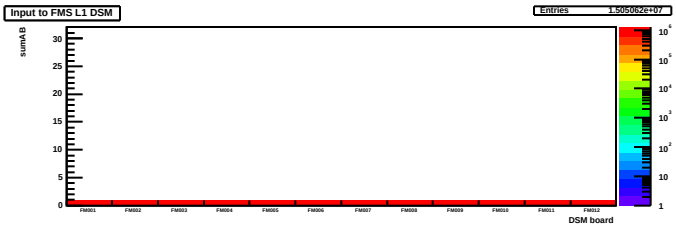

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

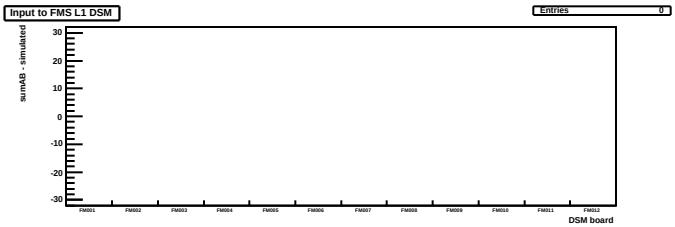

Input to FMS L1 DSM
Entries 1.505062e+07
sumBC
DSM board

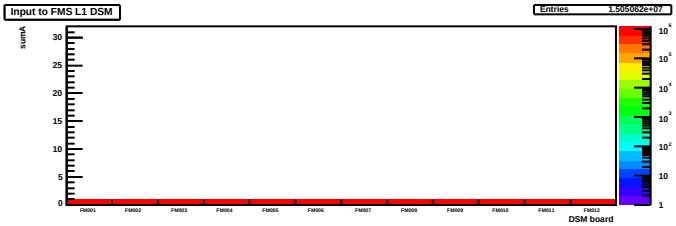

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

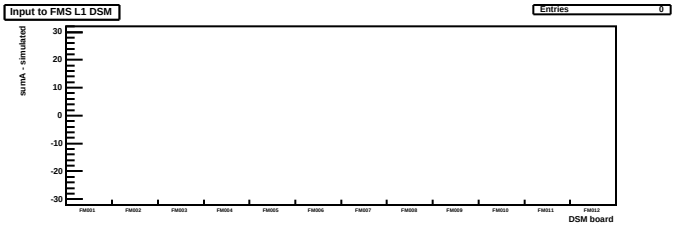

Input to FMS L1 DSM
Entries 1.505062e+07
sumB
DSM board

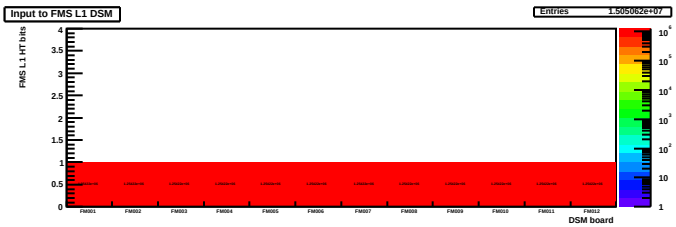

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

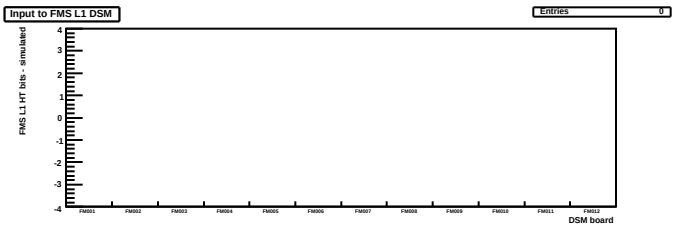

Input to FMS L1 DSM
Entries 1.505062e+07
sumAB
DSM board

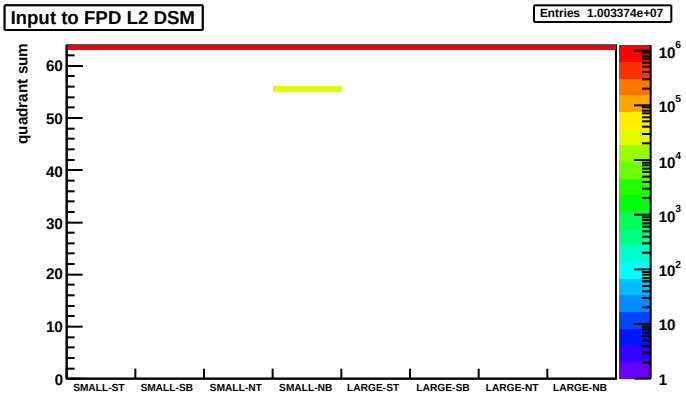

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

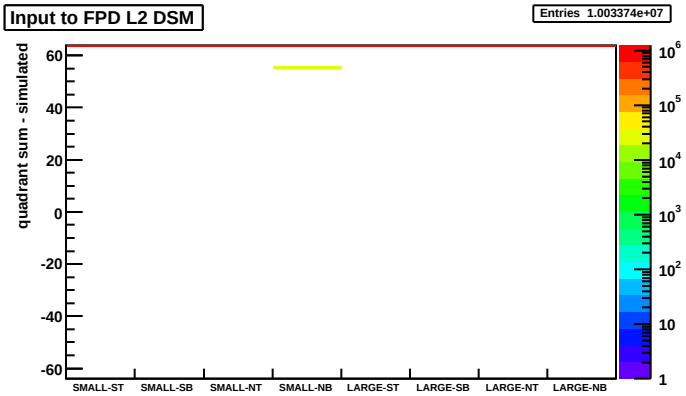

Input to FMS L1 DSM
Entries 1.505062e+07
sumA
DSM board

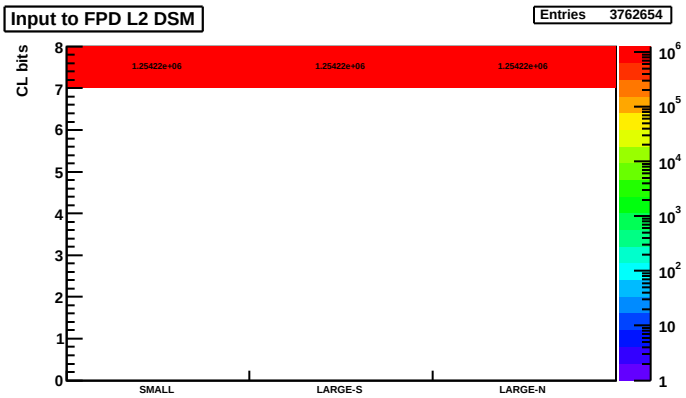

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

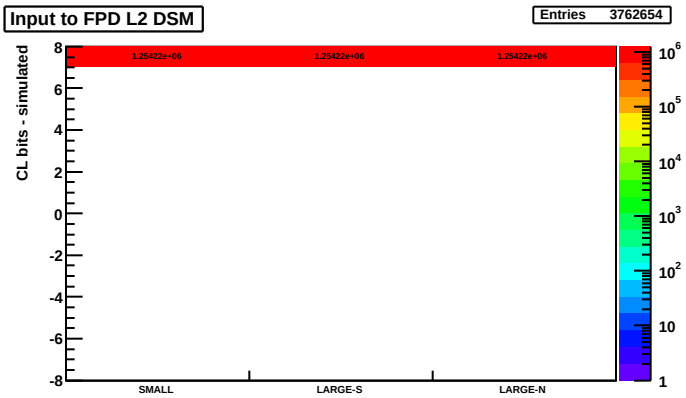

Input to FMS L1 DSM
Entries 1.505062e+07
FMS L1 HT bits
DSM board

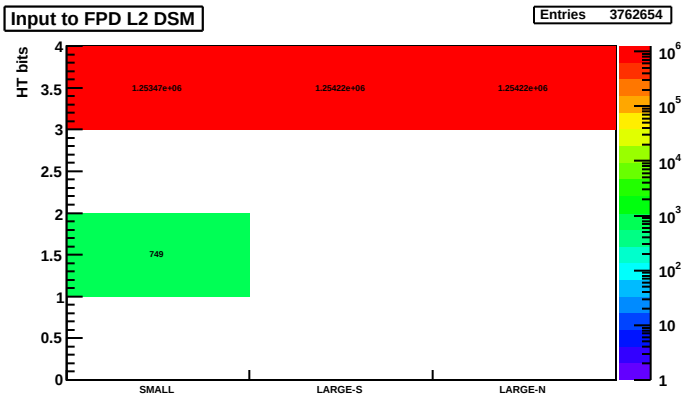

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

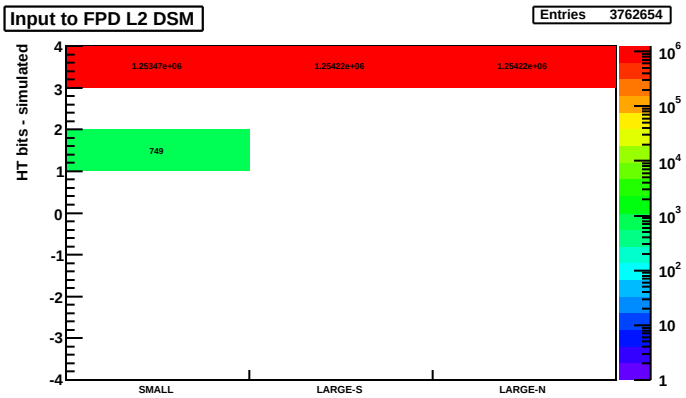

Input to FPD L2 DSM
Entries 1.003374e+07
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

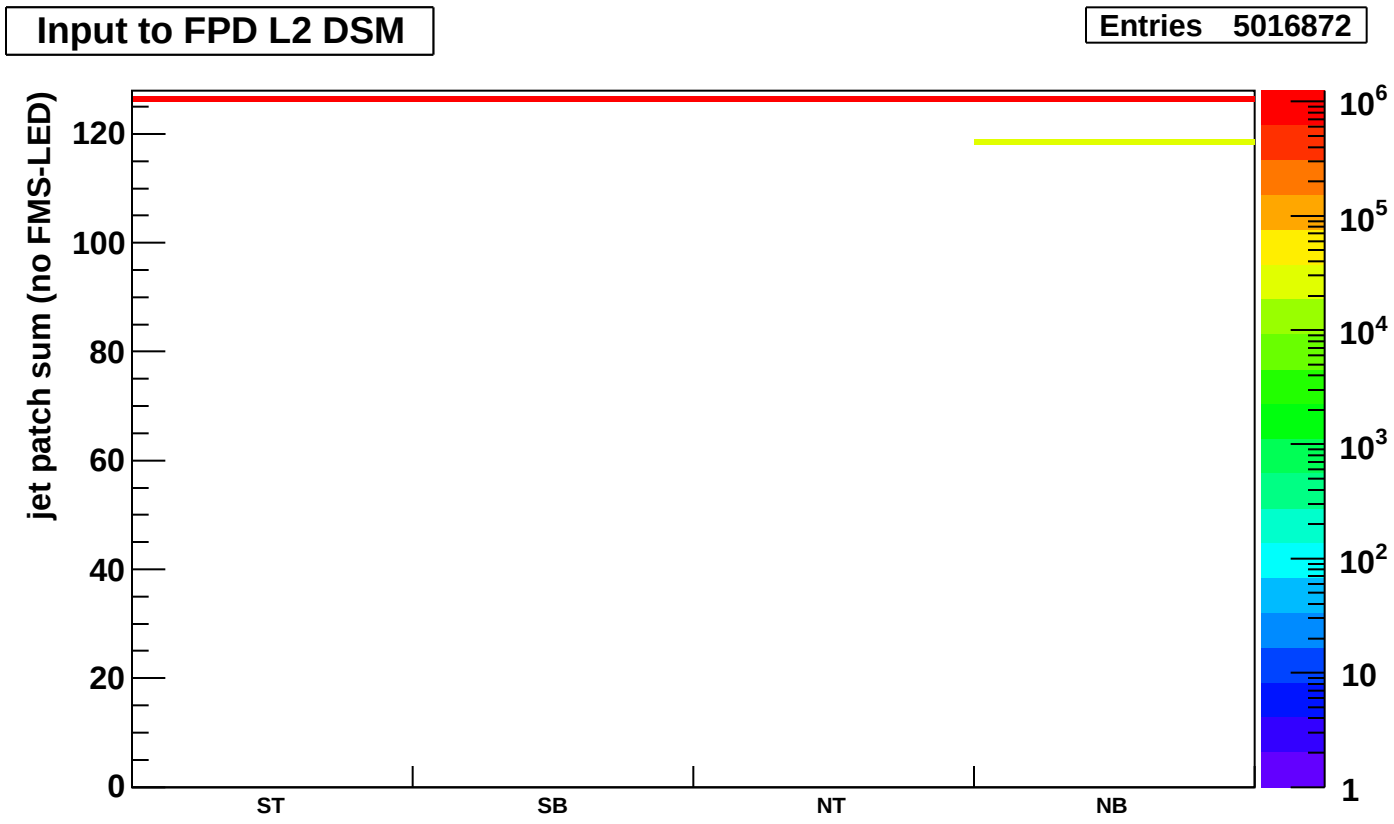

Input to FPD L2 DSM
Entries 1.003374e+07
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

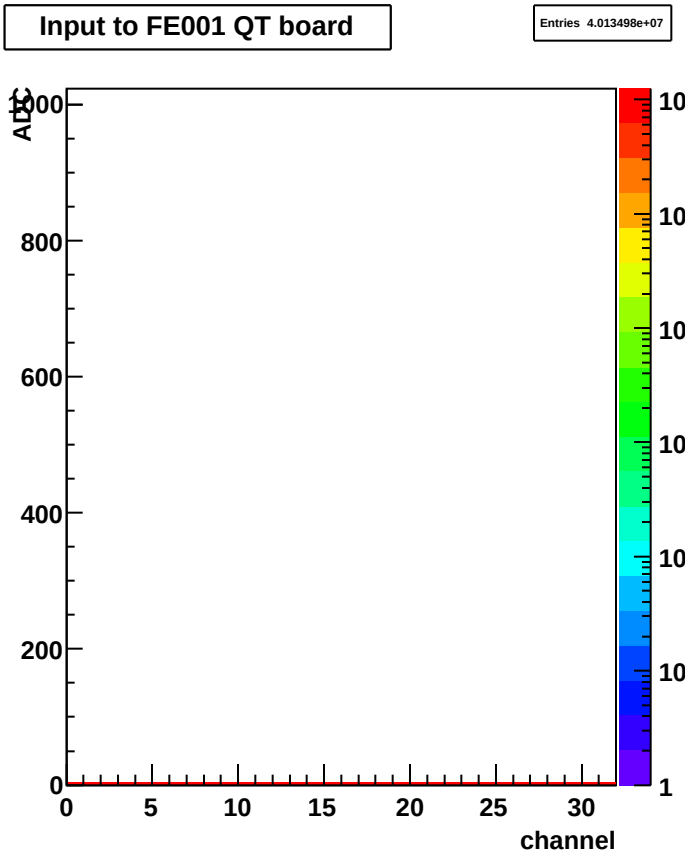

Input to FPD L2 DSM
Entries 3762654
CL bits
1.25422e+06
1.25422e+06
1.25422e+06
SMALL
LARGE-S
LARGE-N

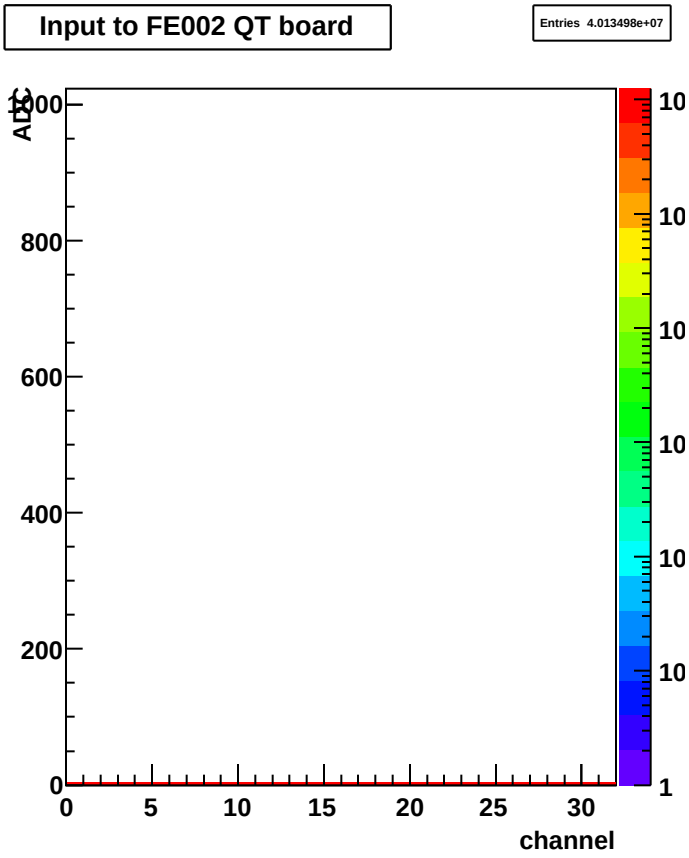

Input to FPD L2 DSM
Entries 3762654
CL bits - simulated
1.25422e+06
1.25422e+06
1.25422e+06
SMALL
LARGE-S
LARGE-N

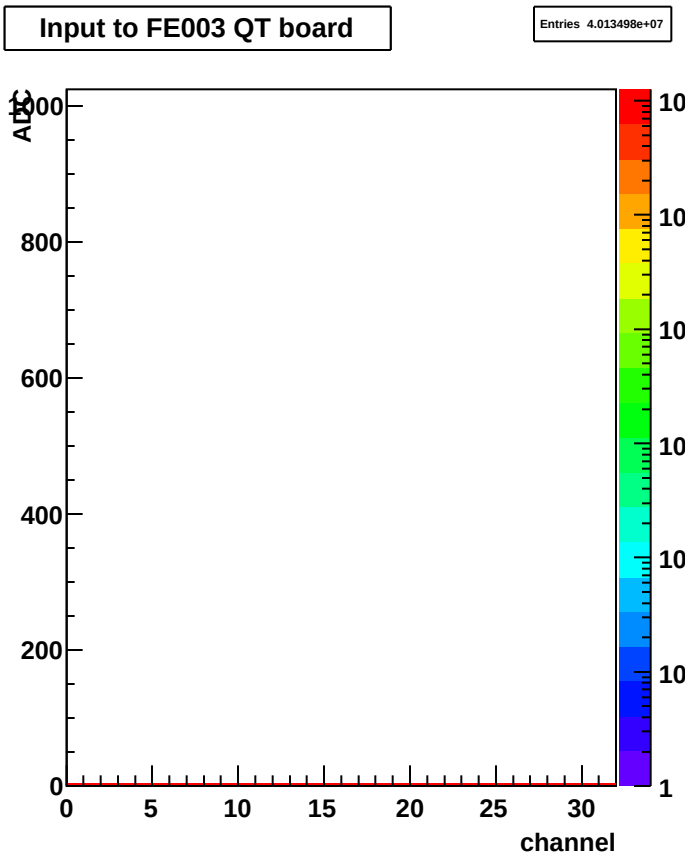

Input to FPD L2 DSM
Entries 3762654
HT bits
1.25347e+06
1.25422e+06
1.25422e+06
749
SMALL
LARGE-S
LARGE-N

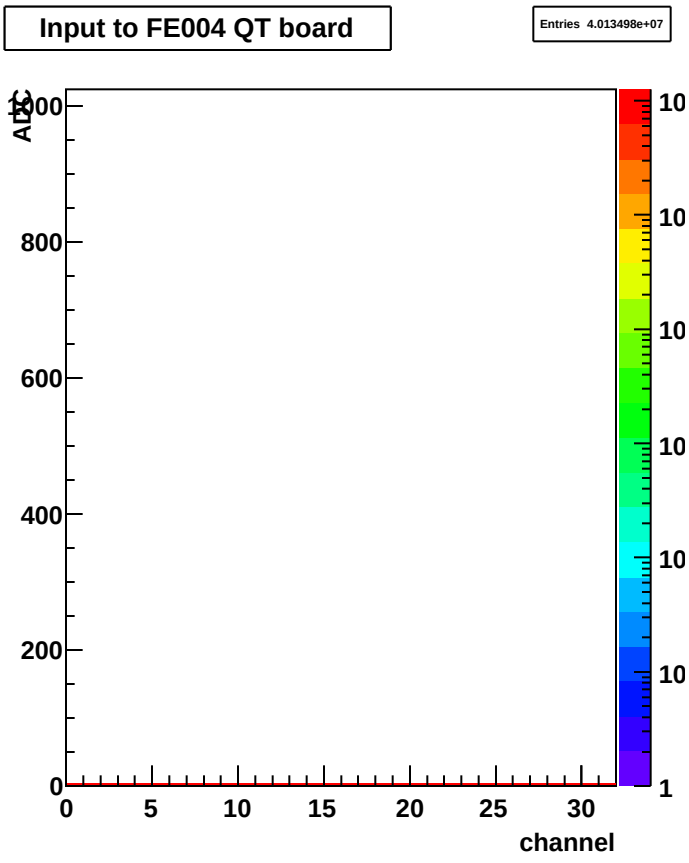

Input to FPD L2 DSM
Entries 3762654
HT bits - simulated
1.25347e+06
1.25422e+06
1.25422e+06
749
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 5016872
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

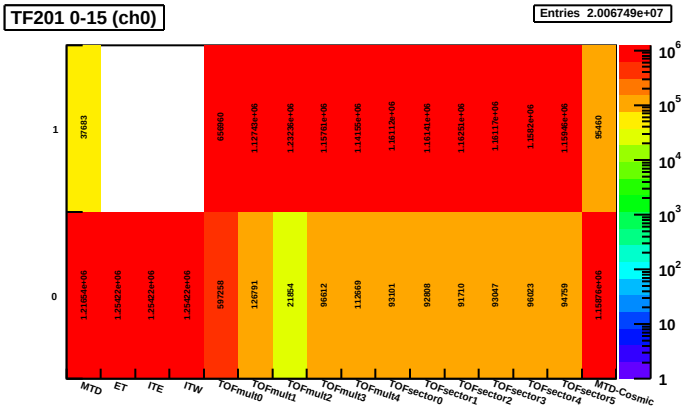

Input to FE001 QT board
Entries 4.013498e+07
ADC
channel


Input to FE002 QT board
Entries 4.013498e+07
ADC
channel

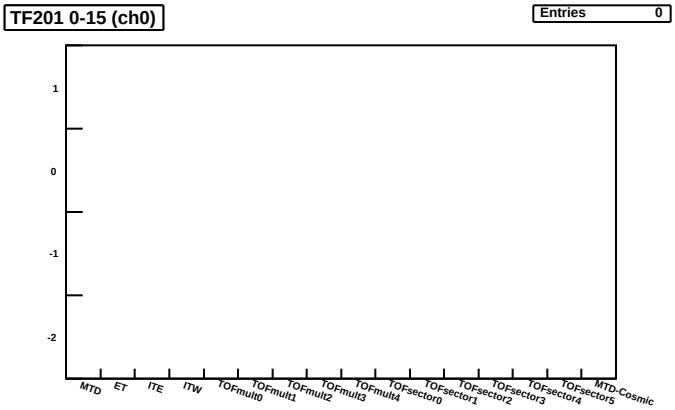

Input to FE003 QT board
Entries 4.013498e+07
ADC
channel


Input to FE004 QT board
Entries 4.013498e+07
ADC
channel

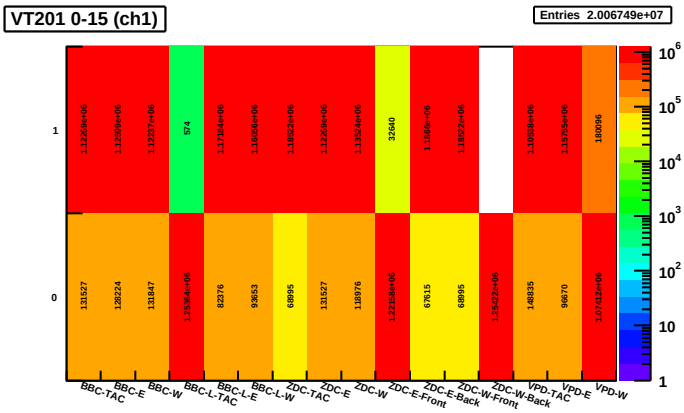
TF201 0-15 (ch0)



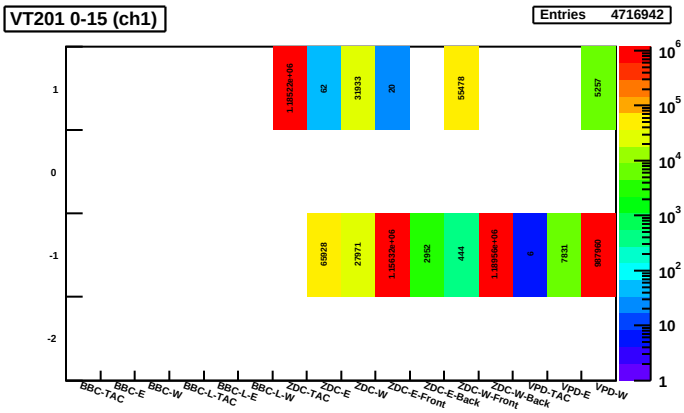
TF201 0-15 (ch0)



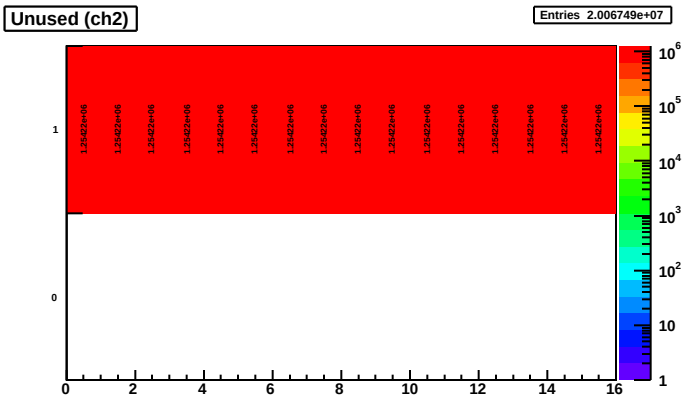
VT201 0-15 (ch1)

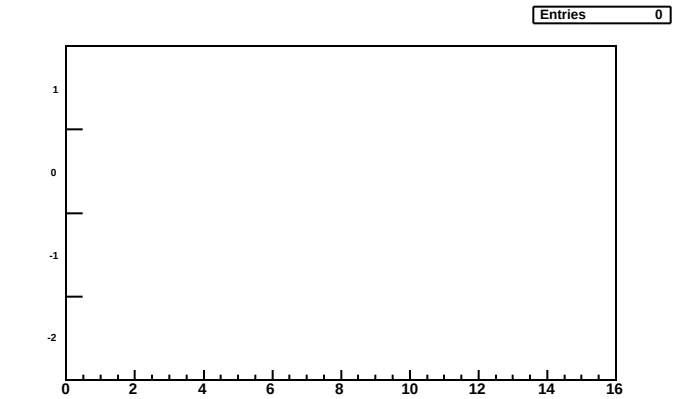


VT201 0-15 (ch1)

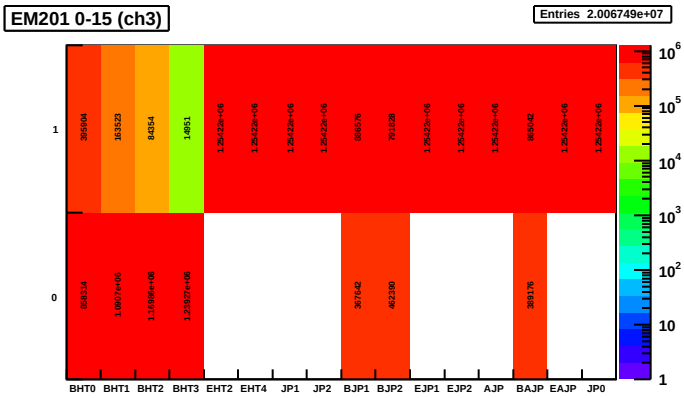


Unused (ch2)

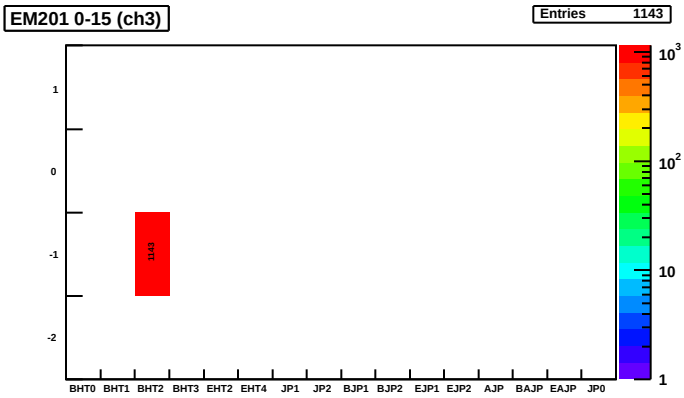




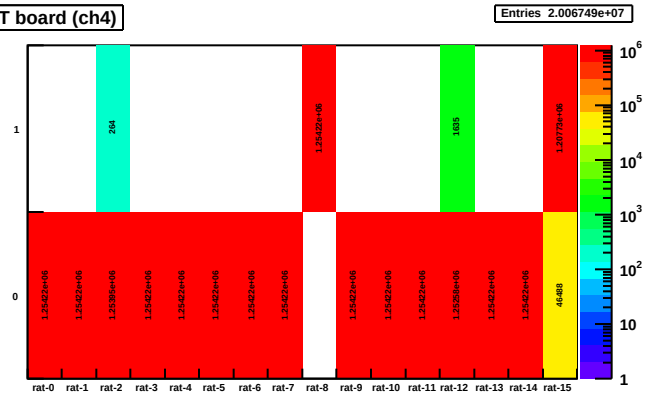
EM201 0-15 (ch3)

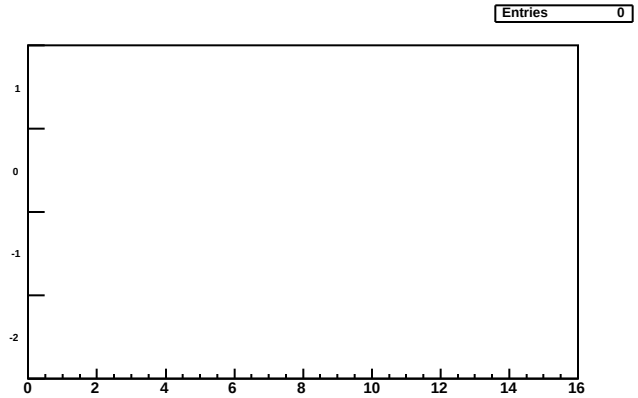


EM201 0-15 (ch3)

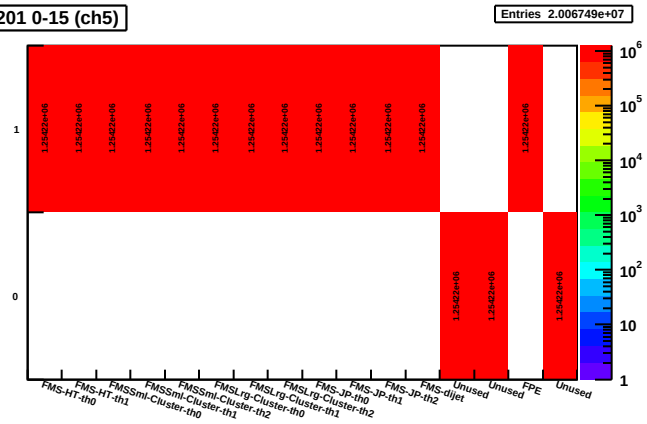


RAT board (ch4)

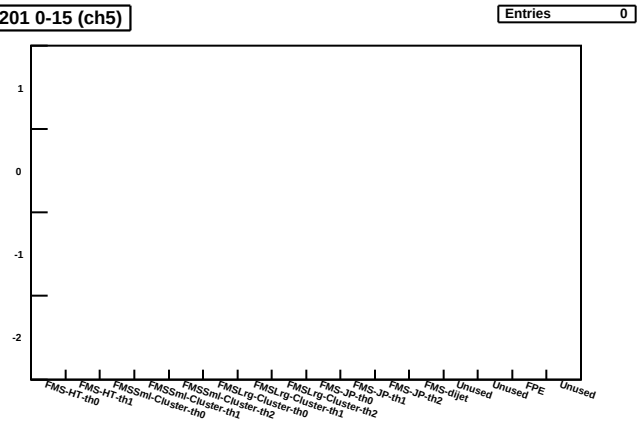




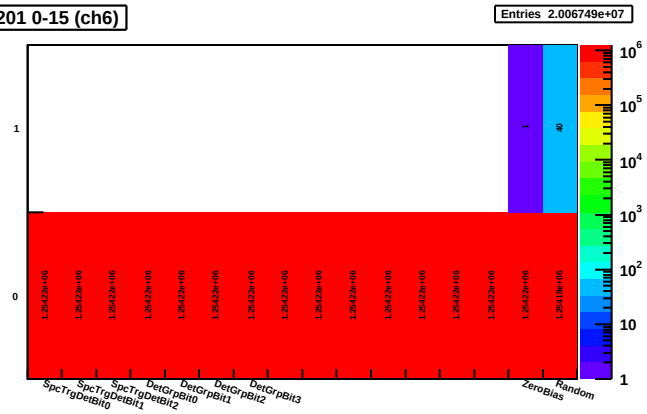
FP201 0-15 (ch5)

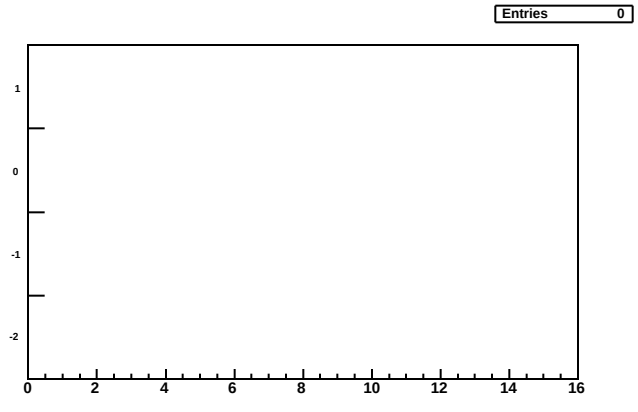
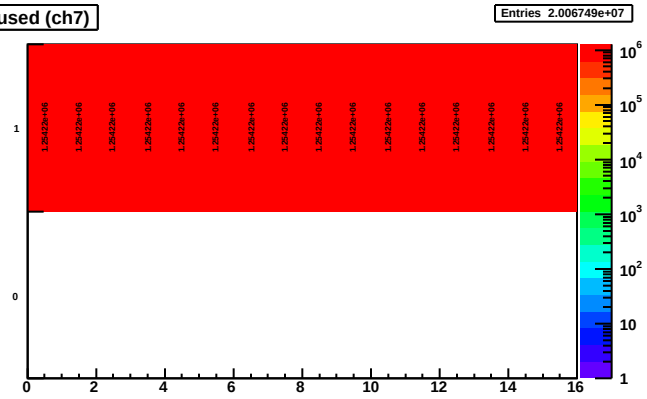
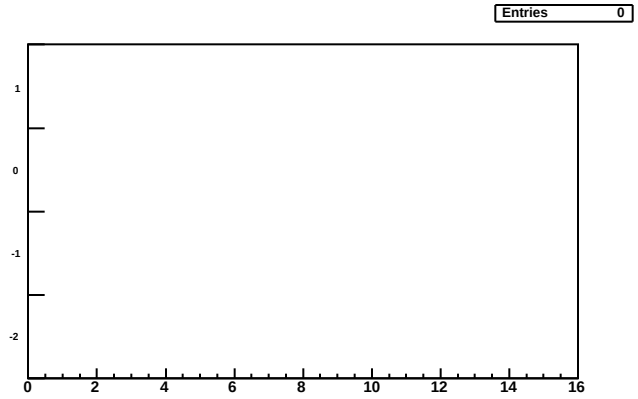


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)