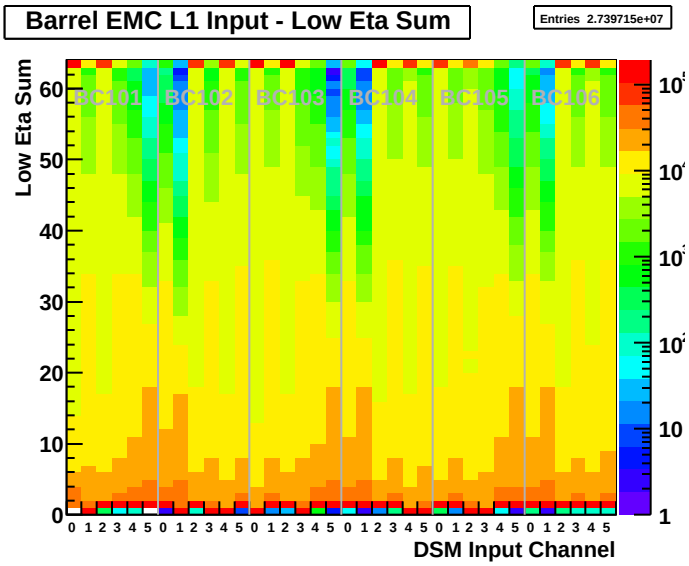
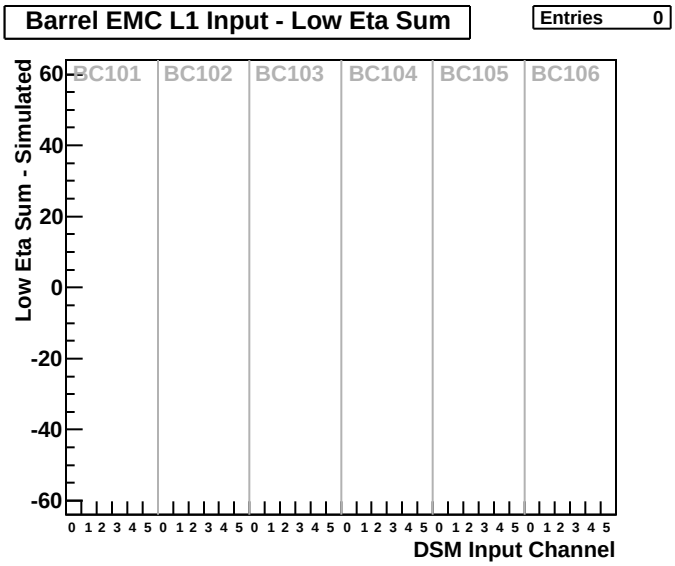
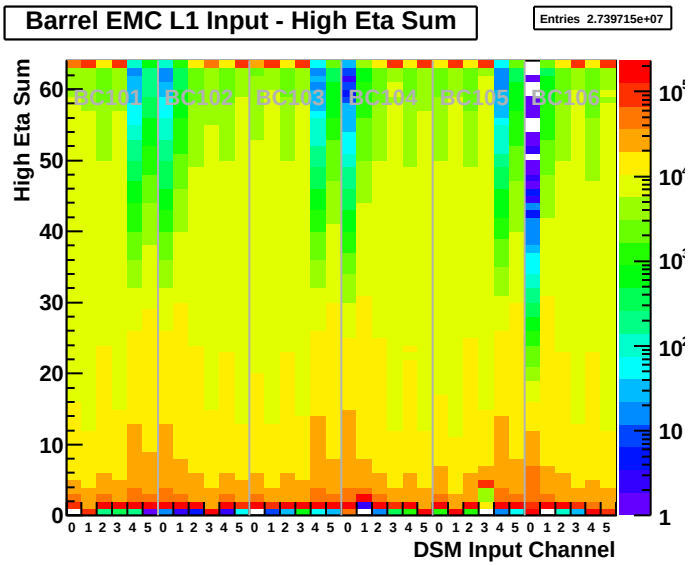
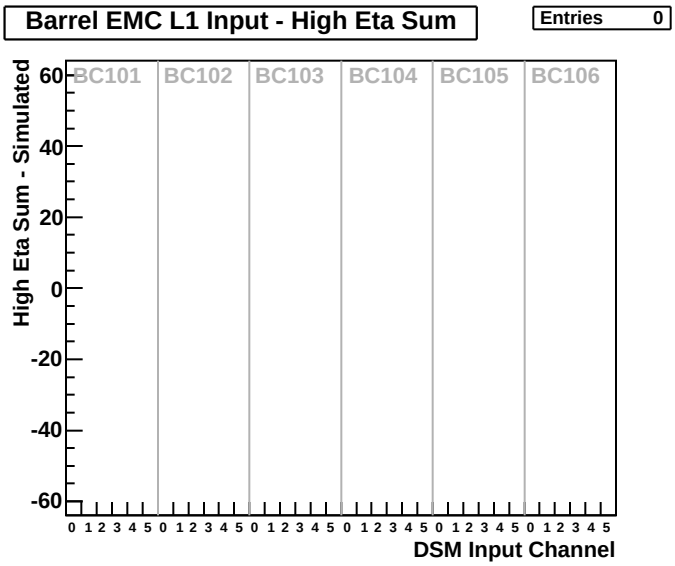
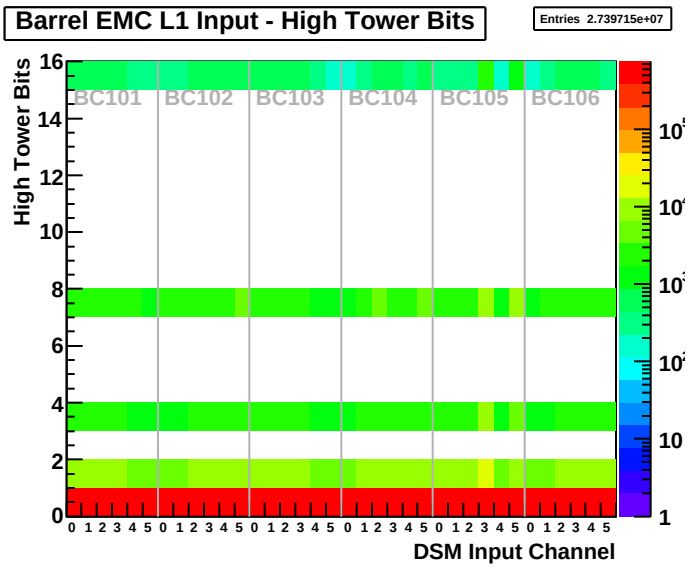
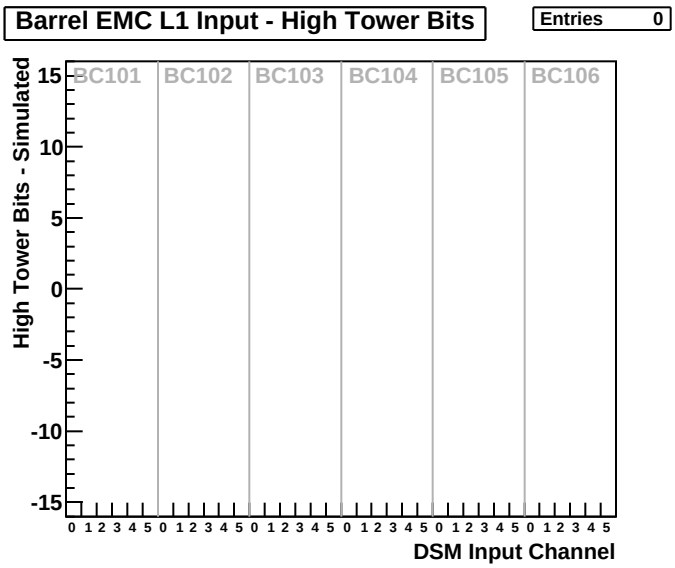

Barrel EMC L1 Input - Low Eta Sum
Entries 2.739715e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

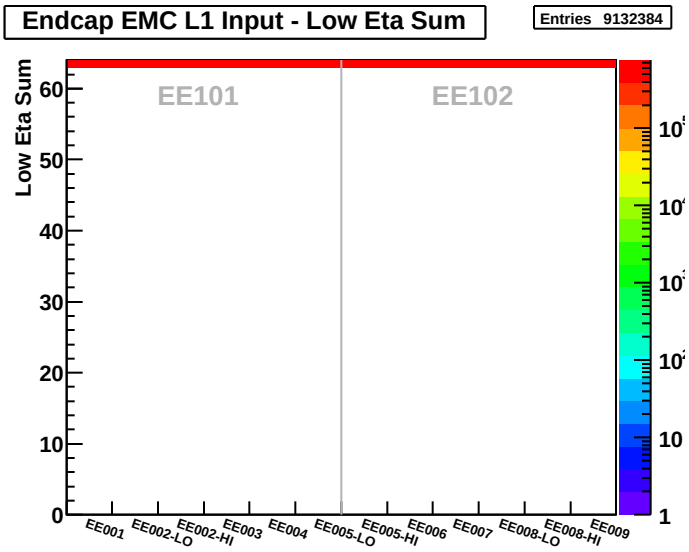

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

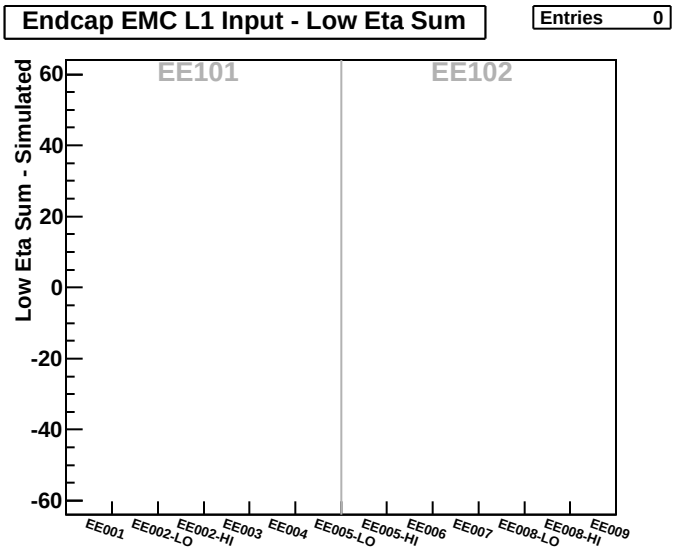

Barrel EMC L1 Input - High Eta Sum
Entries 2.739715e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

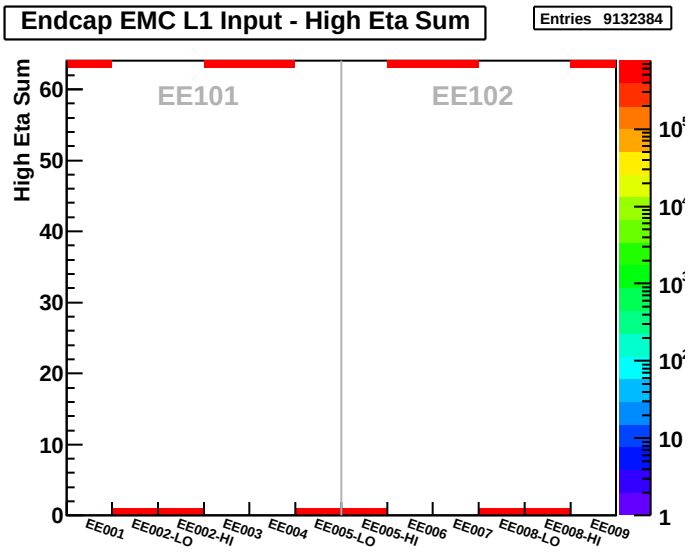

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

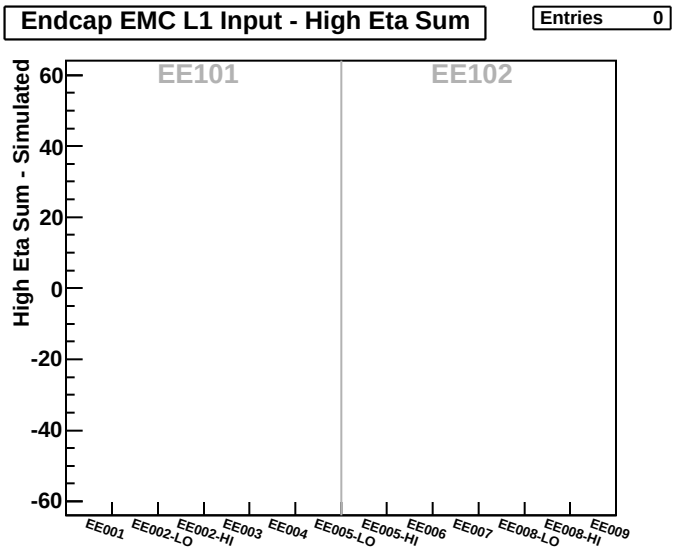

Barrel EMC L1 Input - High Tower Bits
Entries 2.739715e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

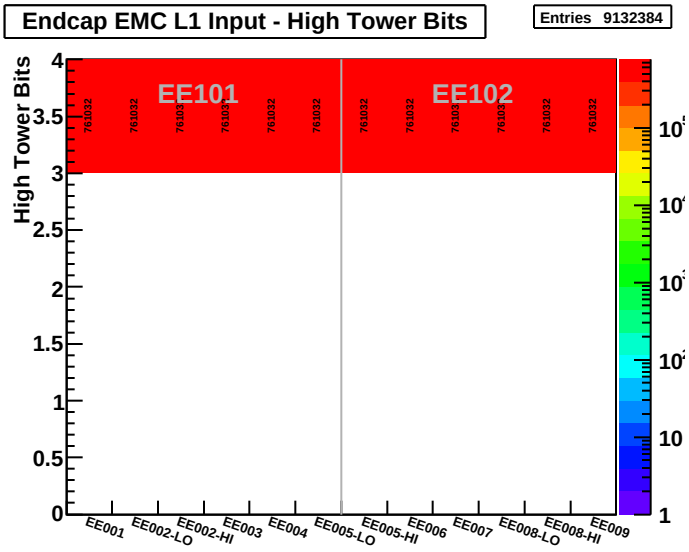

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

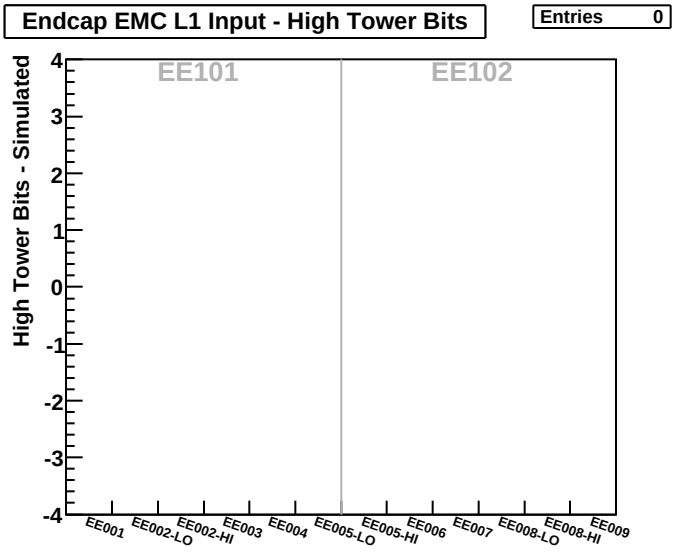

Endcap EMC L1 Input - Low Eta Sum
Entries 9132384
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

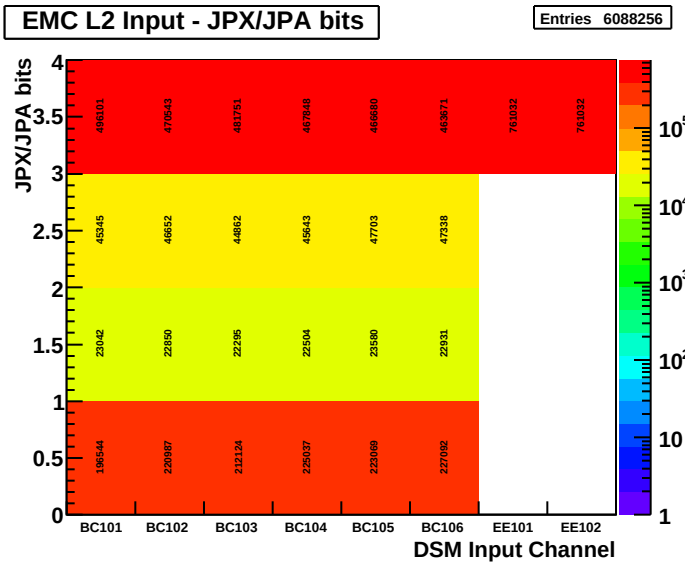

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

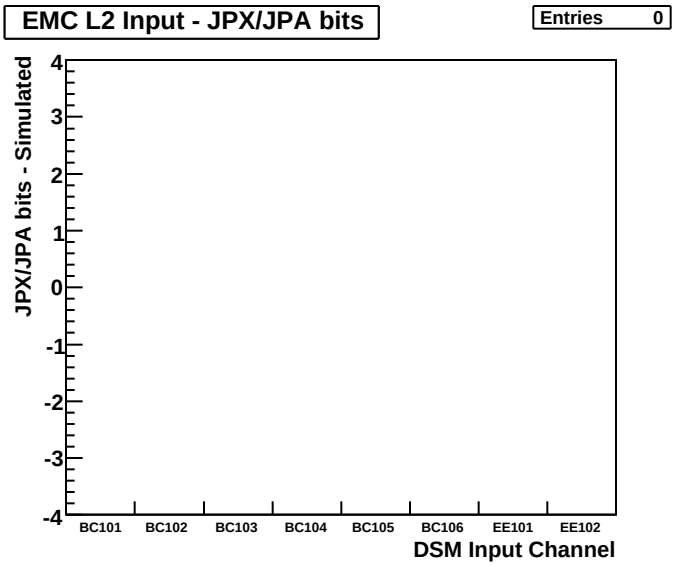

Endcap EMC L1 Input - High Eta Sum
Entries 9132384
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

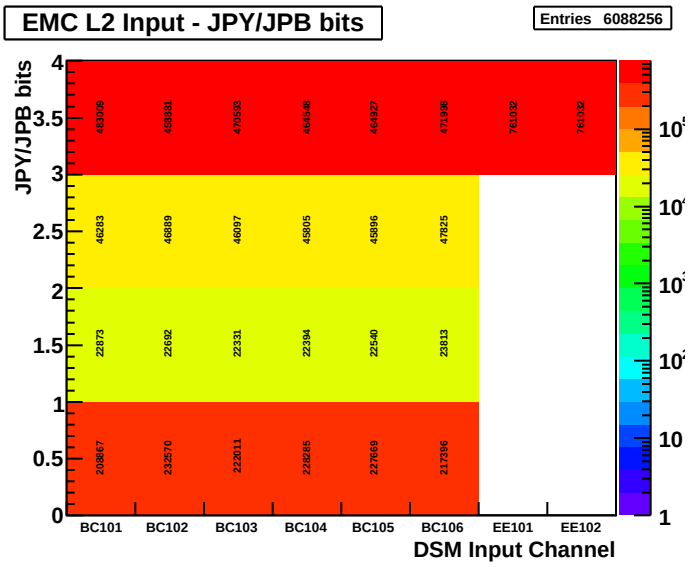

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

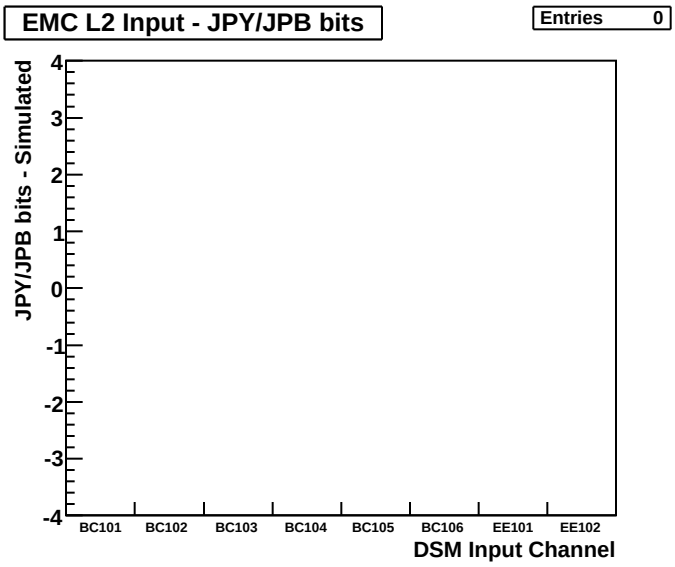

Endcap EMC L1 Input - High Tower Bits
Entries 9132384
High Tower Bits
EE101
EE102
761032
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

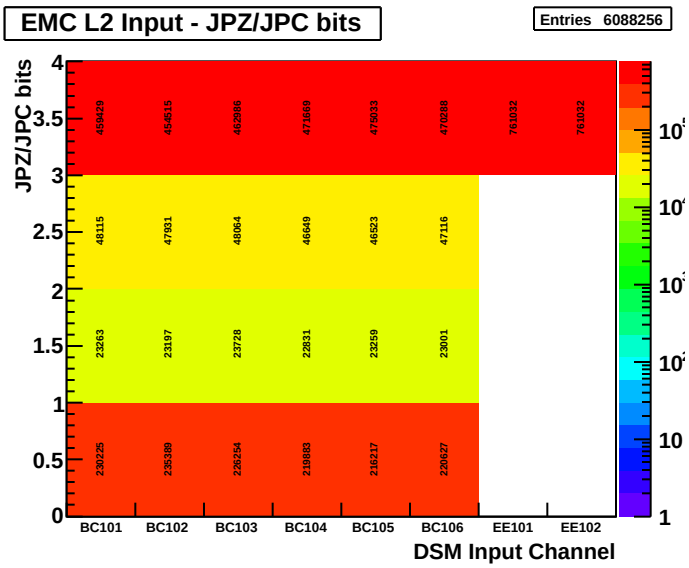

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

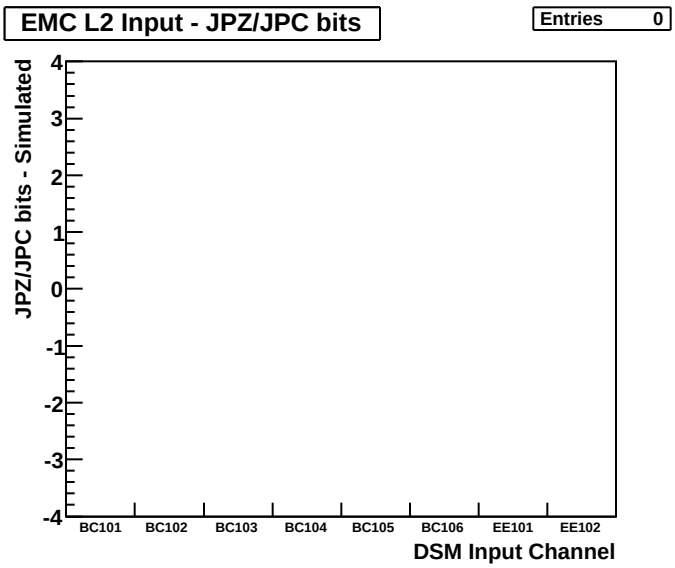

EMC L2 Input - JPX/JPA bits
Entries 6088256
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
496101 470643 481751 487848 466680 453871 761032 761032
45345 46652 44862 45643 47703 47338
23042 22850 22295 22504 23580 22931
196544 220987 212124 225037 223069 227092

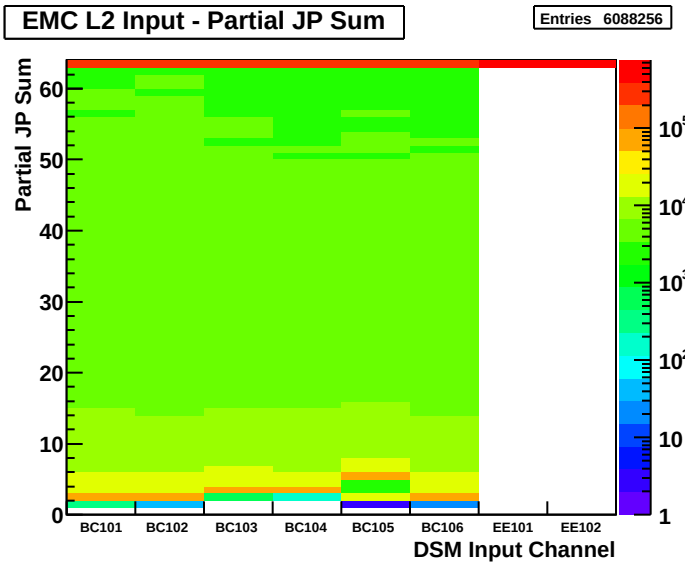

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

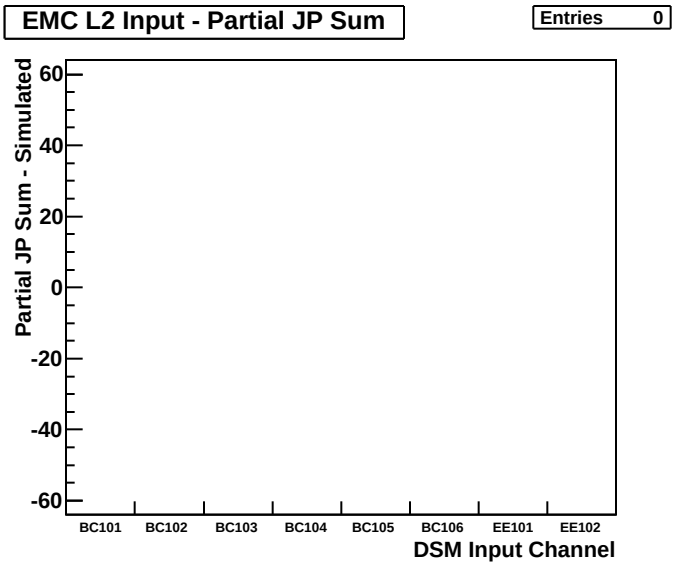

EMC L2 Input - JPY/JPB bits
Entries 6088256
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
483009 458881 470503 464548 464927 471998 761032 761032
46283 46889 46097 45805 45896 47825
22873 22692 22331 22394 22540 23813
208867 232570 222011 226285 227669 217396

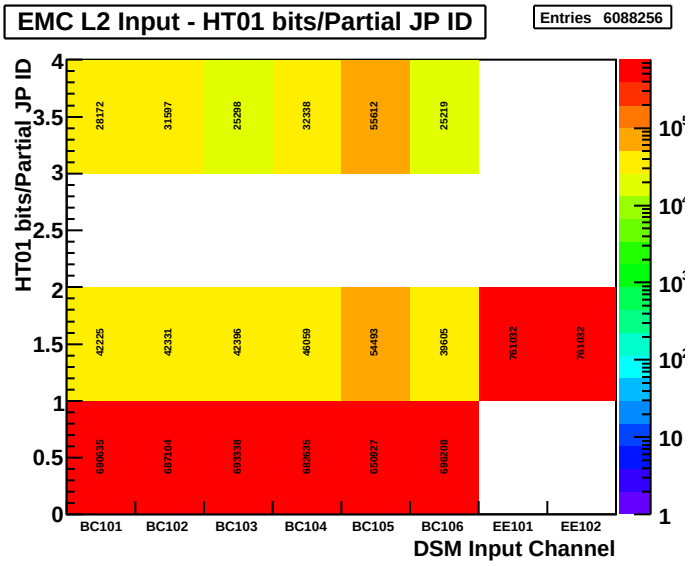

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

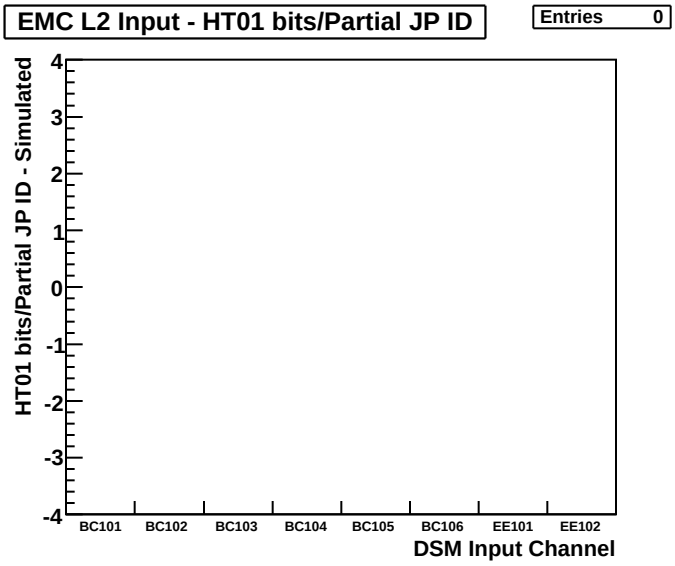

EMC L2 Input - JPZ/JPC bits
Entries 6088256
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
459429 454515 462986 471669 479033 470288 761032 761032
48115 47931 48064 46649 46523 47116
23263 23197 23728 22831 23259 23001
230225 235389 226254 219883 216217 220627

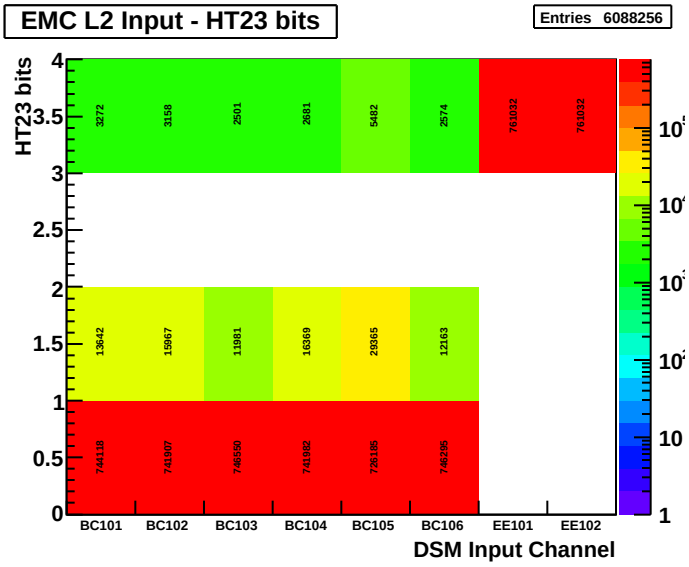

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

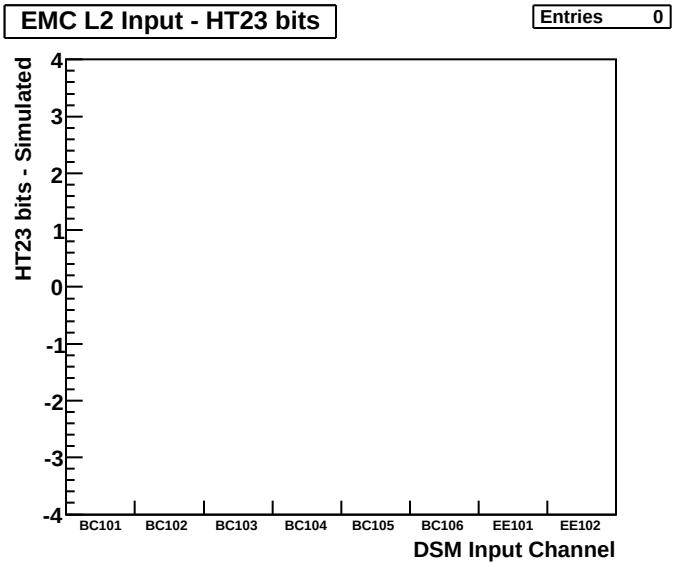

EMC L2 Input - Partial JP Sum
Entries 6088256
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

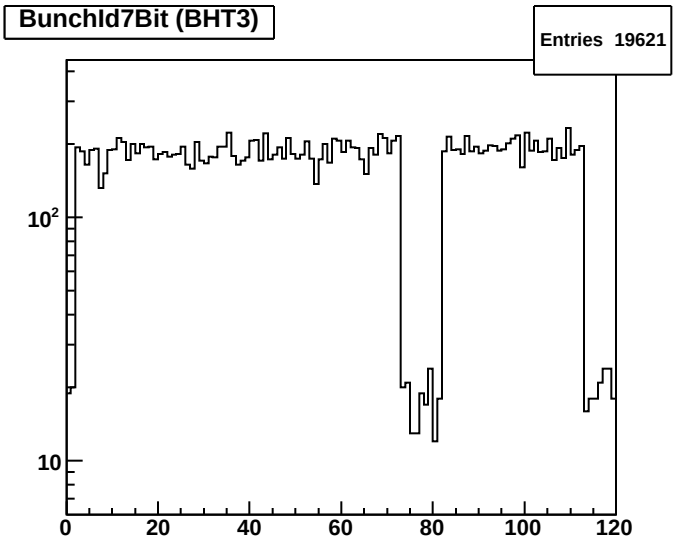

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

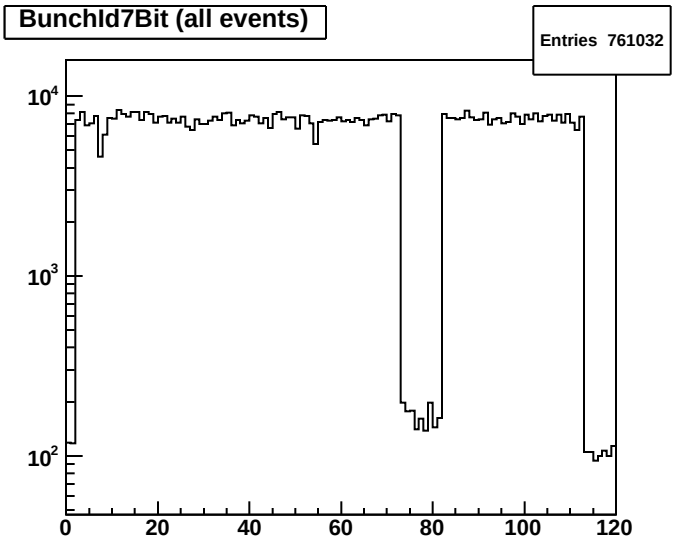

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6088256
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

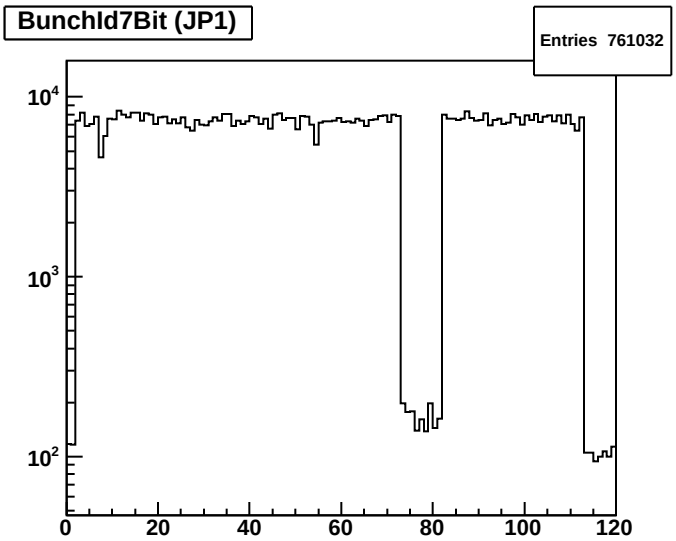

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

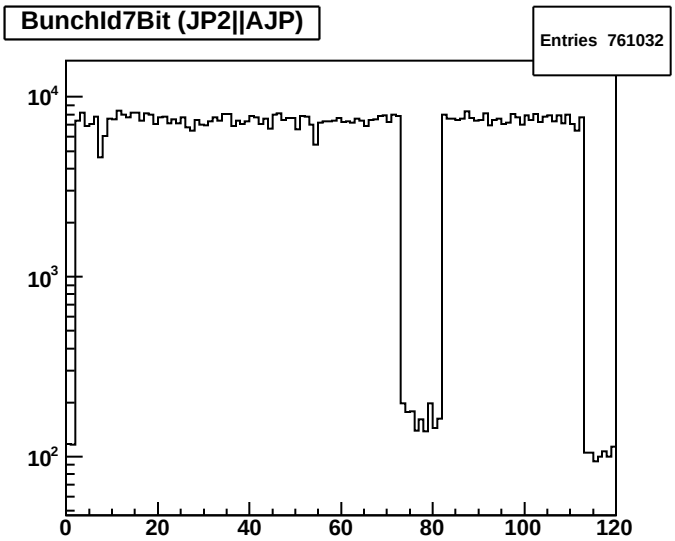

EMC L2 Input - HT23 bits
Entries 6088256
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

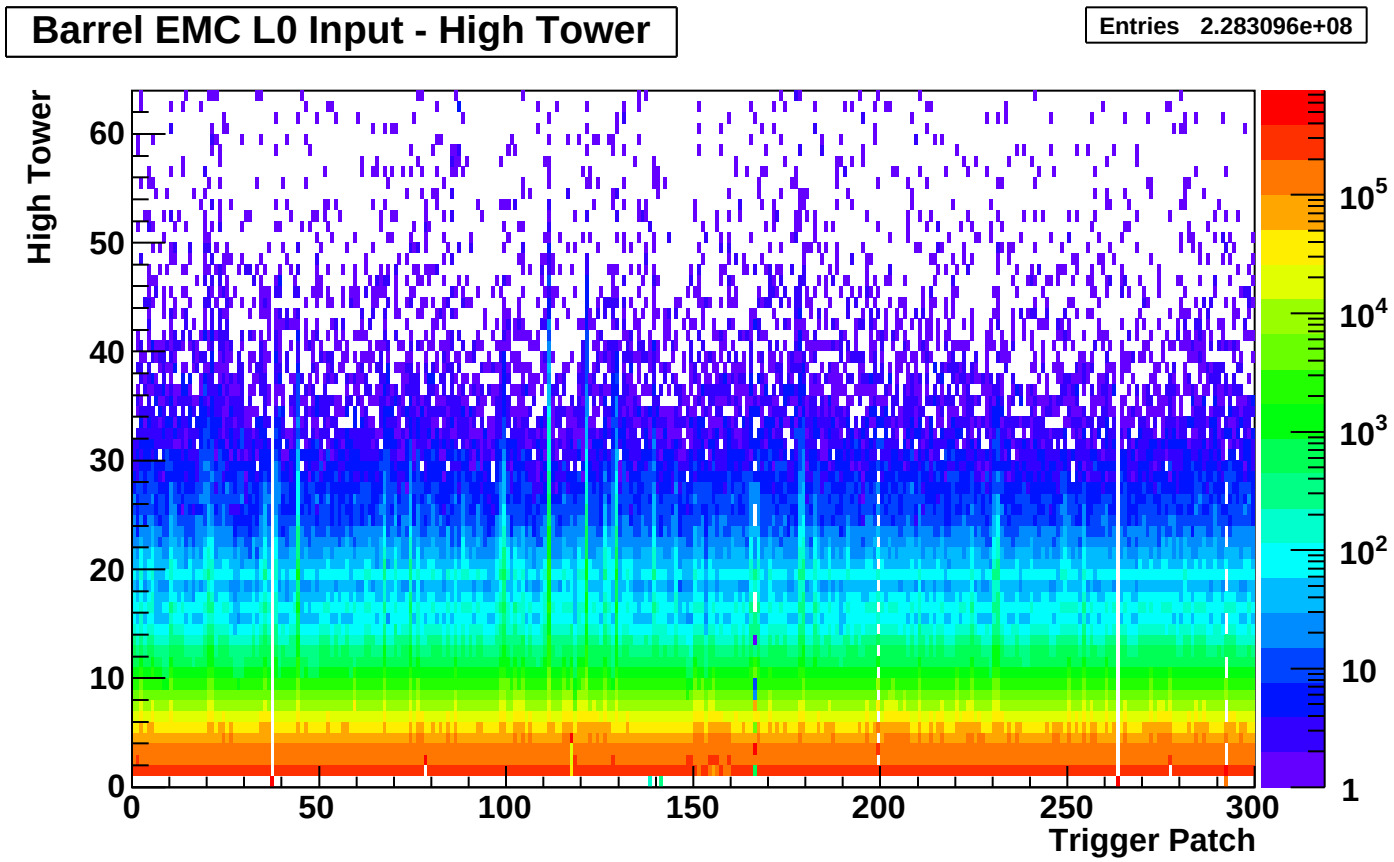

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

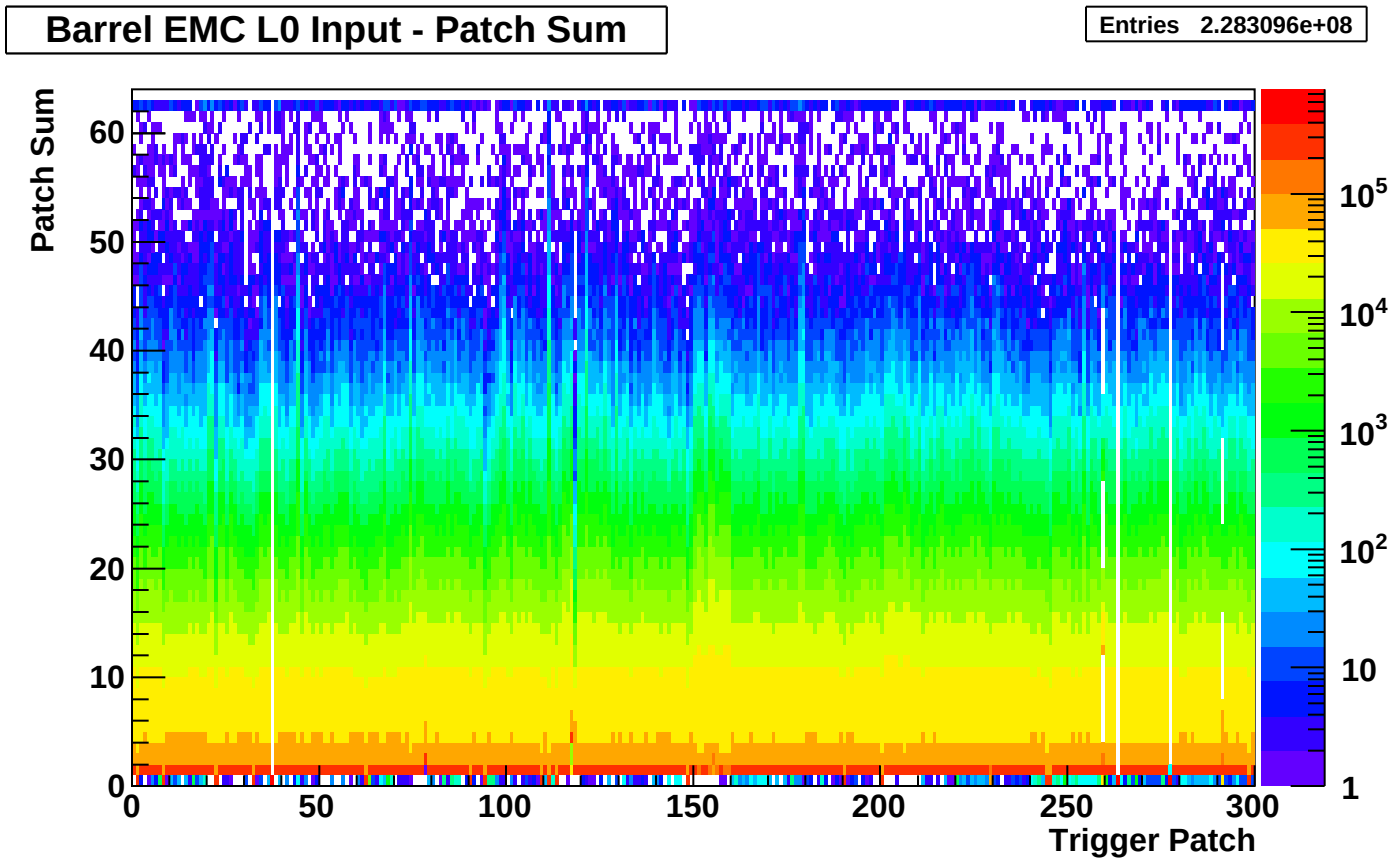

BunchId7Bit (BHT3)
Entries 19621
10^2
10
0
20
40
60
80
100
120

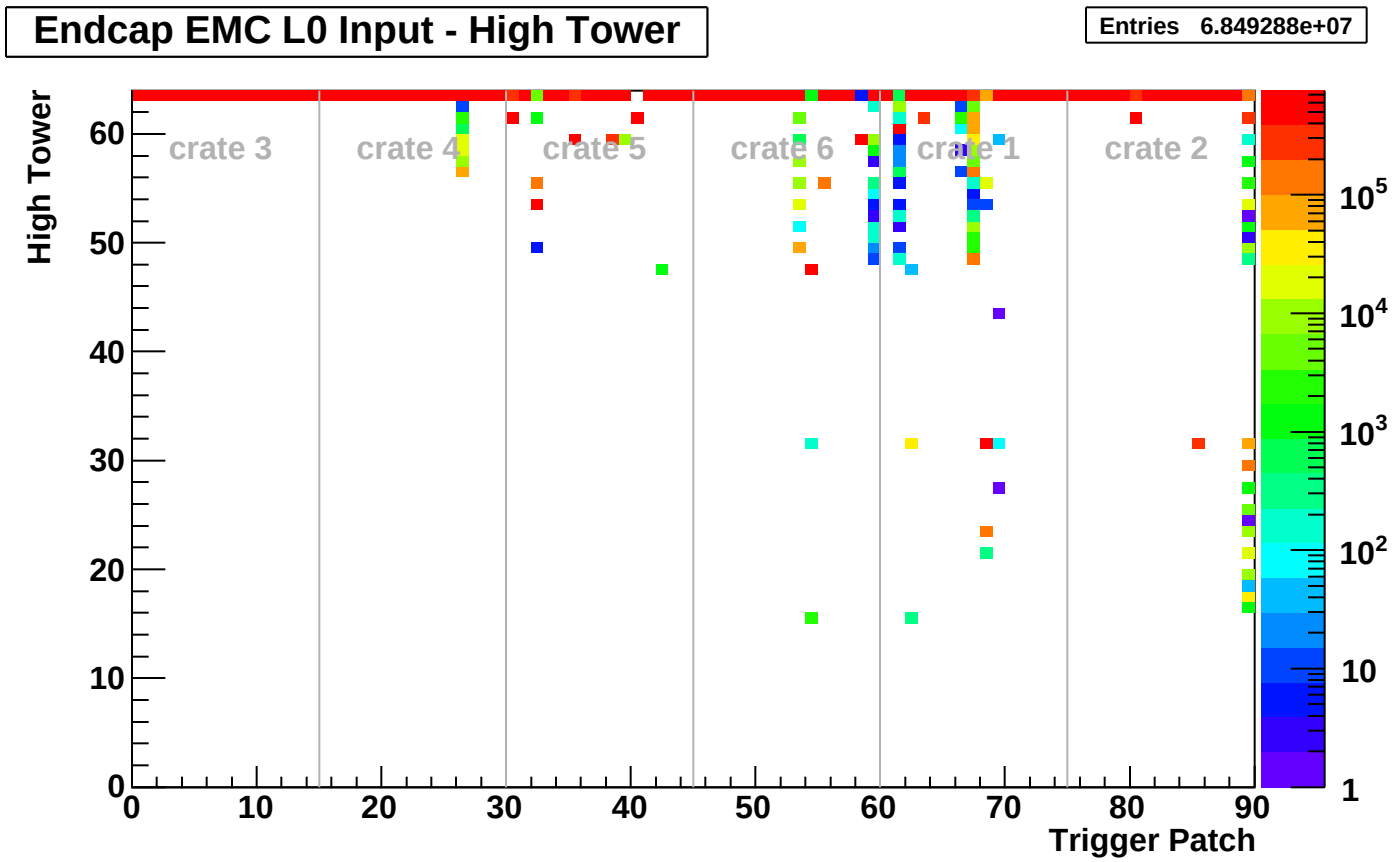

BunchId7Bit (all events)
Entries 761032
10^4
10^3
10^2
0
20
40
60
80
100
120

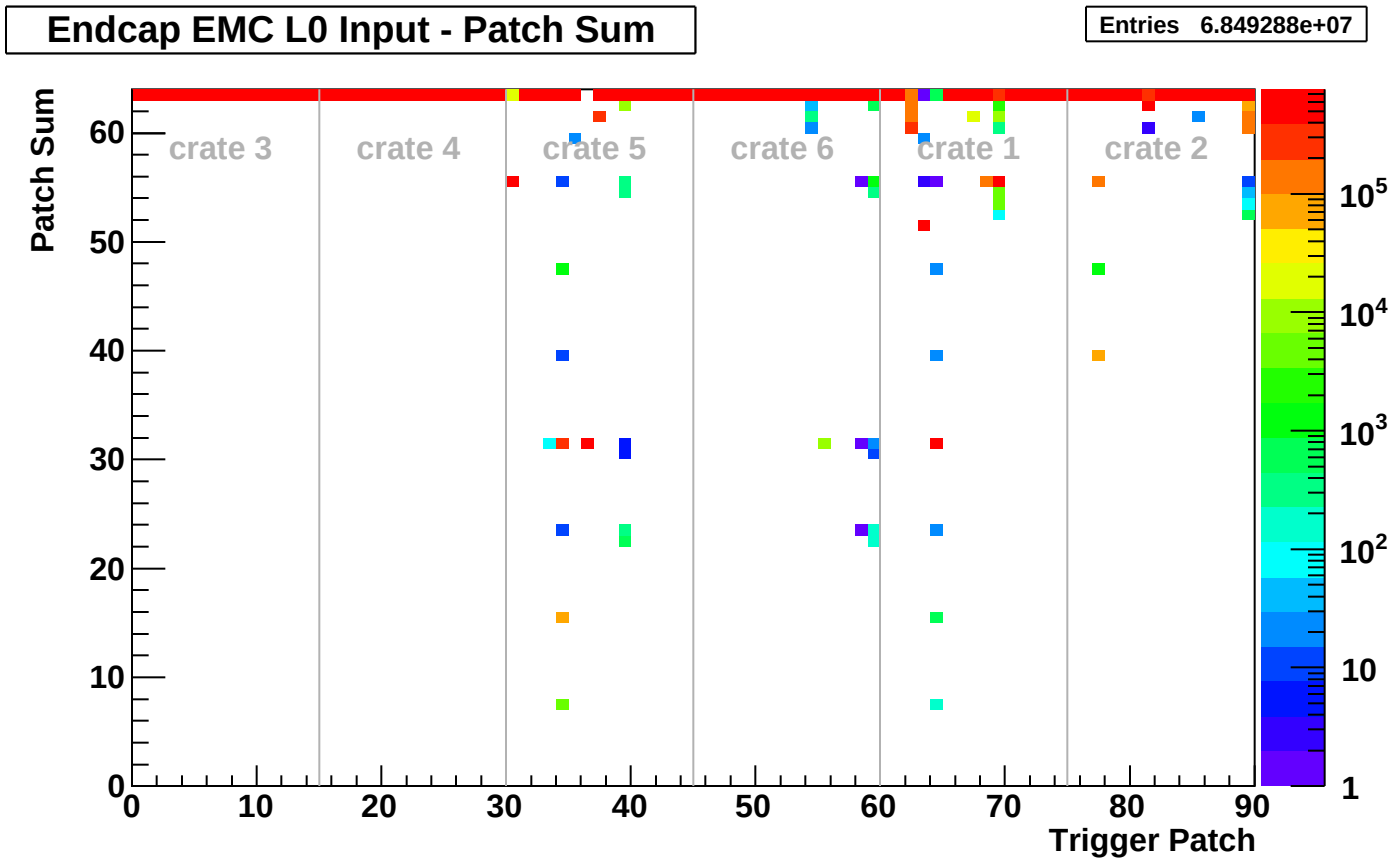

BunchId7Bit (JP1)
Entries 761032
10^4
10^3
10^2
0
20
40
60
80
100
120

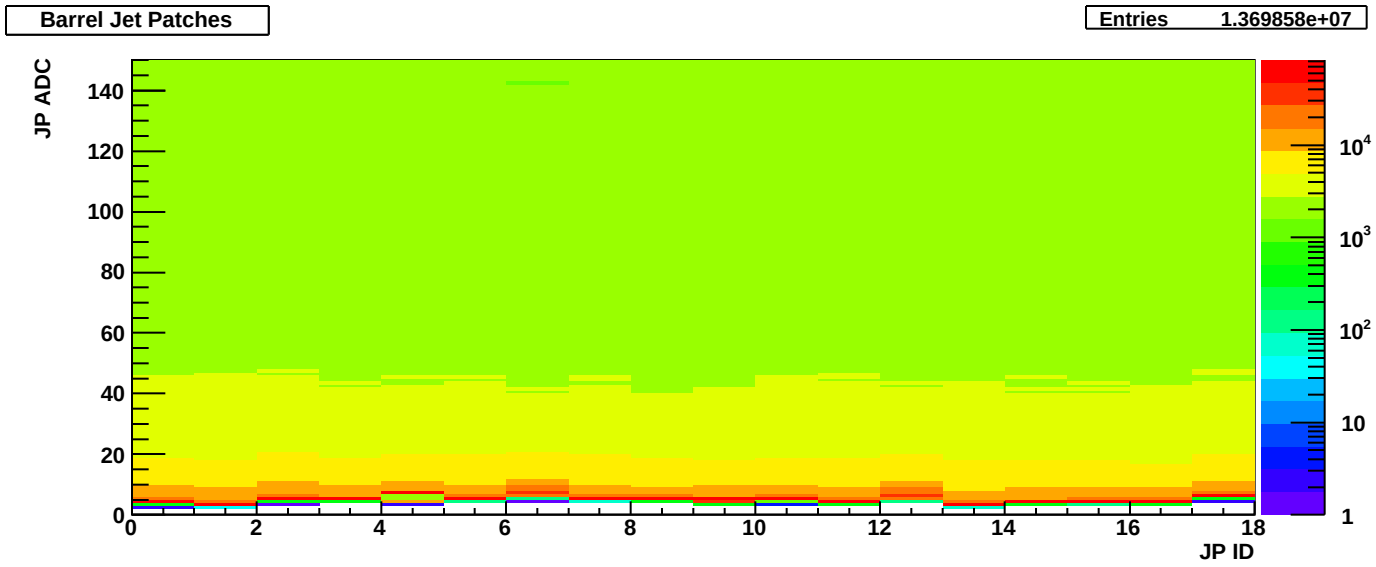

BunchId7Bit (JP2||AJP)
Entries 761032
10^4
10^3
10^2
0
20
40
60
80
100
120

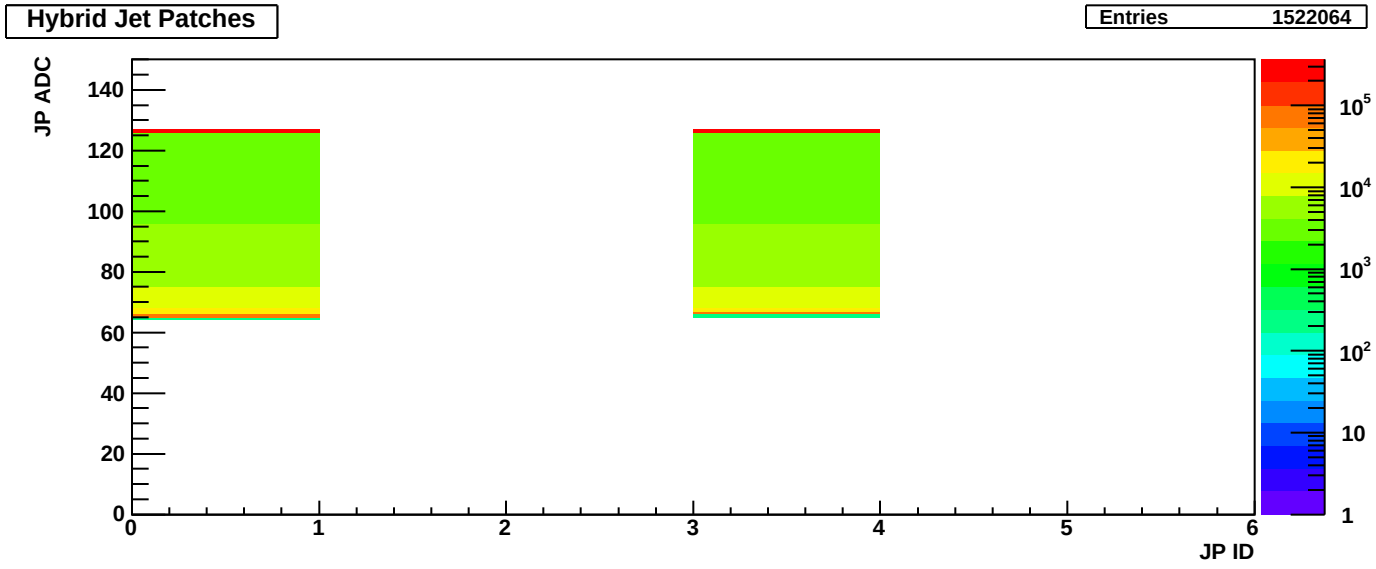

Barrel EMC L0 Input - High Tower
Entries 2.283096e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

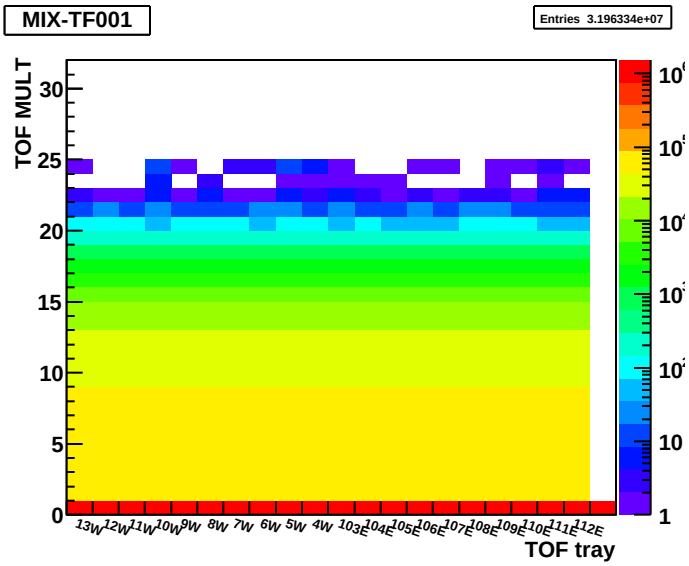

Barrel EMC L0 Input - Patch Sum
Entries 2.283096e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

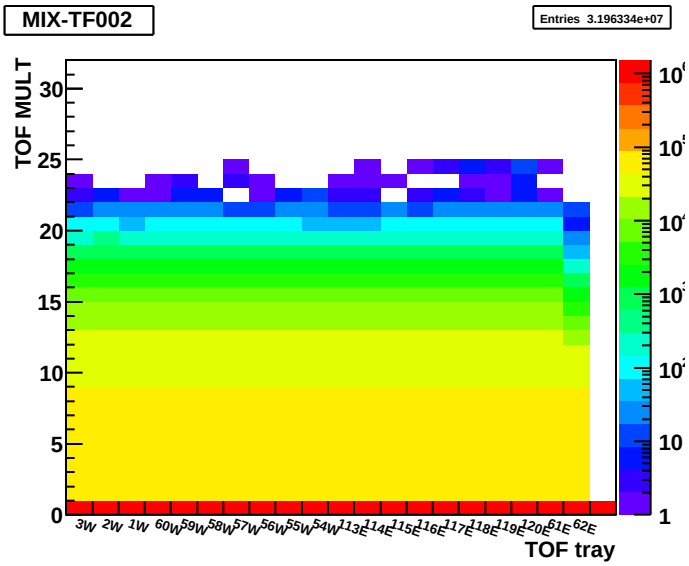

Endcap EMC L0 Input - High Tower
Entries 6.849288e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

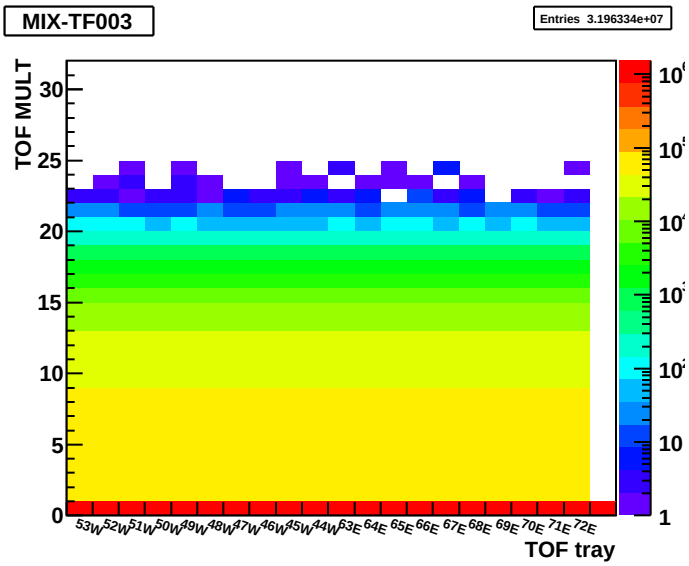

Endcap EMC L0 Input - Patch Sum
Entries 6.849288e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

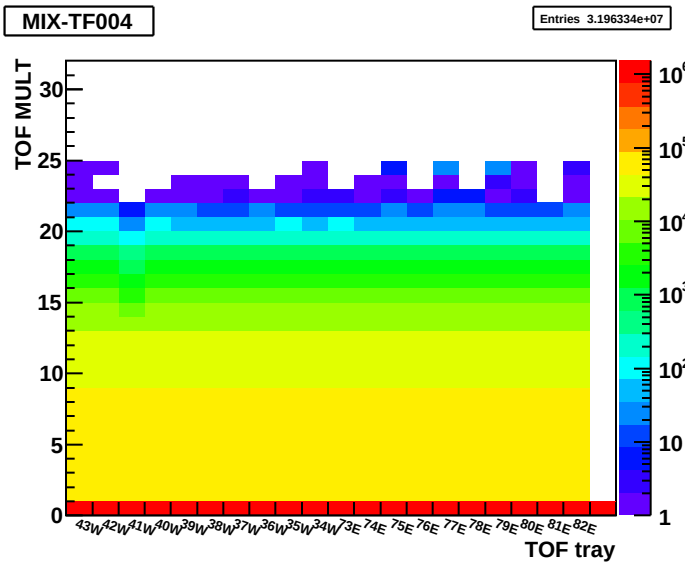

Barrel Jet Patches
Entries 1.369858e+07
JP ADC
JP ID

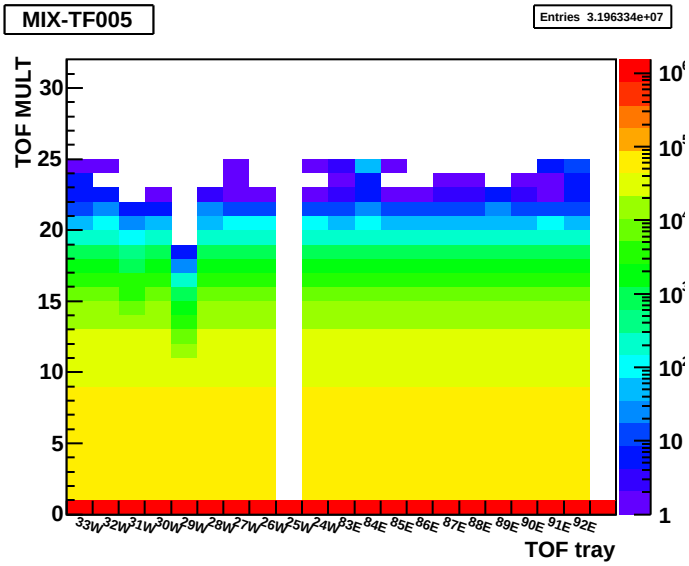

Hybrid Jet Patches
Entries 1522064
JP ADC
JP ID

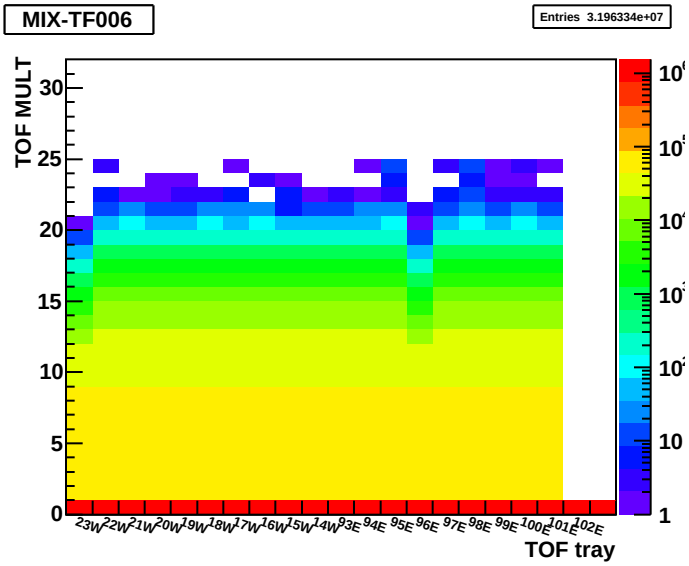

MIX-TF001
Entries 3.196334e+07
TOF MULT
TOF tray

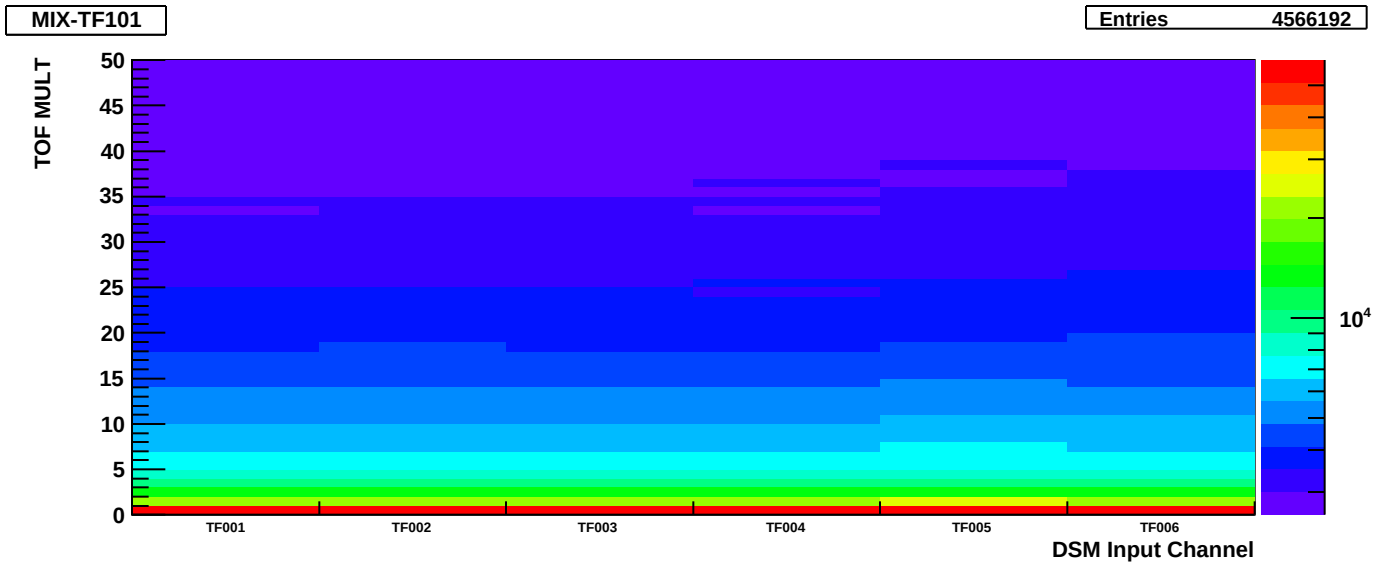

MIX-TF002
Entries 3.196334e+07
TOF MULT
TOF tray

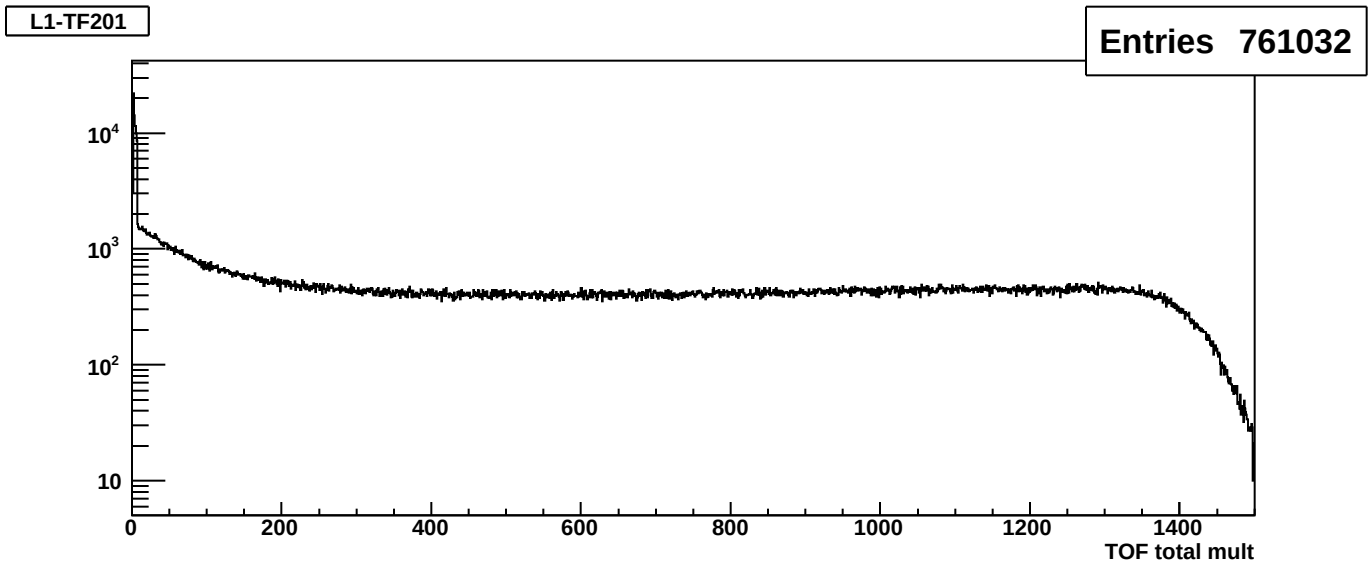

MIX-TF003
Entries 3.196334e+07
TOF MULT
TOF tray

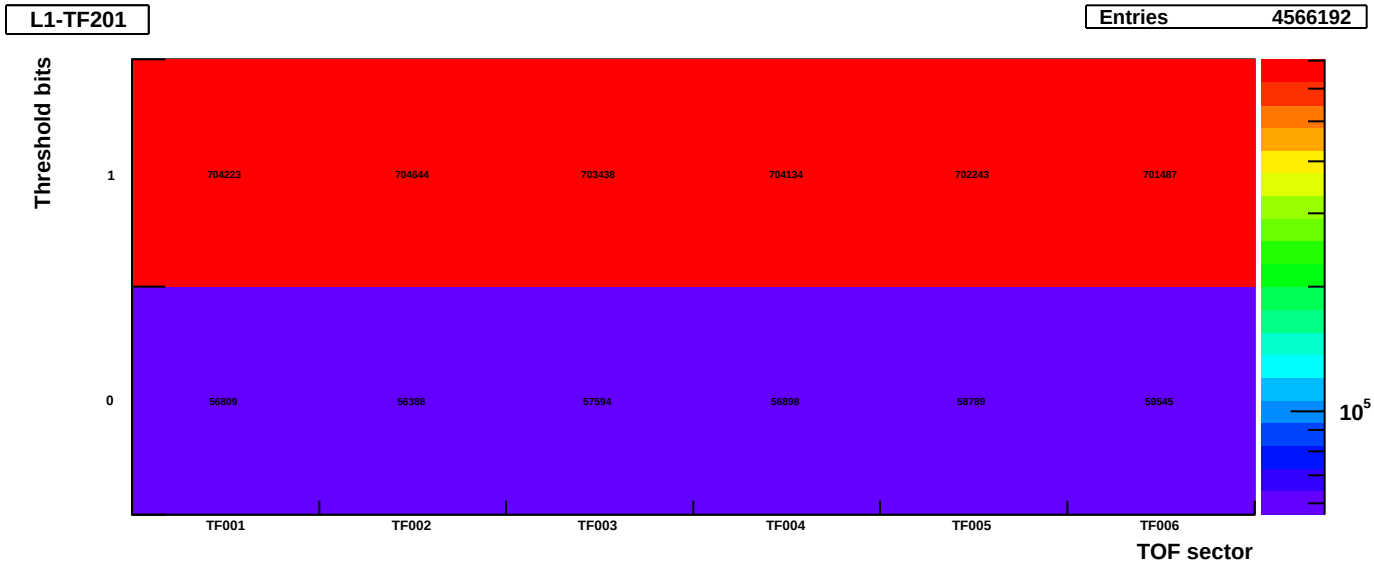

MIX-TF004
Entries 3.196334e+07
TOF MULT
TOF tray

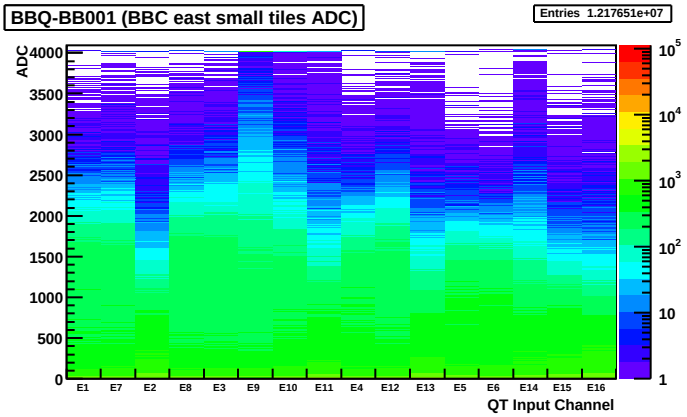

MIX-TF005
Entries 3.196334e+07
TOF MULT
TOF tray

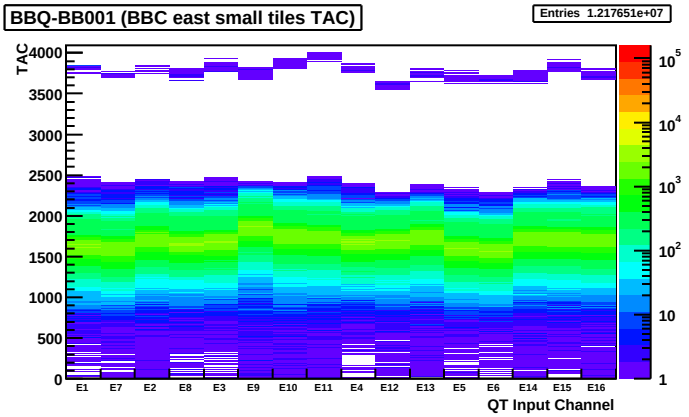

MIX-TF006
Entries 3.196334e+07
TOF MULT
TOF tray

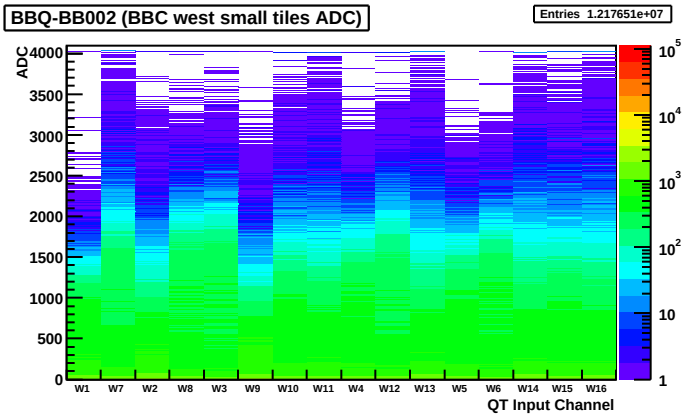

MIX-TF101
Entries 4566192
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

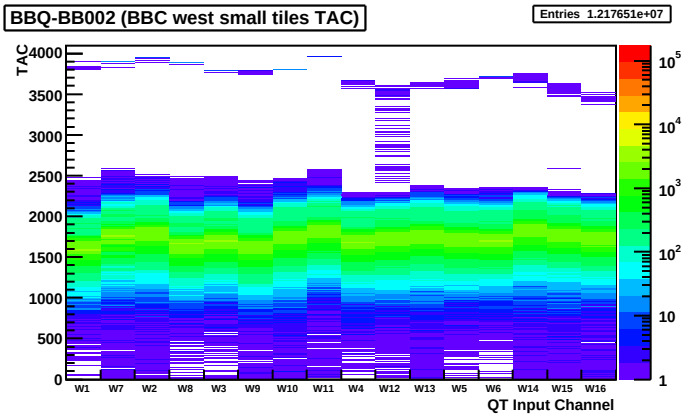

L1-TF201
Entries 761032
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

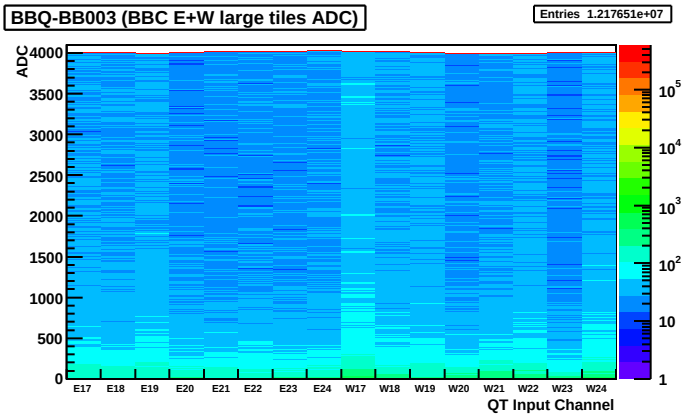

L1-TF201
Entries 4566192
Threshold bits
1
0
704223
704644
703438
704134
702243
701487
56809
56388
57594
56898
58789
59545
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

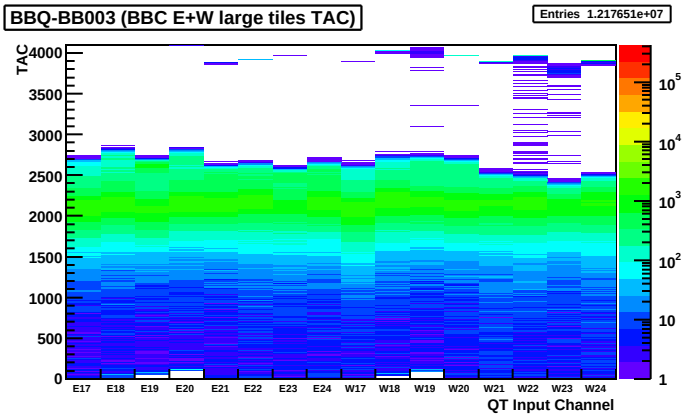

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.217651e+07
ADC
QT Input Channel

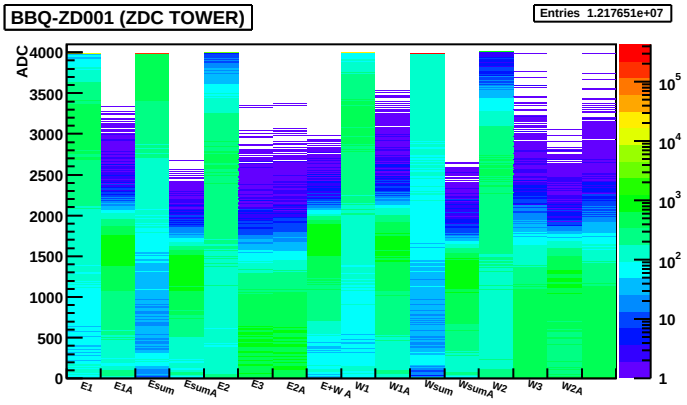

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.217651e+07
TAC
QT Input Channel

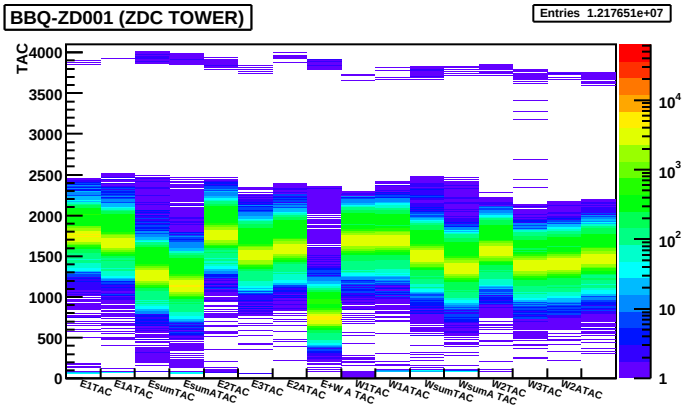

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.217651e+07
ADC
QT Input Channel

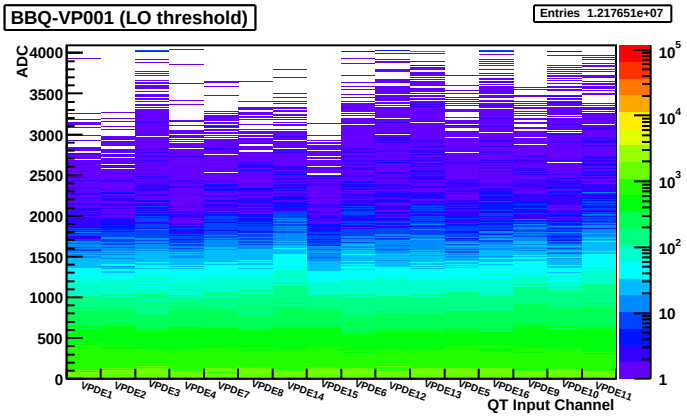

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.217651e+07
TAC
QT Input Channel

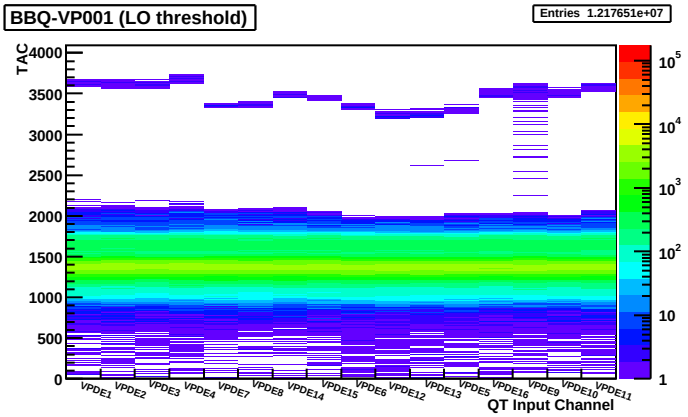

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.217651e+07
ADC
QT Input Channel

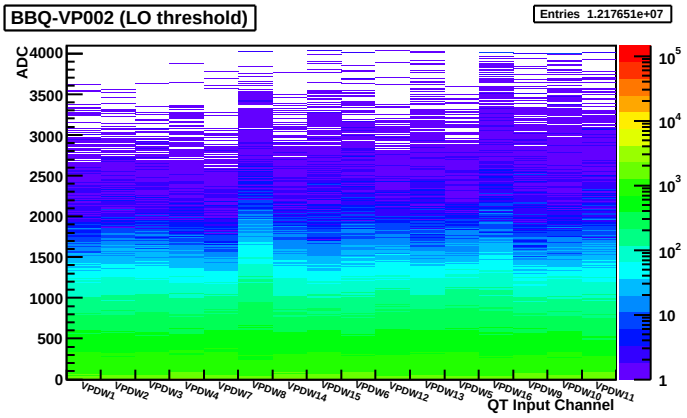

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.217651e+07
TAC
QT Input Channel

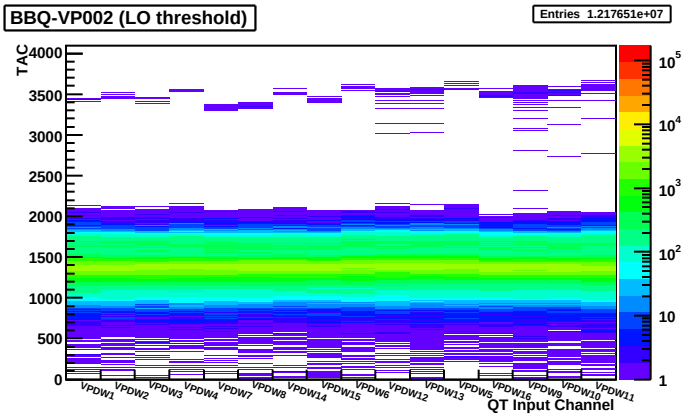

BBQ-ZD001 (ZDC TOWER)
Entries 1.217651e+07
ADC

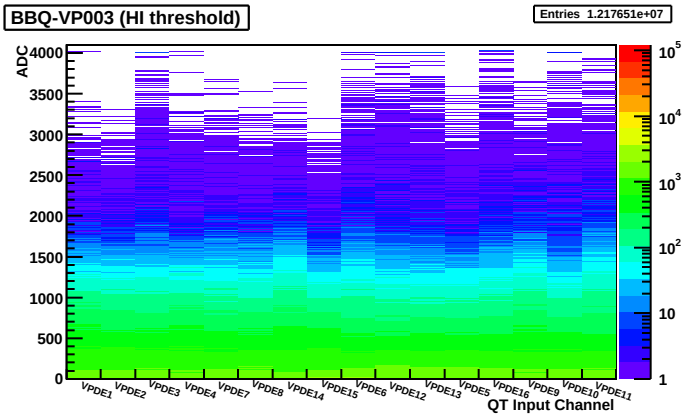

BBQ-ZD001 (ZDC TOWER)
Entries 1.217651e+07
TAC

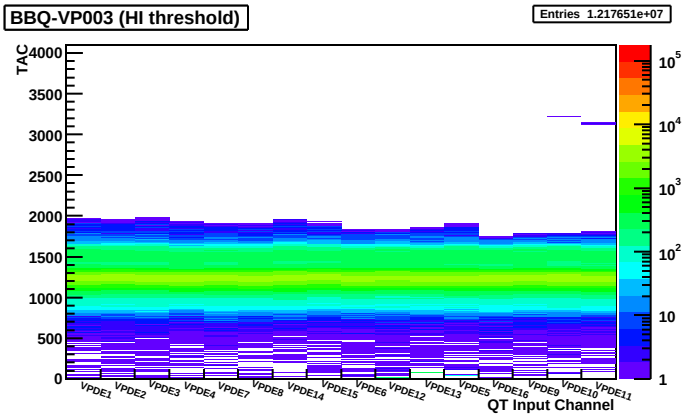

BBQ-VP001 (LO threshold)
Entries 1.217651e+07
ADC
QT Input Channel

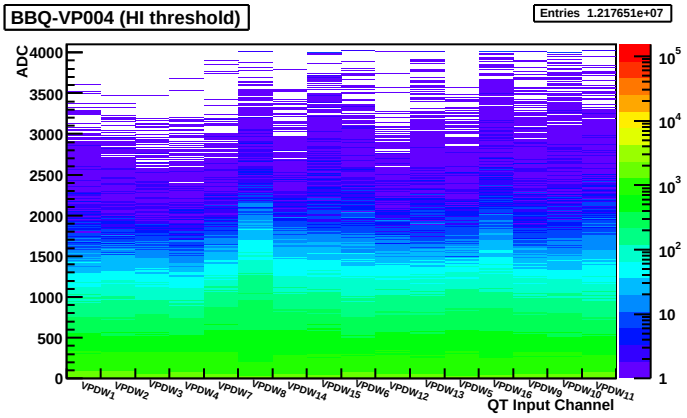

BBQ-VP001 (LO threshold)
Entries 1.217651e+07
TAC
QT Input Channel

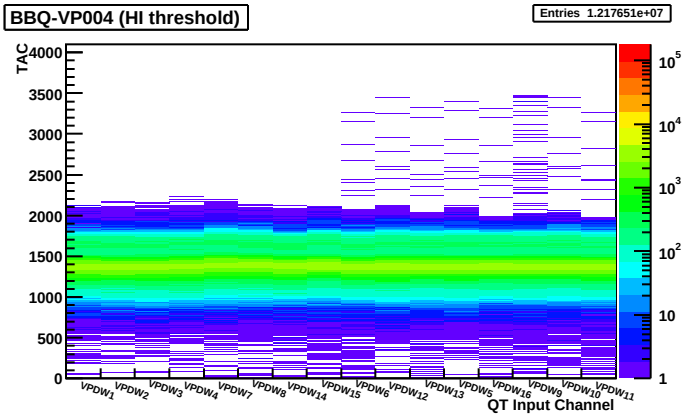

BBQ-VP002 (LO threshold)
Entries 1.217651e+07
ADC
QT Input Channel

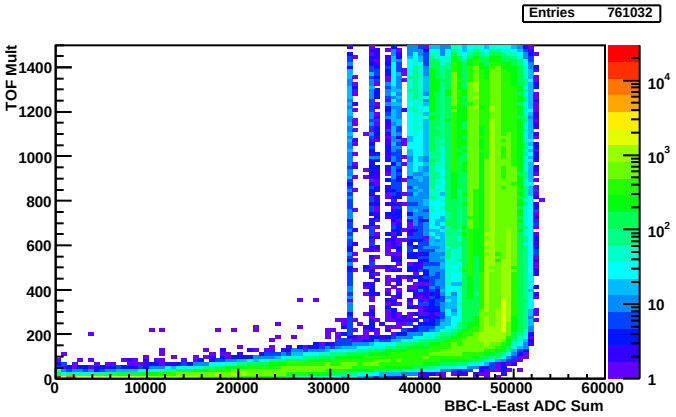

BBQ-VP002 (LO threshold)
Entries 1.217651e+07
TAC
QT Input Channel

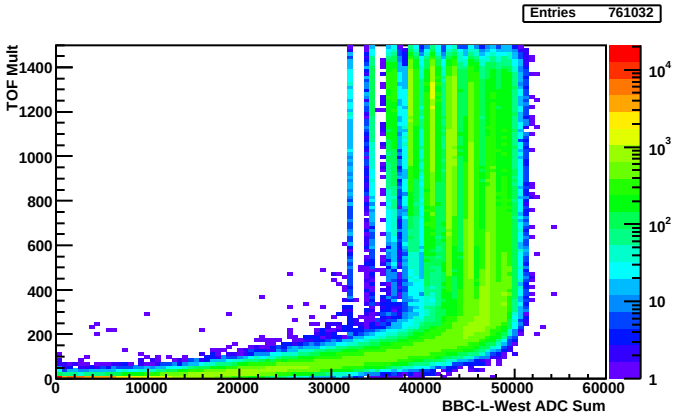

BBQ-VP003 (HI threshold)
Entries 1.217651e+07
ADC
QT Input Channel

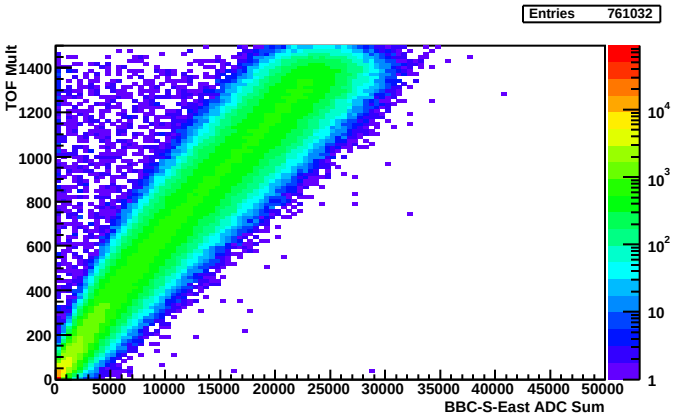

BBQ-VP003 (HI threshold)
Entries 1.217651e+07
TAC
QT Input Channel

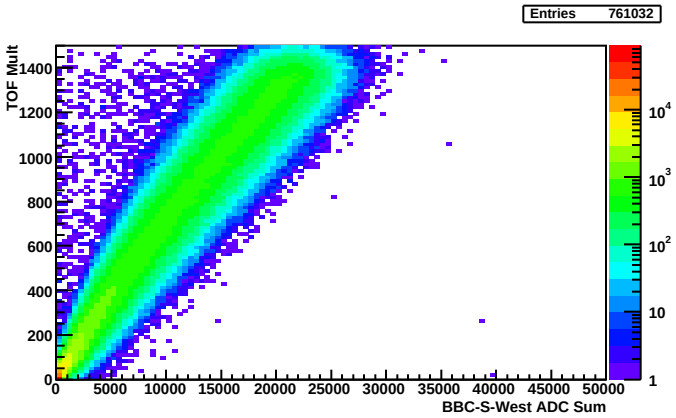

BBQ-VP004 (HI threshold)
Entries 1.217651e+07
ADC
QT Input Channel

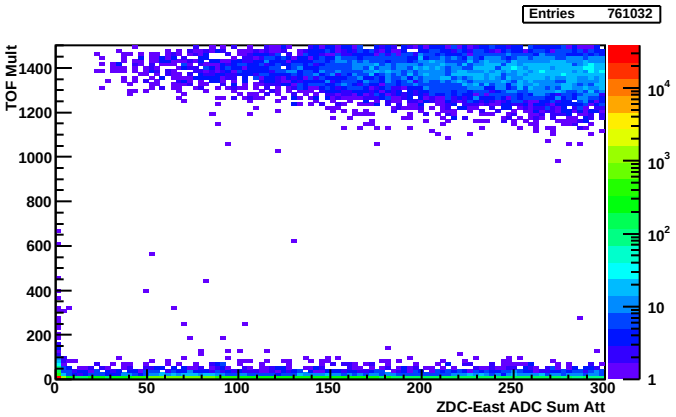

BBQ-VP004 (HI threshold)
Entries 1.217651e+07
TAC
QT Input Channel

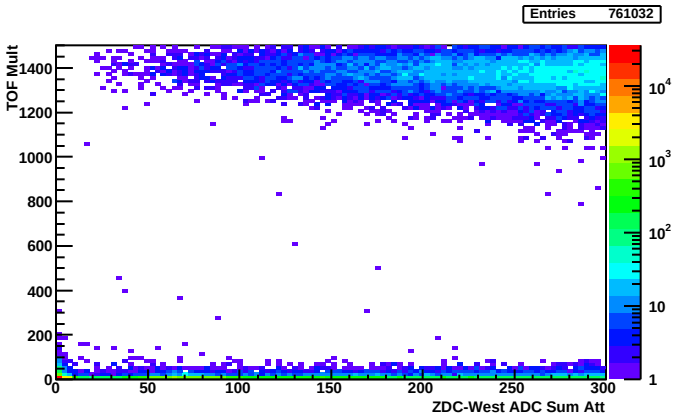

Entries 761032
TOF Mult
BBC-L-East ADC Sum

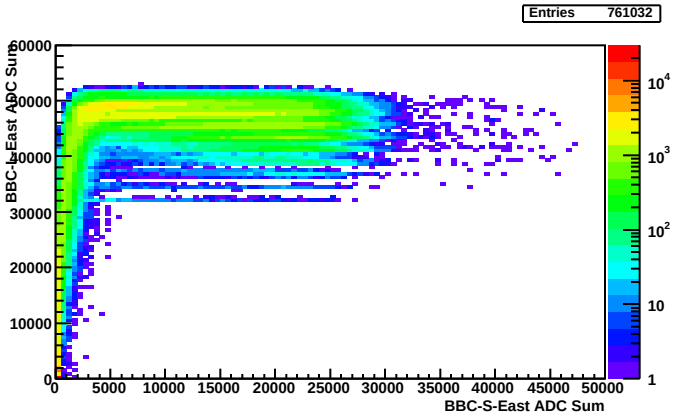

Entries 761032
TOF Mult
BBC-L-West ADC Sum

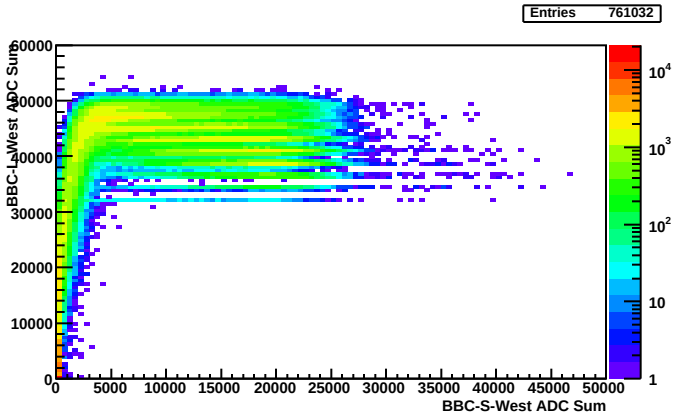

Entries 761032
TOF Mult
BBC-S-East ADC Sum

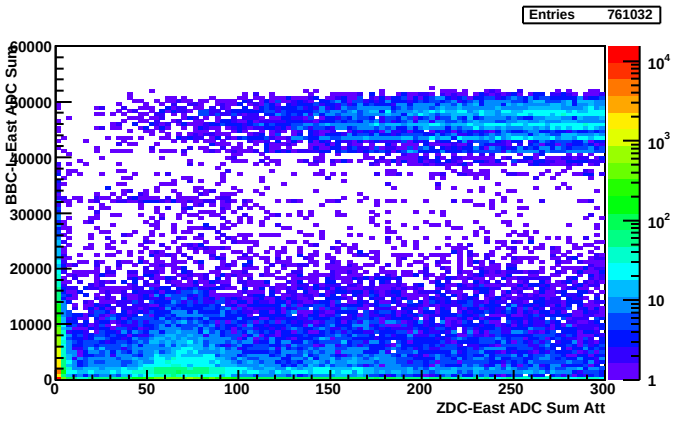

Entries 761032
TOF Mult
BBC-S-West ADC Sum

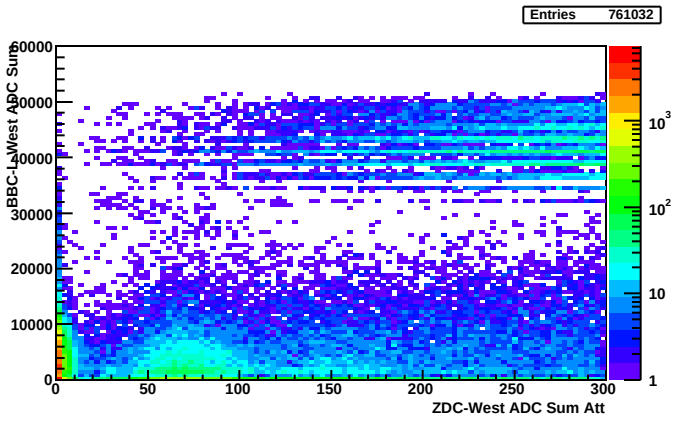

Entries 761032
TOF Mult
ZDC-East ADC Sum Att

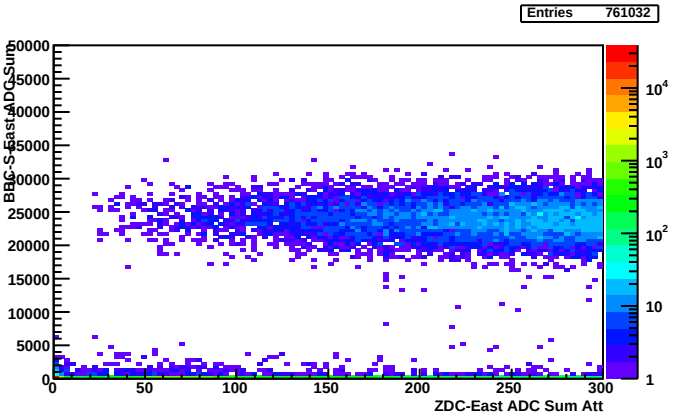

Entries 761032
TOF Mult
ZDC-West ADC Sum Att

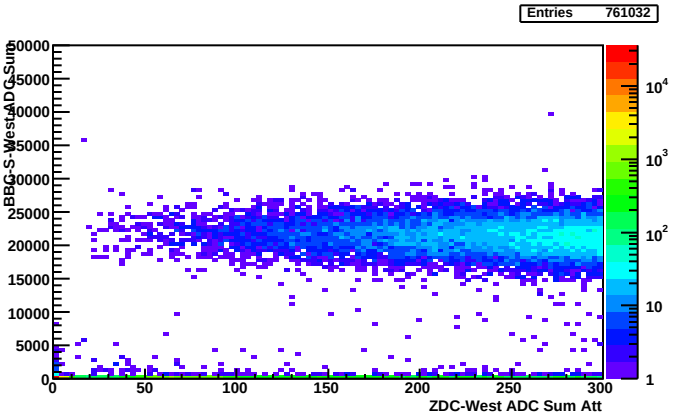

Entries 761032
BBC-L-East ADC Sum
BBC-S-East ADC Sum

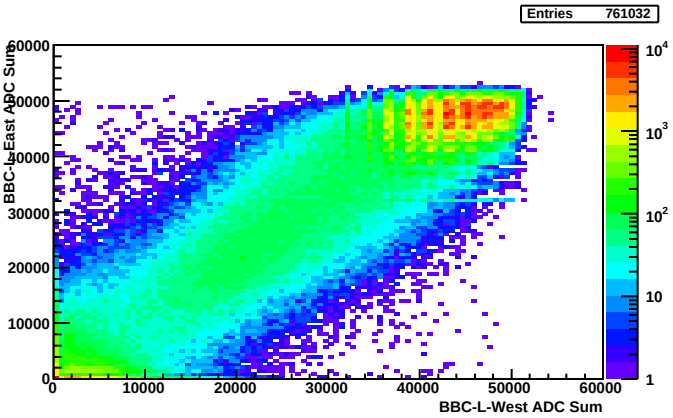

Entries 761032
BBC-L-West ADC Sum
BBC-S-West ADC Sum

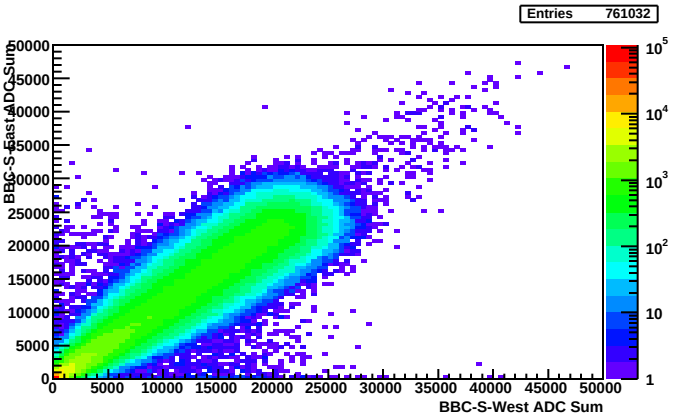

Entries 761032
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

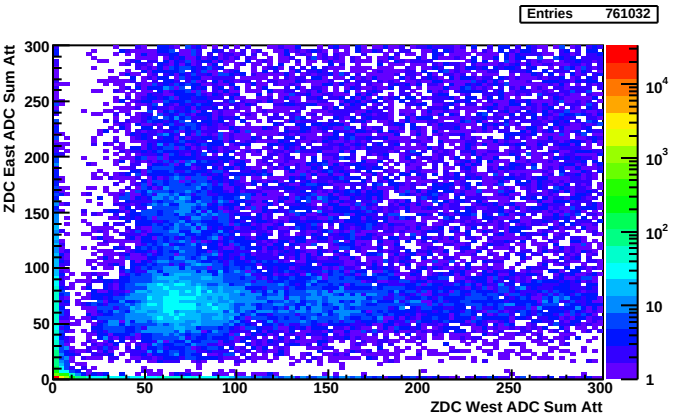

Entries 761032
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

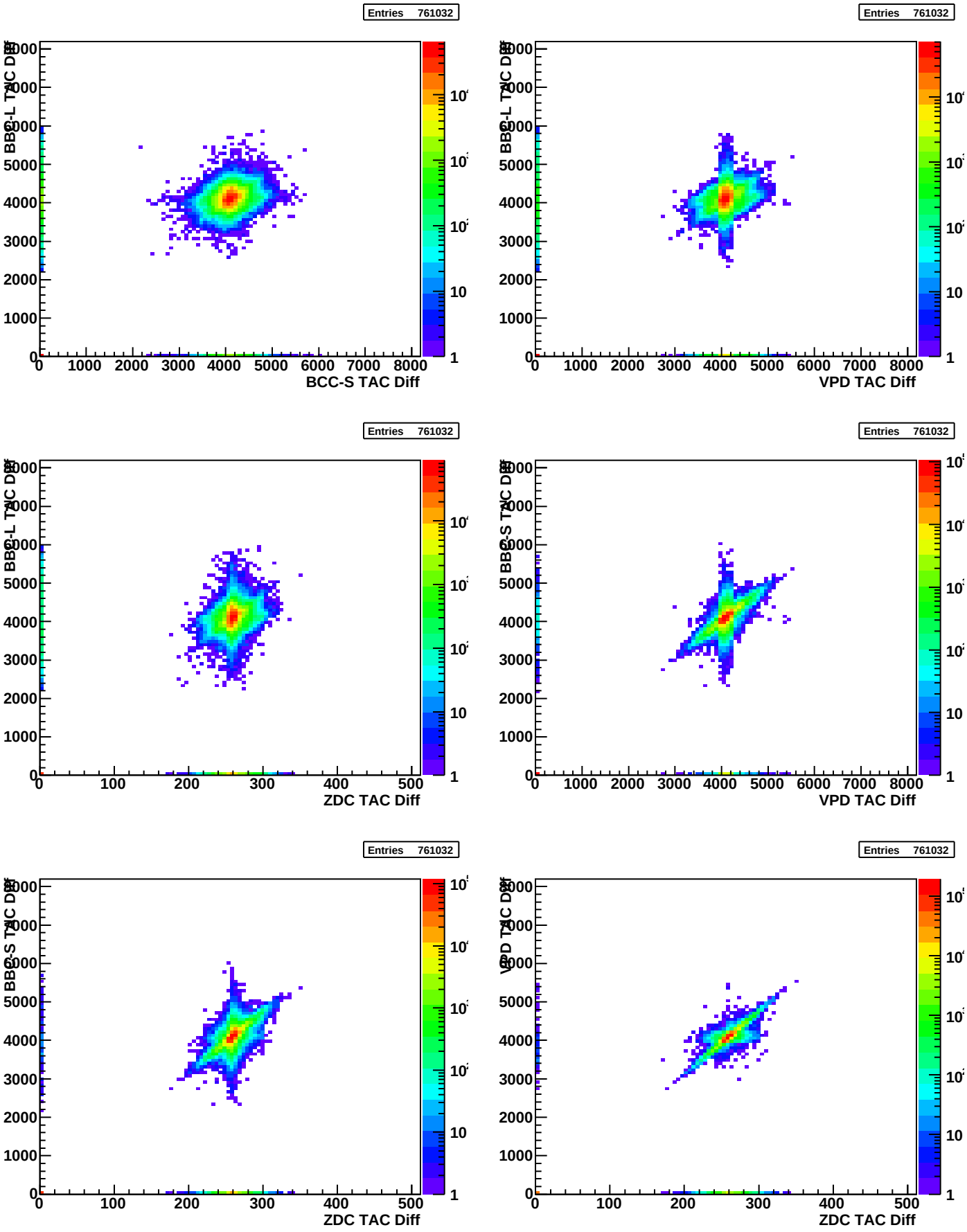

Entries 761032
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

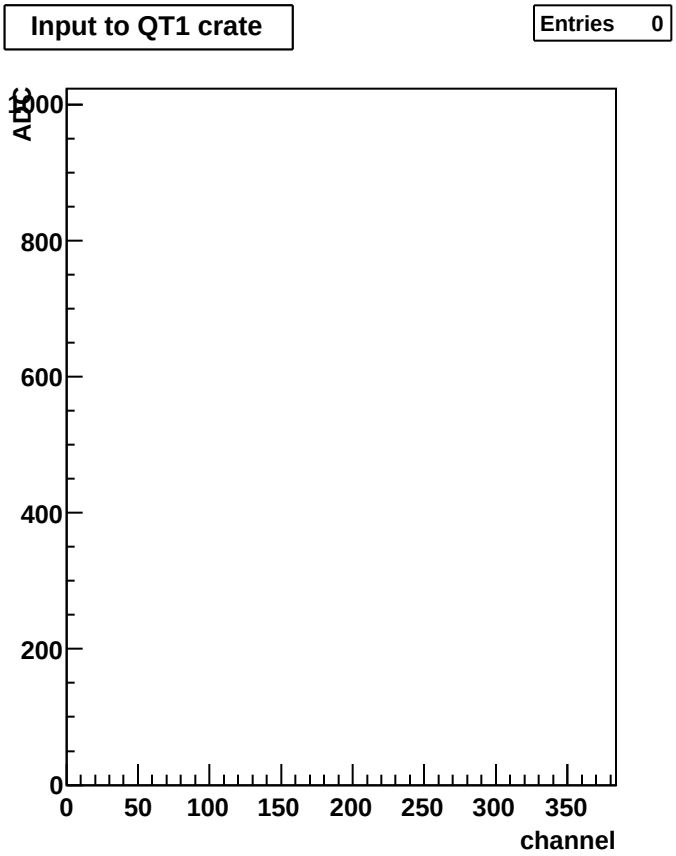

Entries 761032
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

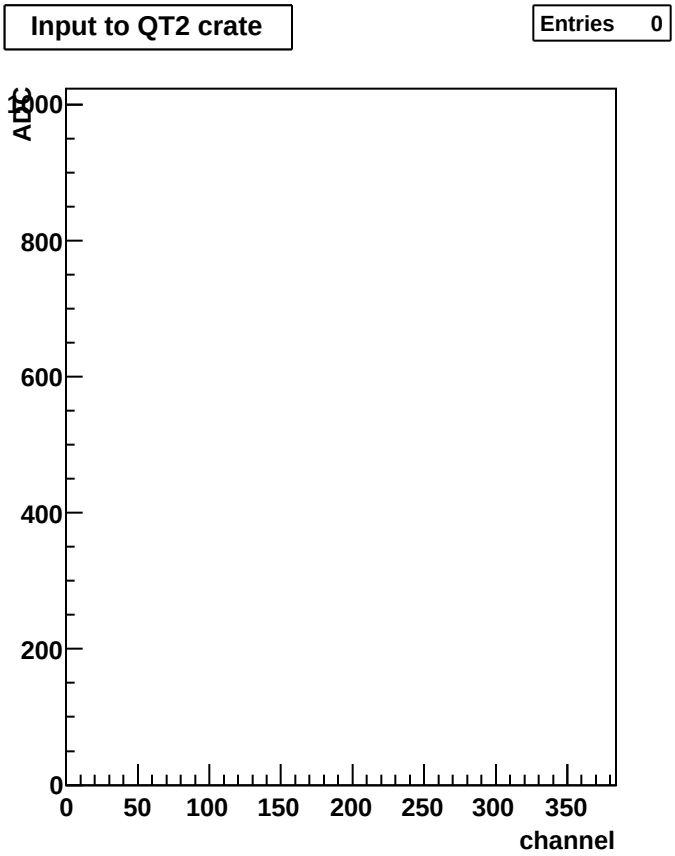

Entries 761032
BBC-L-East ADC Sum
BBC-L-West ADC Sum

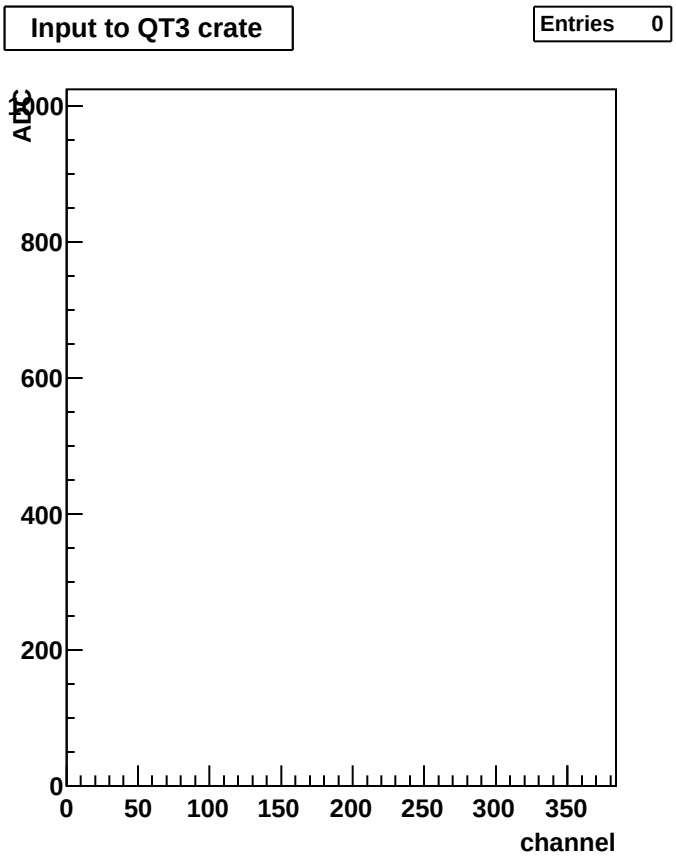

Entries 761032
BBC-S-East ADC Sum
BBC-S-West ADC Sum

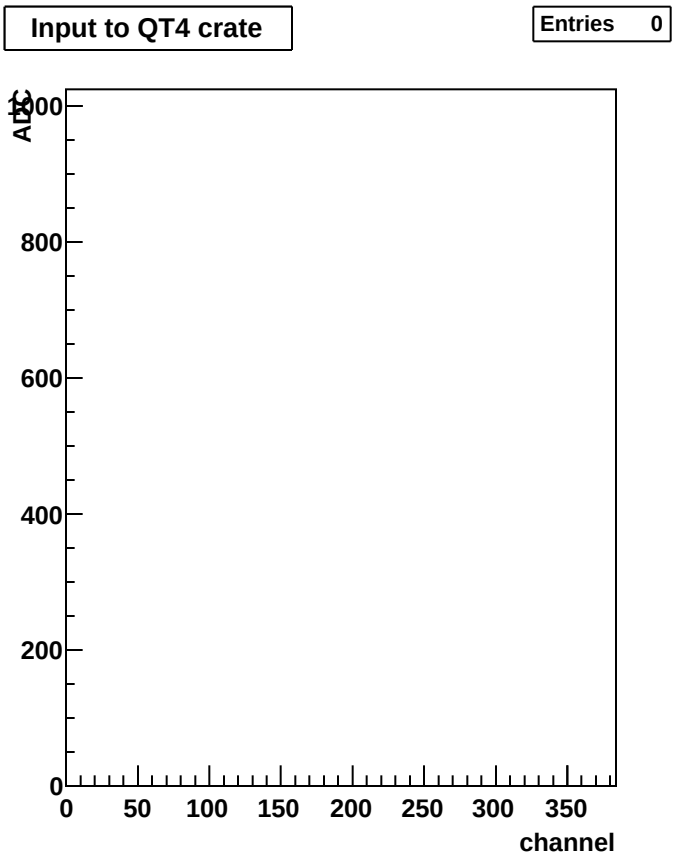

Entries 761032
ZDC East ADC Sum Att
ZDC West ADC Sum Att

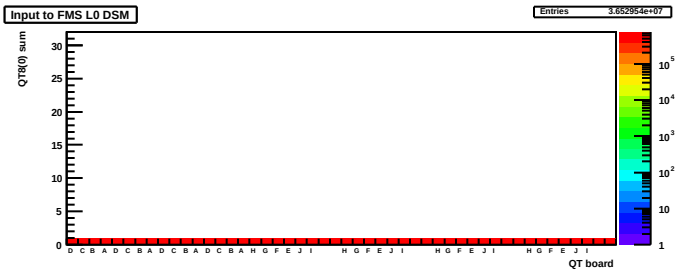

Entries 761032
BBC-L TAC Diff
BCC-S TAC Diff
Entries 761032
BBC-L TAC Diff
VPD TAC Diff
Entries 761032
BBC-L TAC Diff
ZDC TAC Diff
Entries 761032
BBC-S TAC Diff
VPD TAC Diff
Entries 761032
BBC-S TAC Diff
ZDC TAC Diff
Entries 761032
VPD TAC Diff
ZDC TAC Diff

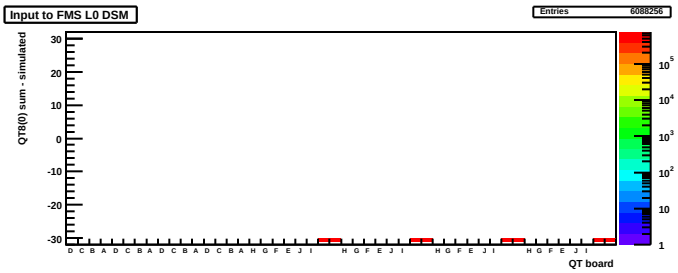

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

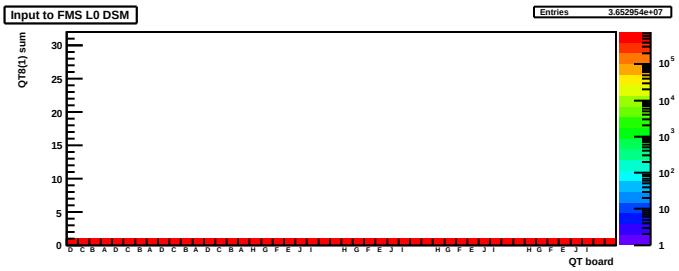

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

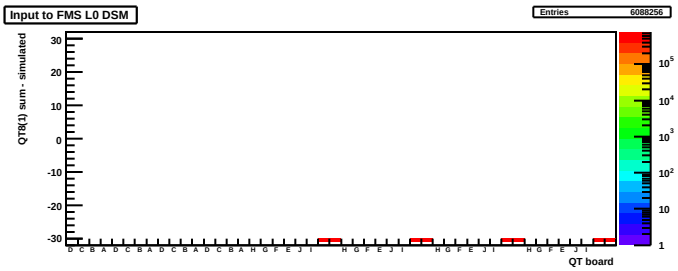

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

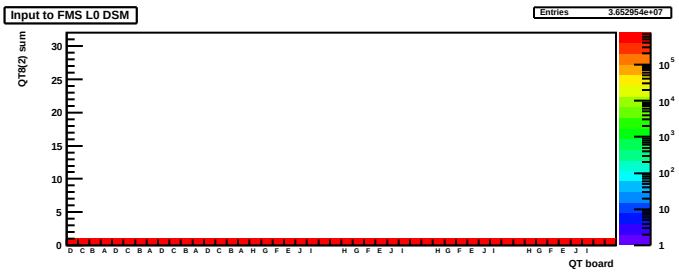

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

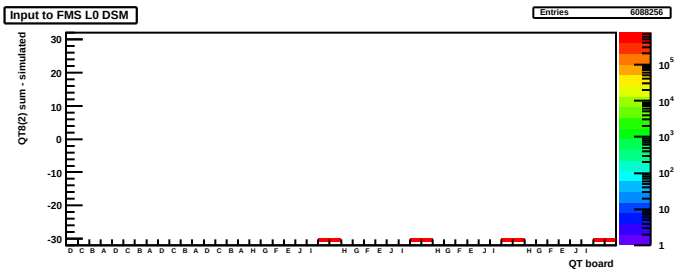

Input to FMS L0 DSM
Entries 3.652954e+07
QT8(0) sum
QT board

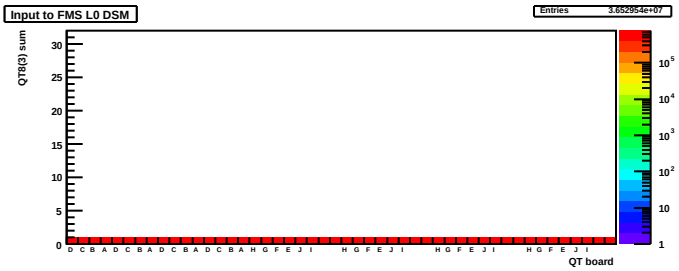

Input to FMS L0 DSM
Entries 6088256
QT8(0) sum - simulated
QT board

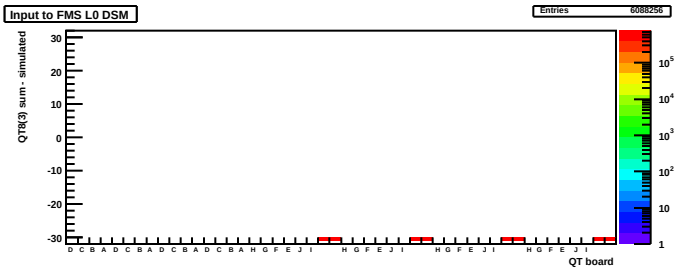

Input to FMS L0 DSM
Entries 3.652954e+07
QT8(1) sum
QT board

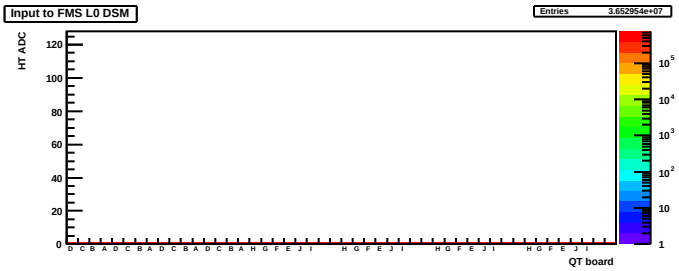

Input to FMS L0 DSM
Entries 6088256
QT8(1) sum - simulated
QT board

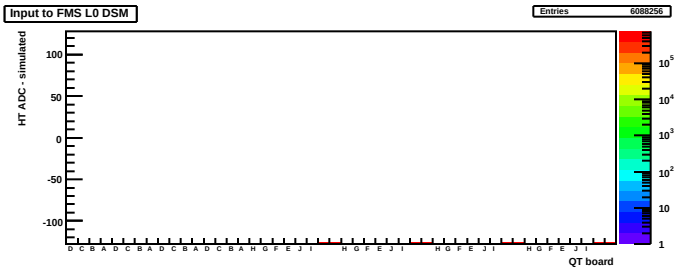

Input to FMS L0 DSM
Entries 3.652954e+07
QT8(2) sum
QT board

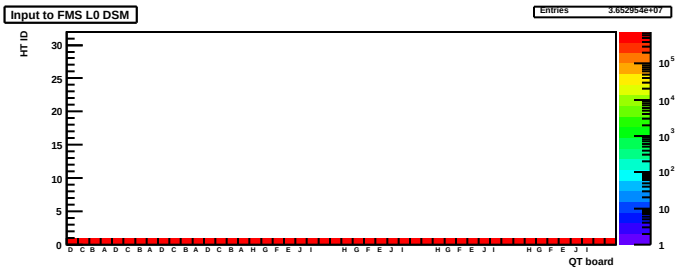

Input to FMS L0 DSM
Entries 6088256
QT8(2) sum - simulated
QT board

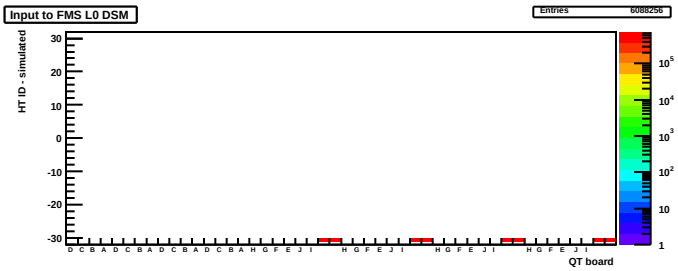

Input to FMS L0 DSM
Entries 3.652954e+07
QT8(3) sum
QT board

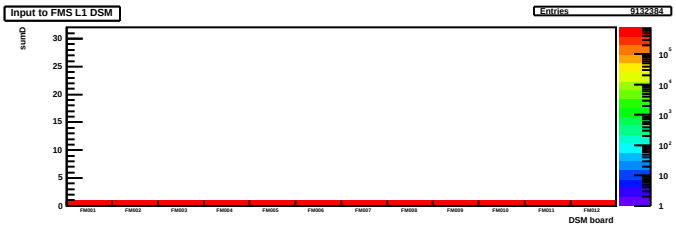

Input to FMS L0 DSM
Entries 6088256
QT8(3) sum - simulated
QT board

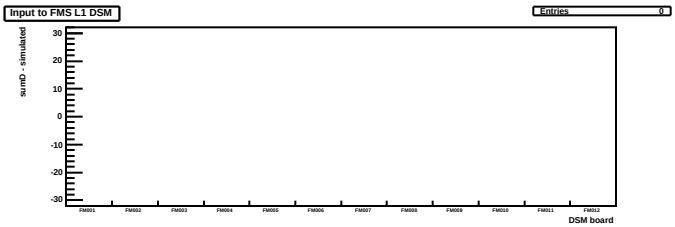

Input to FMS L0 DSM
Entries 3.652954e+07
HT ADC
QT board

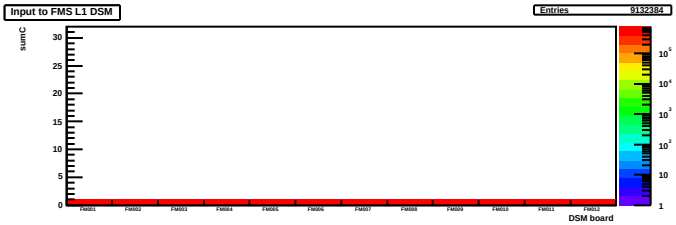

Input to FMS L0 DSM
Entries 6088256
HT ADC - simulated
QT board

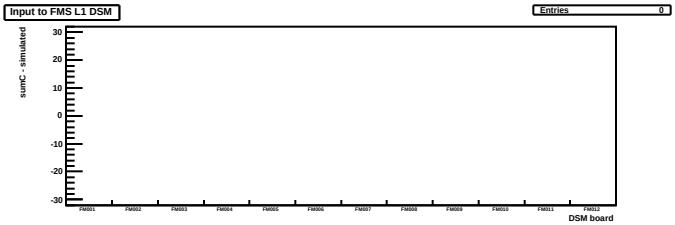

Input to FMS L0 DSM
Entries 3.652954e+07
HT ID
QT board

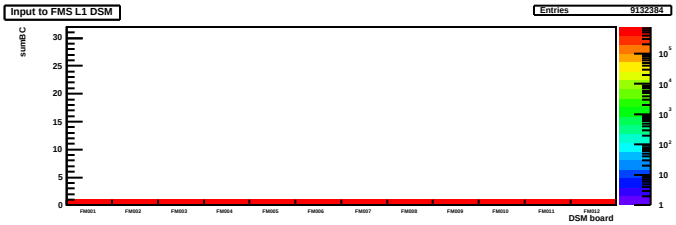

Input to FMS L0 DSM
Entries 6088256
HT ID - simulated
QT board

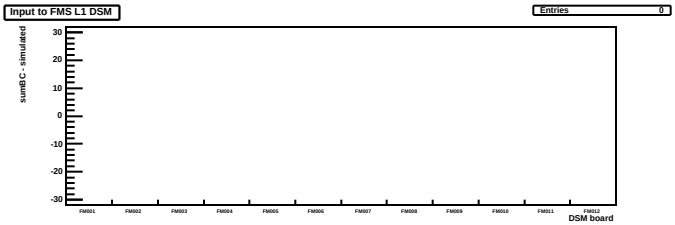

Input to FMS L1 DSM
Entries 9132384
sumD
DSM board

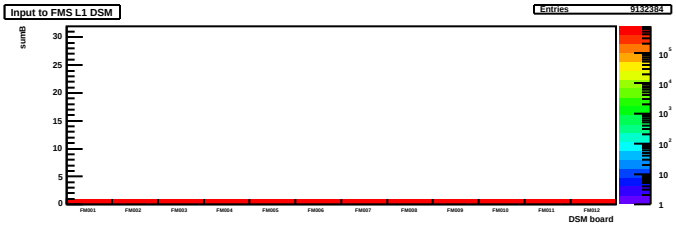

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

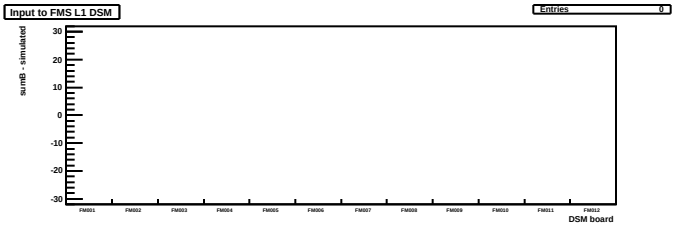

Input to FMS L1 DSM
Entries 9132384
sumC
DSM board

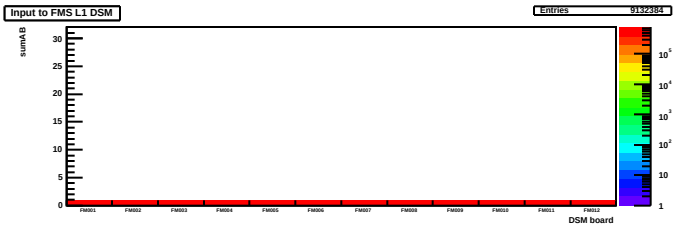

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

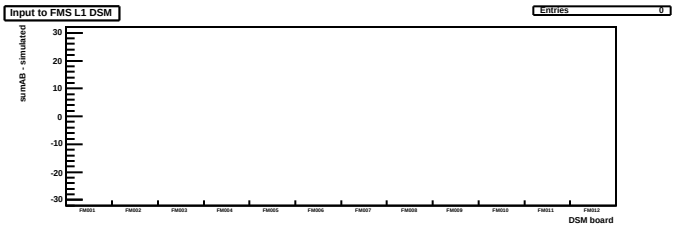

Input to FMS L1 DSM
Entries 9132384
sumBC
DSM board

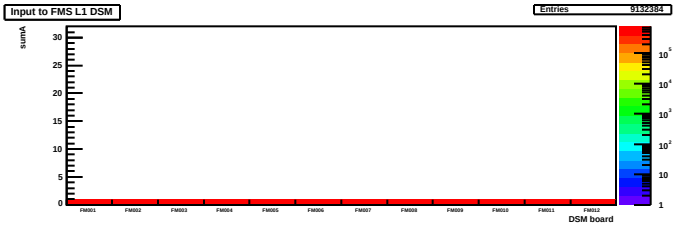

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

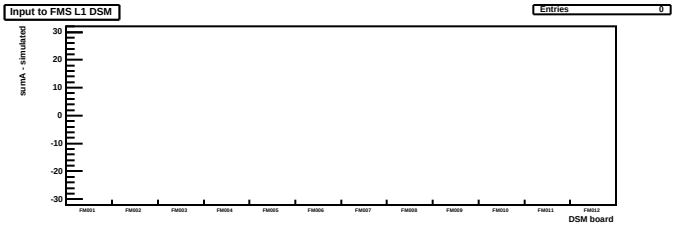

Input to FMS L1 DSM
Entries 9132384
sumB
DSM board

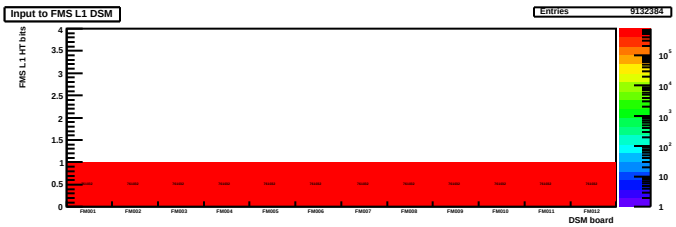

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

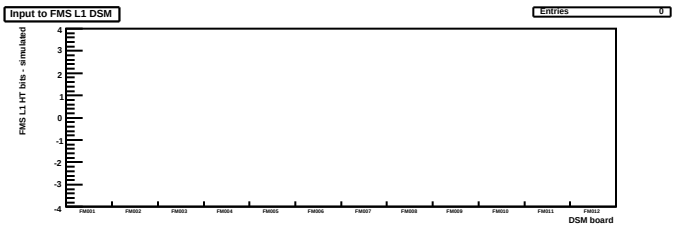

Input to FMS L1 DSM
Entries 9132384
sumAB
DSM board

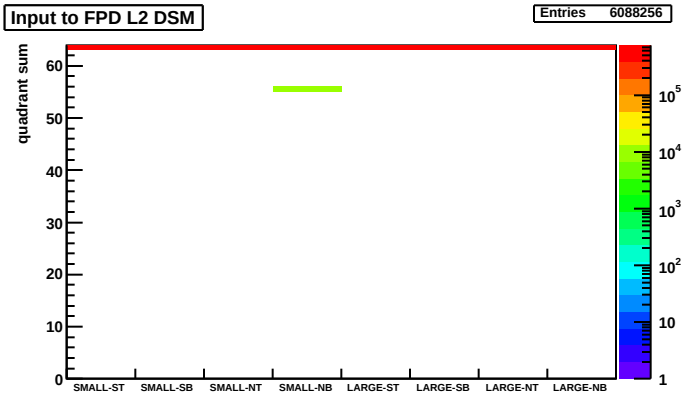

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

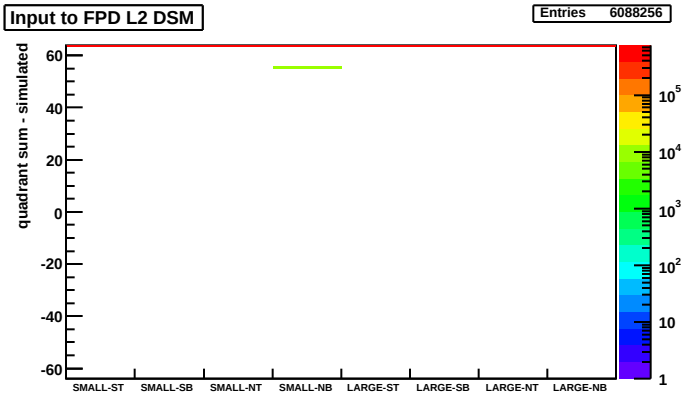

Input to FMS L1 DSM
Entries 9132384
sumA
DSM board

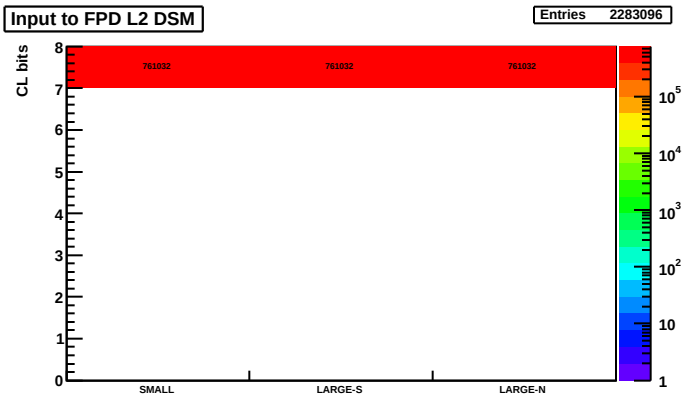

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

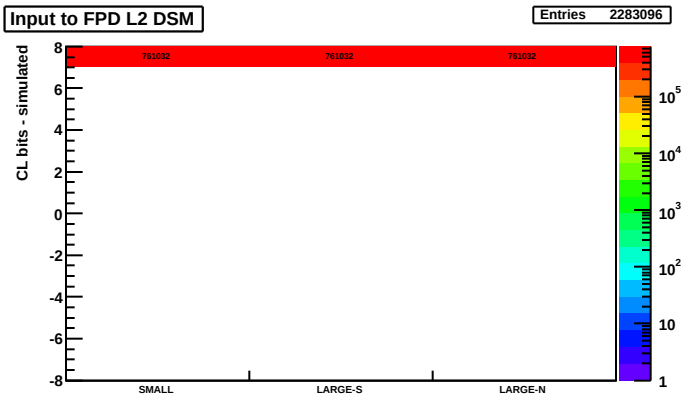

Input to FMS L1 DSM
Entries 9132384
FMS L1 HT bits
DSM board

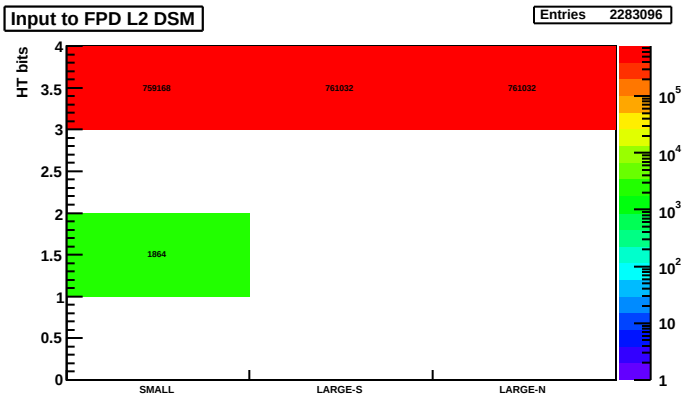

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

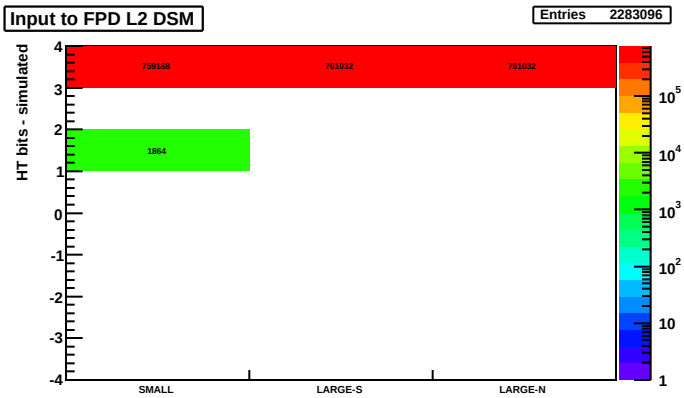

Input to FPD L2 DSM
Entries 6088256
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

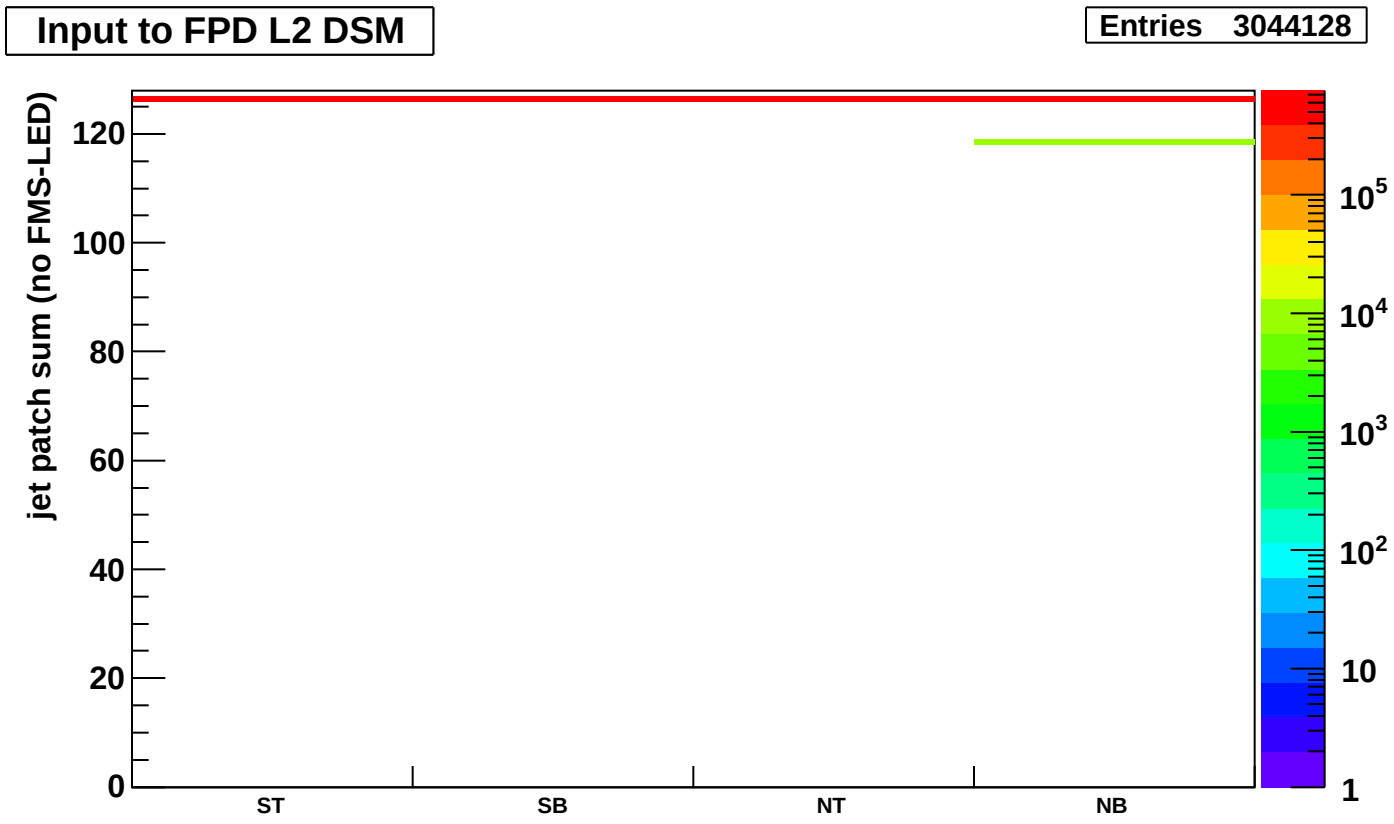

Input to FPD L2 DSM
Entries 6088256
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

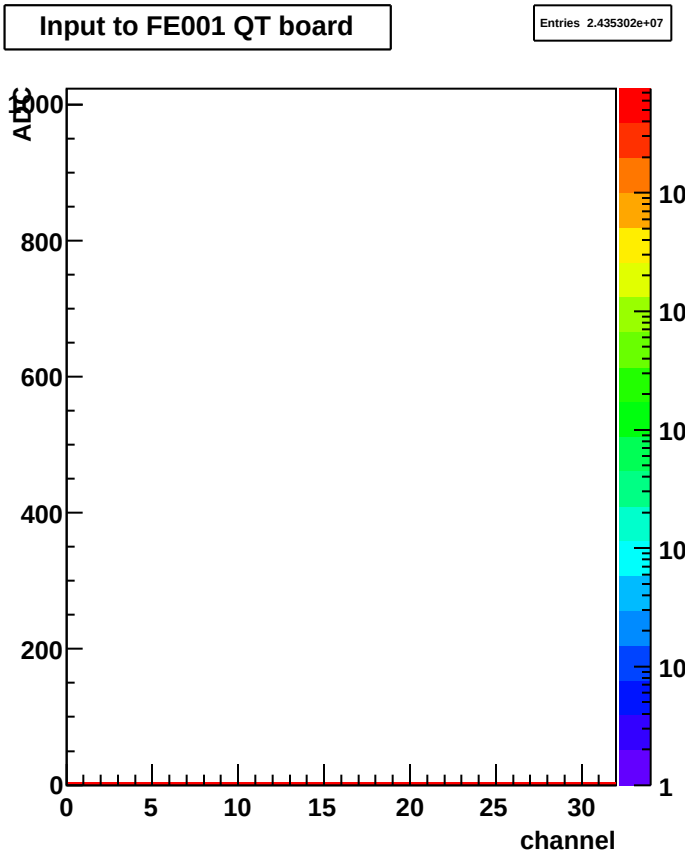

Input to FPD L2 DSM
Entries 2283096
CL bits
761032
761032
761032
SMALL
LARGE-S
LARGE-N

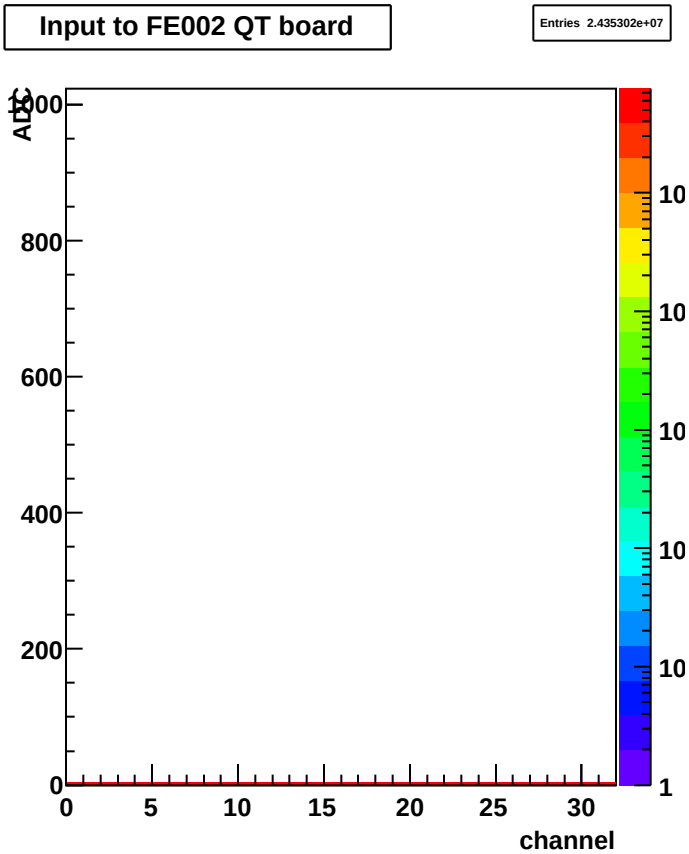

Input to FPD L2 DSM
Entries 2283096
CL bits - simulated
761032
761032
761032
SMALL
LARGE-S
LARGE-N

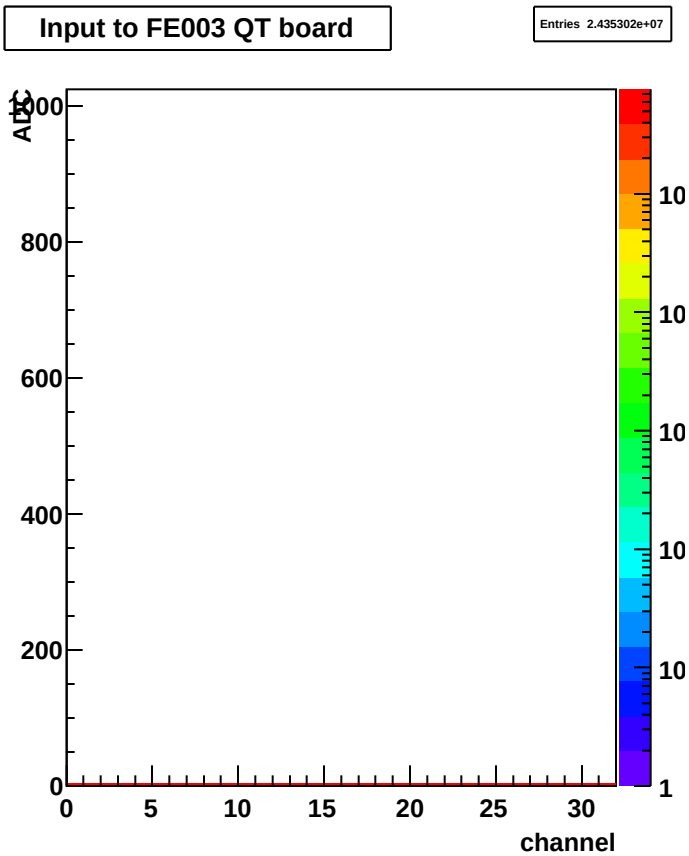

Input to FPD L2 DSM
Entries 2283096
HT bits
759168
761032
761032
1864
SMALL
LARGE-S
LARGE-N

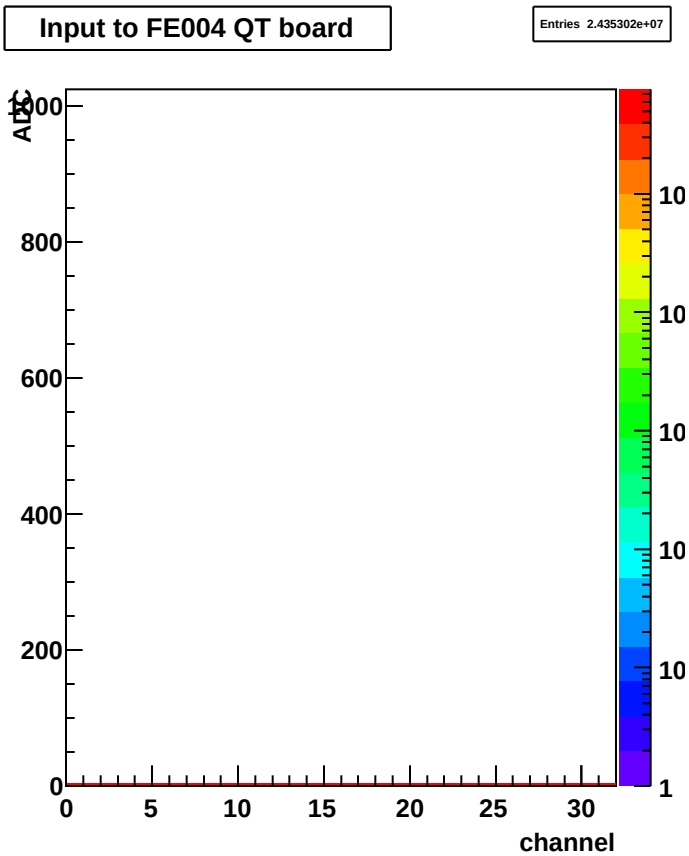

Input to FPD L2 DSM
Entries 2283096
HT bits - simulated
759168
761032
761032
1864
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3044128
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

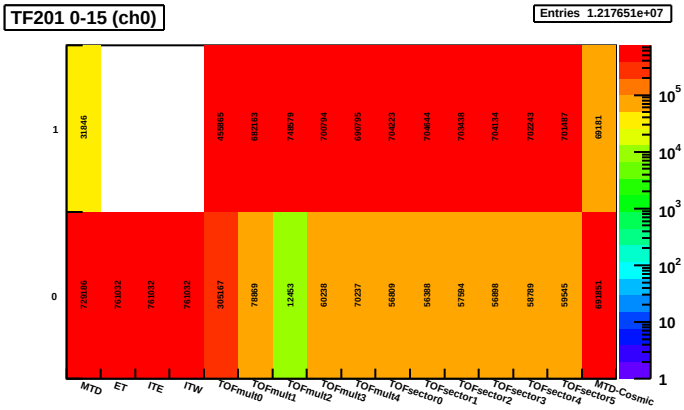

Input to FE001 QT board
Entries 2.435302e+07
ADC
channel


Input to FE002 QT board
Entries 2.435302e+07
ADC
channel

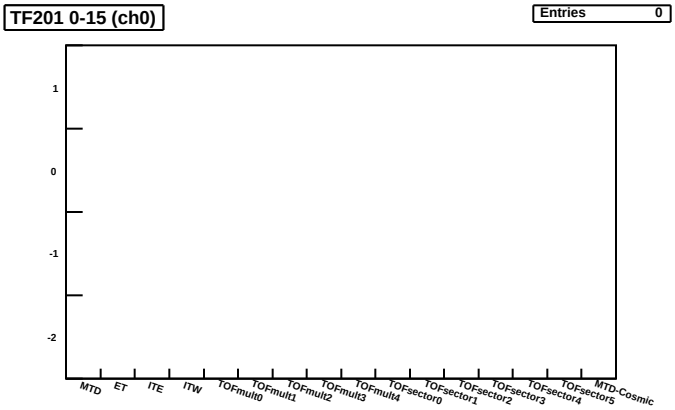

Input to FE003 QT board
Entries 2.435302e+07
ADC
channel


Input to FE004 QT board
Entries 2.435302e+07
ADC
channel

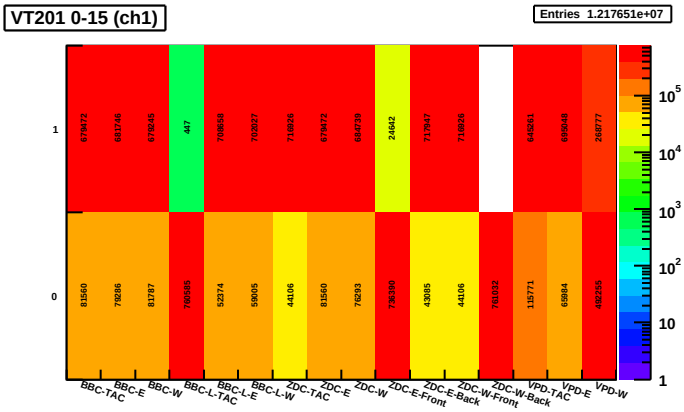
TF201 0-15 (ch0)



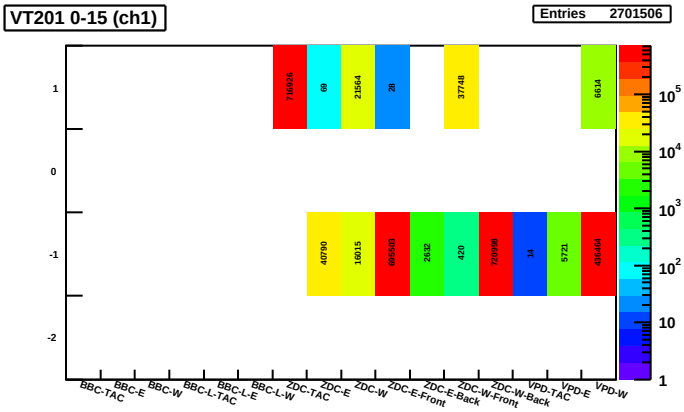
TF201 0-15 (ch0)



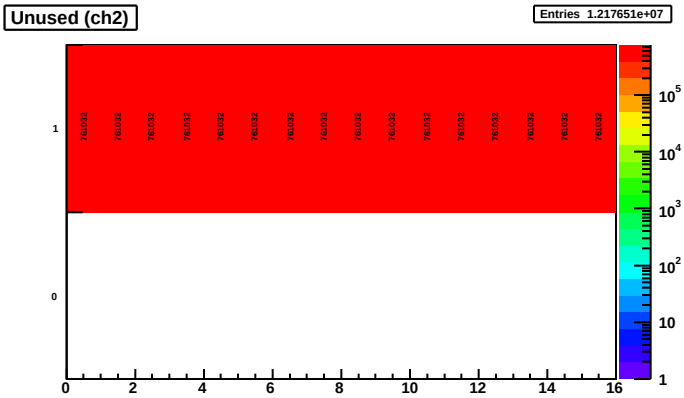
VT201 0-15 (ch1)

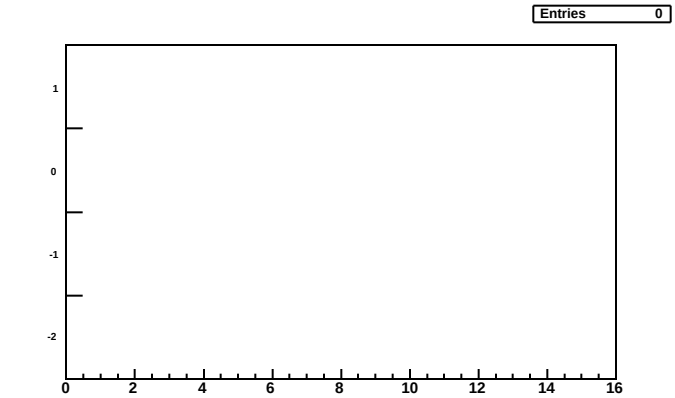


VT201 0-15 (ch1)

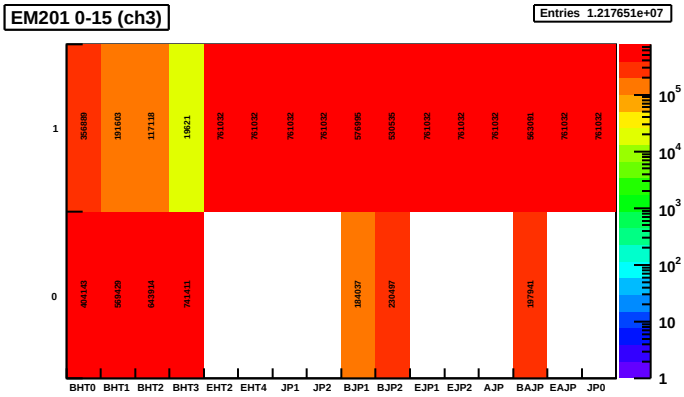


Unused (ch2)

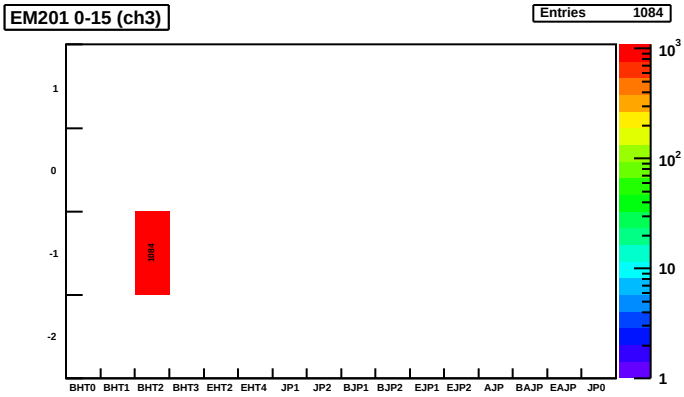




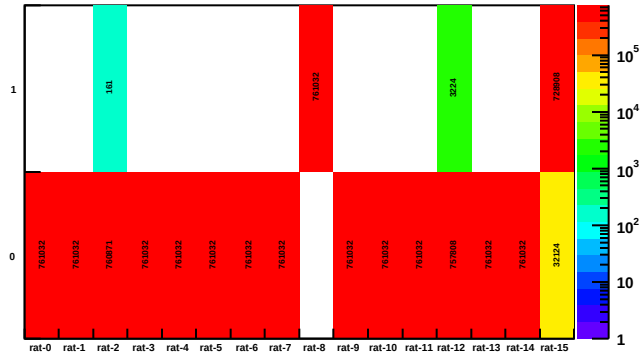
EM201 0-15 (ch3)

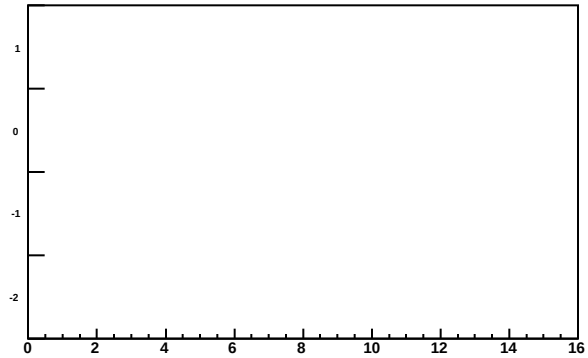


EM201 0-15 (ch3)

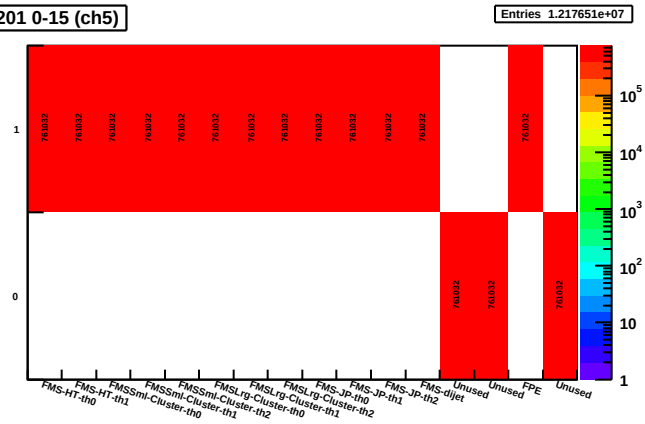


RAT board (ch4)

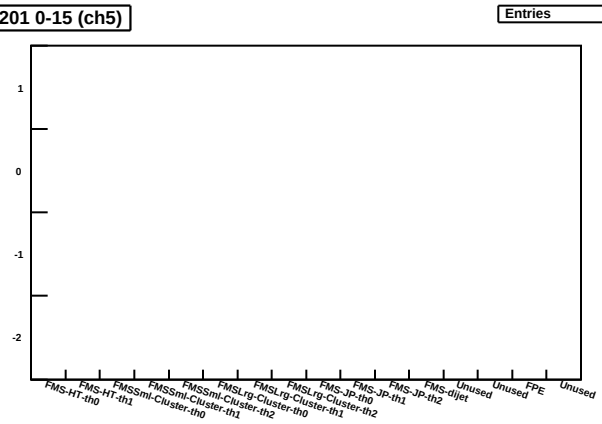




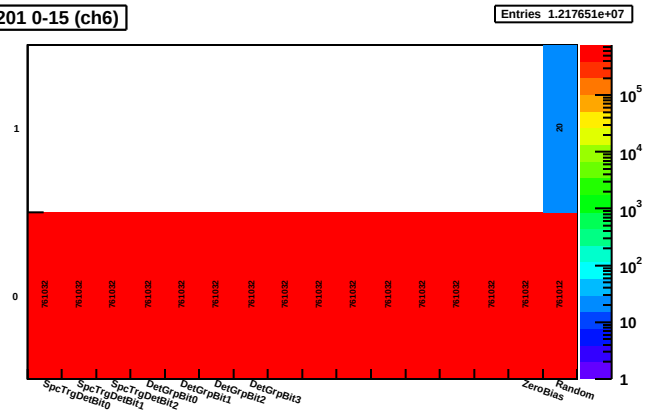
FP201 0-15 (ch5)

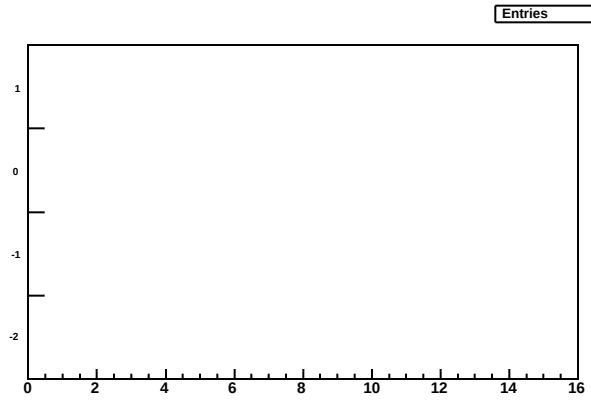
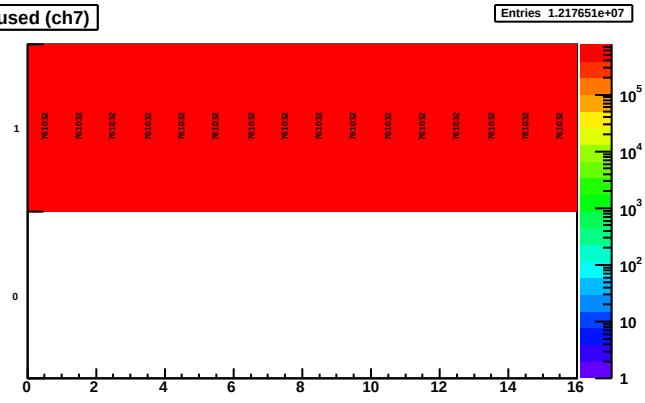
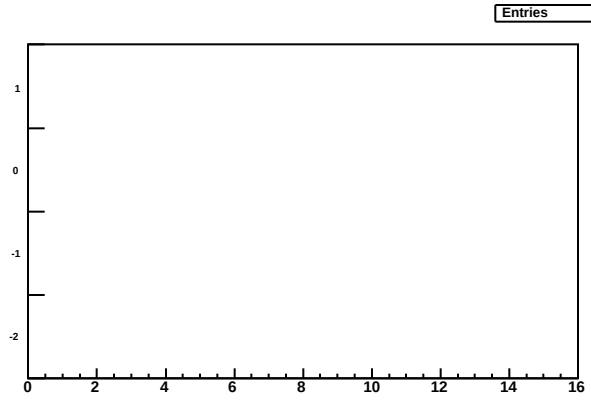


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)