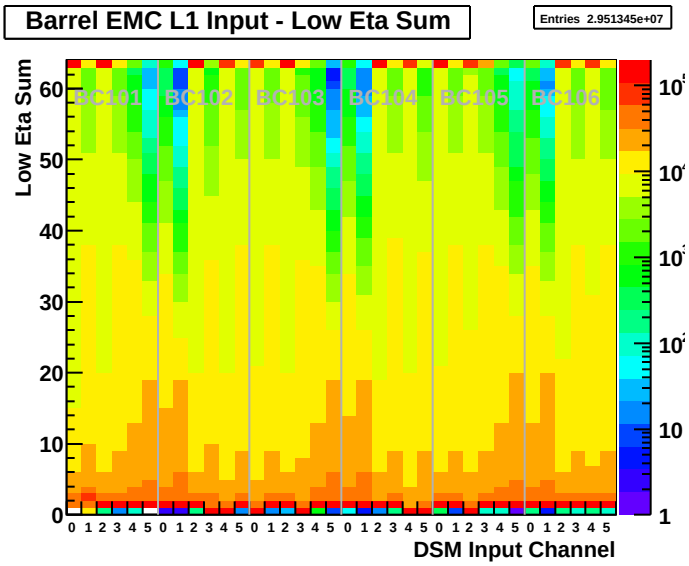
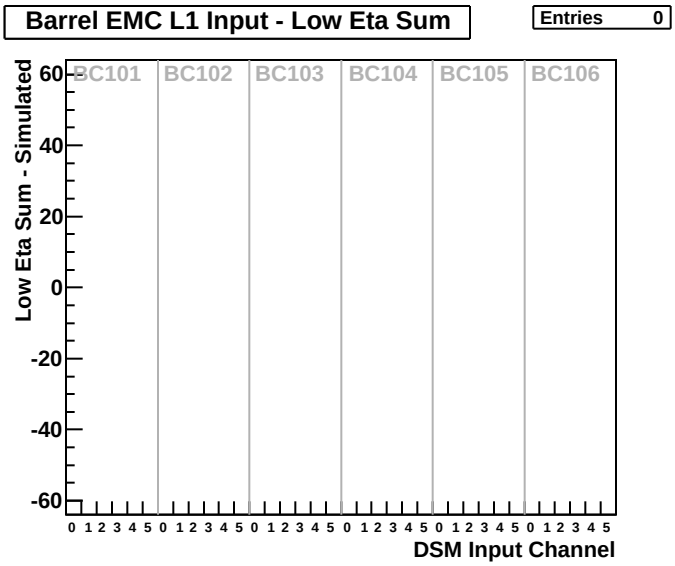
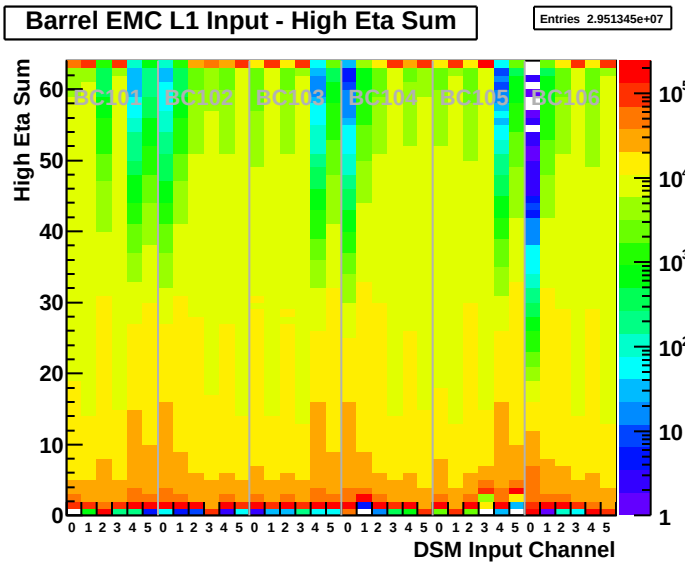
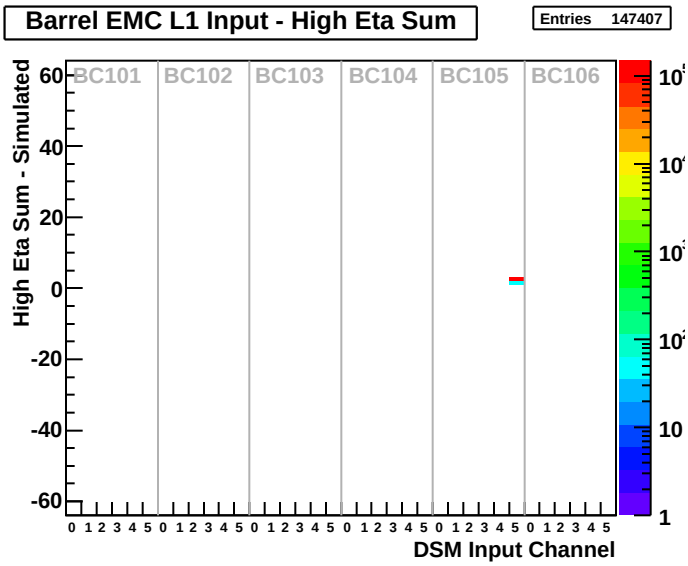
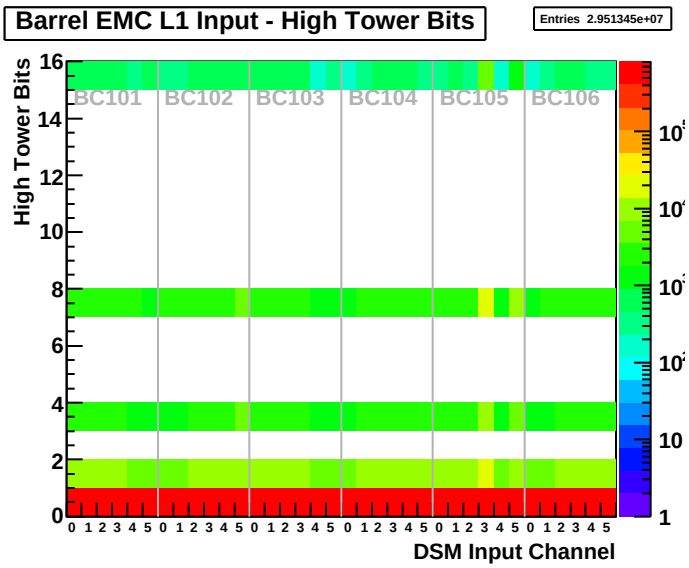
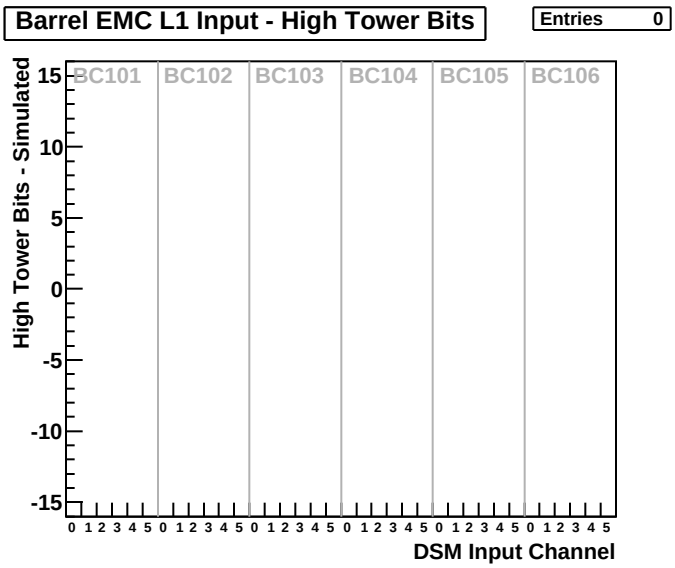

Barrel EMC L1 Input - Low Eta Sum
Entries 2.951345e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

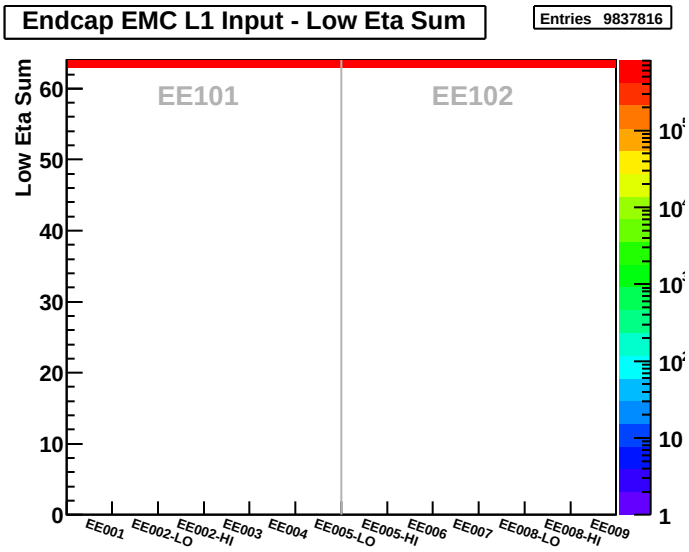

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

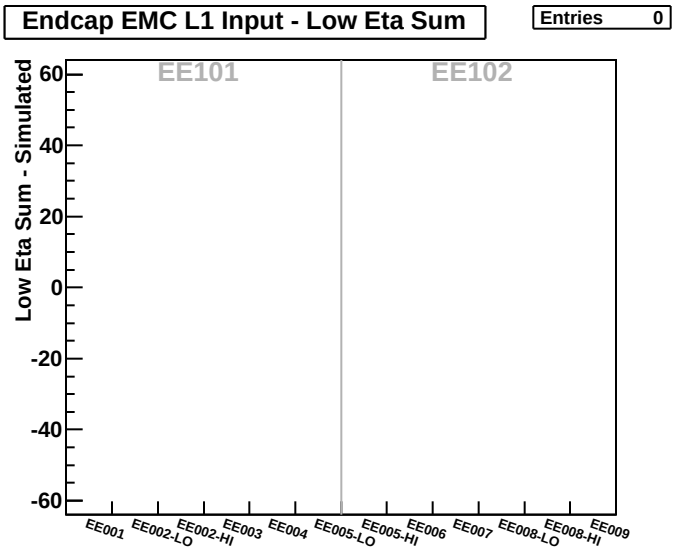

Barrel EMC L1 Input - High Eta Sum
Entries 2.951345e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

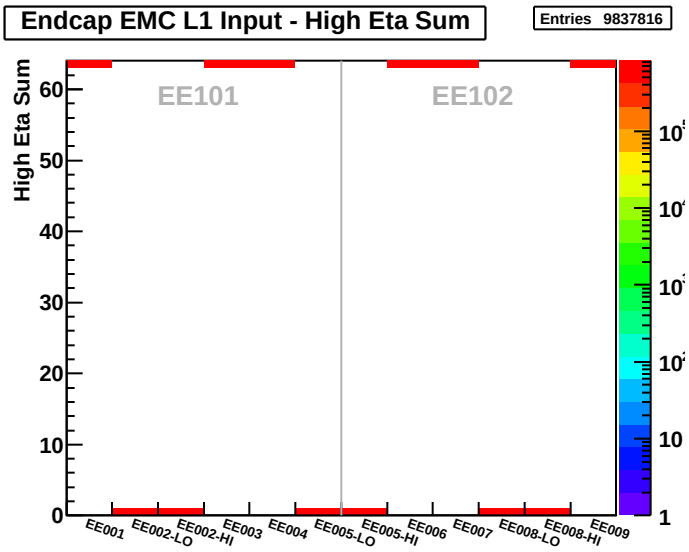

Barrel EMC L1 Input - High Eta Sum
Entries 147407
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

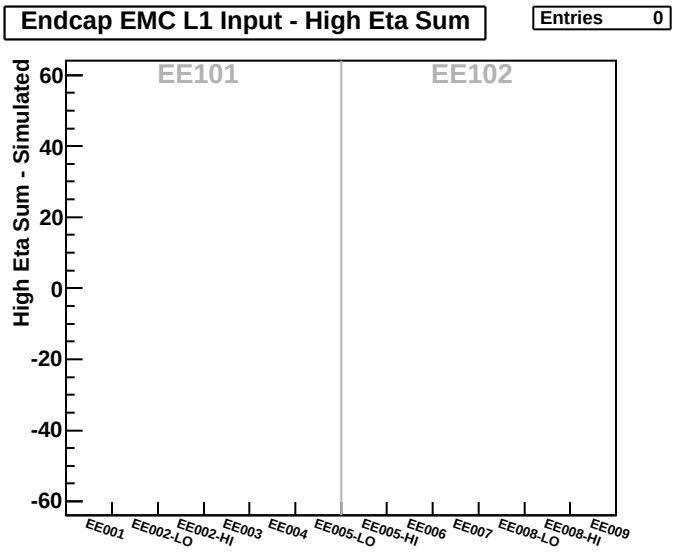

Barrel EMC L1 Input - High Tower Bits
Entries 2.951345e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

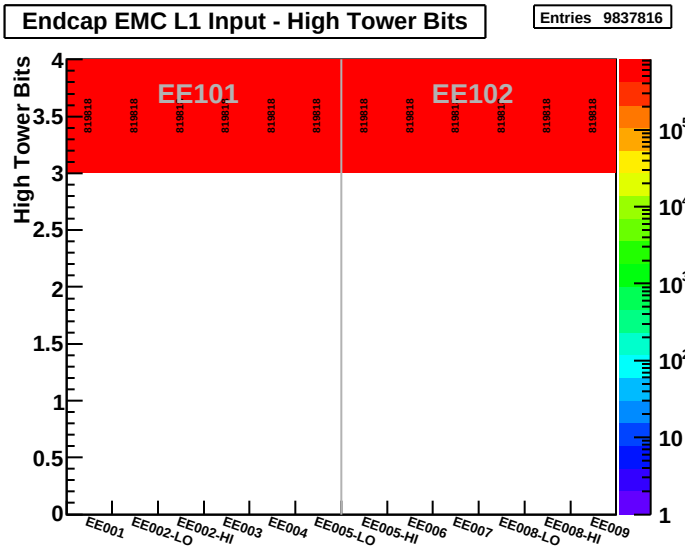

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

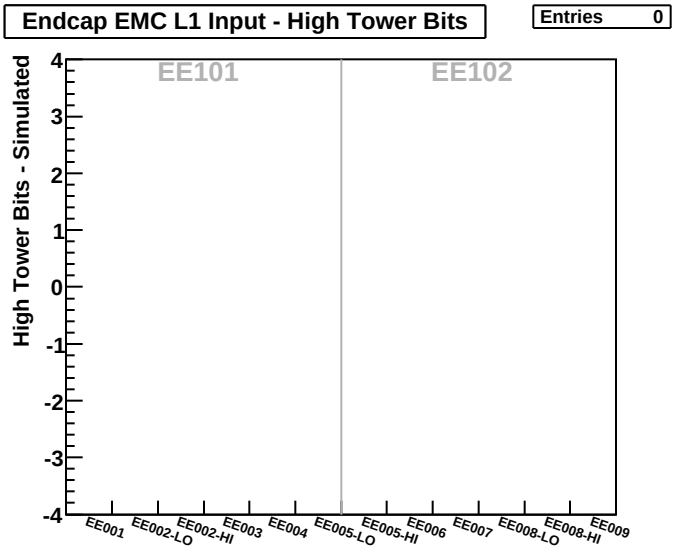

Endcap EMC L1 Input - Low Eta Sum
Entries 9837816
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

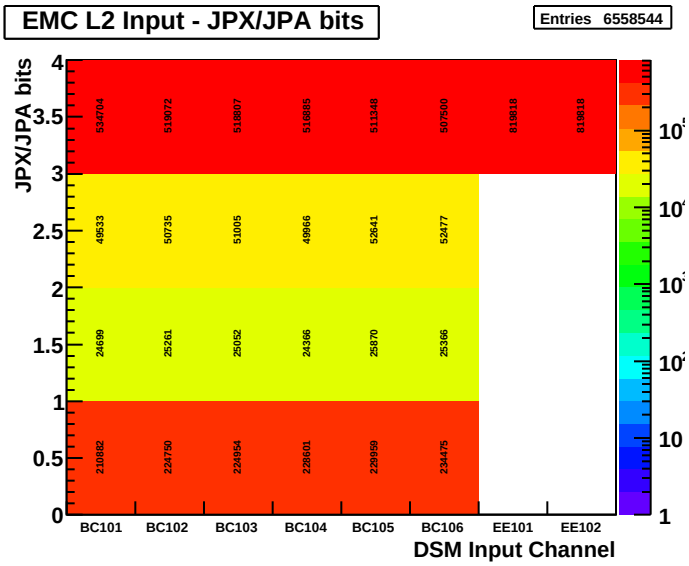

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

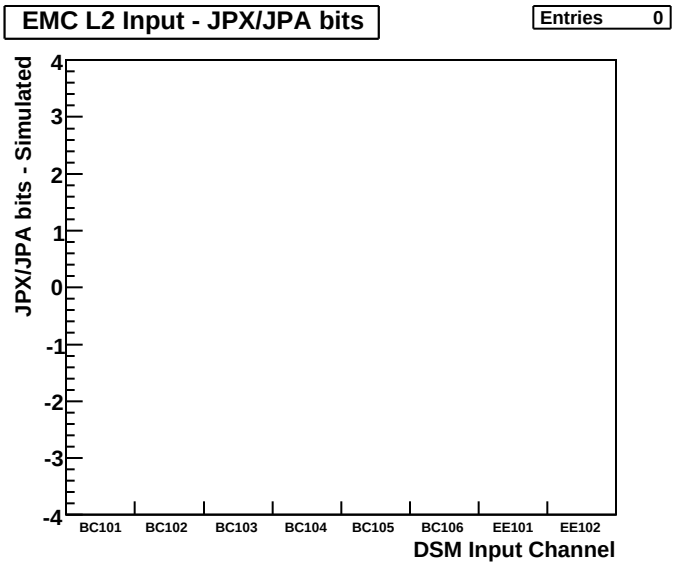

Endcap EMC L1 Input - High Eta Sum
Entries 9837816
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

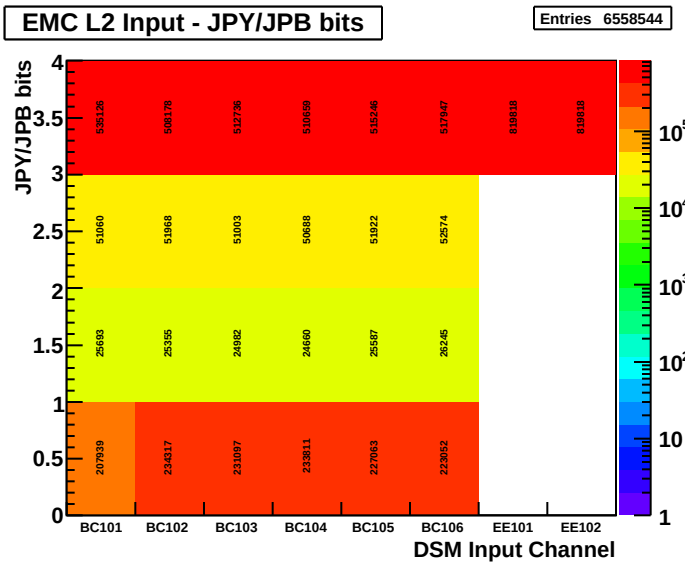

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

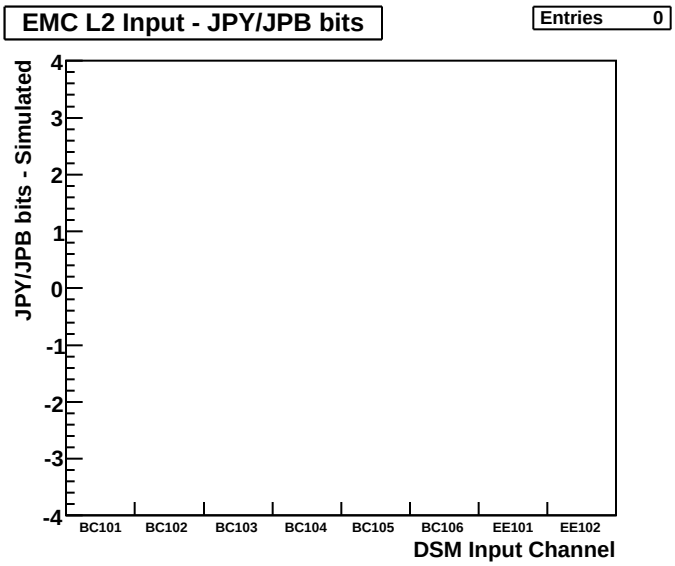

Endcap EMC L1 Input - High Tower Bits
Entries 9837816
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

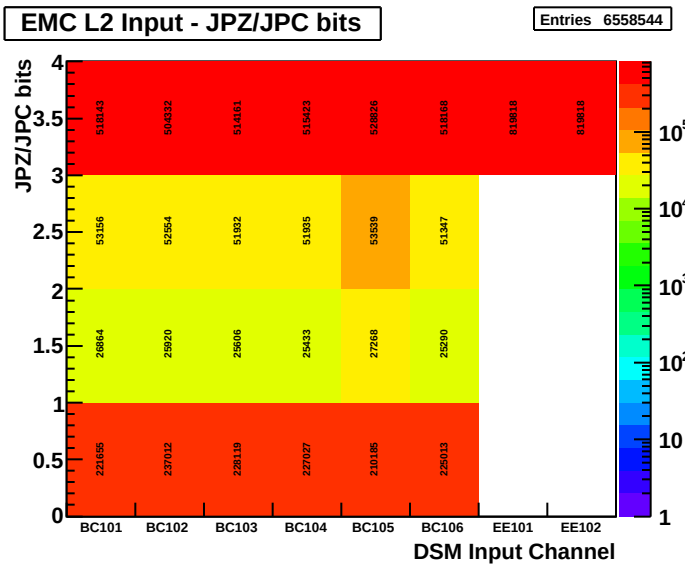

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

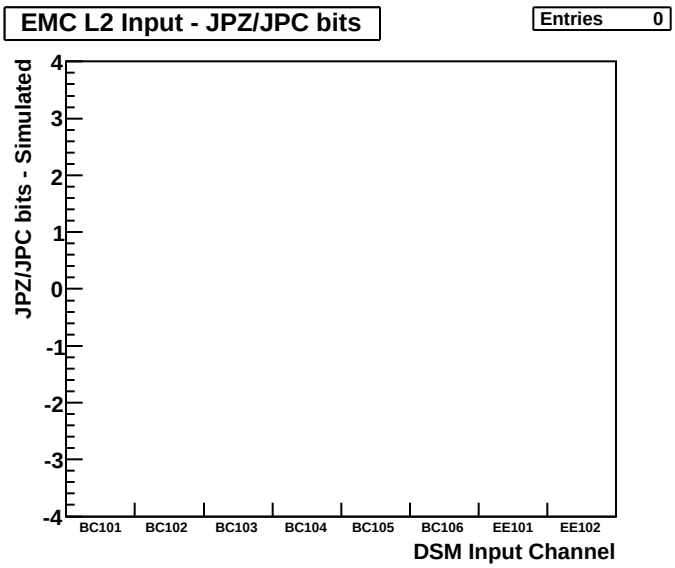

EMC L2 Input - JPX/JPA bits
Entries 6558544
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
534704 519072 518807 516885 511348 507500 819818 819818
49533 50735 51005 49966 52641 52477
24699 25261 25052 24366 25870 25366
210882 224750 224954 228601 229959 234475

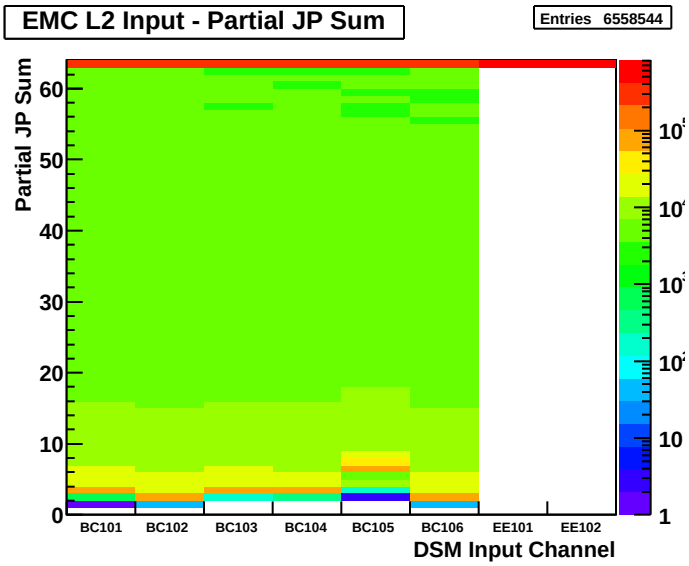

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

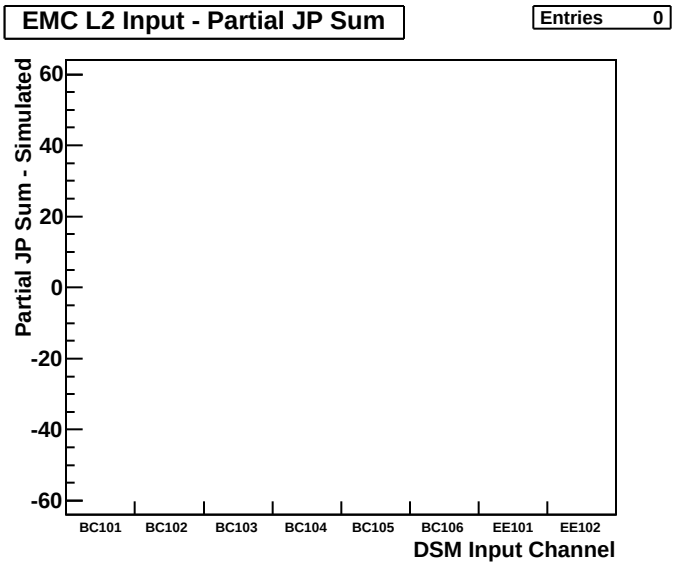

EMC L2 Input - JPY/JPB bits
Entries 6558544
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
535126 508178 512736 510659 515246 517947 819818 819818
51060 51968 51003 50688 51922 52574
25693 25355 24982 24660 25587 26245
207939 234317 231097 233811 227063 223052

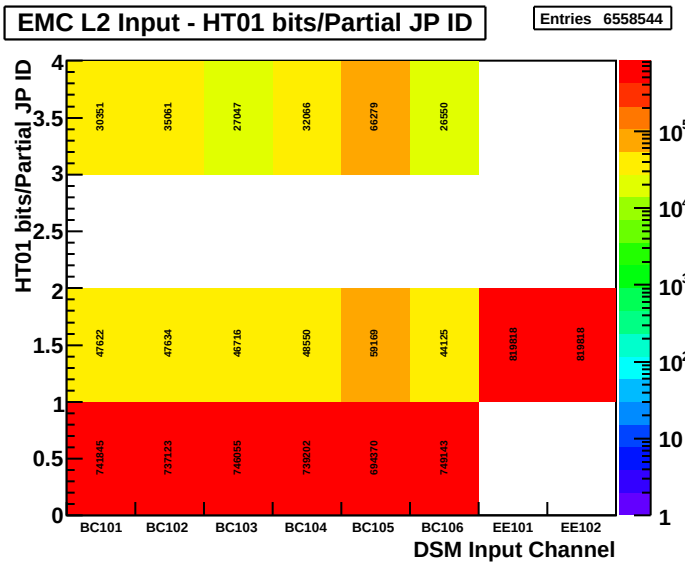

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

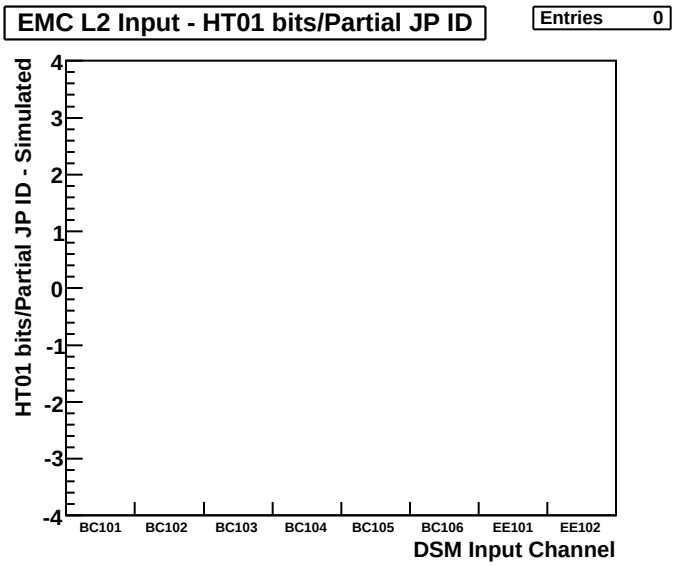

EMC L2 Input - JPZ/JPC bits
Entries 6558544
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
518143 504332 514161 515423 528826 518168 819818 819818
53156 52554 51932 51935 53539 51347
26864 25920 25606 25433 27268 25290
221655 237012 228119 227027 210185 225013

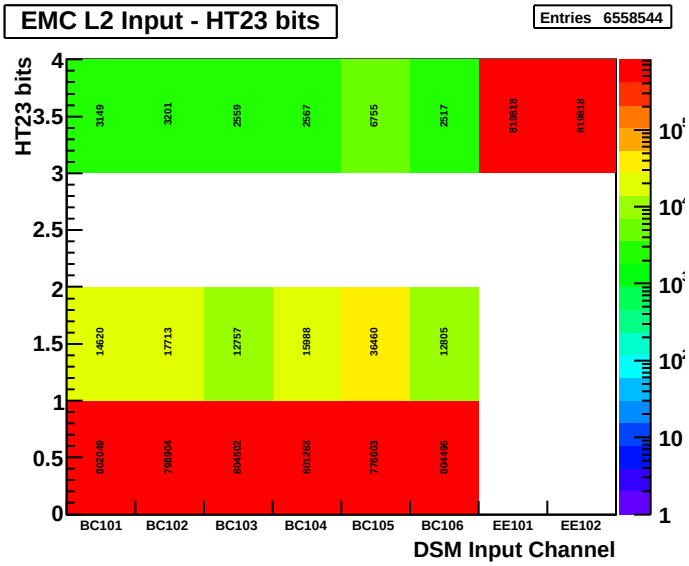

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

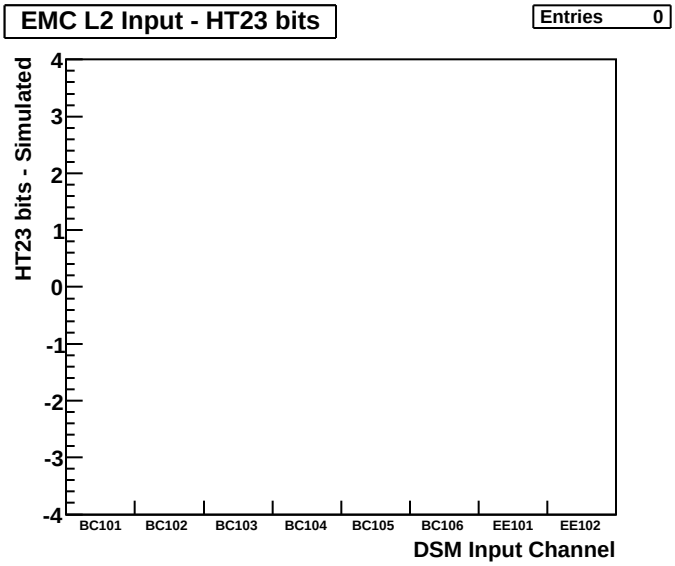

EMC L2 Input - Partial JP Sum
Entries 6558544
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

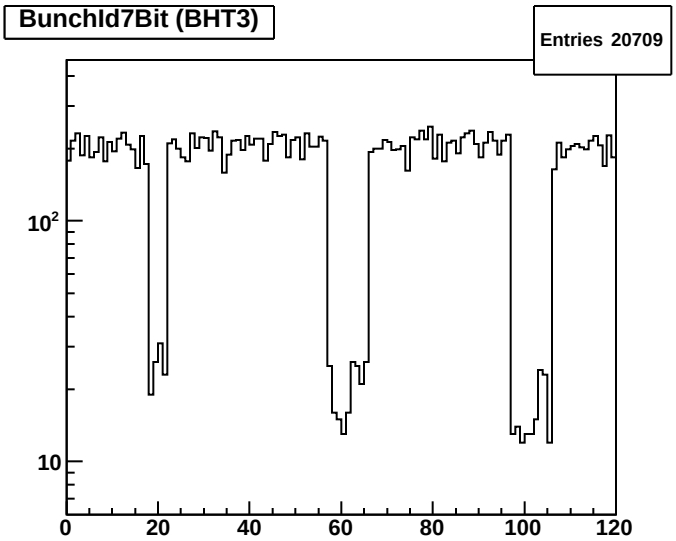

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

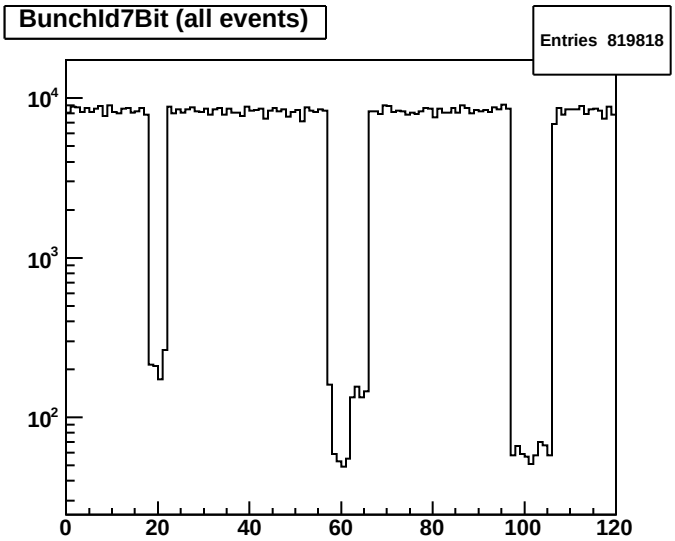

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6558544
HT01 bits/Partial JP ID
30351 35061 27047 32066 66279 26550
47622 47634 46716 48550 59169 44125 818818 818818
741845 737123 746055 739202 694370 749143
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

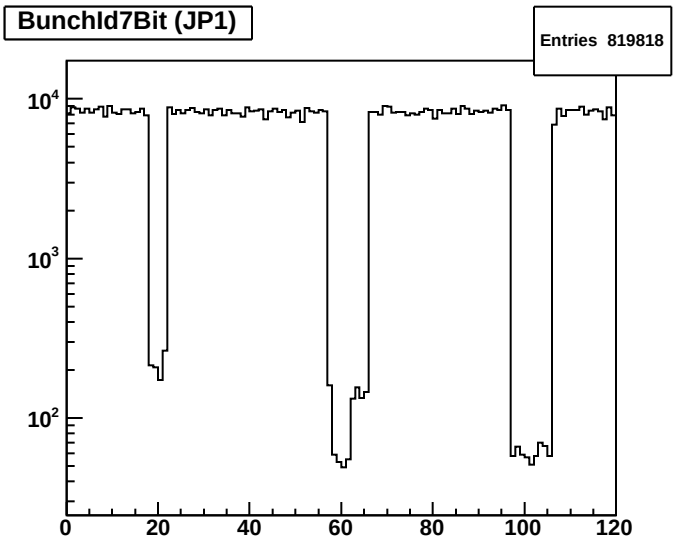

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

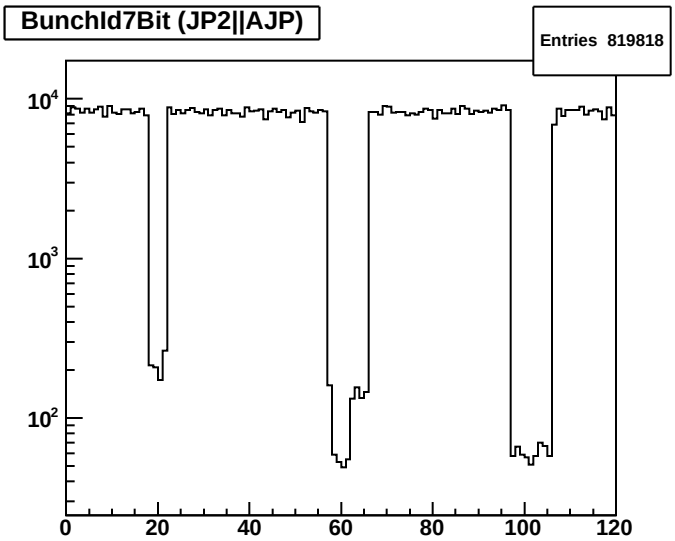

EMC L2 Input - HT23 bits
Entries 6558544
HT23 bits
3149 3201 2559 2567 6755 2517 818818 818818
14620 17713 12757 15988 36460 12805
802049 798304 804502 801263 776603 804496
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

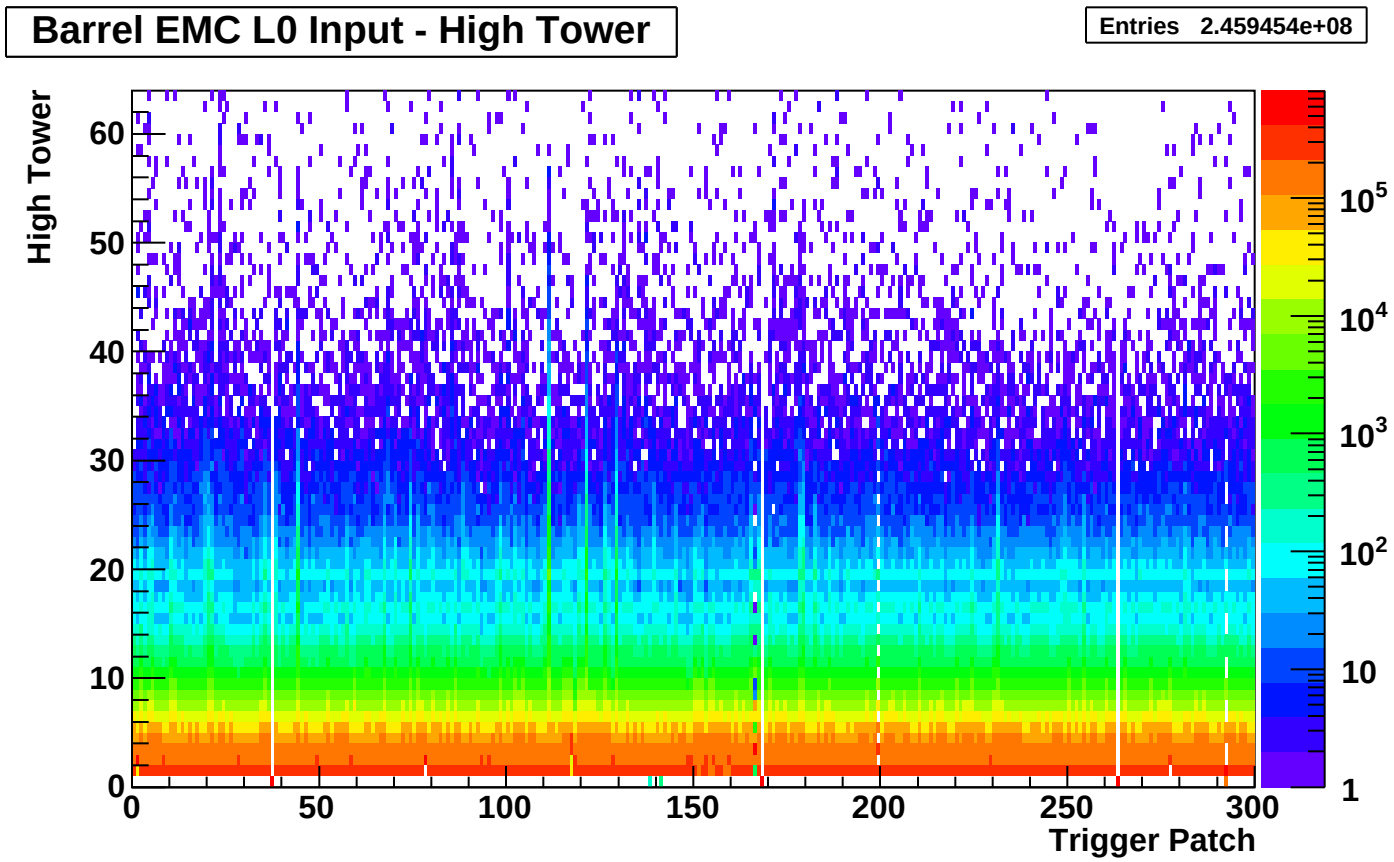

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

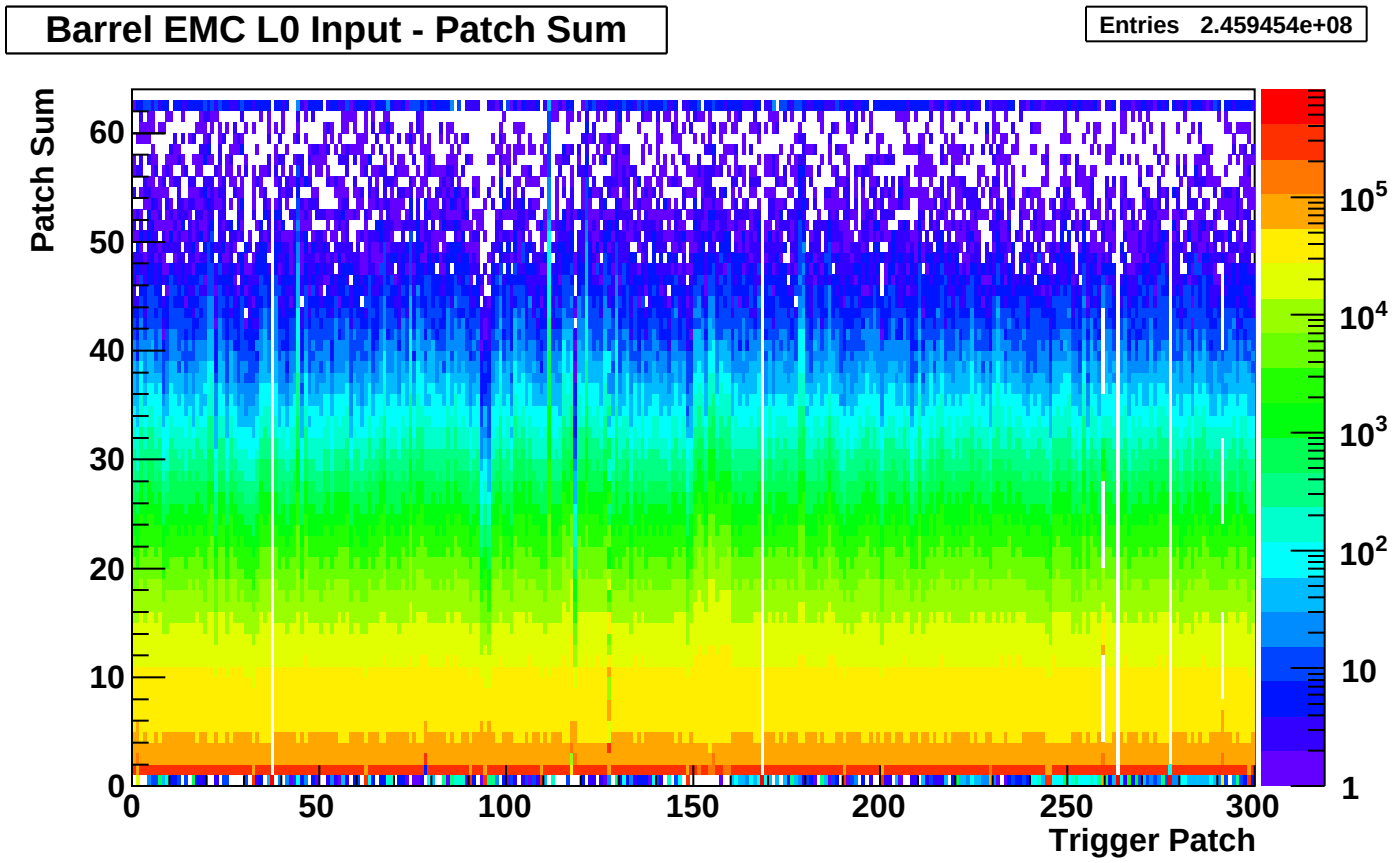

BunchId7Bit (BHT3)
Entries 20709
10
10^2
0
20
40
60
80
100
120

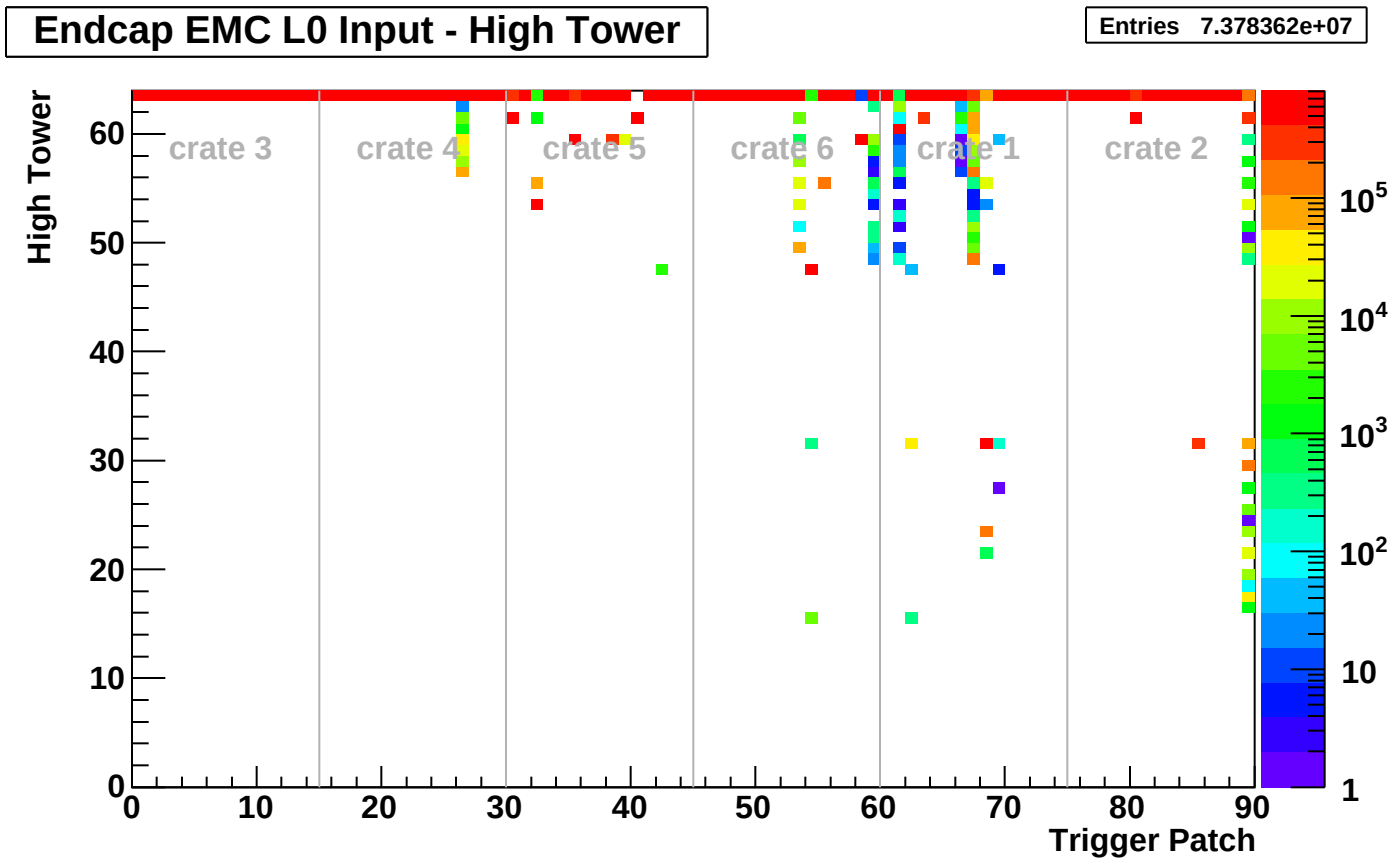

BunchId7Bit (all events)
Entries 819818
10^2
10^3
10^4
0
20
40
60
80
100
120

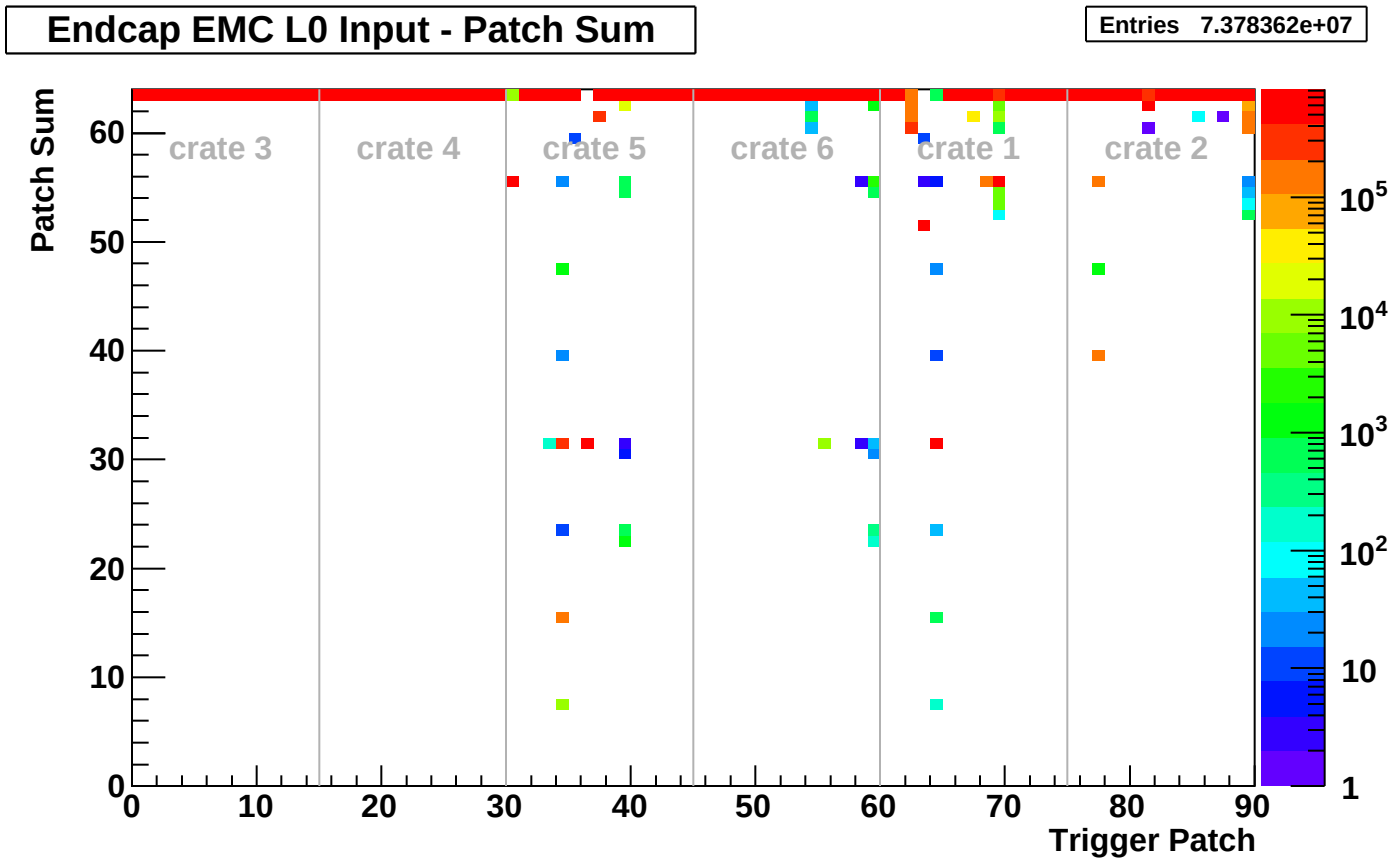

BunchId7Bit (JP1)
Entries 819818
10^2
10^3
10^4
0
20
40
60
80
100
120

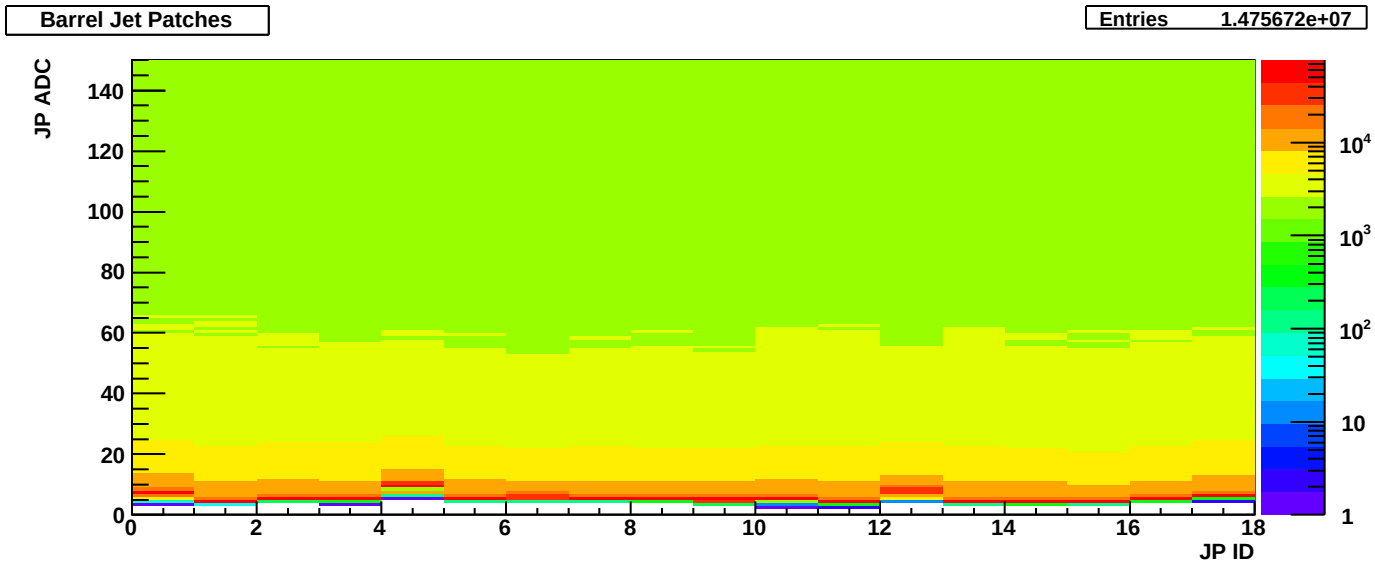

BunchId7Bit (JP2||AJP)
Entries 819818
10^2
10^3
10^4
0
20
40
60
80
100
120

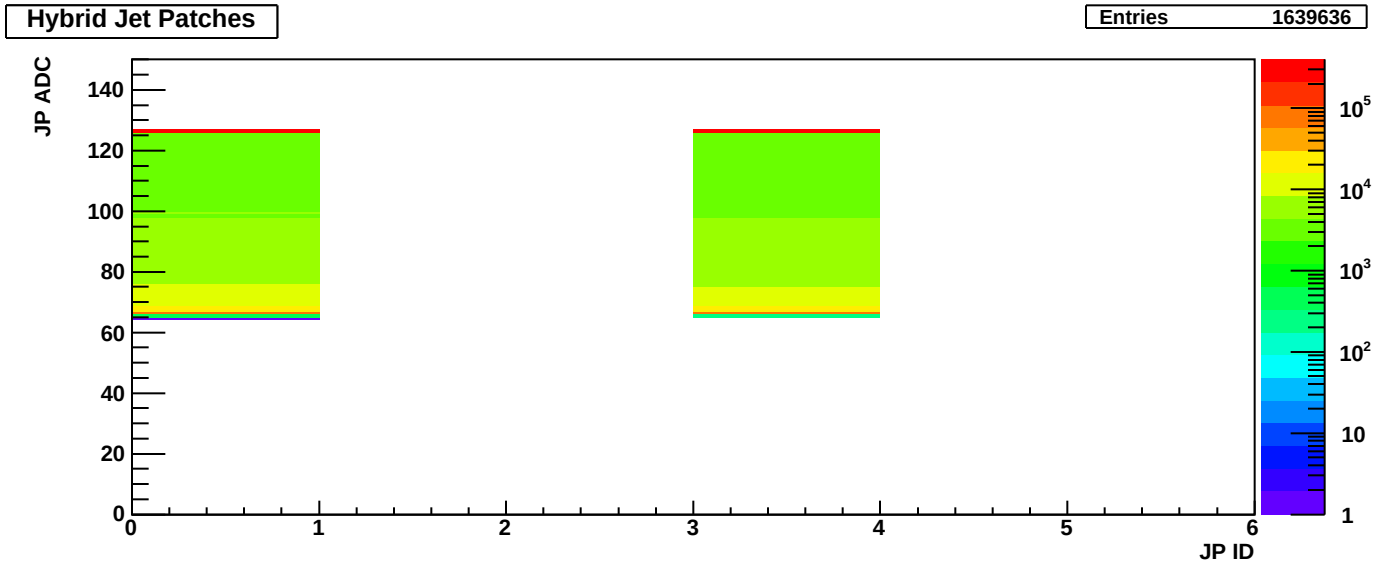

Barrel EMC L0 Input - High Tower
Entries 2.459454e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

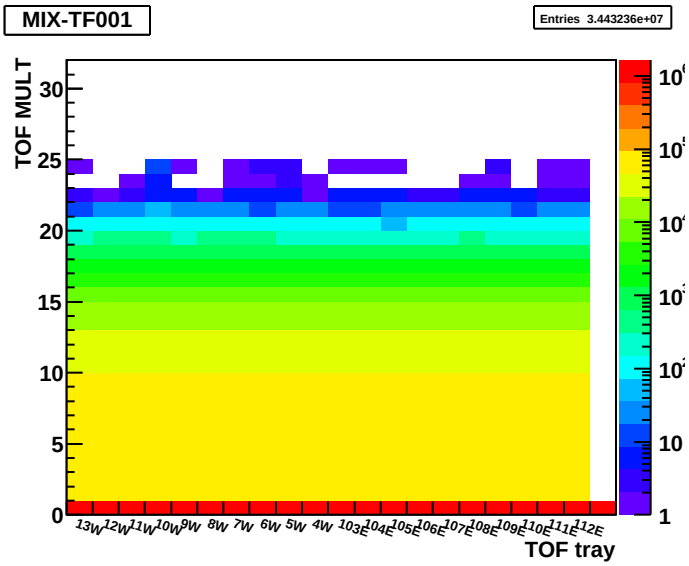

Barrel EMC L0 Input - Patch Sum
Entries 2.459454e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

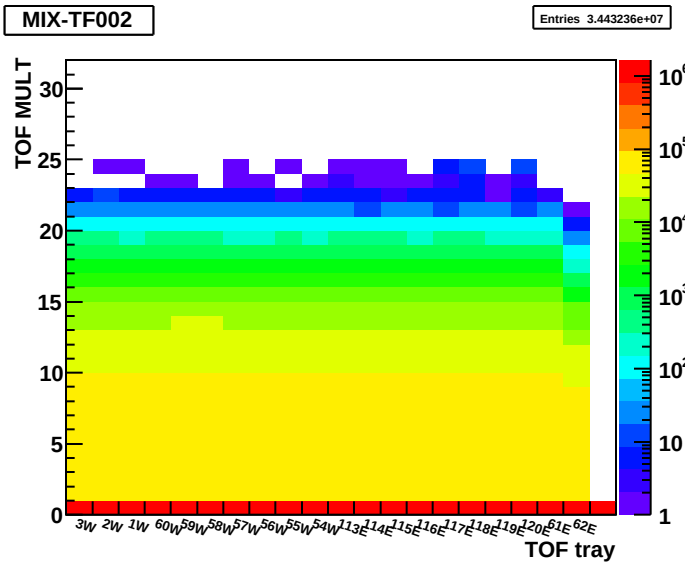

Endcap EMC L0 Input - High Tower
Entries 7.378362e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

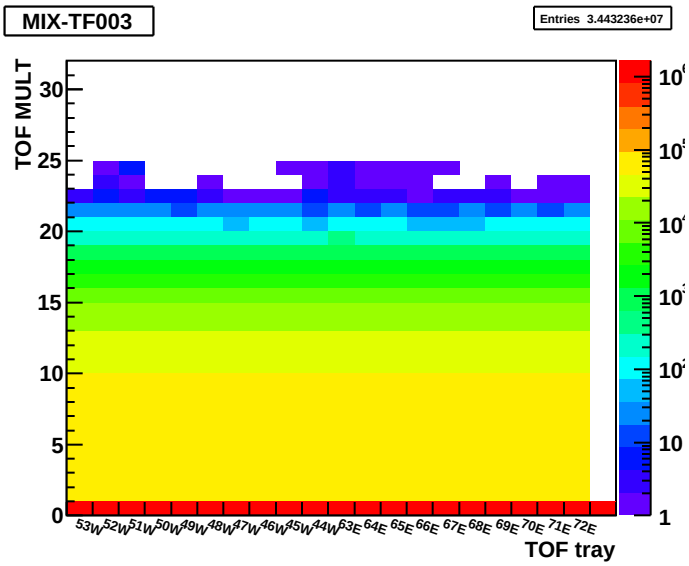

Endcap EMC L0 Input - Patch Sum
Entries 7.378362e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

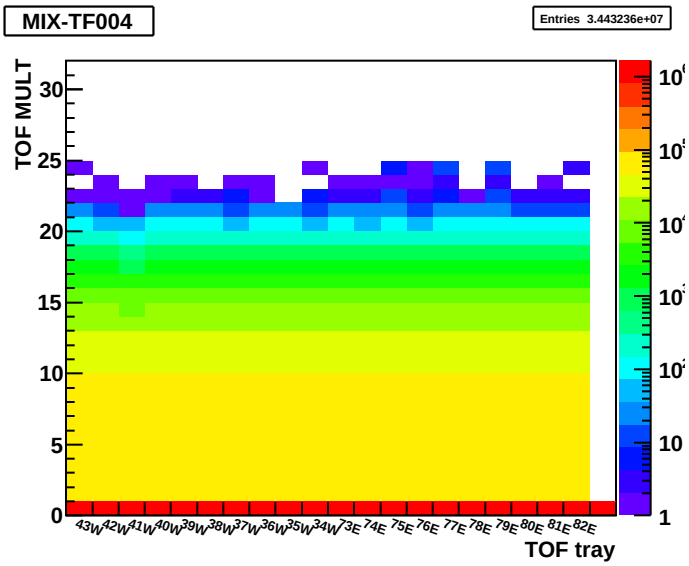

Barrel Jet Patches
Entries 1.475672e+07
JP ADC
JP ID

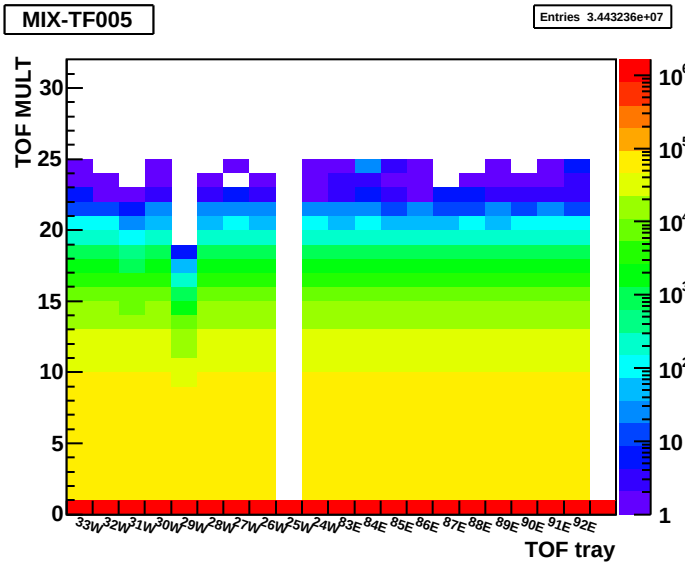

Hybrid Jet Patches
Entries 1639636
JP ADC
JP ID

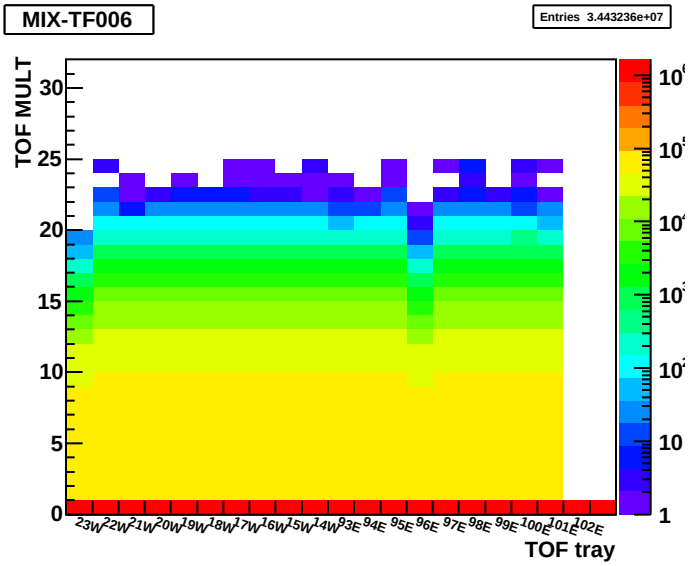

MIX-TF001
Entries 3.443236e+07
TOF MULT
TOF tray

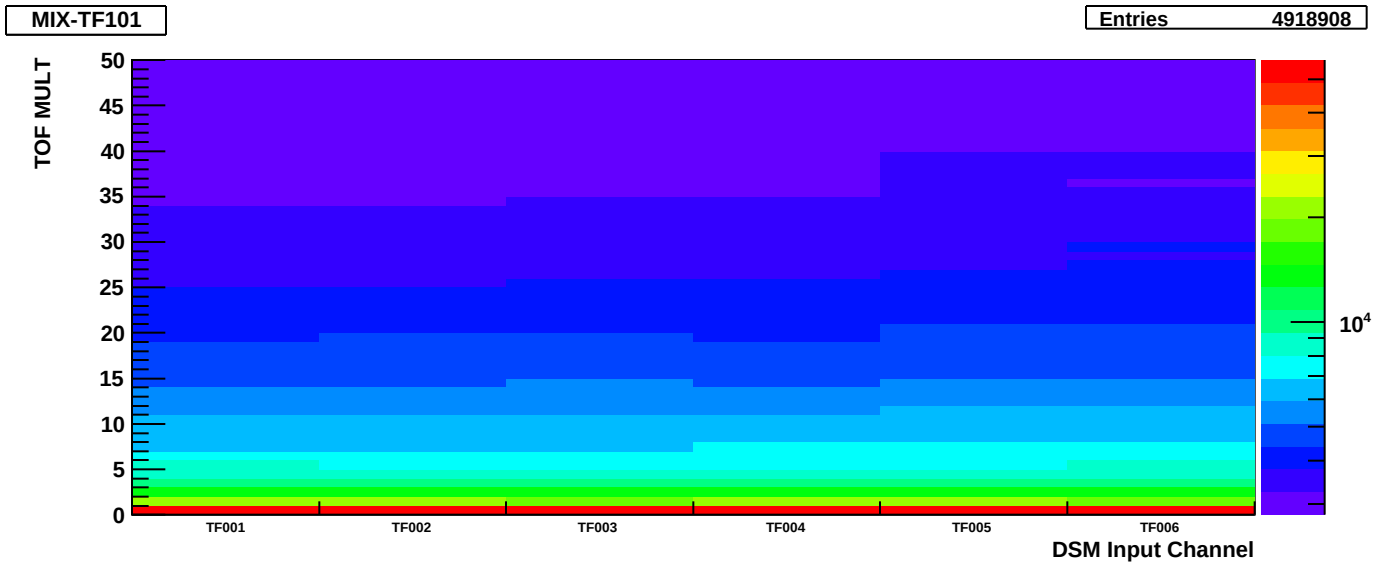

MIX-TF002
Entries 3.443236e+07
TOF MULT
TOF tray

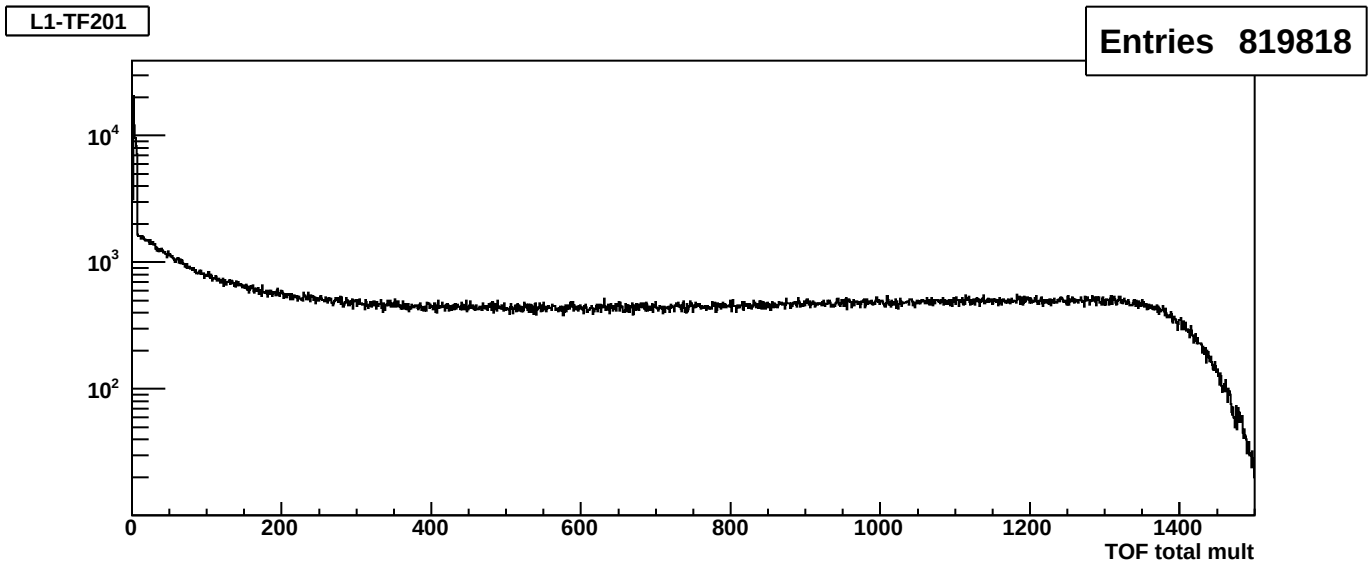

MIX-TF003
Entries 3.443236e+07
TOF MULT
TOF tray

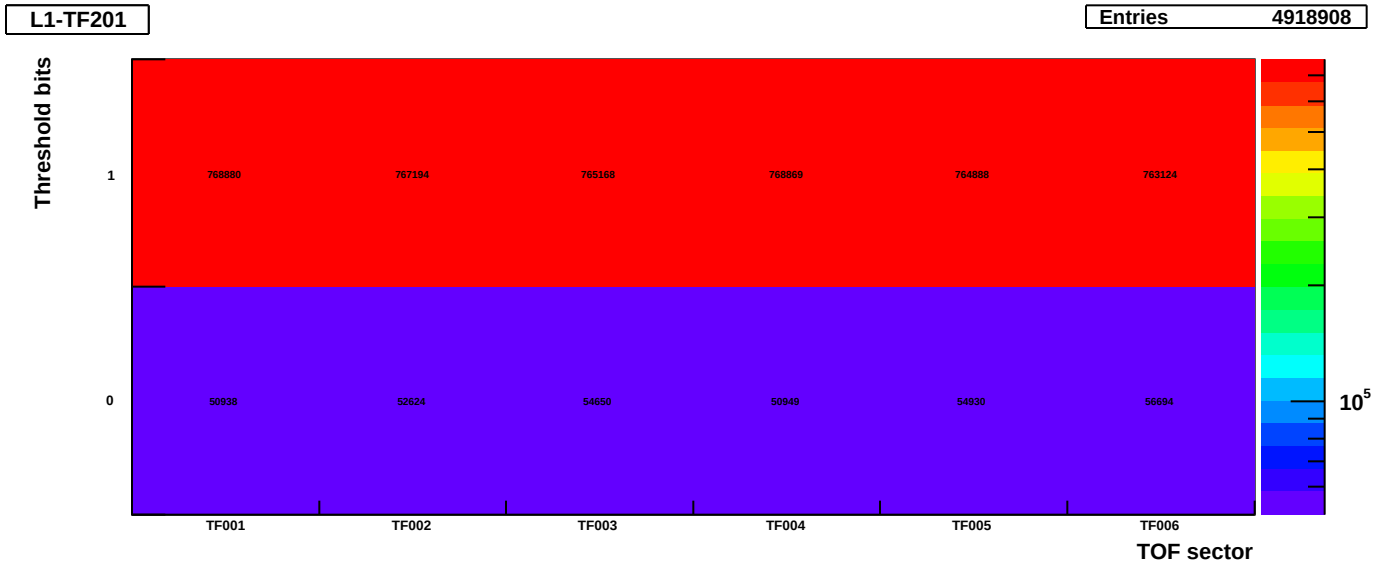

MIX-TF004
Entries 3.443236e+07
TOF MULT
TOF tray

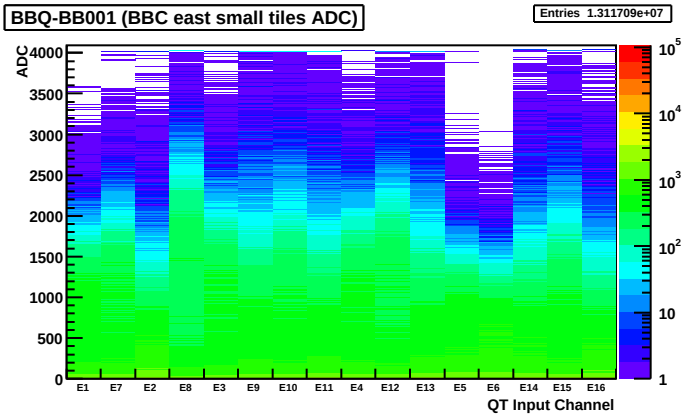

MIX-TF005
Entries 3.443236e+07
TOF MULT
TOF tray

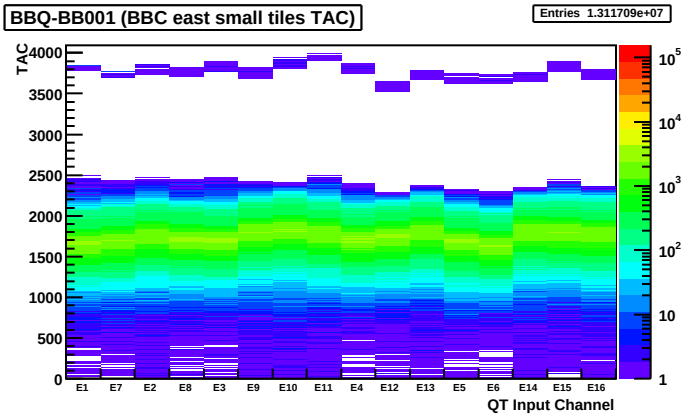

MIX-TF006
Entries 3.443236e+07
TOF MULT
TOF tray

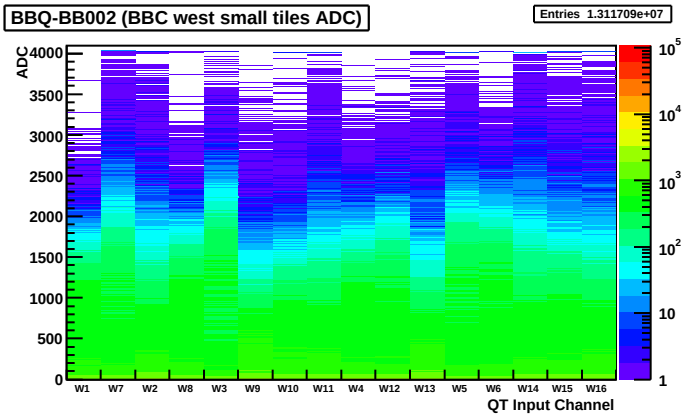

MIX-TF101
Entries 4918908
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

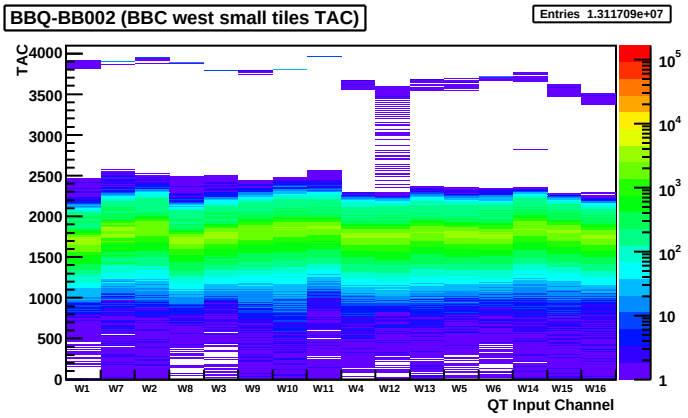

L1-TF201
Entries 819818
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

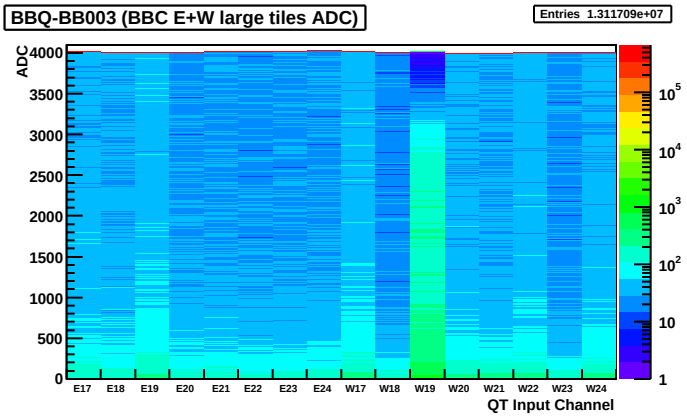

L1-TF201
Entries 4918908
Threshold bits
1
0
768880
767194
765168
768869
764888
763124
50938
52624
54650
50949
54930
56694
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

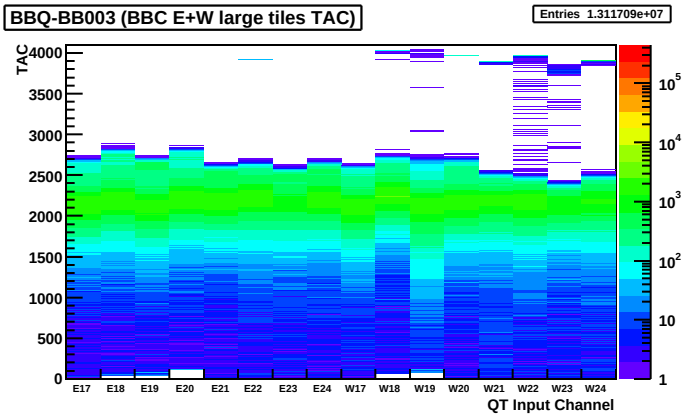

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.311709e+07
ADC
QT Input Channel

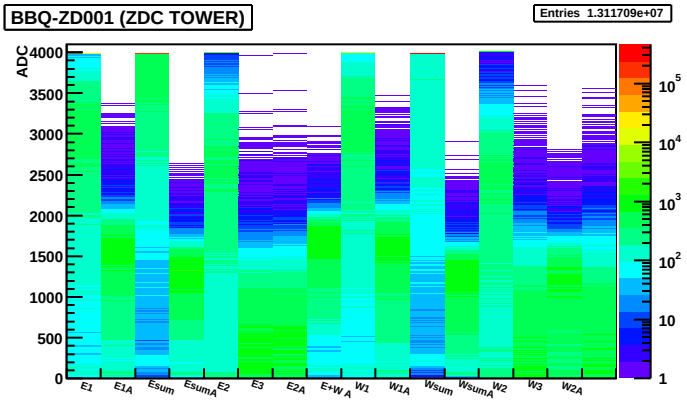

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.311709e+07
TAC
QT Input Channel

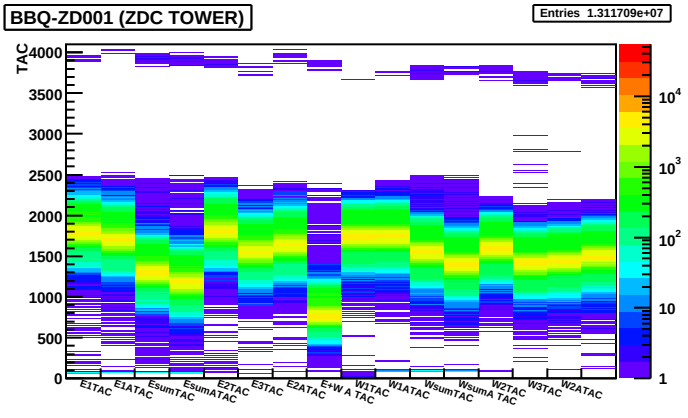

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.311709e+07
ADC
QT Input Channel

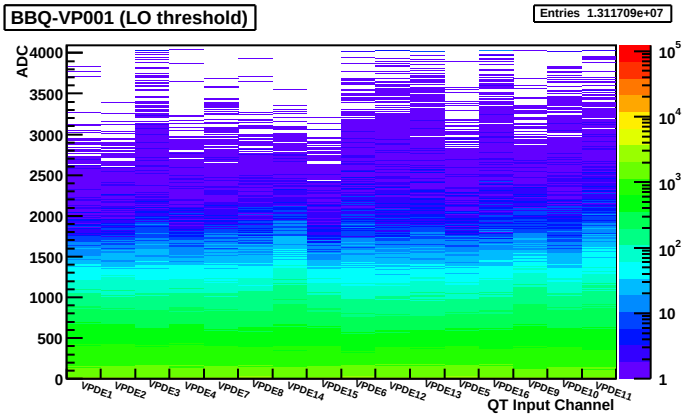

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.311709e+07
TAC
QT Input Channel

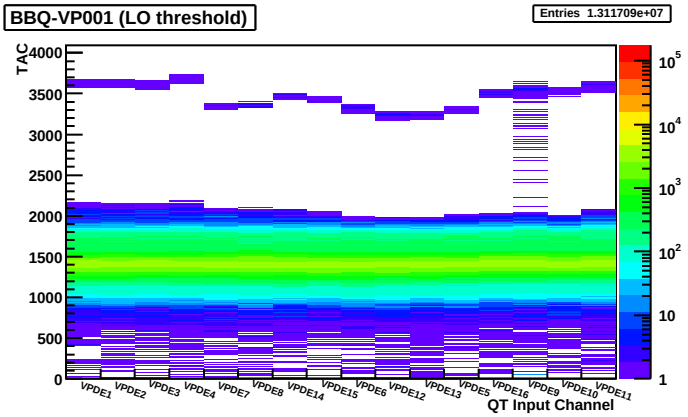

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.311709e+07
ADC
QT Input Channel

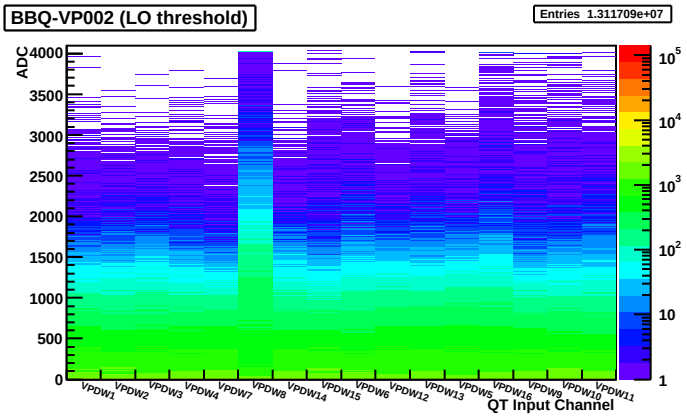

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.311709e+07
TAC
QT Input Channel

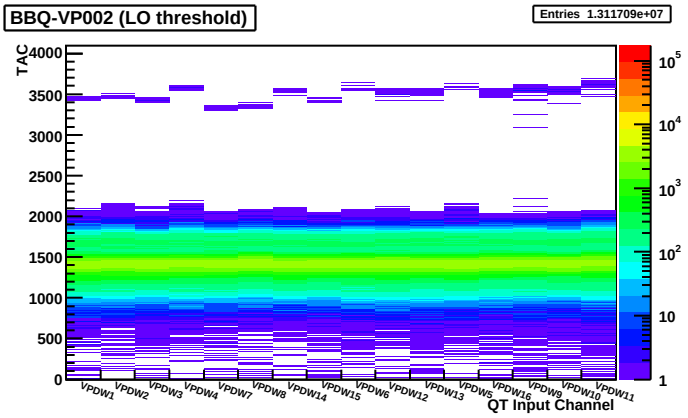

BBQ-ZD001 (ZDC TOWER)
Entries 1.311709e+07
ADC

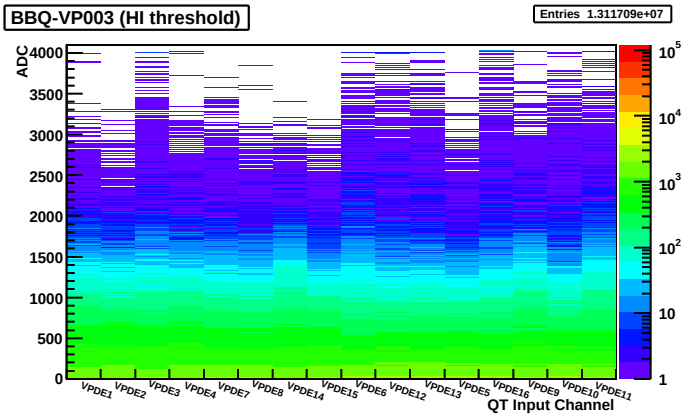

BBQ-ZD001 (ZDC TOWER)
Entries 1.311709e+07
TAC

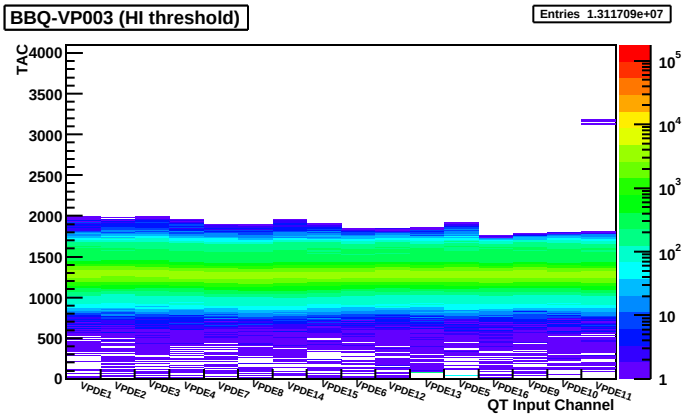

BBQ-VP001 (LO threshold)
Entries 1.311709e+07
ADC
QT Input Channel

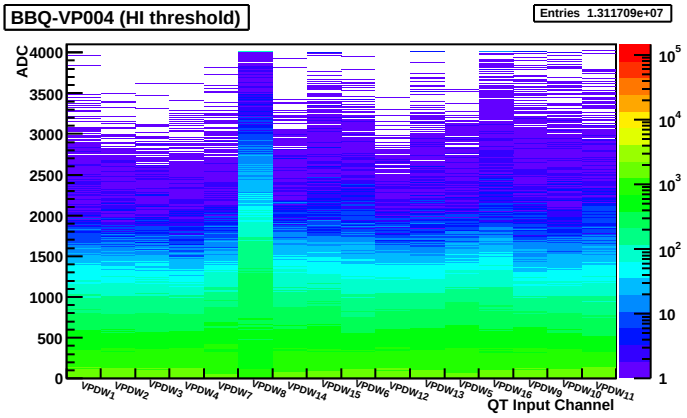

BBQ-VP001 (LO threshold)
Entries 1.311709e+07
TAC
QT Input Channel

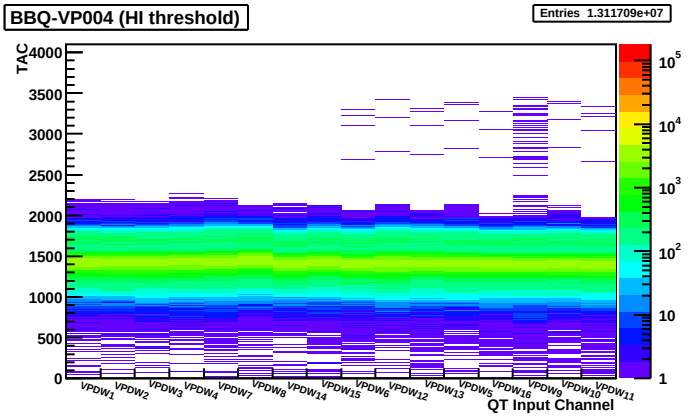

BBQ-VP002 (LO threshold)
Entries 1.311709e+07
ADC
QT Input Channel

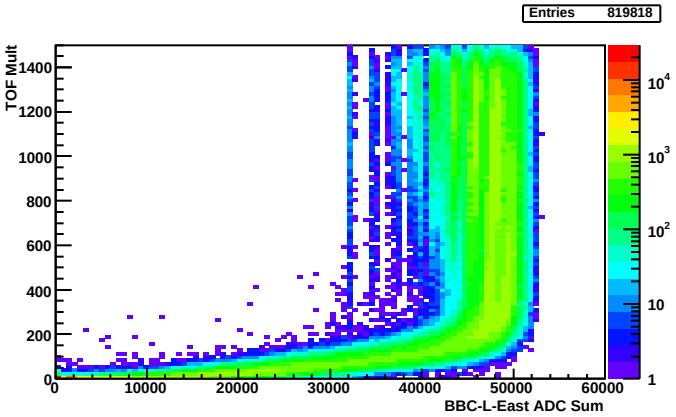

BBQ-VP002 (LO threshold)
Entries 1.311709e+07
TAC
QT Input Channel

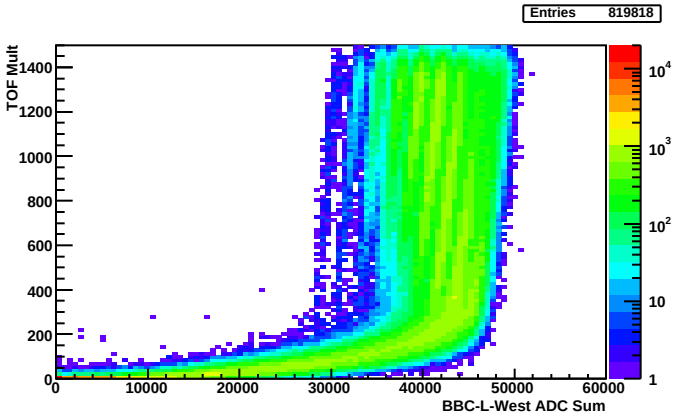

BBQ-VP003 (HI threshold)
Entries 1.311709e+07
ADC
QT Input Channel

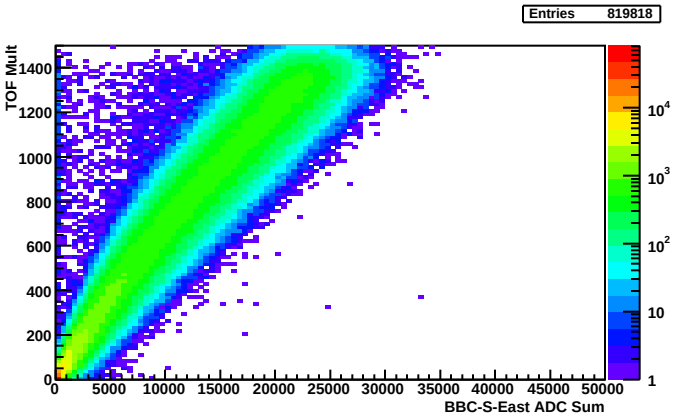

BBQ-VP003 (HI threshold)
Entries 1.311709e+07
TAC
QT Input Channel

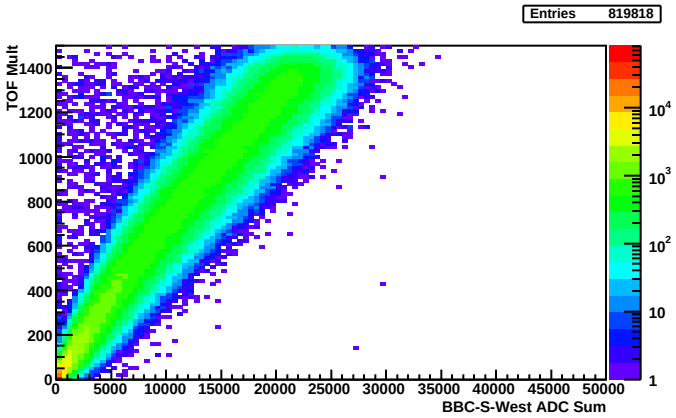

BBQ-VP004 (HI threshold)
Entries 1.311709e+07
ADC
QT Input Channel

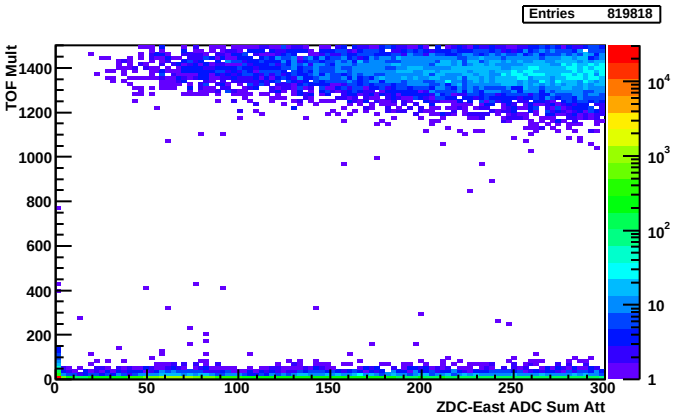

BBQ-VP004 (HI threshold)
Entries 1.311709e+07
TAC
QT Input Channel

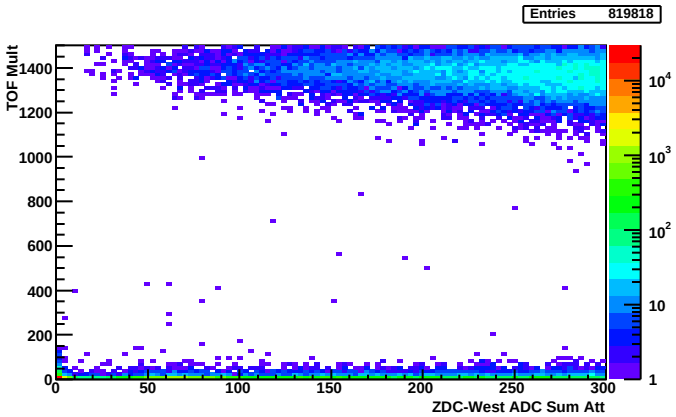

Entries 819818
TOF Mult
BBC-L-East ADC Sum

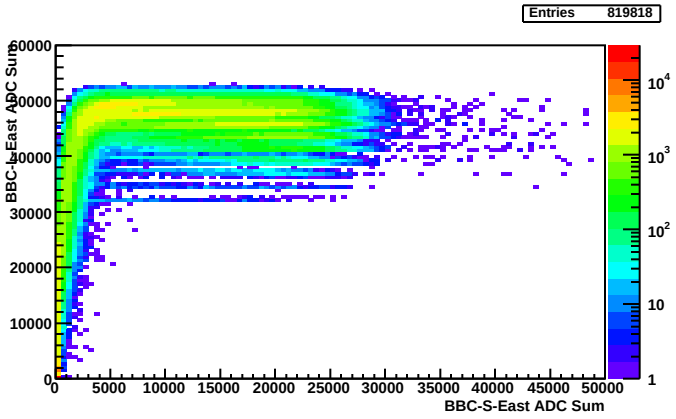

Entries 819818
TOF Mult
BBC-L-West ADC Sum

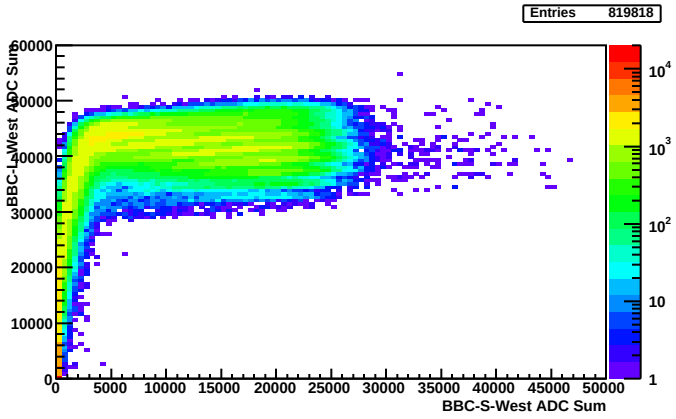

Entries 819818
TOF Mult
BBC-S-East ADC Sum

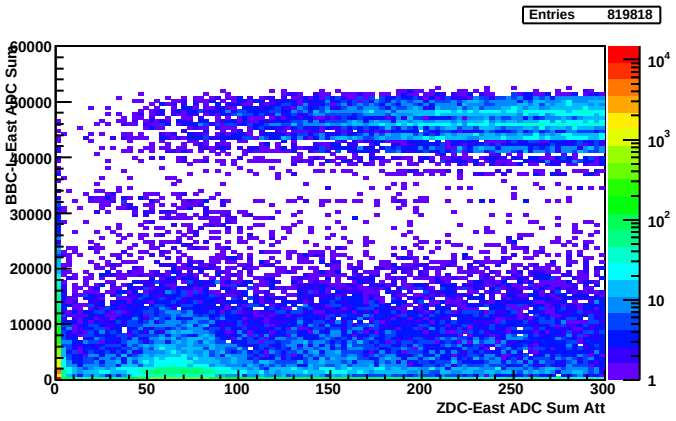

Entries 819818
TOF Mult
BBC-S-West ADC Sum

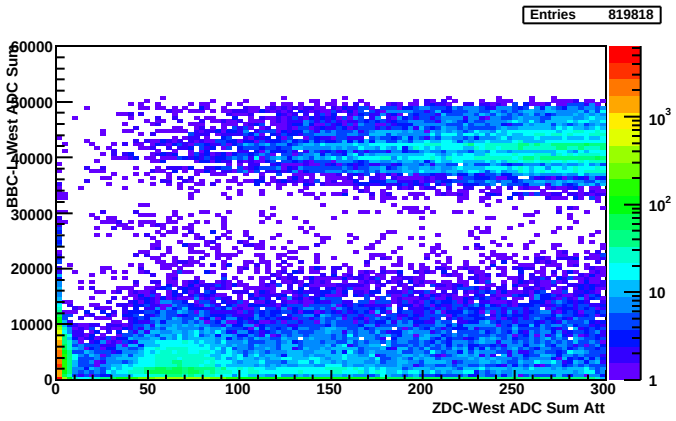

Entries 819818
TOF Mult
ZDC-East ADC Sum Att

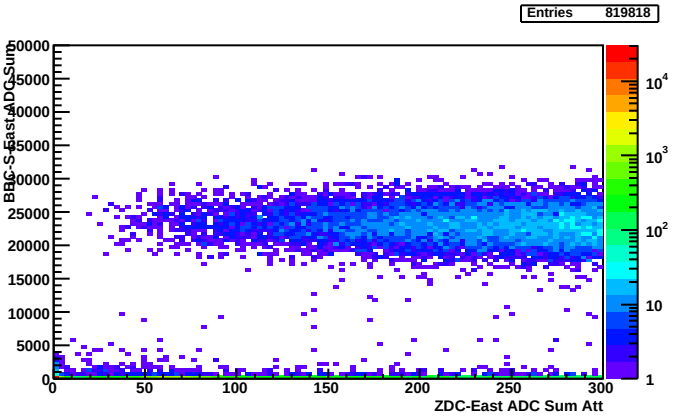

Entries 819818
TOF Mult
ZDC-West ADC Sum Att

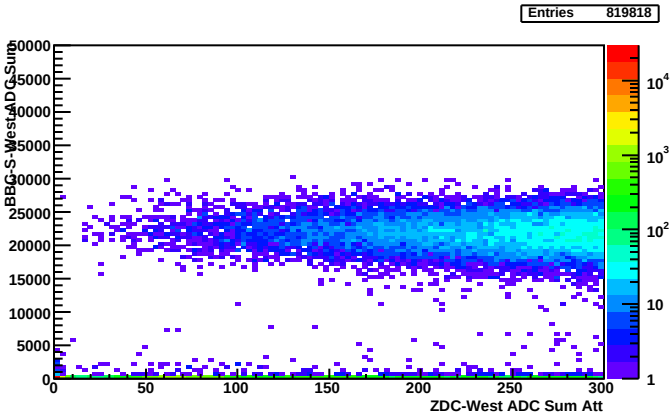

Entries 819818
BBC-L-East ADC Sum
BBC-S-East ADC Sum

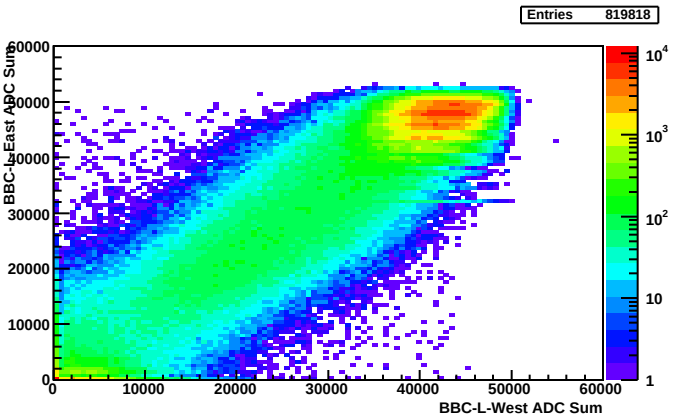

Entries 819818
BBC-L-West ADC Sum
BBC-S-West ADC Sum

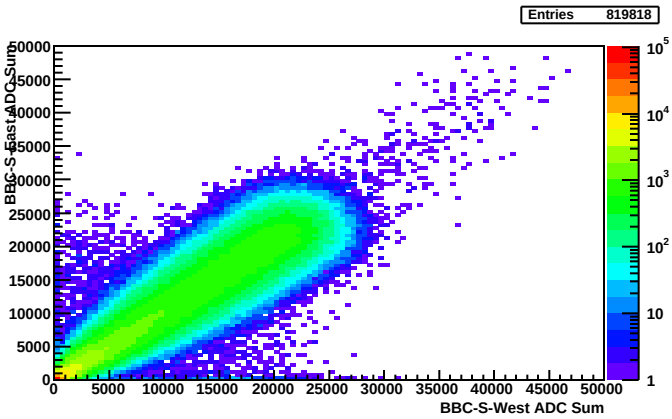

Entries 819818
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

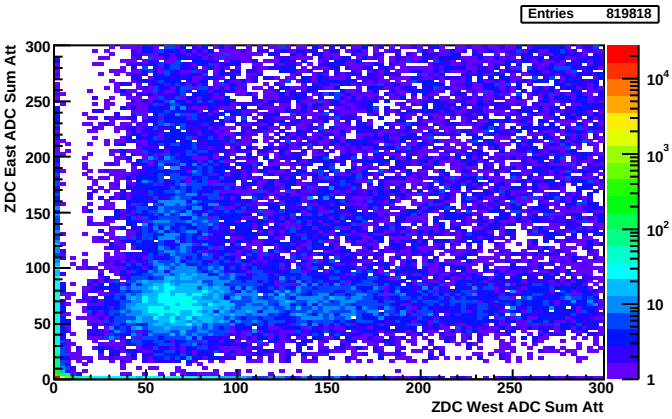

Entries 819818
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

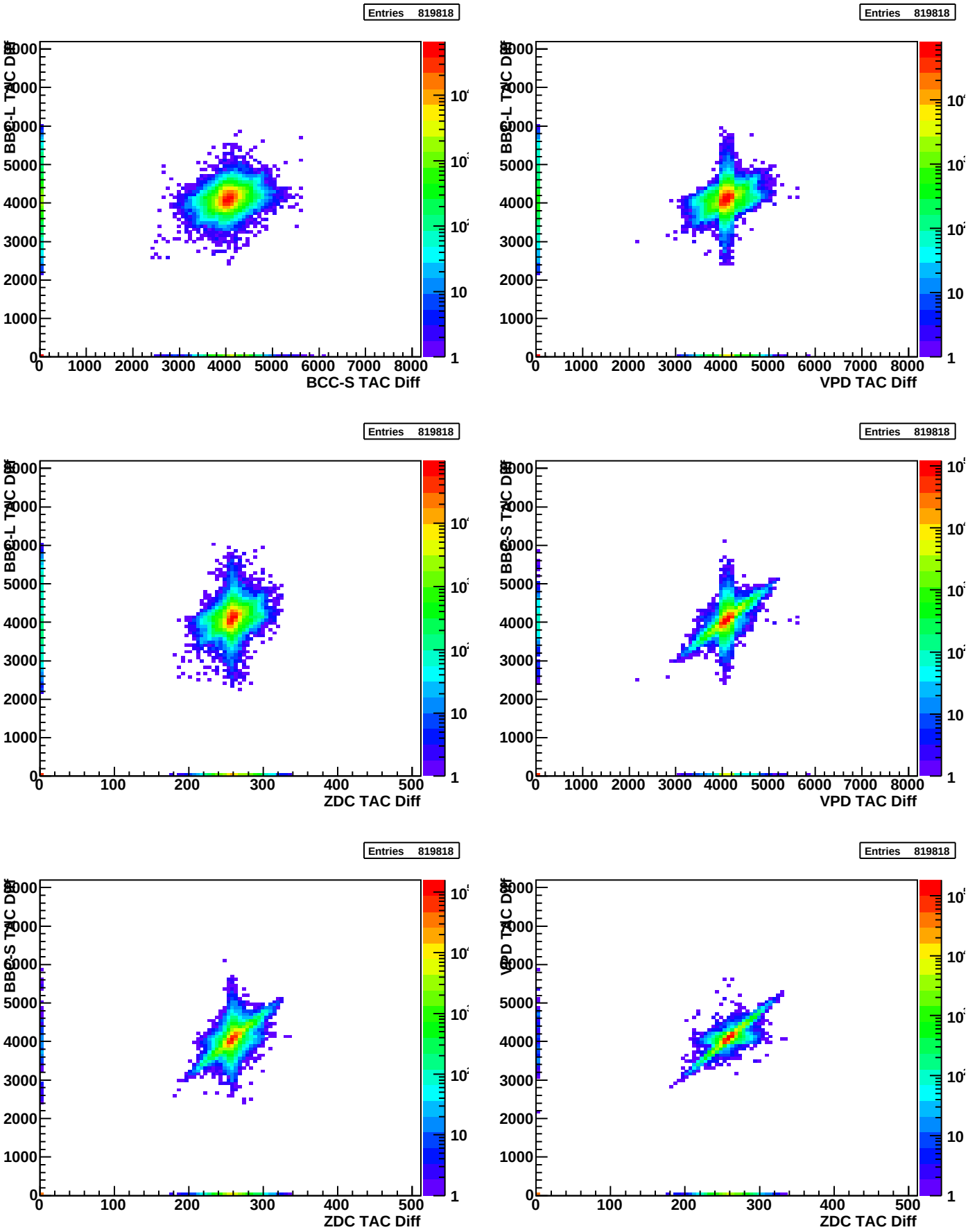

Entries 819818
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

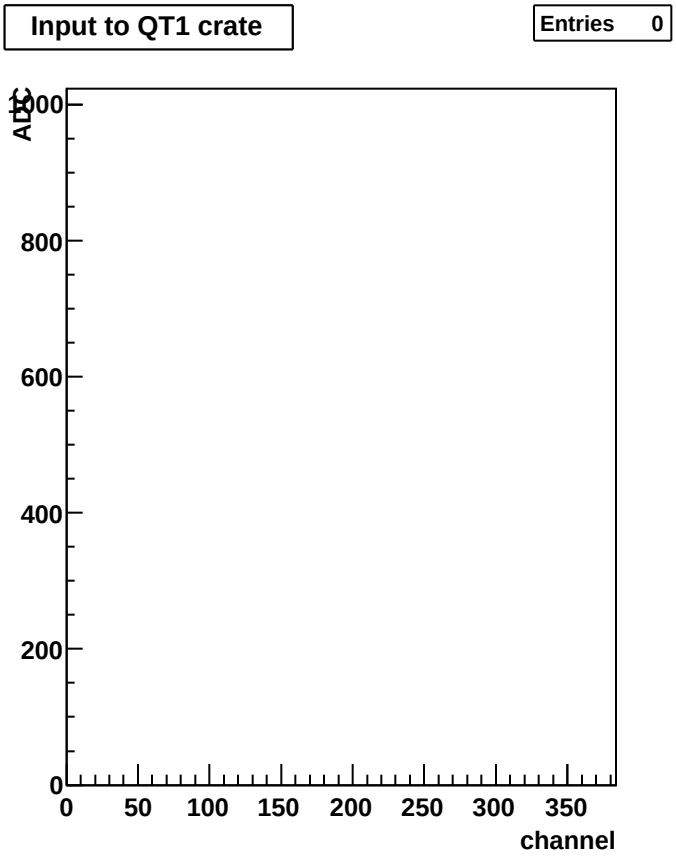

Entries 819818
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

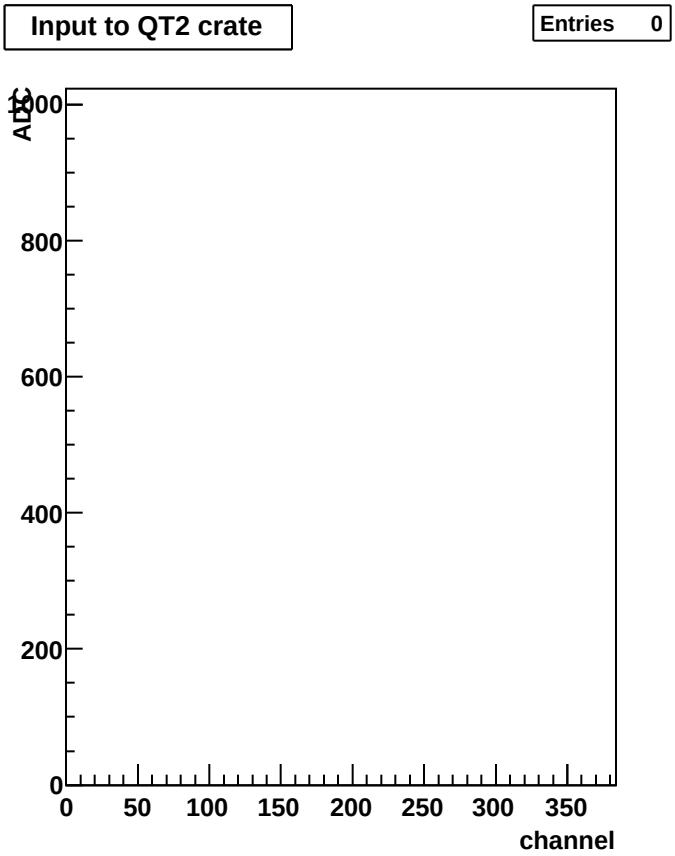

Entries 819818
BBC-L-East ADC Sum
BBC-L-West ADC Sum

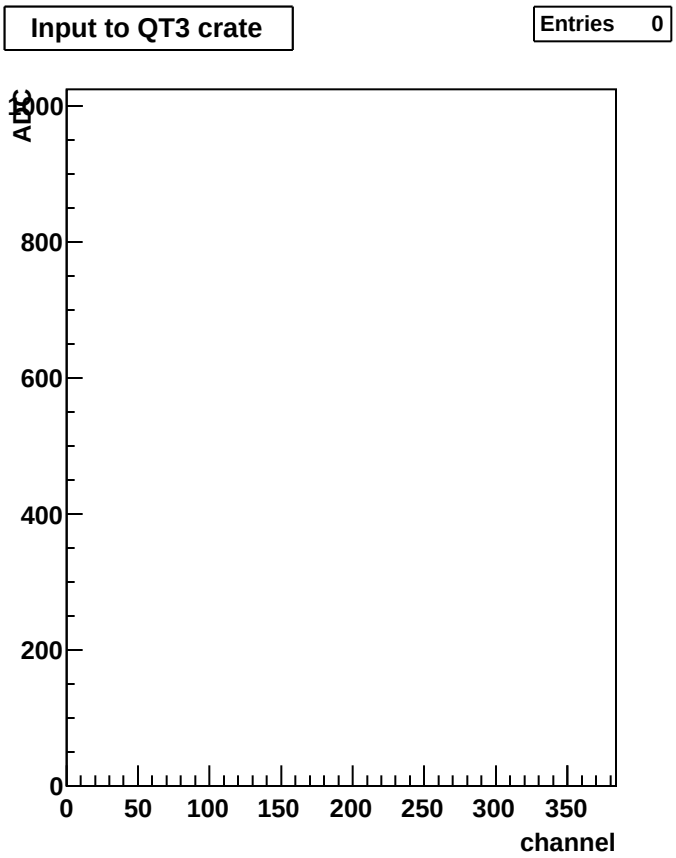

Entries 819818
BBC-S-East ADC Sum
BBC-S-West ADC Sum

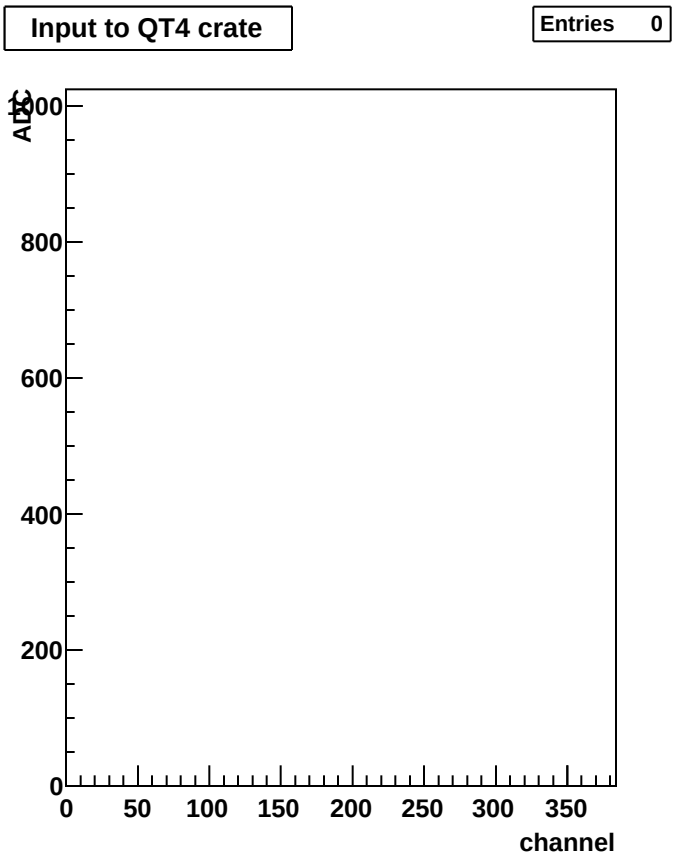

Entries 819818
ZDC East ADC Sum Att
ZDC West ADC Sum Att

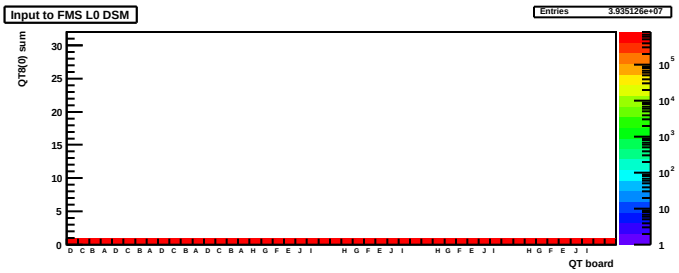

Entries 819818
BBC-L TAC Diff
BCC-S TAC Diff
Entries 819818
BBC-L TAC Diff
VPD TAC Diff
Entries 819818
BBC-L TAC Diff
ZDC TAC Diff
Entries 819818
BBC-S TAC Diff
VPD TAC Diff
Entries 819818
BBC-S TAC Diff
ZDC TAC Diff
Entries 819818
VPD TAC Diff
ZDC TAC Diff

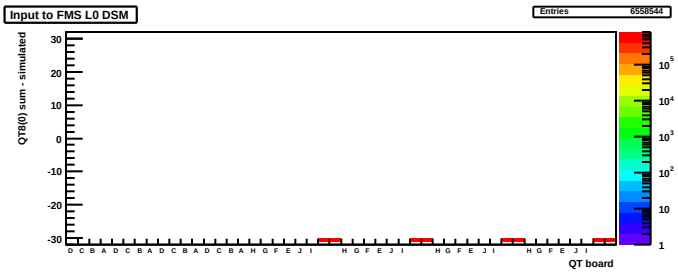

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

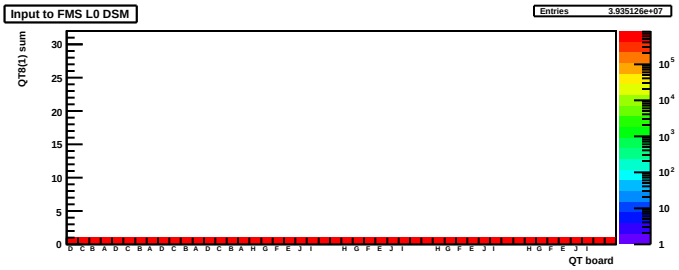

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

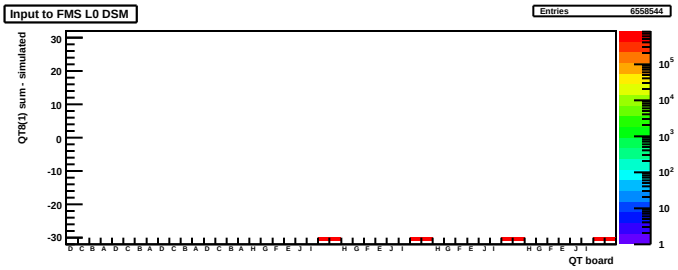

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

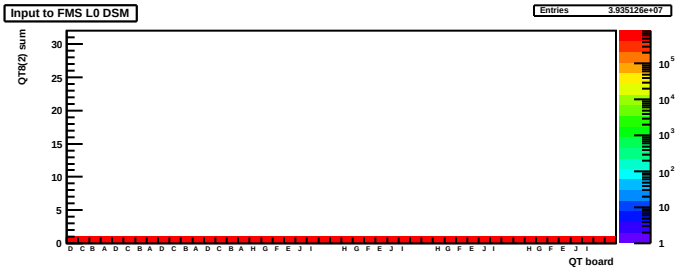

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

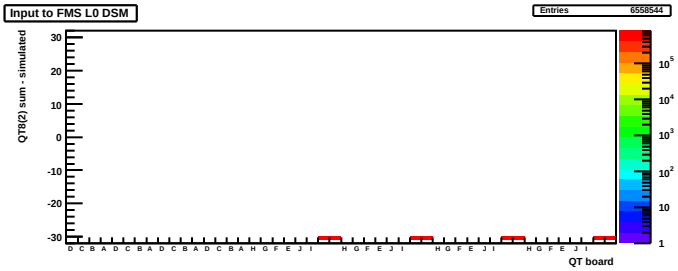

Input to FMS L0 DSM
Entries 3.935126e+07
QT8(0) sum
QT board

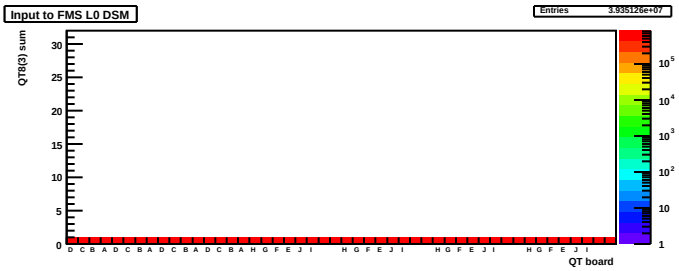

Input to FMS L0 DSM
Entries 6558544
QT8(0) sum - simulated
QT board

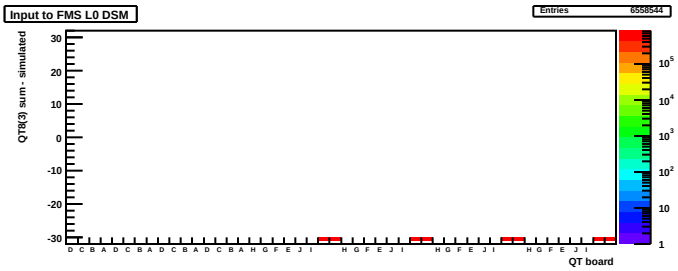

Input to FMS L0 DSM
Entries 3.935126e+07
QT8(1) sum
QT board

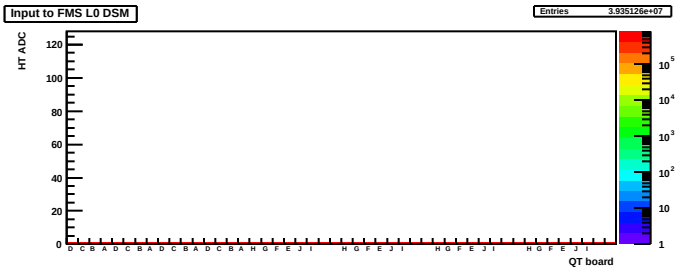

Input to FMS L0 DSM
Entries 6558544
QT8(1) sum - simulated
QT board

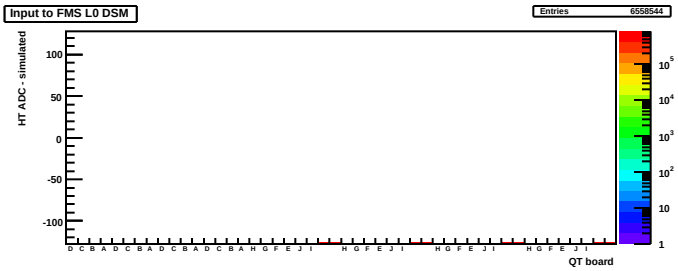

Input to FMS L0 DSM
Entries 3.935126e+07
QT8(2) sum
QT board

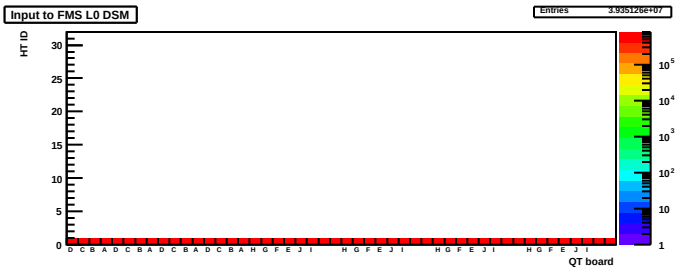

Input to FMS L0 DSM
Entries 6558544
QT8(2) sum - simulated
QT board

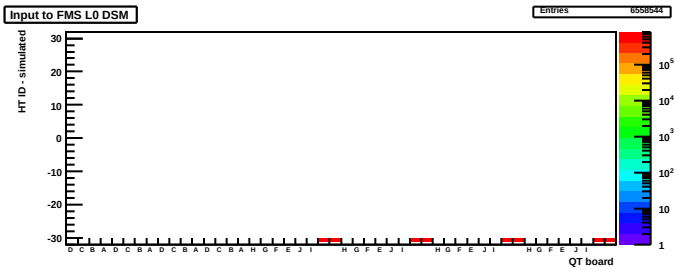

Input to FMS L0 DSM
Entries 3.935126e+07
QT8(3) sum
QT board

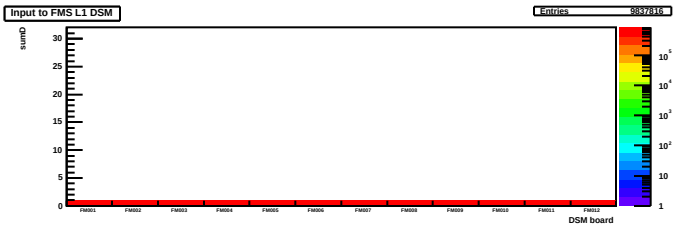

Input to FMS L0 DSM
Entries 6558544
QT8(3) sum - simulated
QT board

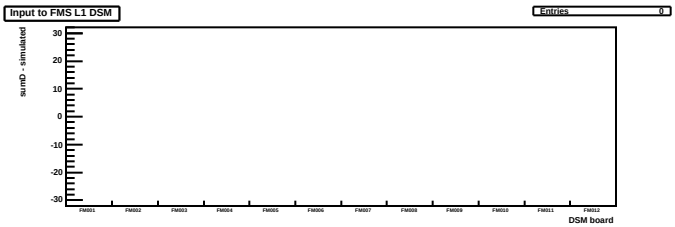

Input to FMS L0 DSM
Entries 3.935126e+07
HT ADC
QT board

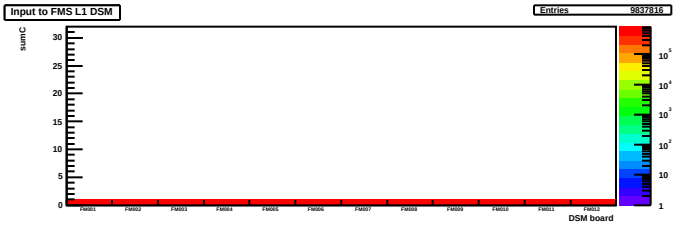

Input to FMS L0 DSM
Entries 6558544
HT ADC - simulated
QT board

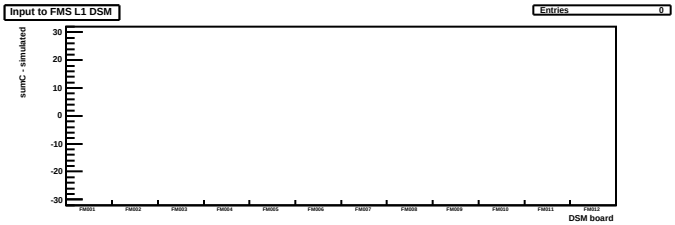

Input to FMS L0 DSM
Entries 3.935126e+07
HT ID
QT board

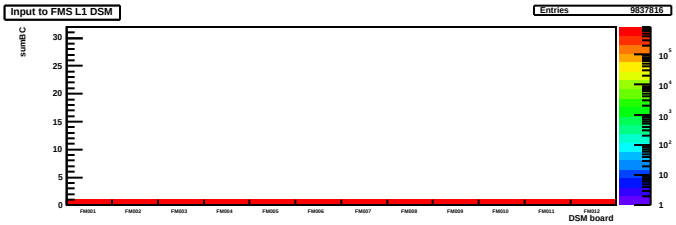

Input to FMS L0 DSM
Entries 6558544
HT ID - simulated
QT board

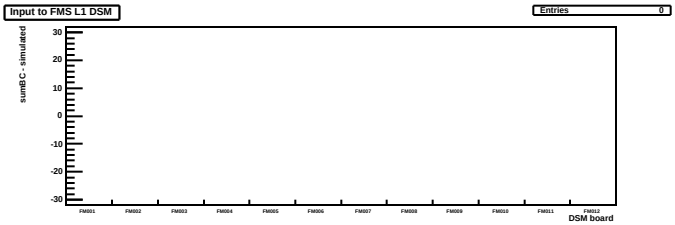

Input to FMS L1 DSM
Entries 9837816
sumD
DSM board

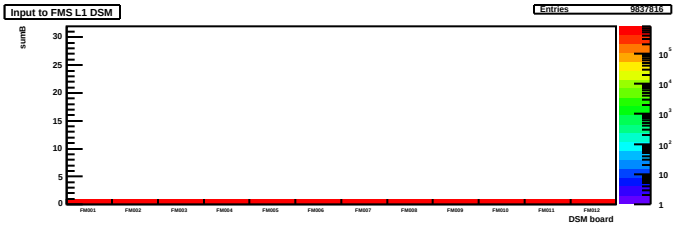

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

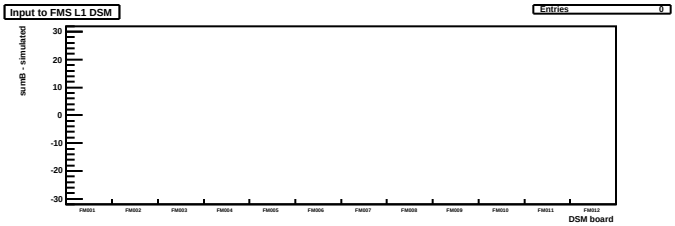

Input to FMS L1 DSM
Entries 9837816
sumC
DSM board

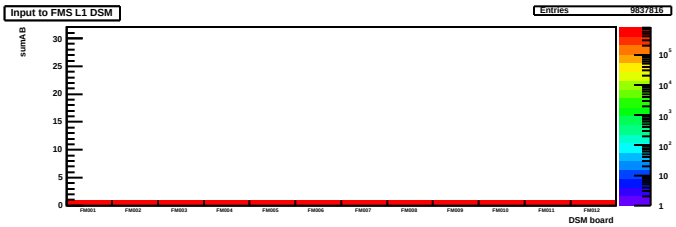

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

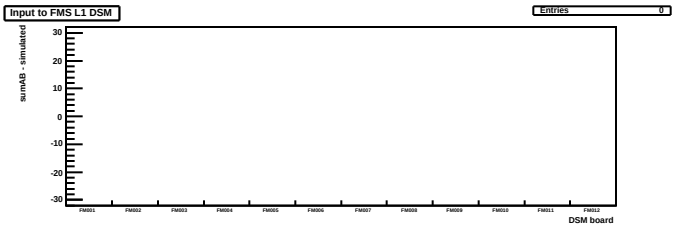

Input to FMS L1 DSM
Entries 9837816
sumBC
DSM board

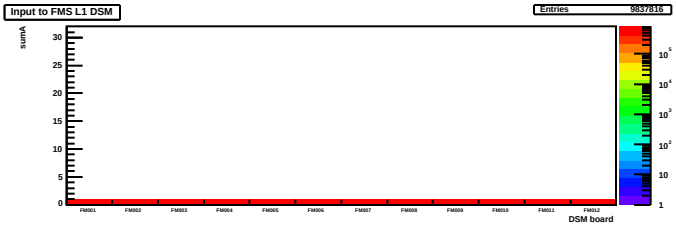

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

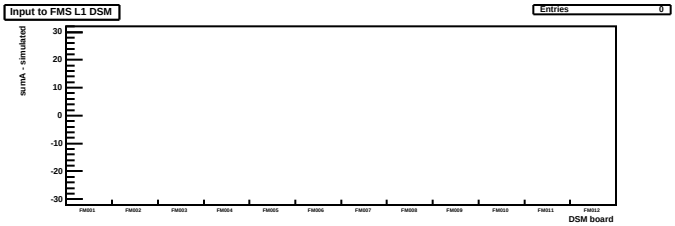

Input to FMS L1 DSM
Entries 9837816
sumB
DSM board

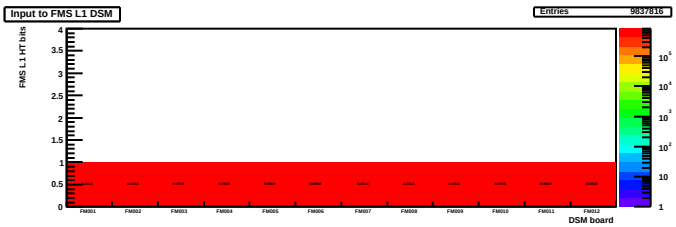

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

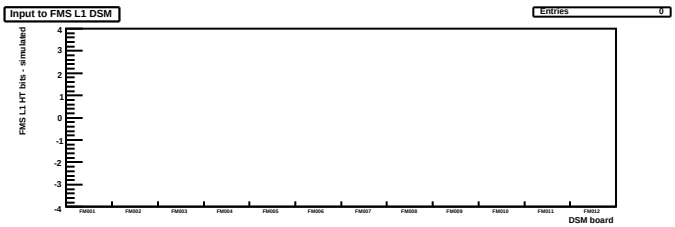

Input to FMS L1 DSM
Entries 9837816
sumAB
DSM board

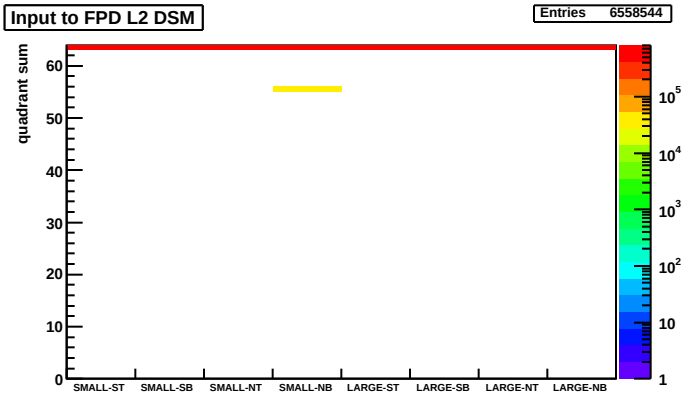

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

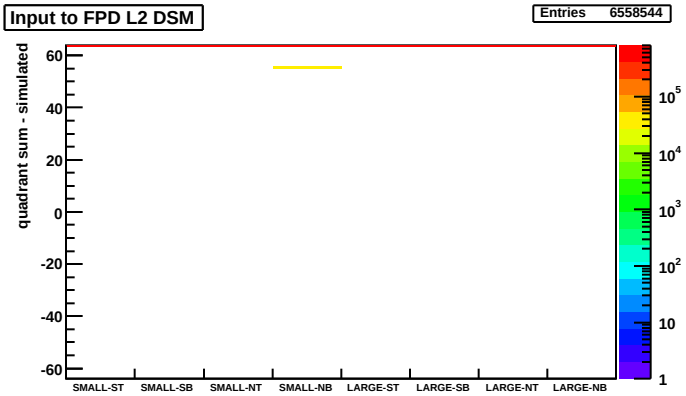

Input to FMS L1 DSM
Entries 9837816
sumA
DSM board

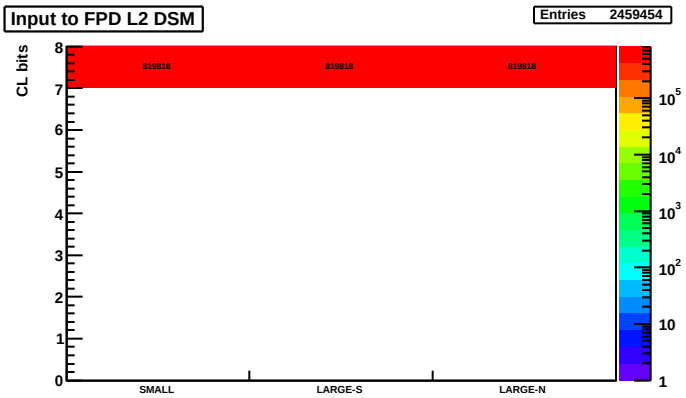

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

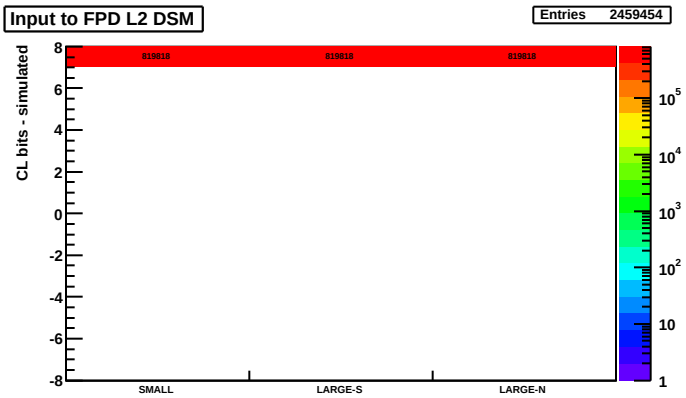

Input to FMS L1 DSM
Entries 9837816
FMS L1 HT bits
DSM board

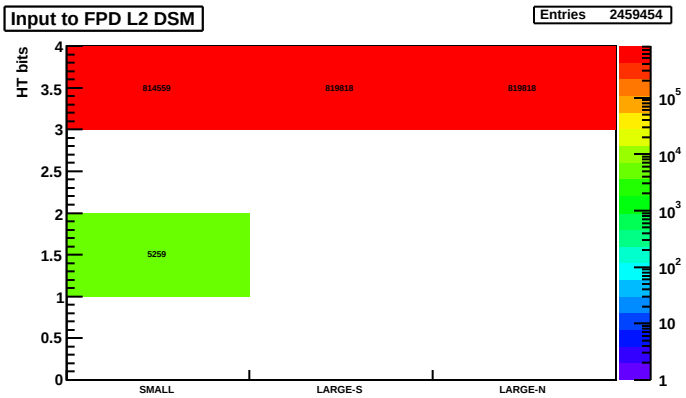

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

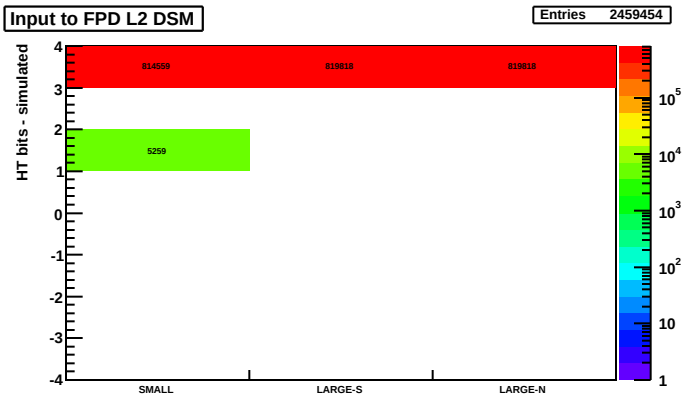

Input to FPD L2 DSM
Entries 6558544
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

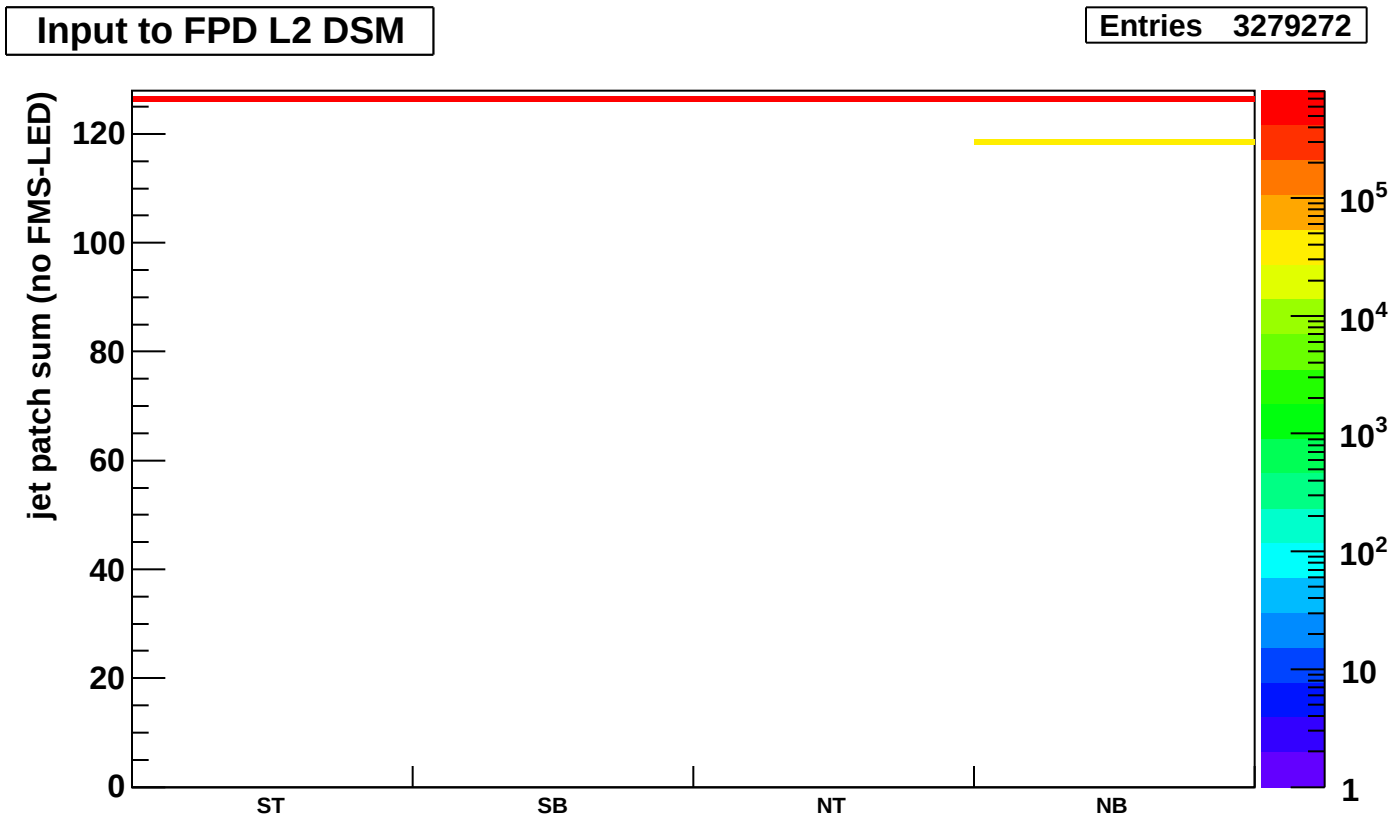

Input to FPD L2 DSM
Entries 6558544
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

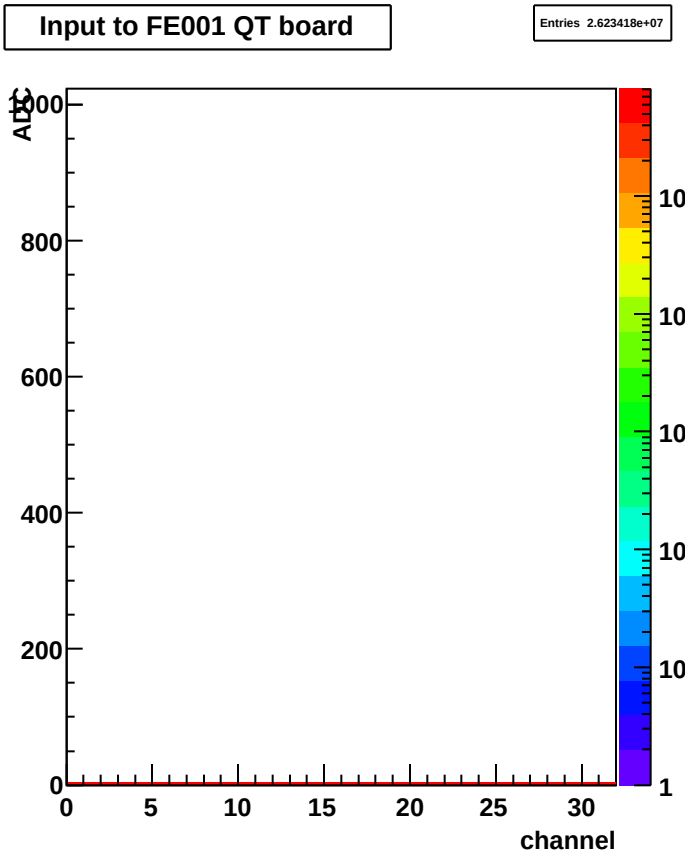

Input to FPD L2 DSM
Entries 2459454
CL bits
819818
819818
819818
SMALL
LARGE-S
LARGE-N

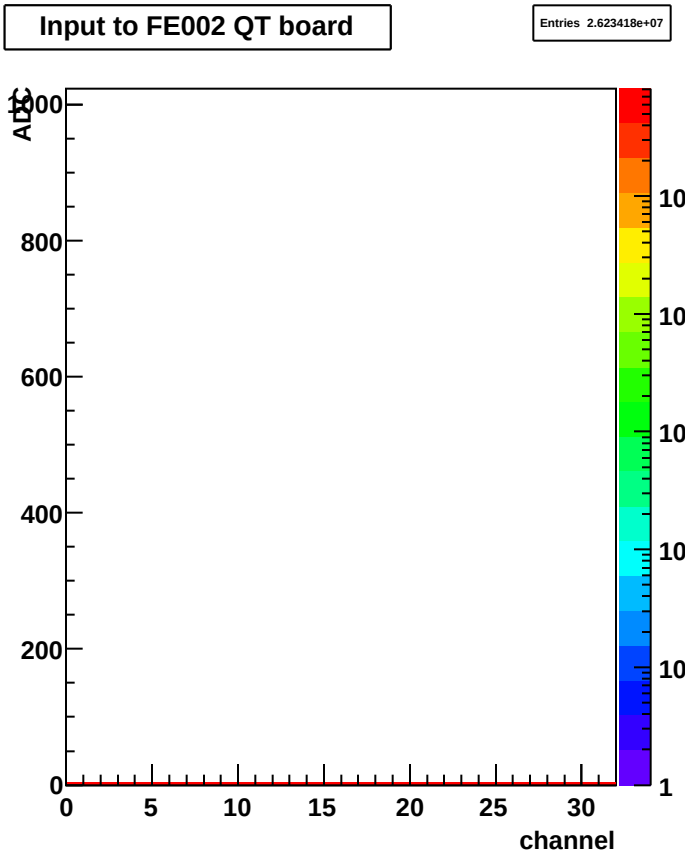

Input to FPD L2 DSM
Entries 2459454
CL bits - simulated
819818
819818
819818
SMALL
LARGE-S
LARGE-N

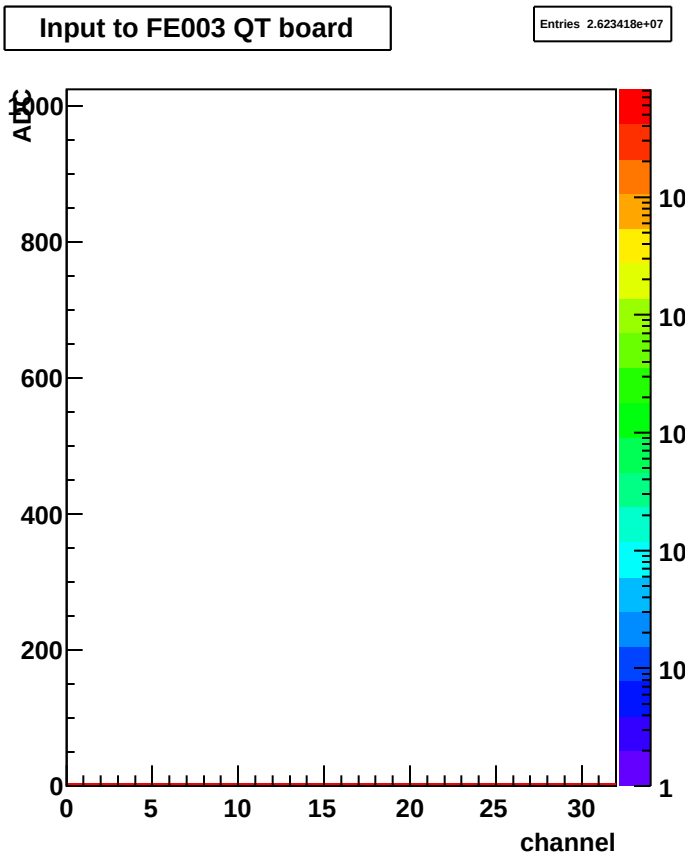

Input to FPD L2 DSM
Entries 2459454
HT bits
814559
819818
819818
5259
SMALL
LARGE-S
LARGE-N

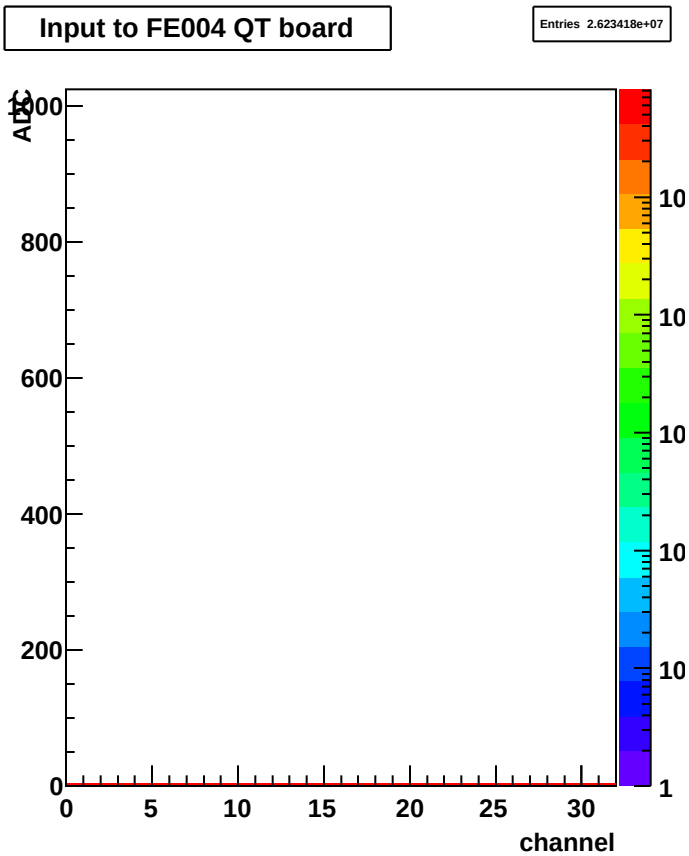

Input to FPD L2 DSM
Entries 2459454
HT bits - simulated
814559
819818
819818
5259
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3279272
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

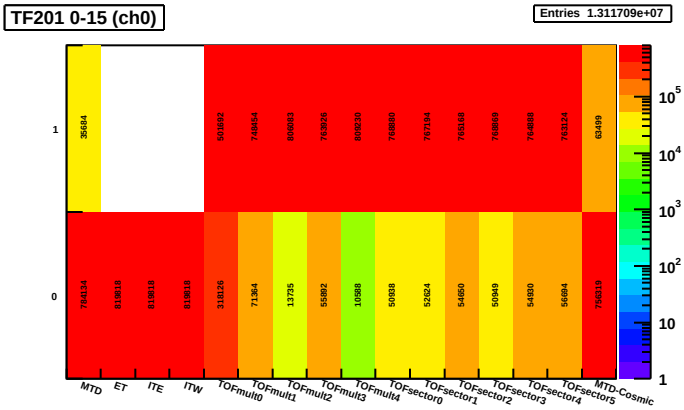

Input to FE001 QT board
Entries 2.623418e+07
ADC
channel


Input to FE002 QT board
Entries 2.623418e+07
ADC
channel

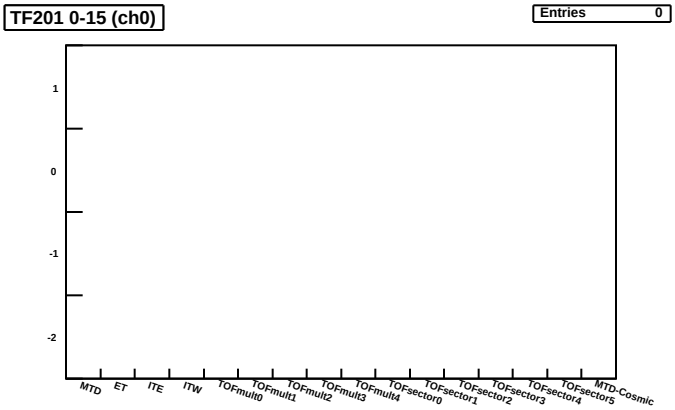

Input to FE003 QT board
Entries 2.623418e+07
ADC
channel


Input to FE004 QT board
Entries 2.623418e+07
ADC
channel

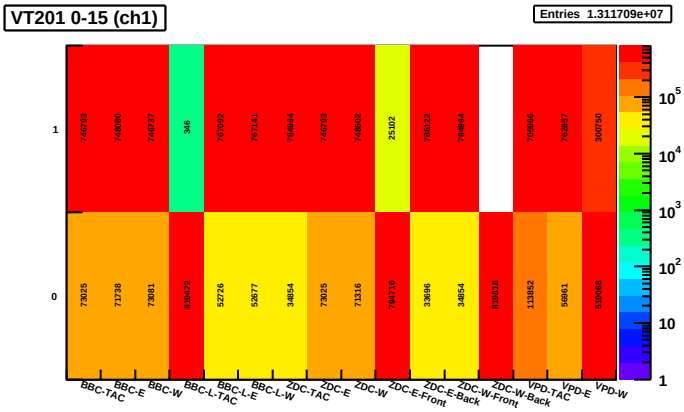
TF201 0-15 (ch0)



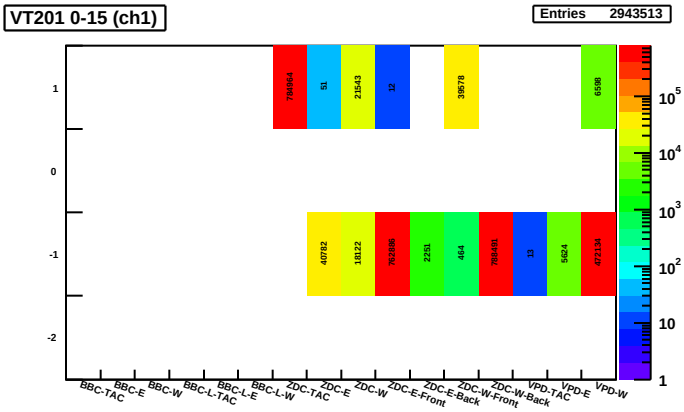
TF201 0-15 (ch0)



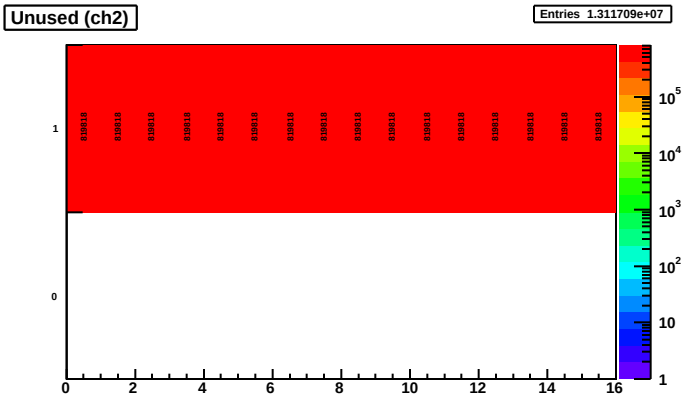
VT201 0-15 (ch1)

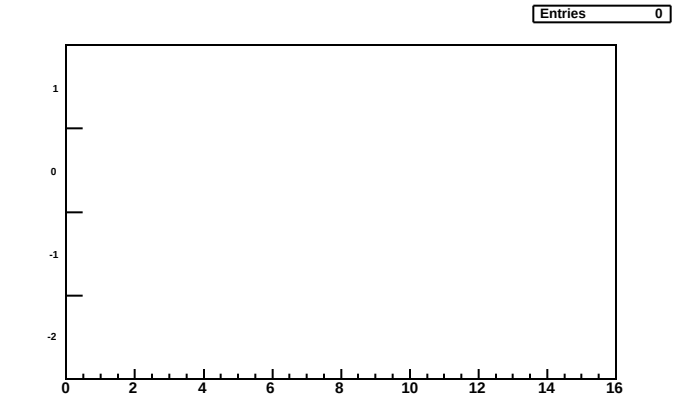


VT201 0-15 (ch1)

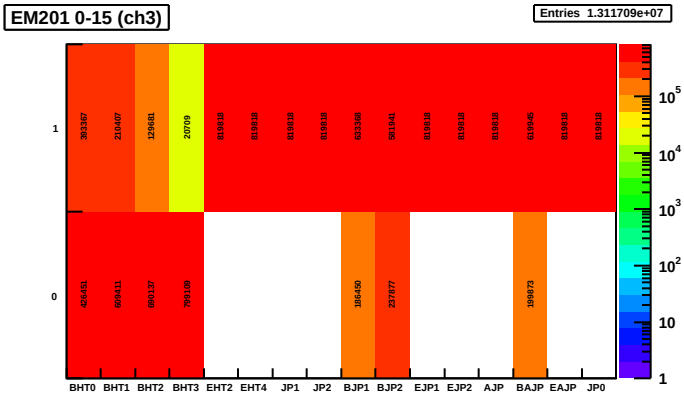


Unused (ch2)

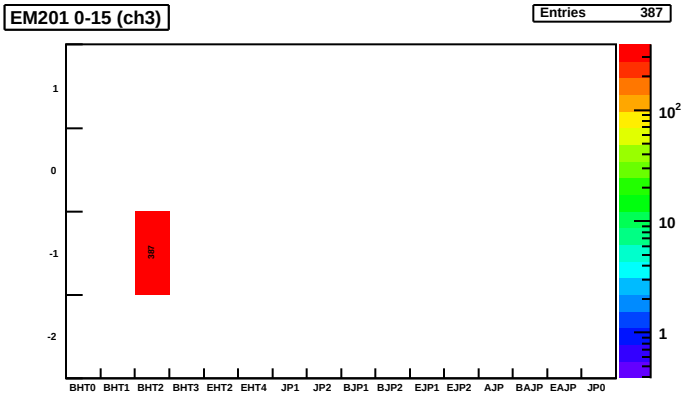




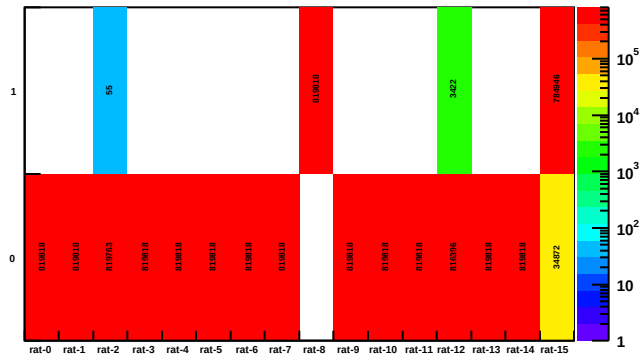
EM201 0-15 (ch3)

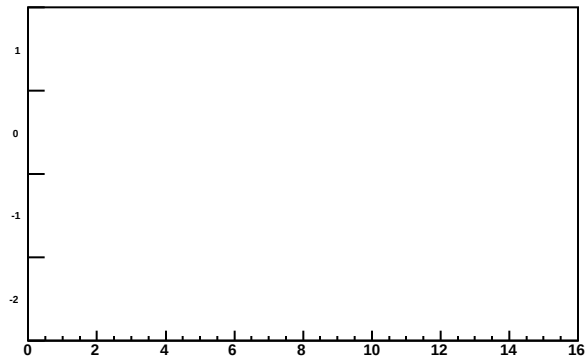


EM201 0-15 (ch3)

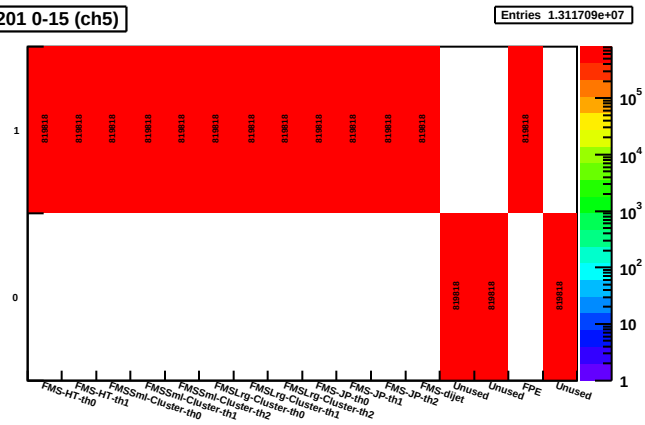


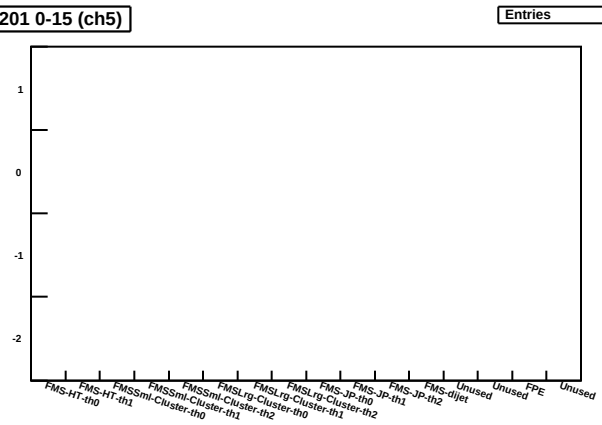
RAT board (ch4)



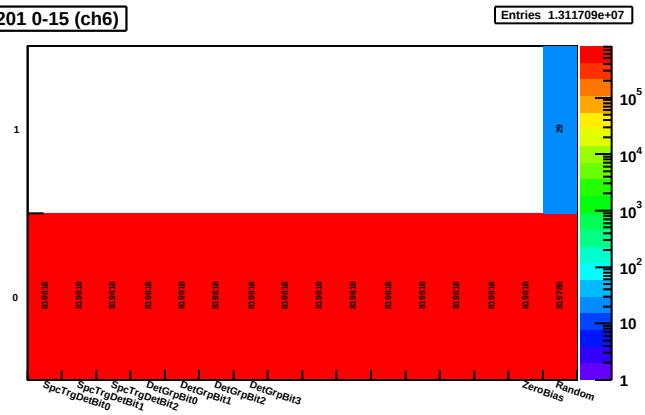


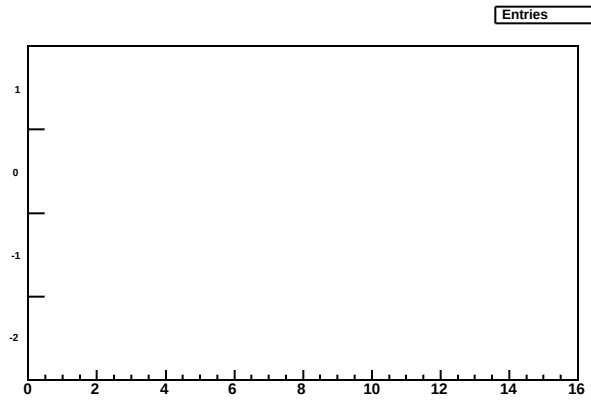
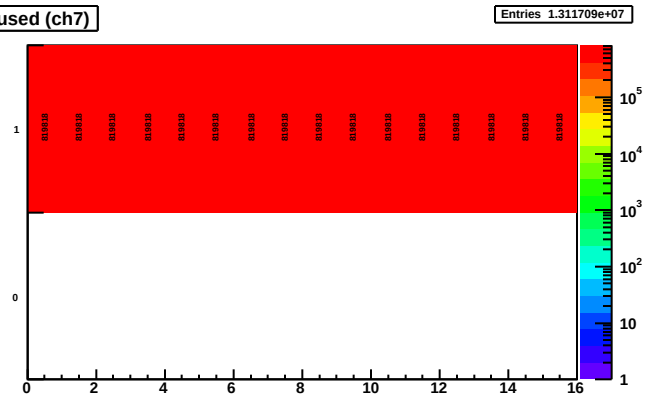
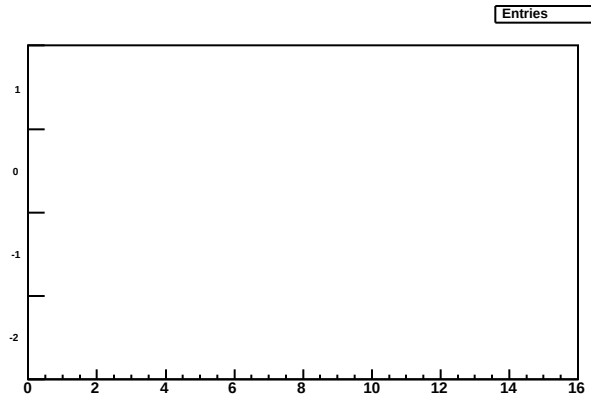
FP201 0-15 (ch5)





ST201 0-15 (ch6)





Unused (ch7)