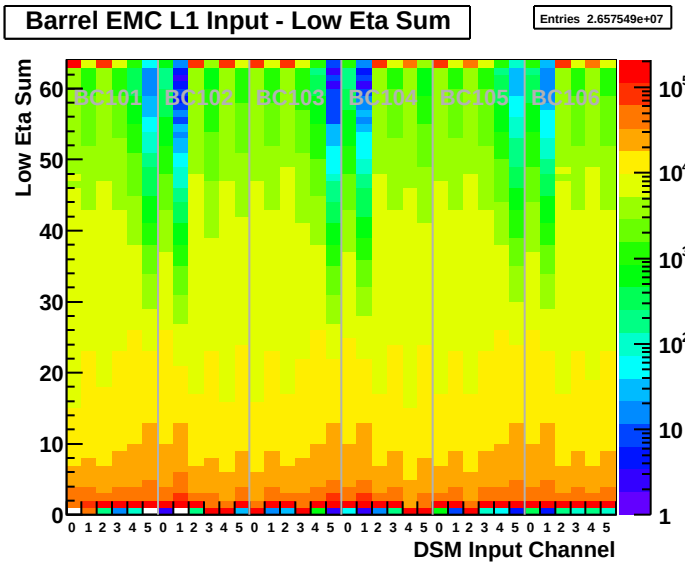
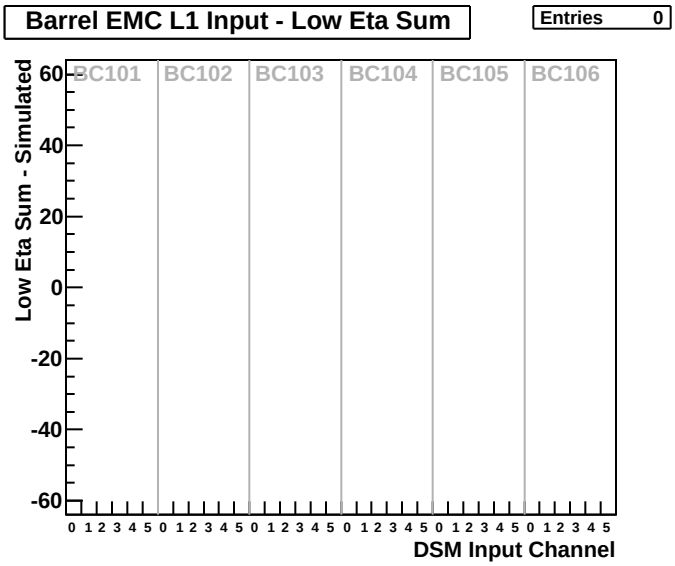
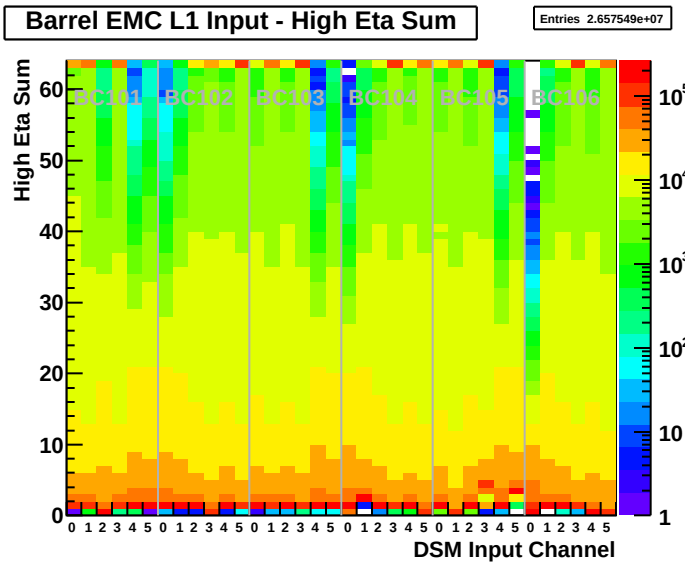
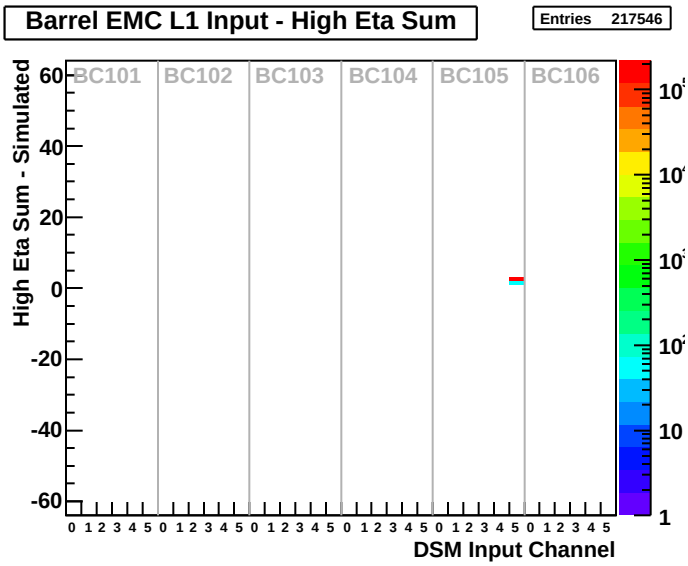
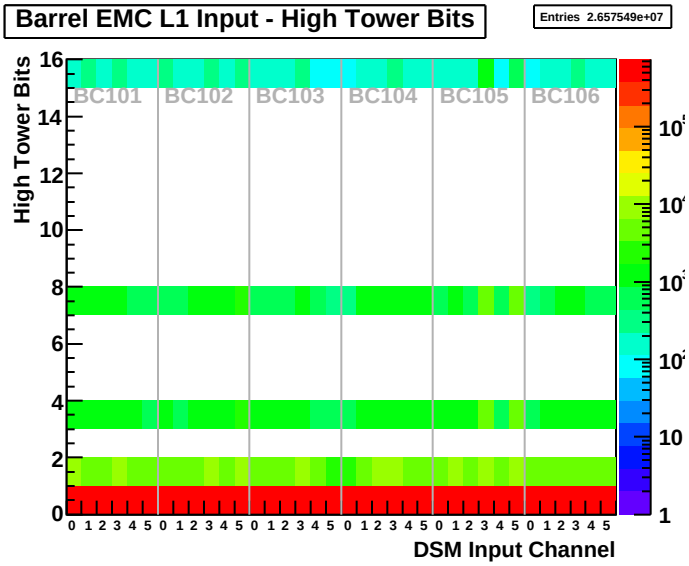
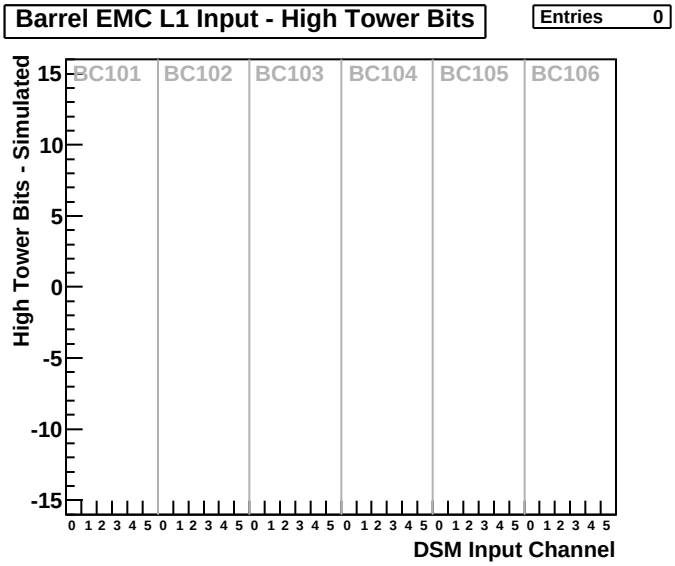

Barrel EMC L1 Input - Low Eta Sum
Entries 2.657549e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

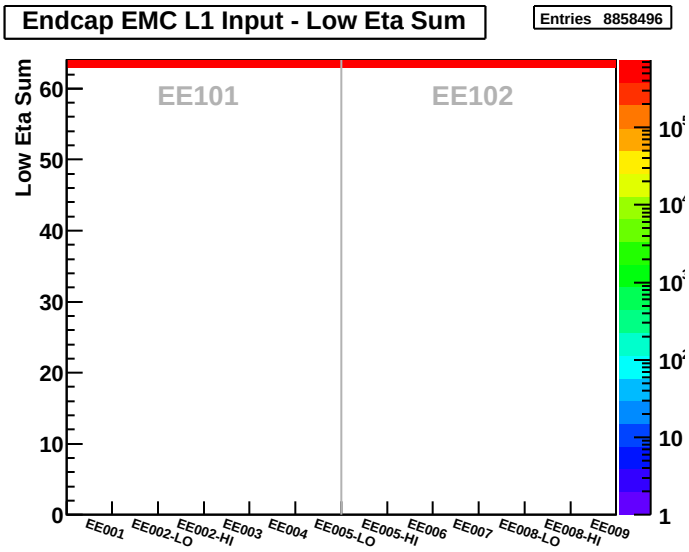

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

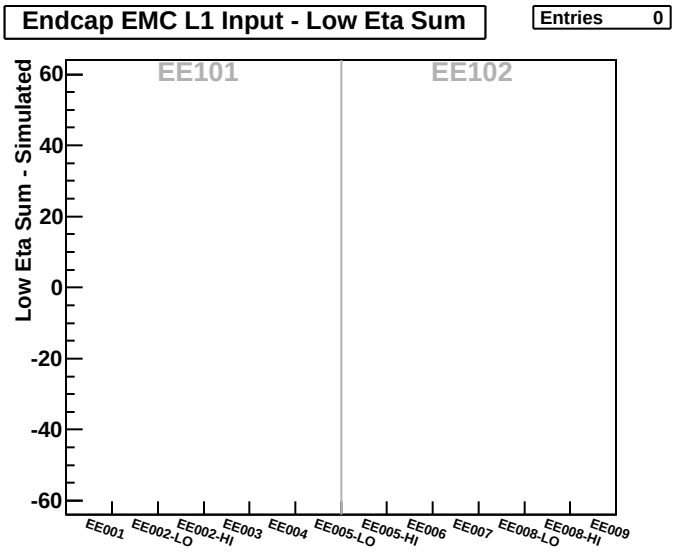

Barrel EMC L1 Input - High Eta Sum
Entries 2.657549e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

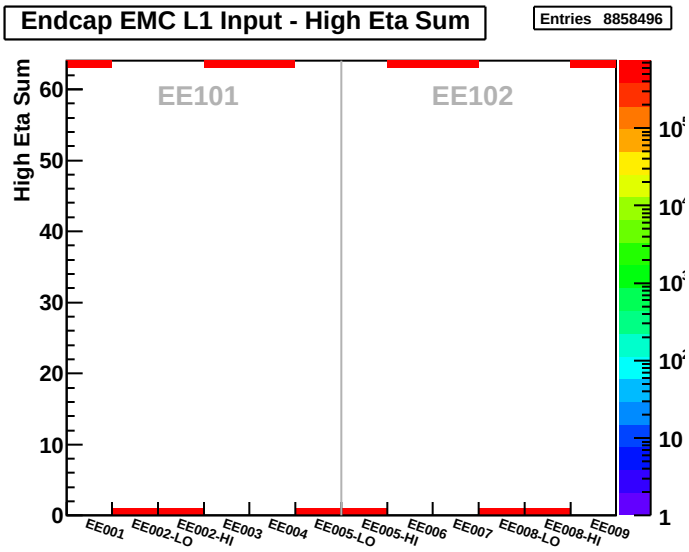

Barrel EMC L1 Input - High Eta Sum
Entries 217546
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

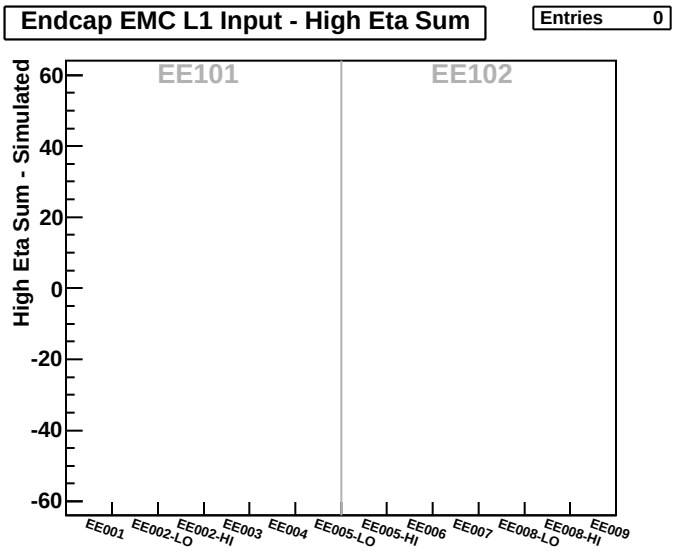

Barrel EMC L1 Input - High Tower Bits
Entries 2.657549e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

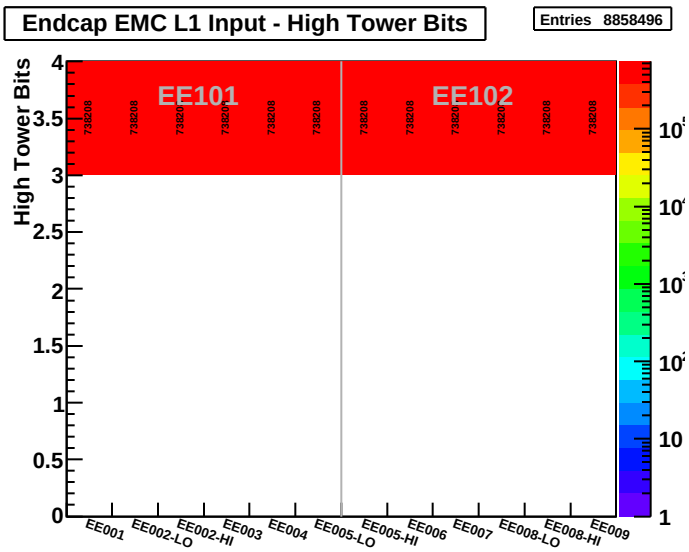

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

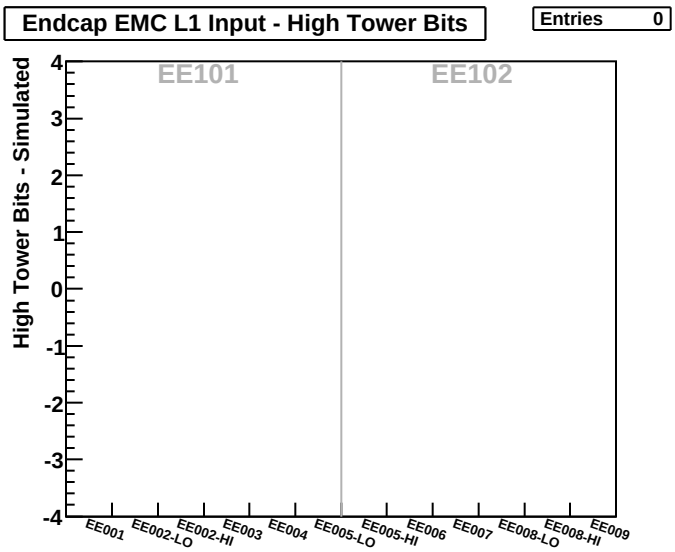

Endcap EMC L1 Input - Low Eta Sum
Entries 8858496
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

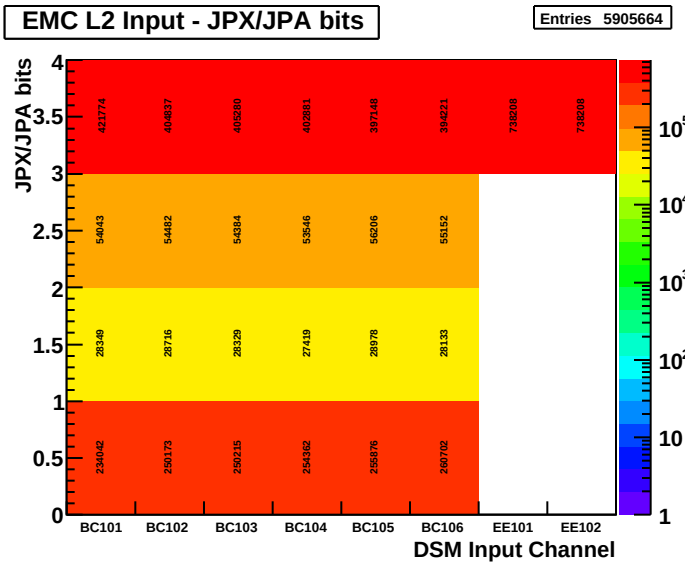

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

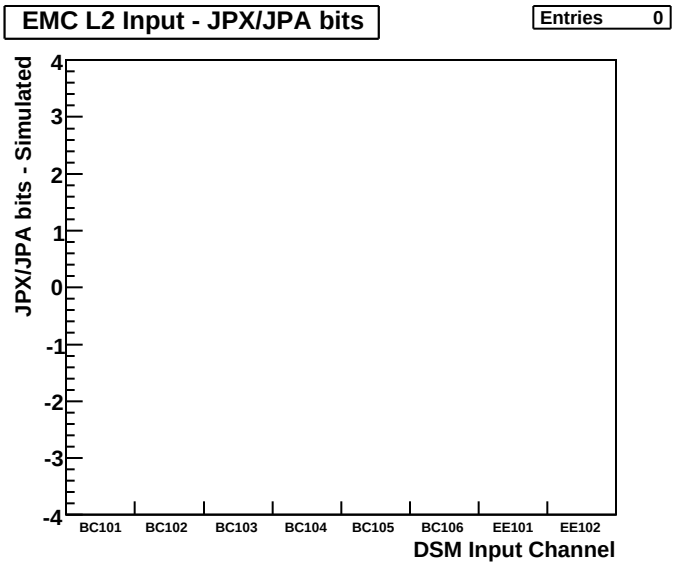

Endcap EMC L1 Input - High Eta Sum
Entries 8858496
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

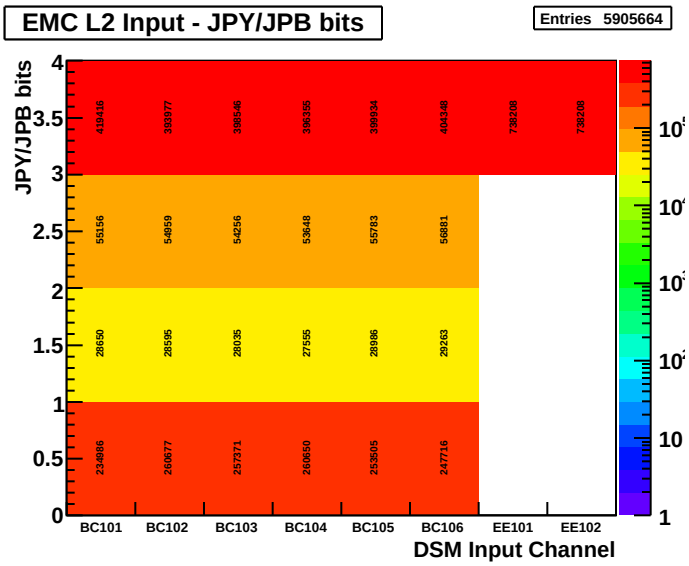

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

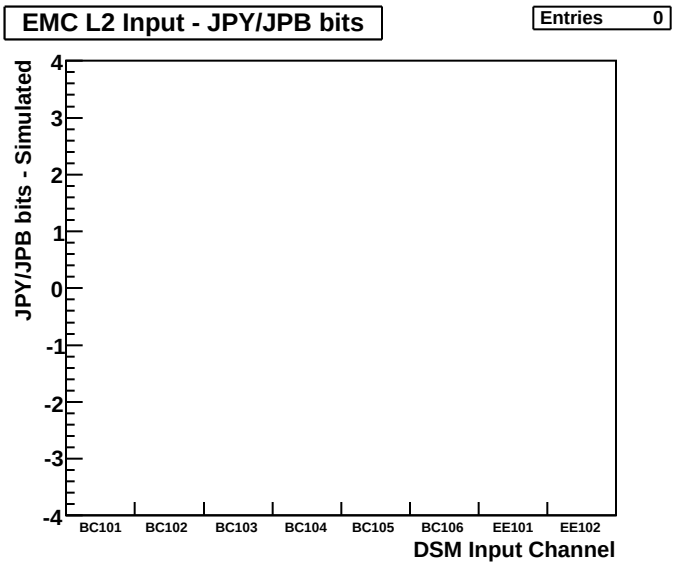

Endcap EMC L1 Input - High Tower Bits
Entries 8858496
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

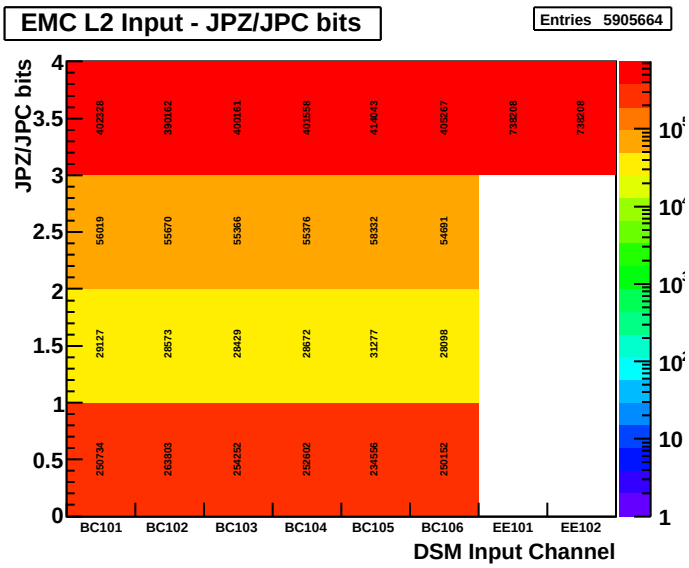

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

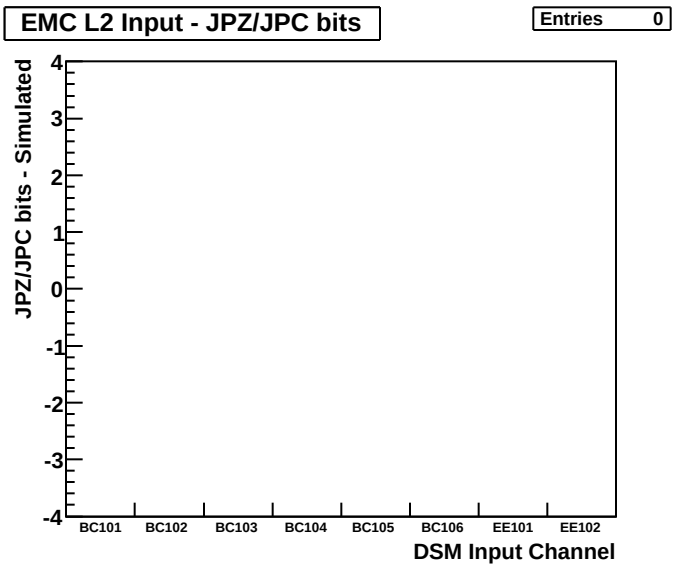

EMC L2 Input - JPX/JPA bits
Entries 5905664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
421774 404837 405280 402881 397148 394221 738208 738208
54043 54482 54384 53546 56206 55152
28349 28716 28329 27419 28978 28133
234042 250173 250215 254362 255876 260702

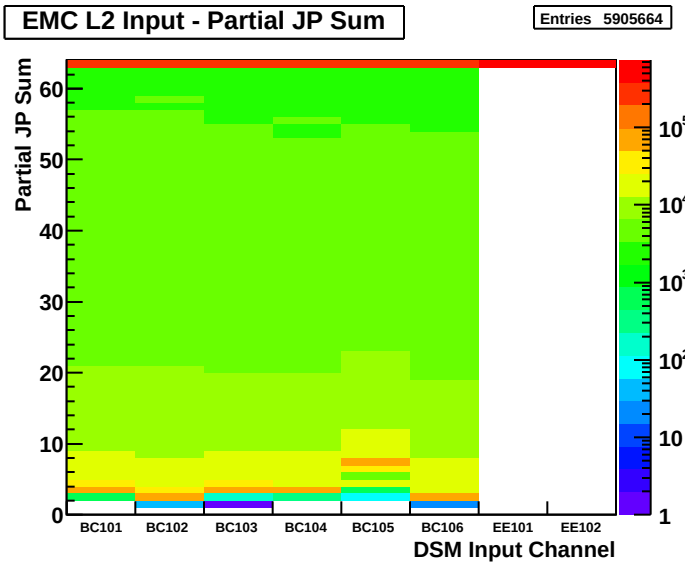

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

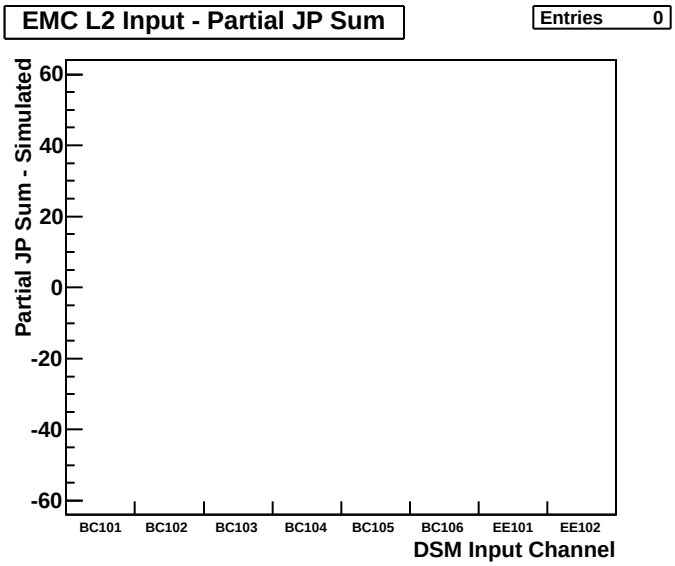

EMC L2 Input - JPY/JPB bits
Entries 5905664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
419416 393977 398546 396355 399934 404348 738208 738208
55156 54959 54256 53648 55783 56881
28650 28595 28035 27555 28986 29263
234986 260677 257371 260650 253505 247716

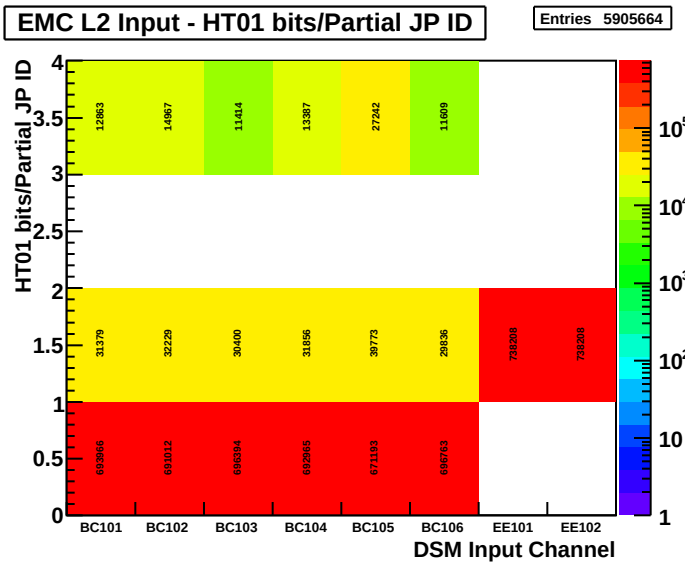

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

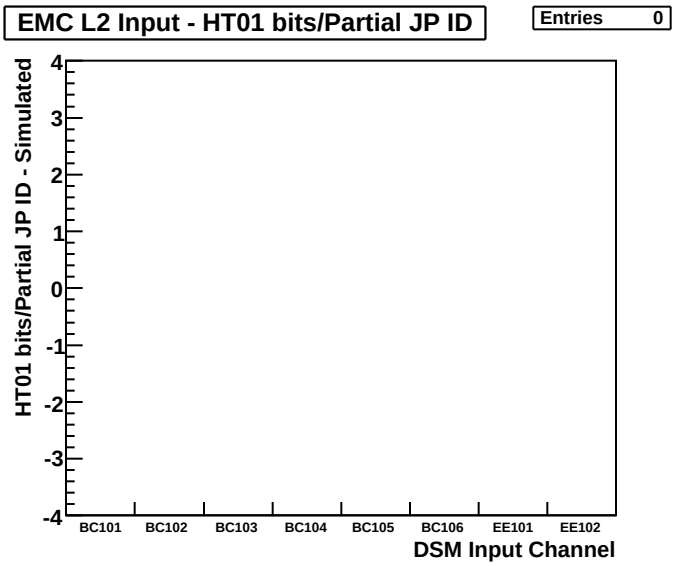

EMC L2 Input - JPZ/JPC bits
Entries 5905664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
402328 390162 400161 403558 414043 403267 738208 738208
56019 55670 55366 55376 58332 54691
29127 28673 28429 28672 31277 28098
250734 263803 254252 252602 234556 250152

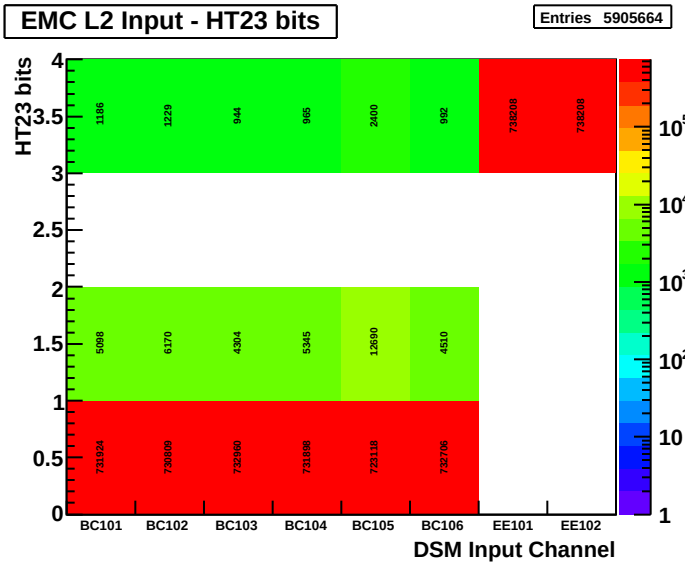

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

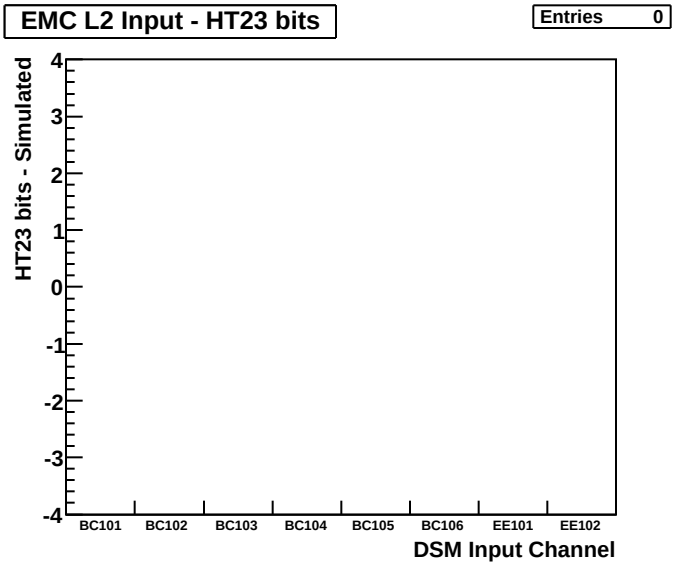

EMC L2 Input - Partial JP Sum
Entries 5905664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

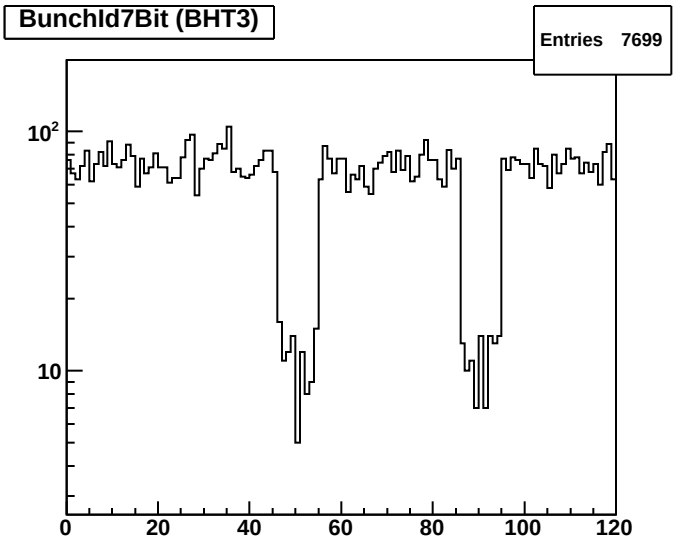

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

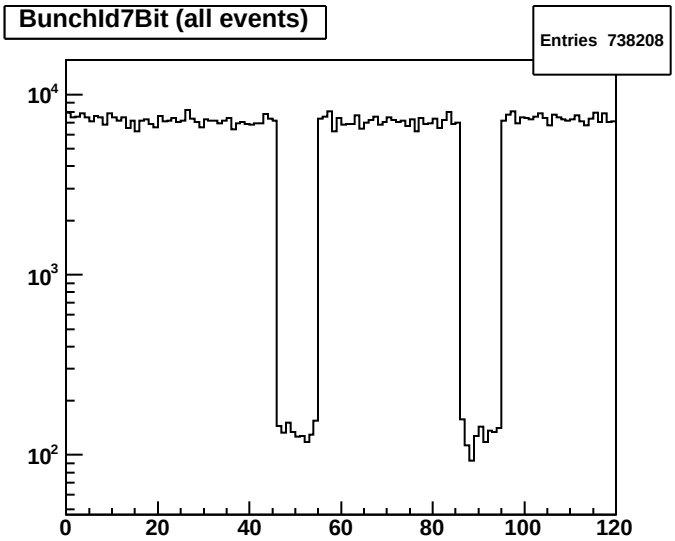

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5905664
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

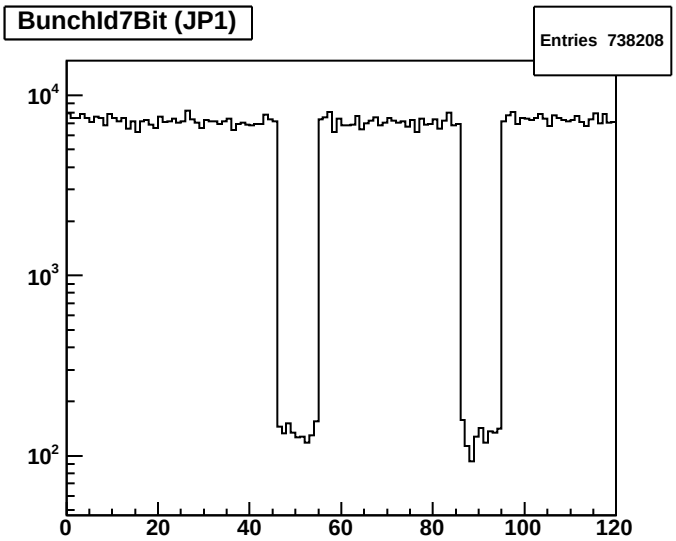

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

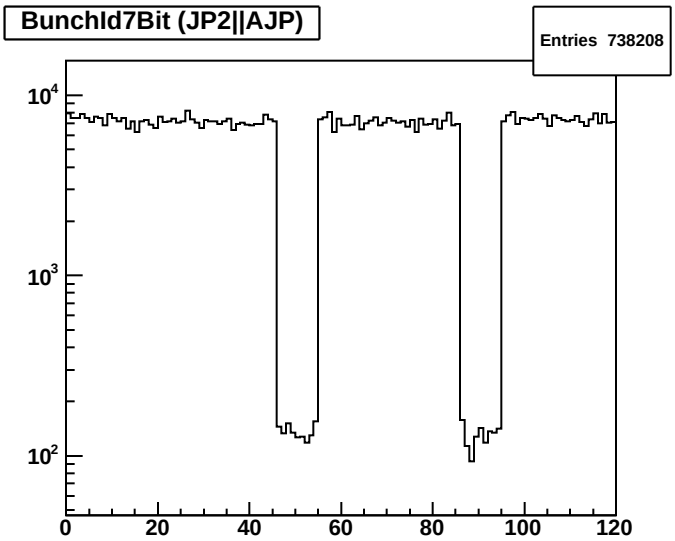

EMC L2 Input - HT23 bits
Entries 5905664
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

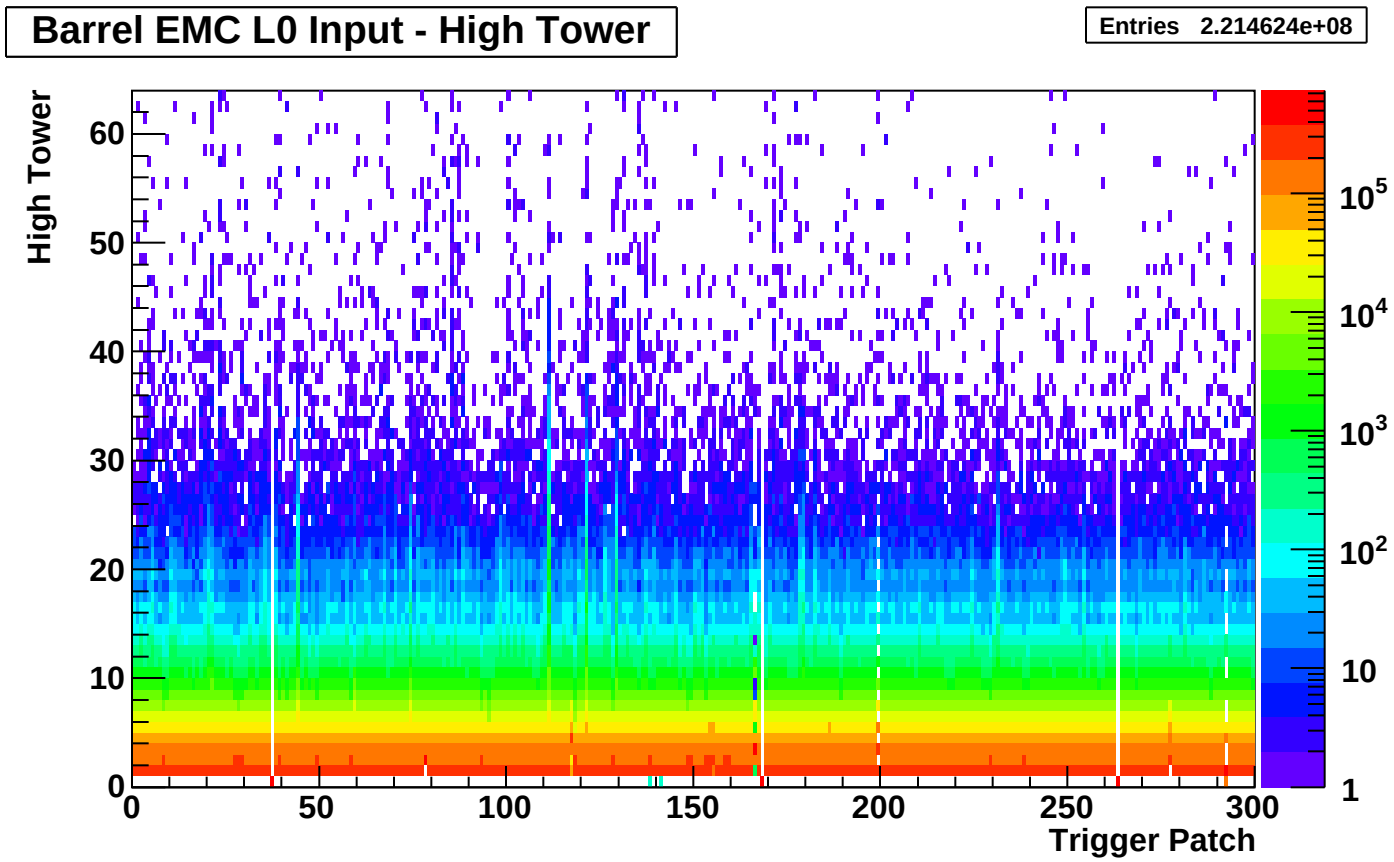

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

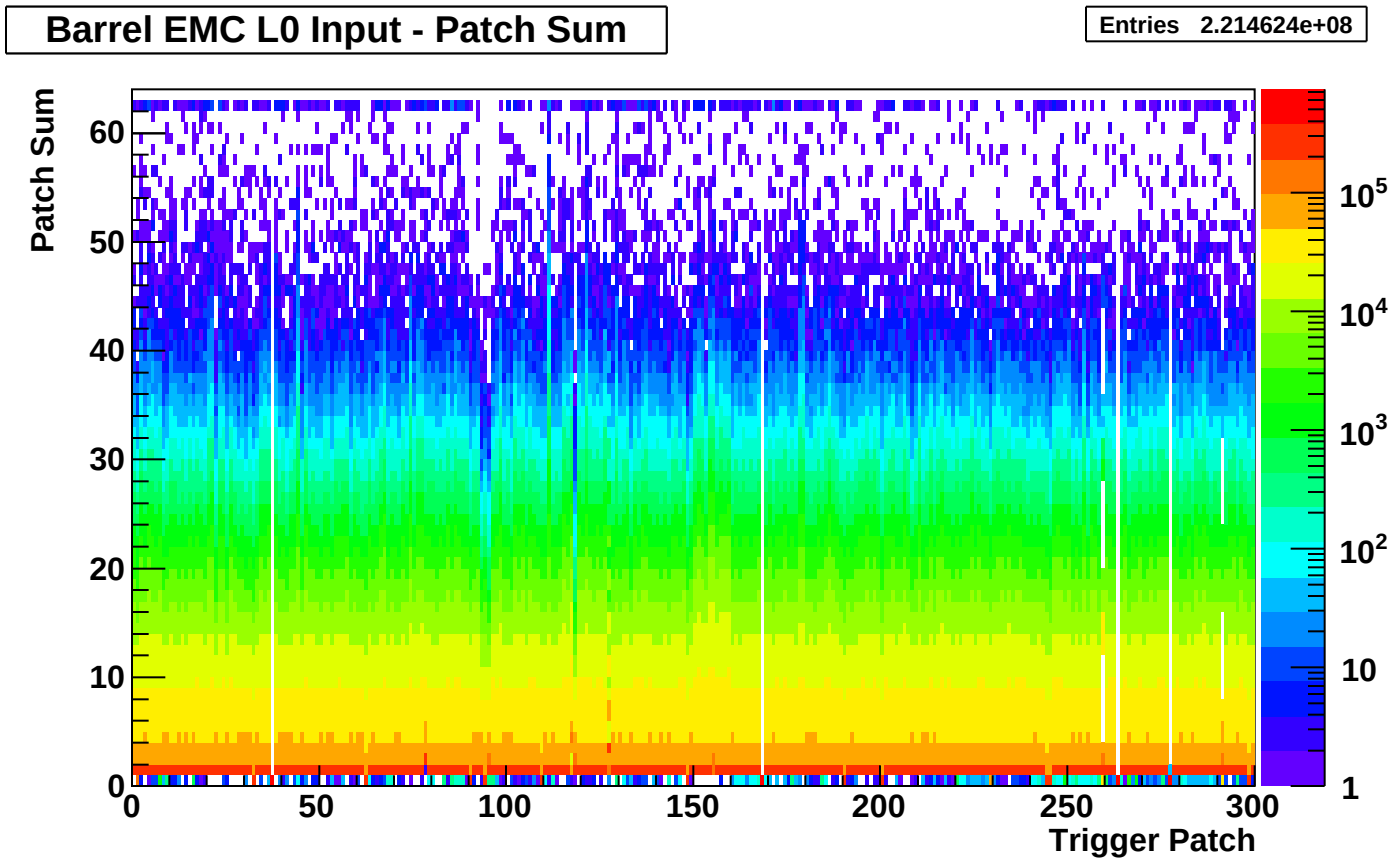

BunchId7Bit (BHT3)
Entries 7699
10^2
10
0
20
40
60
80
100
120

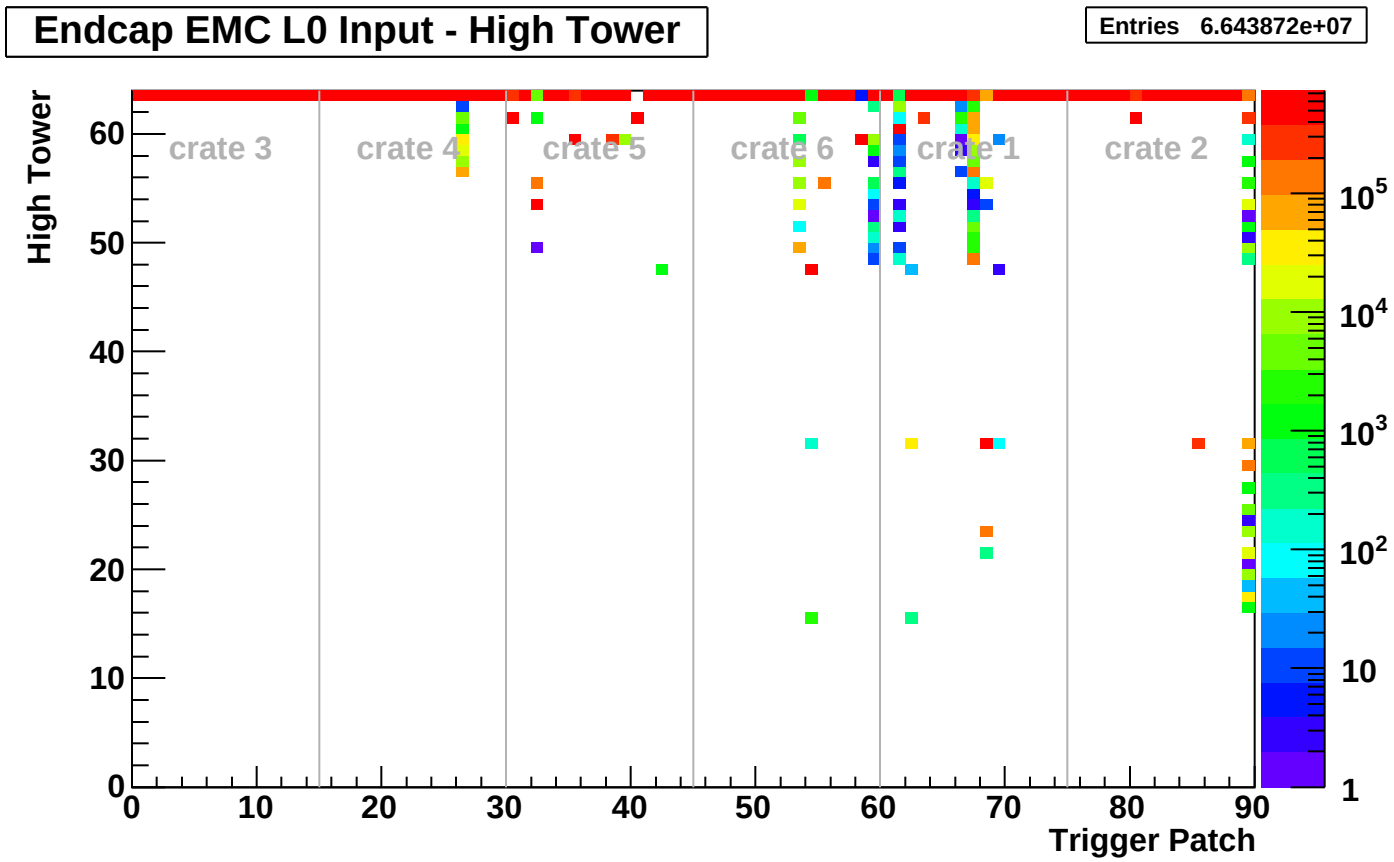

BunchId7Bit (all events)
Entries 738208
10^4
10^3
10^2
0
20
40
60
80
100
120

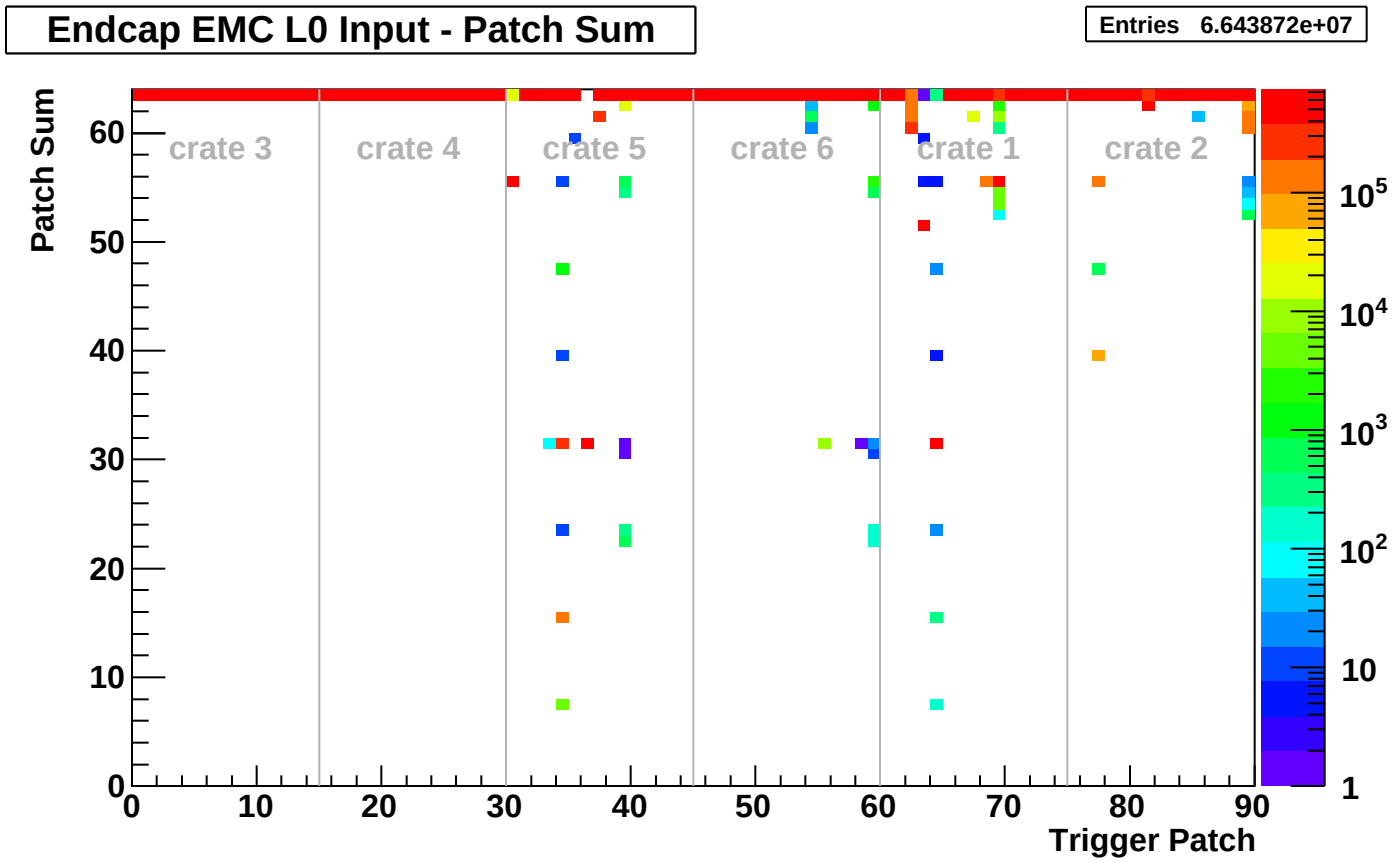

BunchId7Bit (JP1)
Entries 738208
10^4
10^3
10^2
0
20
40
60
80
100
120

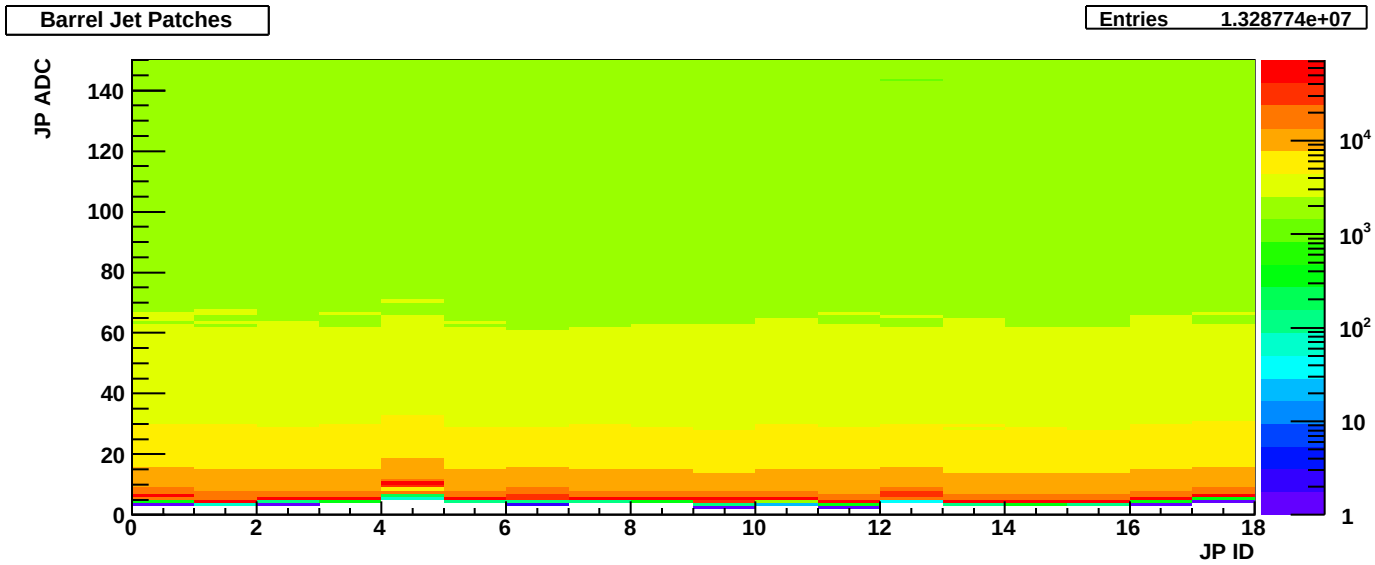

BunchId7Bit (JP2||AJP)
Entries 738208
10^4
10^3
10^2
0
20
40
60
80
100
120

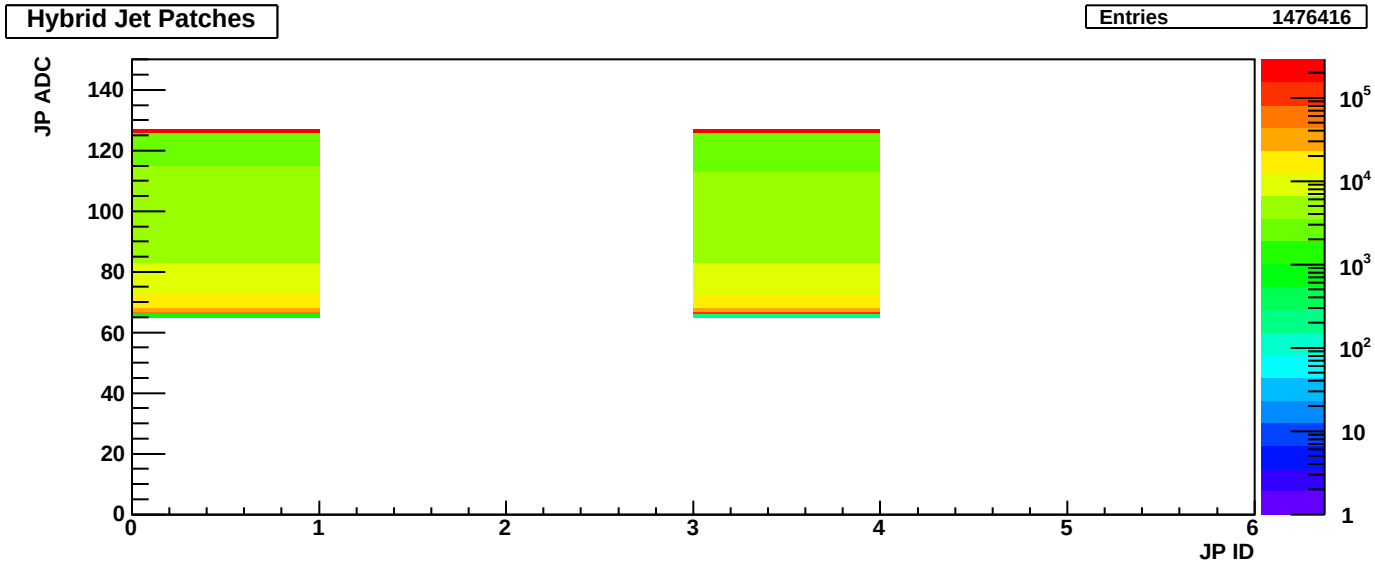

Barrel EMC L0 Input - High Tower
Entries 2.214624e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

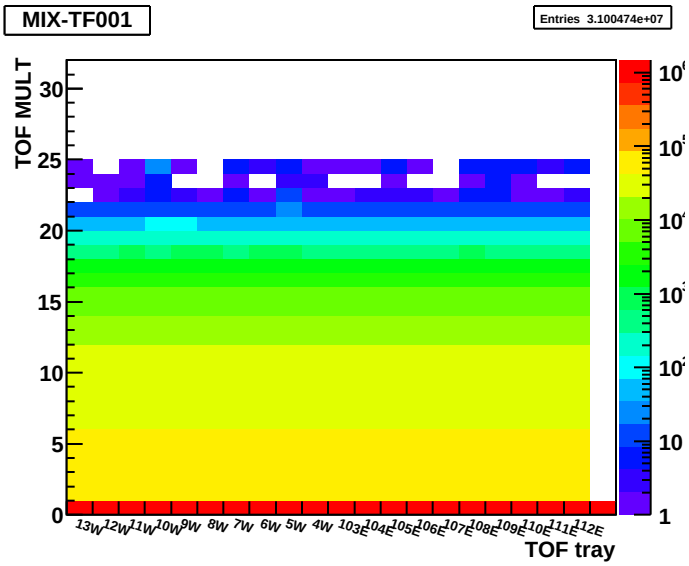

Barrel EMC L0 Input - Patch Sum
Entries 2.214624e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

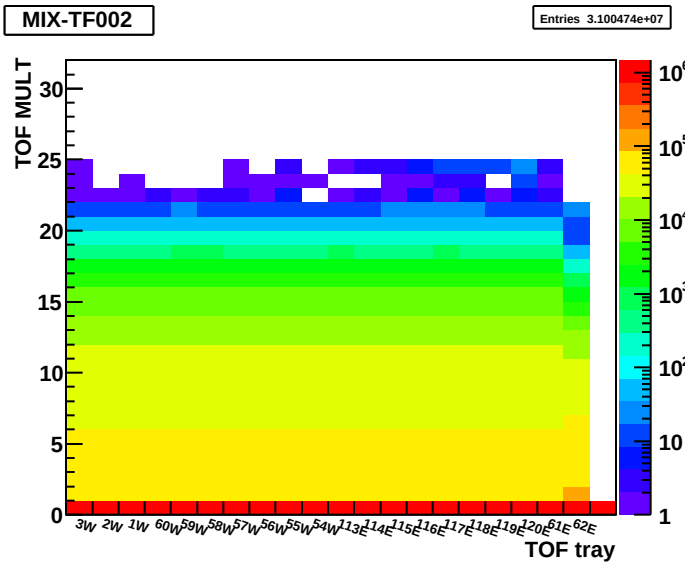

Endcap EMC L0 Input - High Tower
Entries 6.643872e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

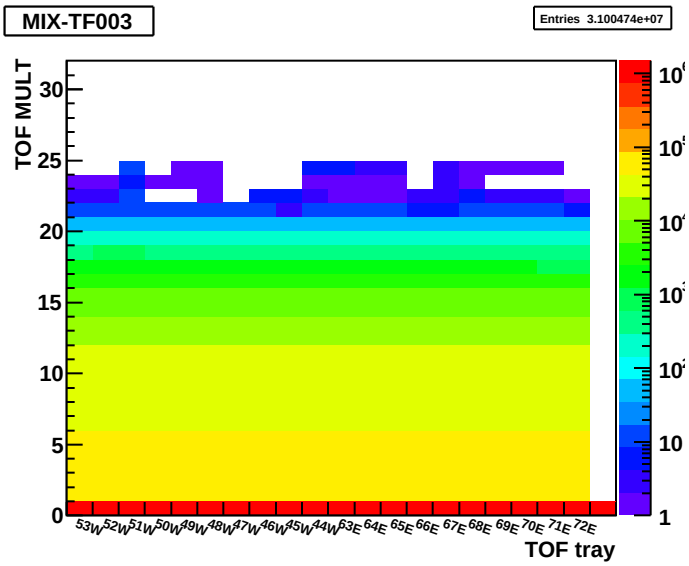

Endcap EMC L0 Input - Patch Sum
Entries 6.643872e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

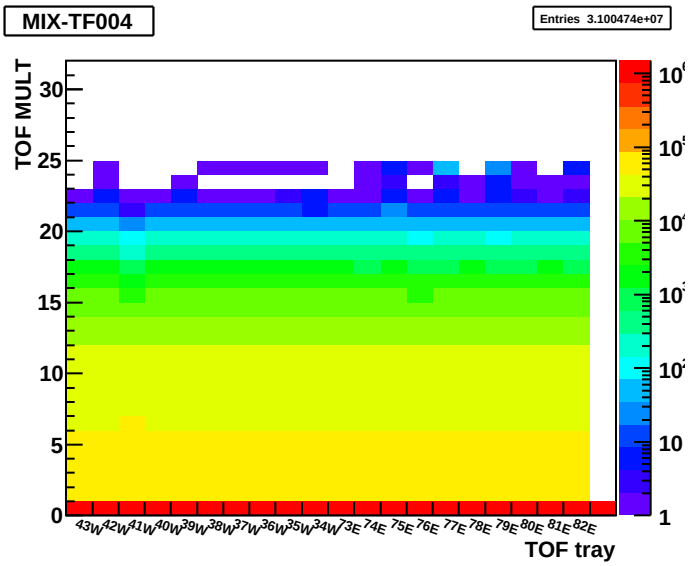

Barrel Jet Patches
Entries 1.328774e+07
JP ADC
JP ID

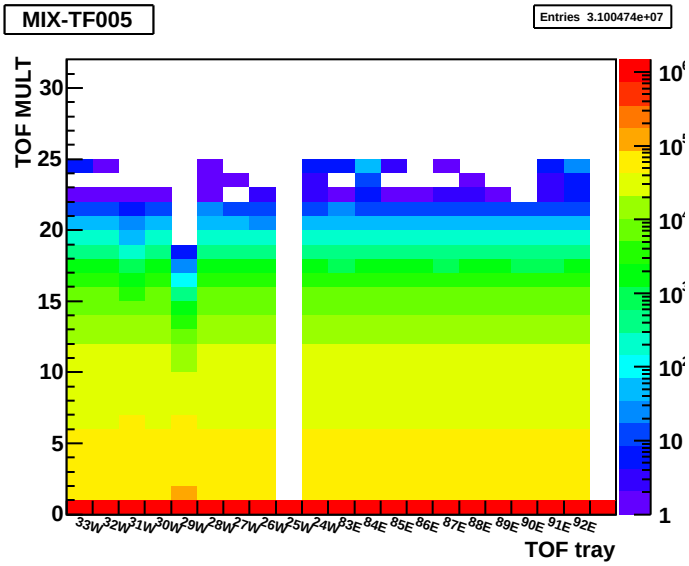

Hybrid Jet Patches
Entries 1476416
JP ADC
JP ID

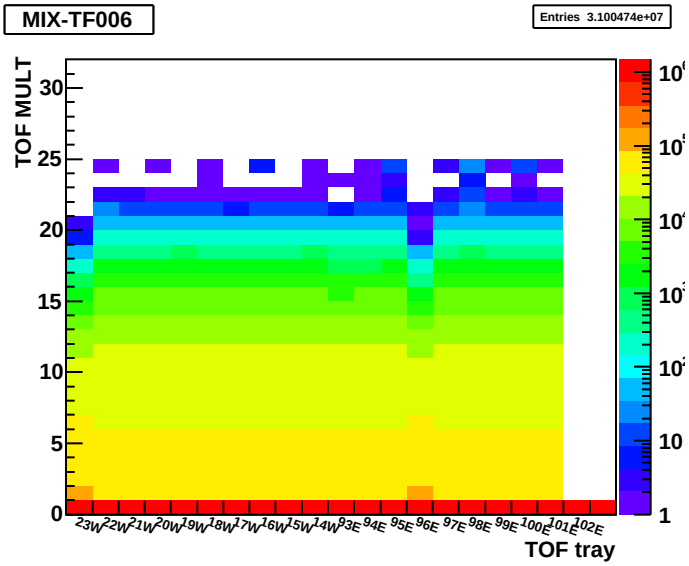

MIX-TF001
Entries 3.100474e+07
TOF MULT
TOF tray

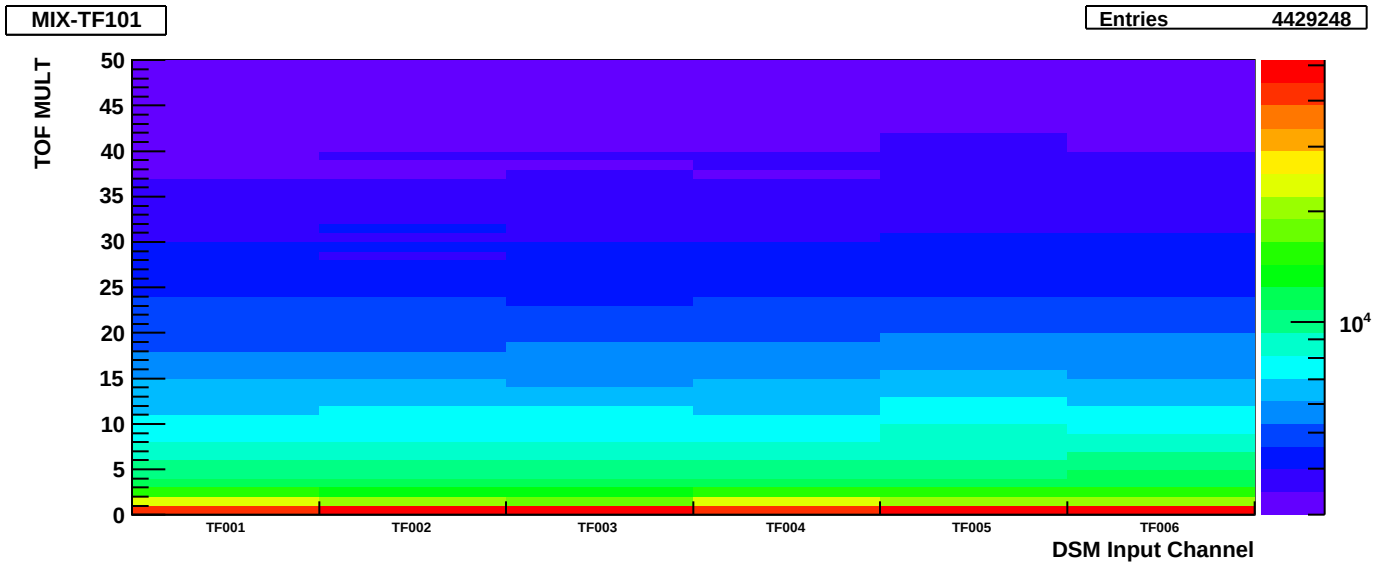

MIX-TF002
Entries 3.100474e+07
TOF MULT
TOF tray

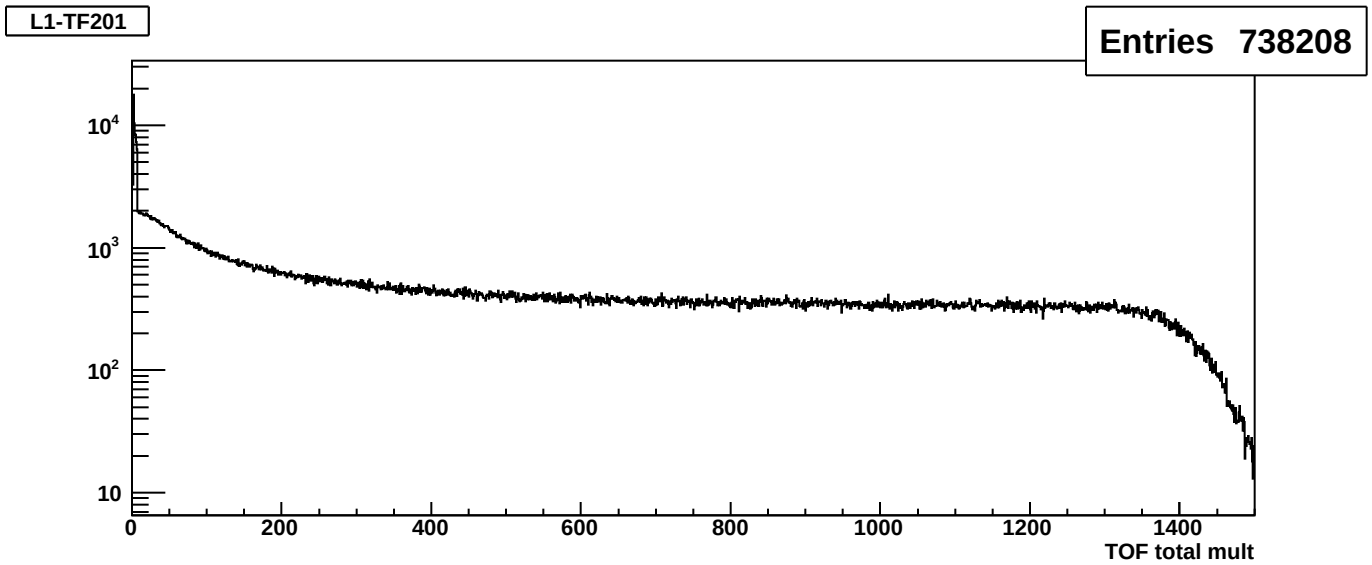

MIX-TF003
Entries 3.100474e+07
TOF MULT
TOF tray

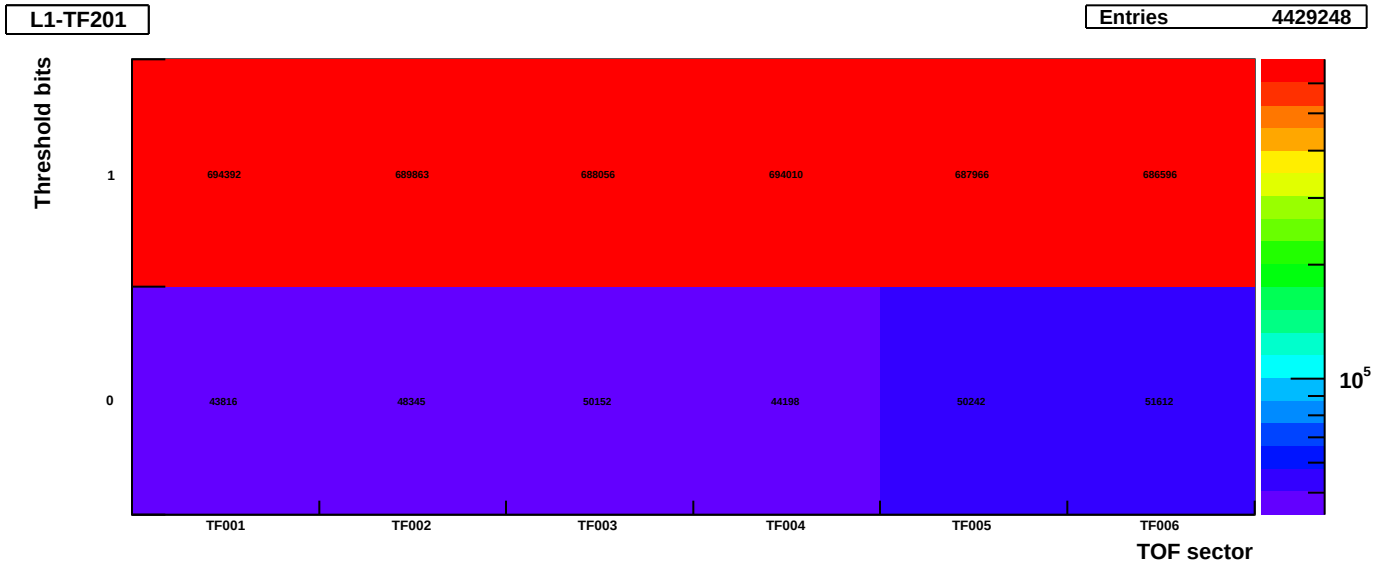

MIX-TF004
Entries 3.100474e+07
TOF MULT
TOF tray

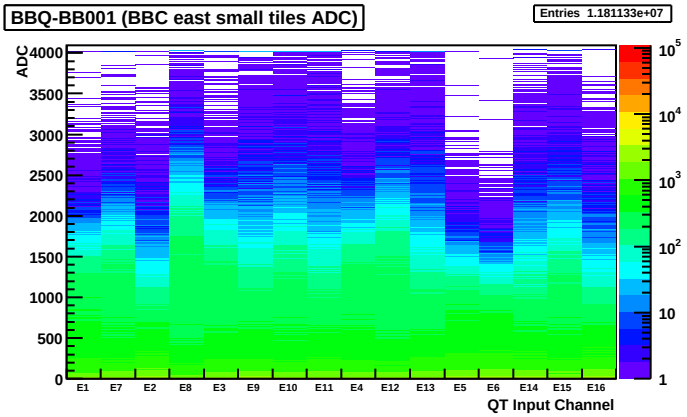

MIX-TF005
Entries 3.100474e+07
TOF MULT
TOF tray

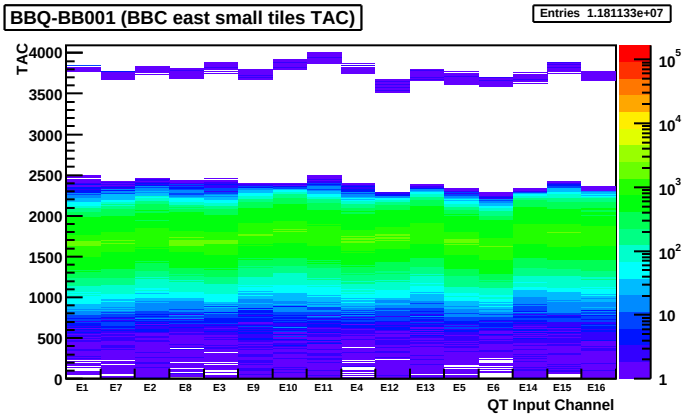

MIX-TF006
Entries 3.100474e+07
TOF MULT
TOF tray

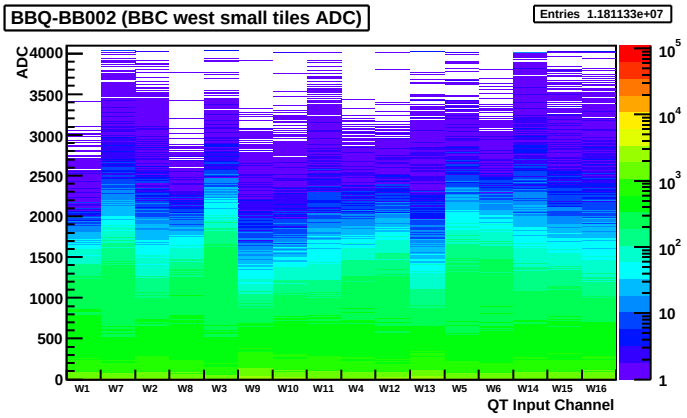

MIX-TF101
Entries 4429248
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

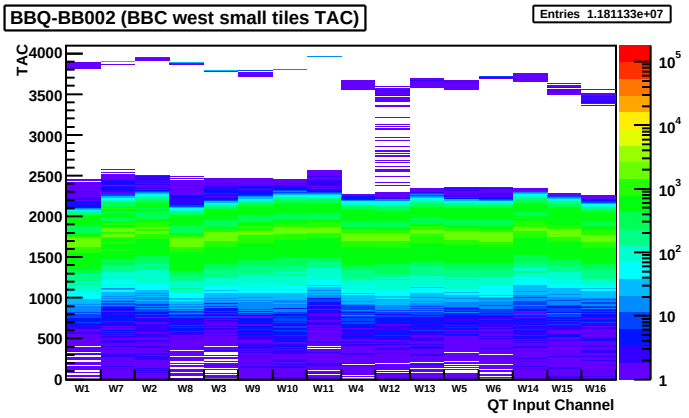

L1-TF201
Entries 738208
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

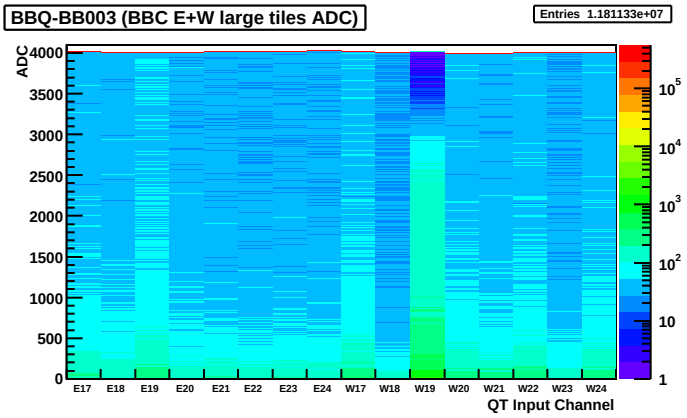

L1-TF201
Entries 4429248
Threshold bits
1
0
694392
689863
688056
694010
687966
686596
43816
48345
50152
44198
50242
51612
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

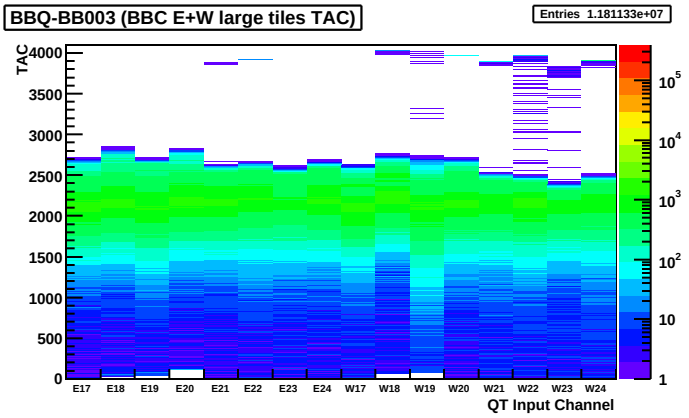

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.181133e+07
ADC
QT Input Channel

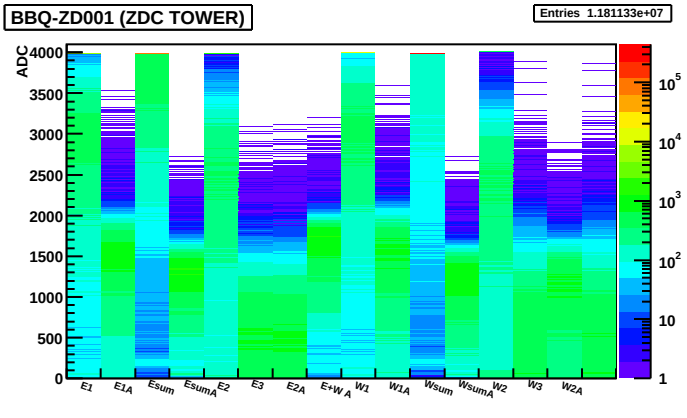

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.181133e+07
TAC
QT Input Channel

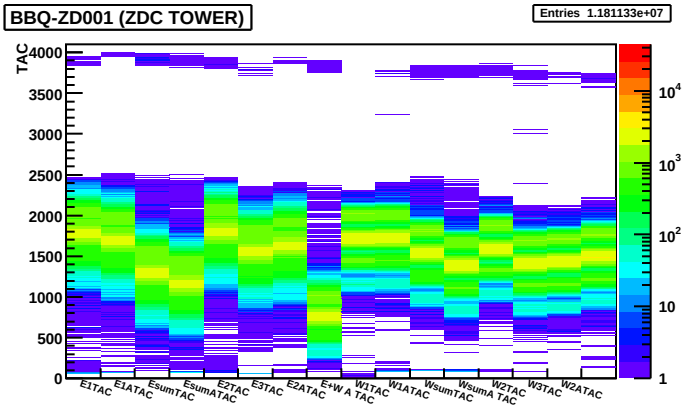

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.181133e+07
ADC
QT Input Channel

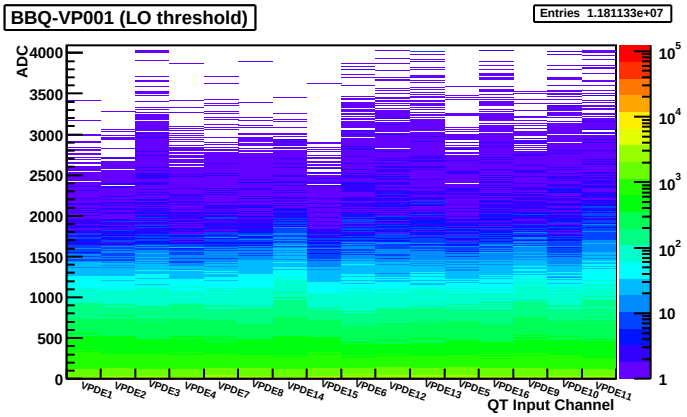

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.181133e+07
TAC
QT Input Channel

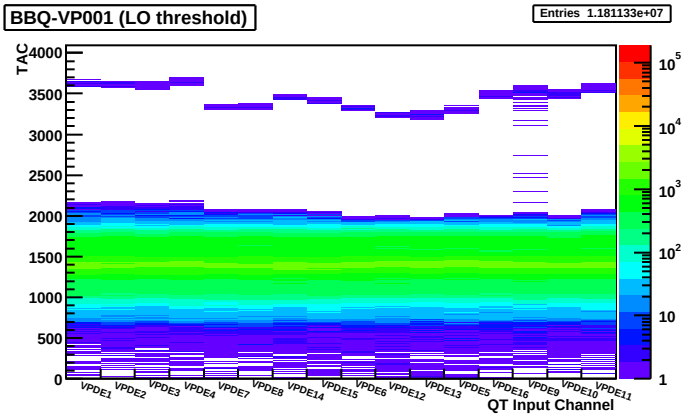

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.181133e+07
ADC
QT Input Channel

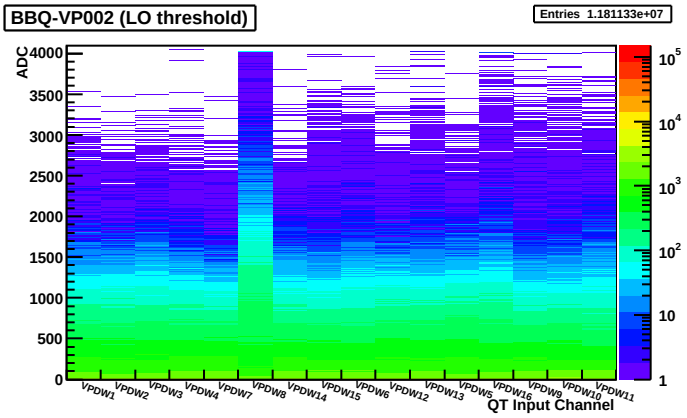

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.181133e+07
TAC
QT Input Channel

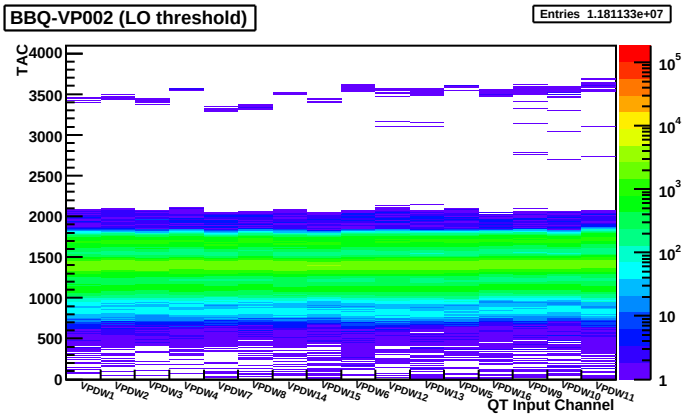

BBQ-ZD001 (ZDC TOWER)
Entries 1.181133e+07
ADC

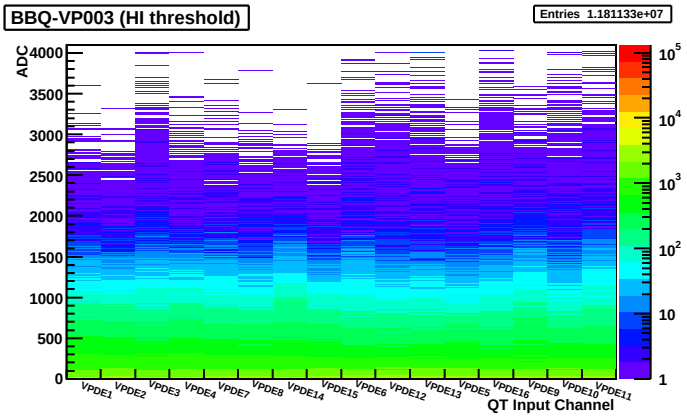

BBQ-ZD001 (ZDC TOWER)
Entries 1.181133e+07
TAC

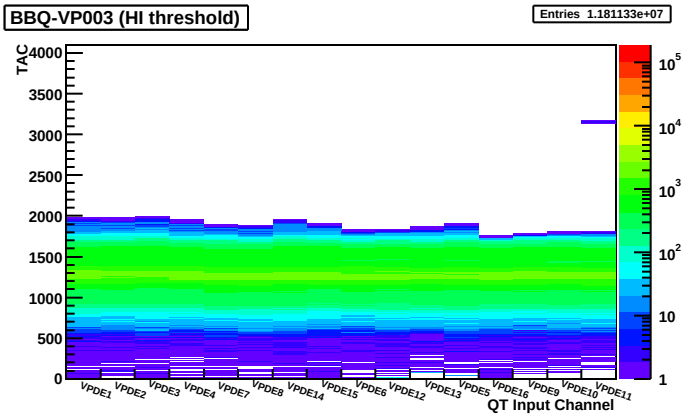

BBQ-VP001 (LO threshold)
Entries 1.181133e+07
ADC
QT Input Channel

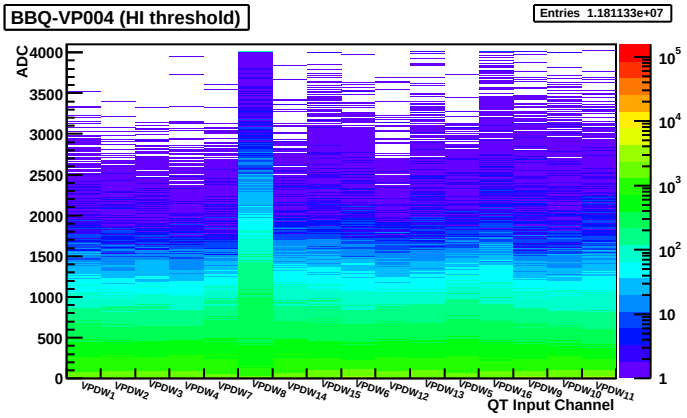

BBQ-VP001 (LO threshold)
Entries 1.181133e+07
TAC
QT Input Channel

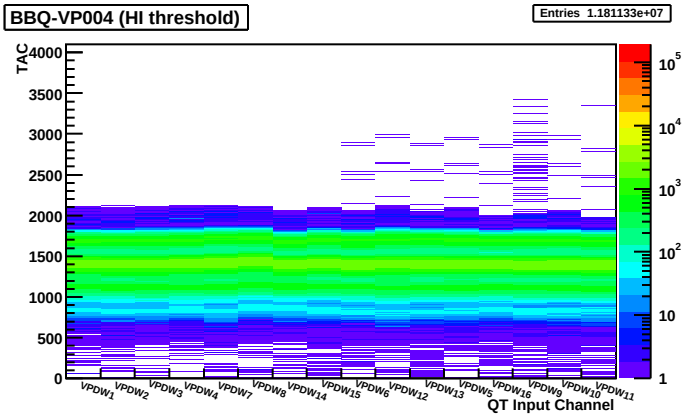

BBQ-VP002 (LO threshold)
Entries 1.181133e+07
ADC
QT Input Channel

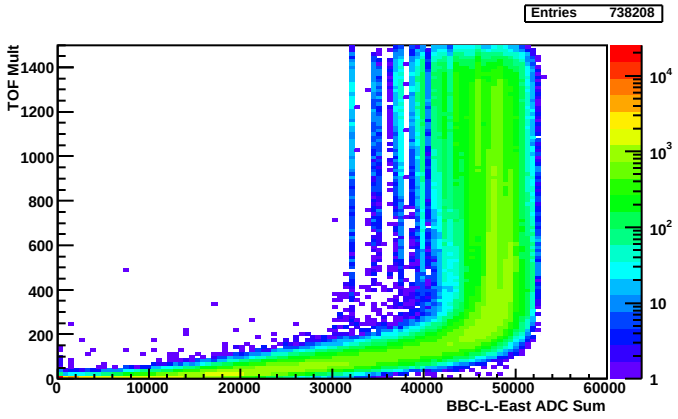

BBQ-VP002 (LO threshold)
Entries 1.181133e+07
TAC
QT Input Channel

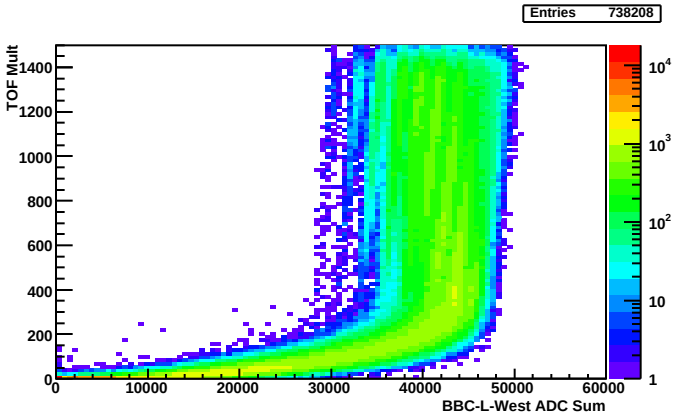

BBQ-VP003 (HI threshold)
Entries 1.181133e+07
ADC
QT Input Channel

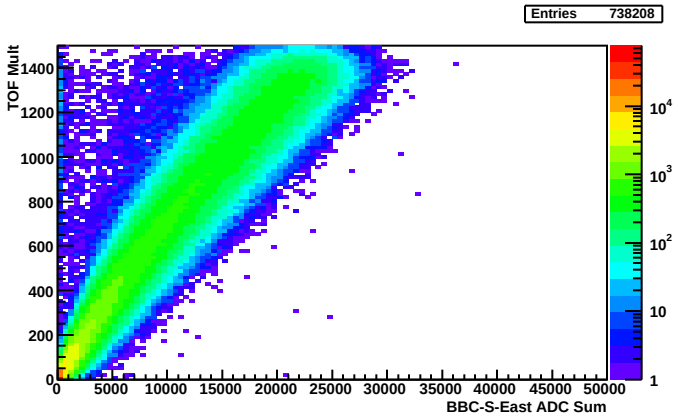

BBQ-VP003 (HI threshold)
Entries 1.181133e+07
TAC
QT Input Channel

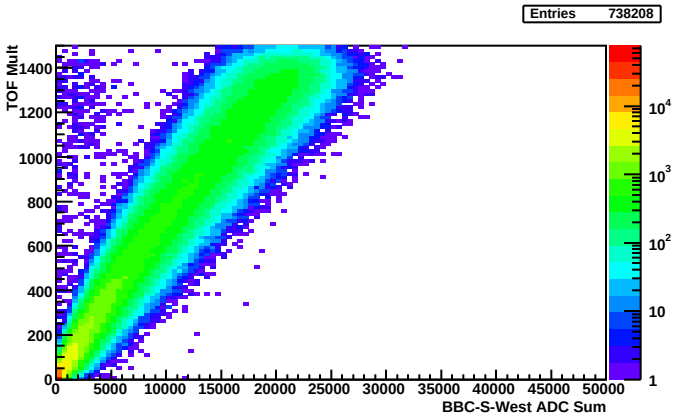

BBQ-VP004 (HI threshold)
Entries 1.181133e+07
ADC
QT Input Channel

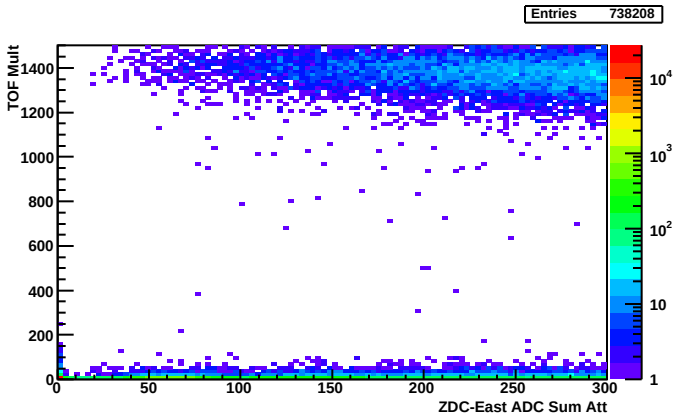

BBQ-VP004 (HI threshold)
Entries 1.181133e+07
TAC
QT Input Channel

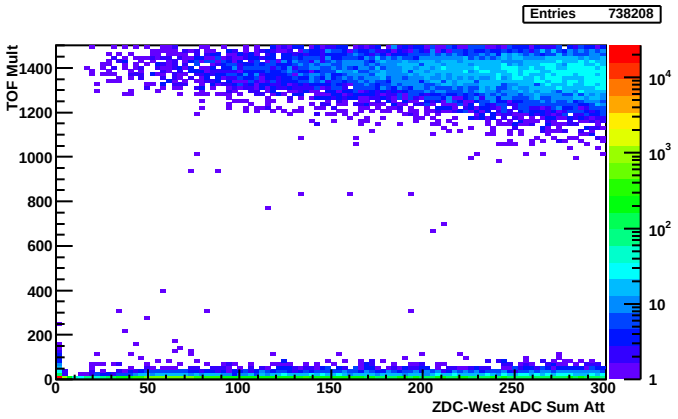

Entries 738208
TOF Mult
BBC-L-East ADC Sum

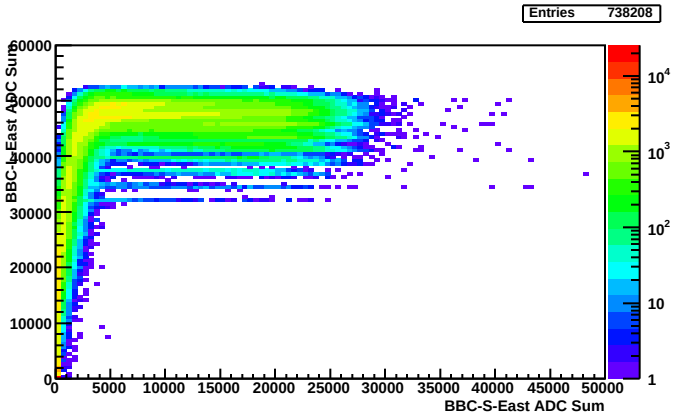

Entries 738208
TOF Mult
BBC-L-West ADC Sum

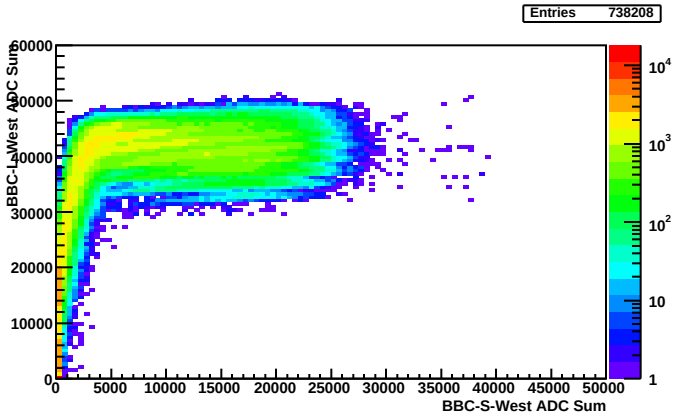

Entries 738208
TOF Mult
BBC-S-East ADC Sum

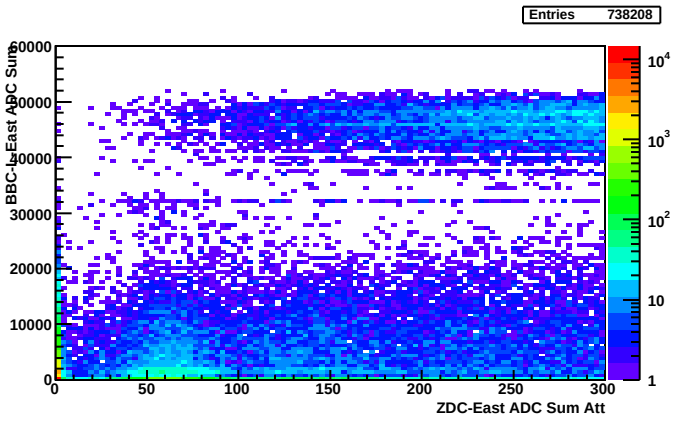

Entries 738208
TOF Mult
BBC-S-West ADC Sum

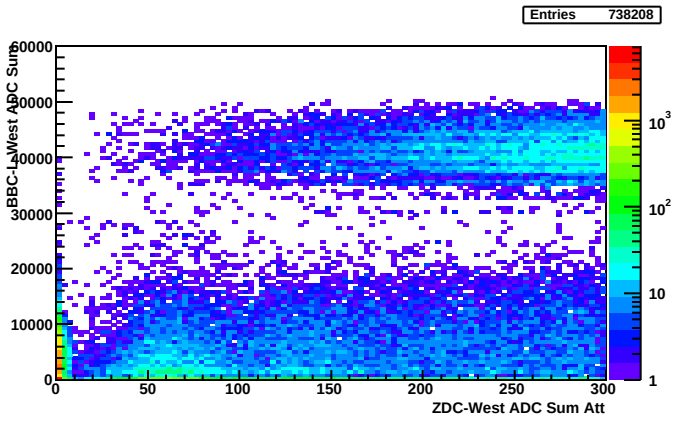

Entries 738208
TOF Mult
ZDC-East ADC Sum Att

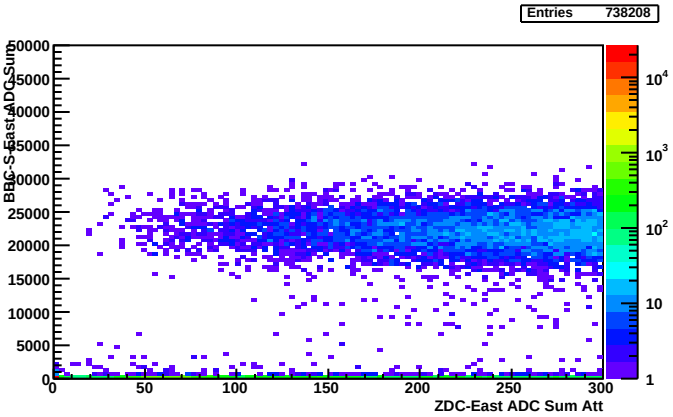

Entries 738208
TOF Mult
ZDC-West ADC Sum Att

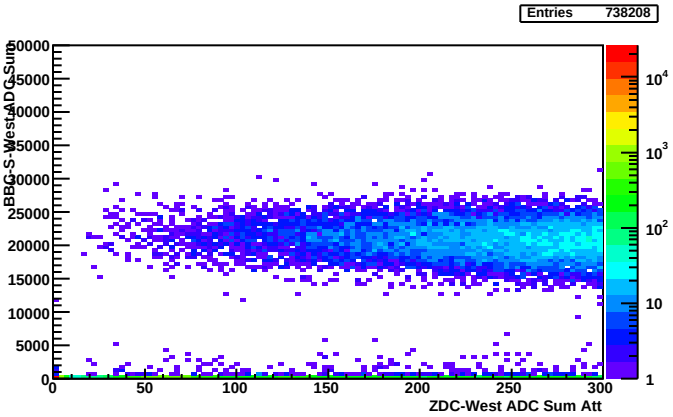

Entries 738208
BBC-L-East ADC Sum
BBC-S-East ADC Sum

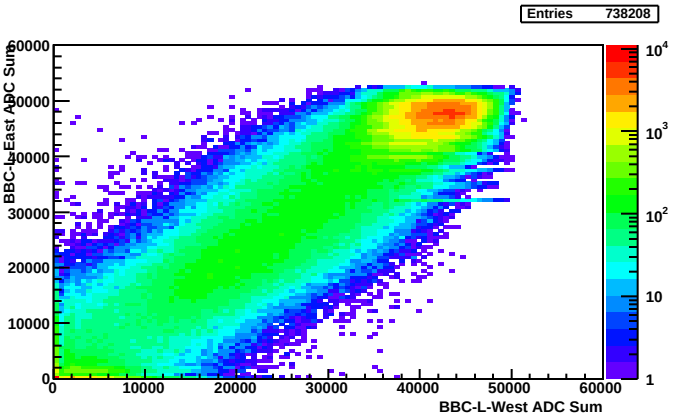

Entries 738208
BBC-L-West ADC Sum
BBC-S-West ADC Sum

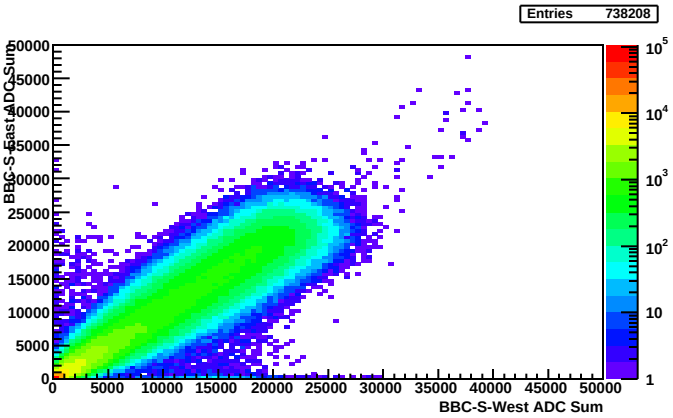

Entries 738208
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

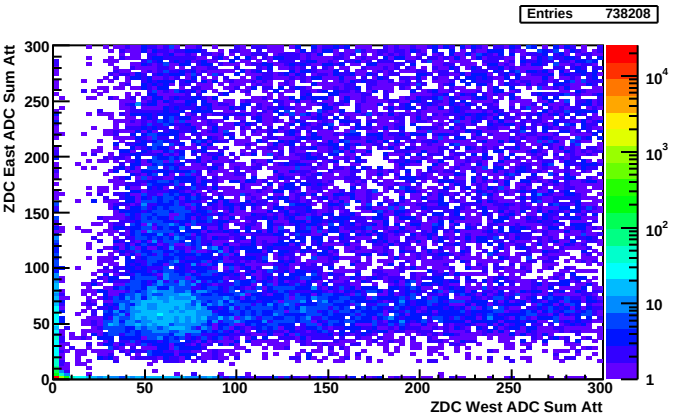

Entries 738208
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

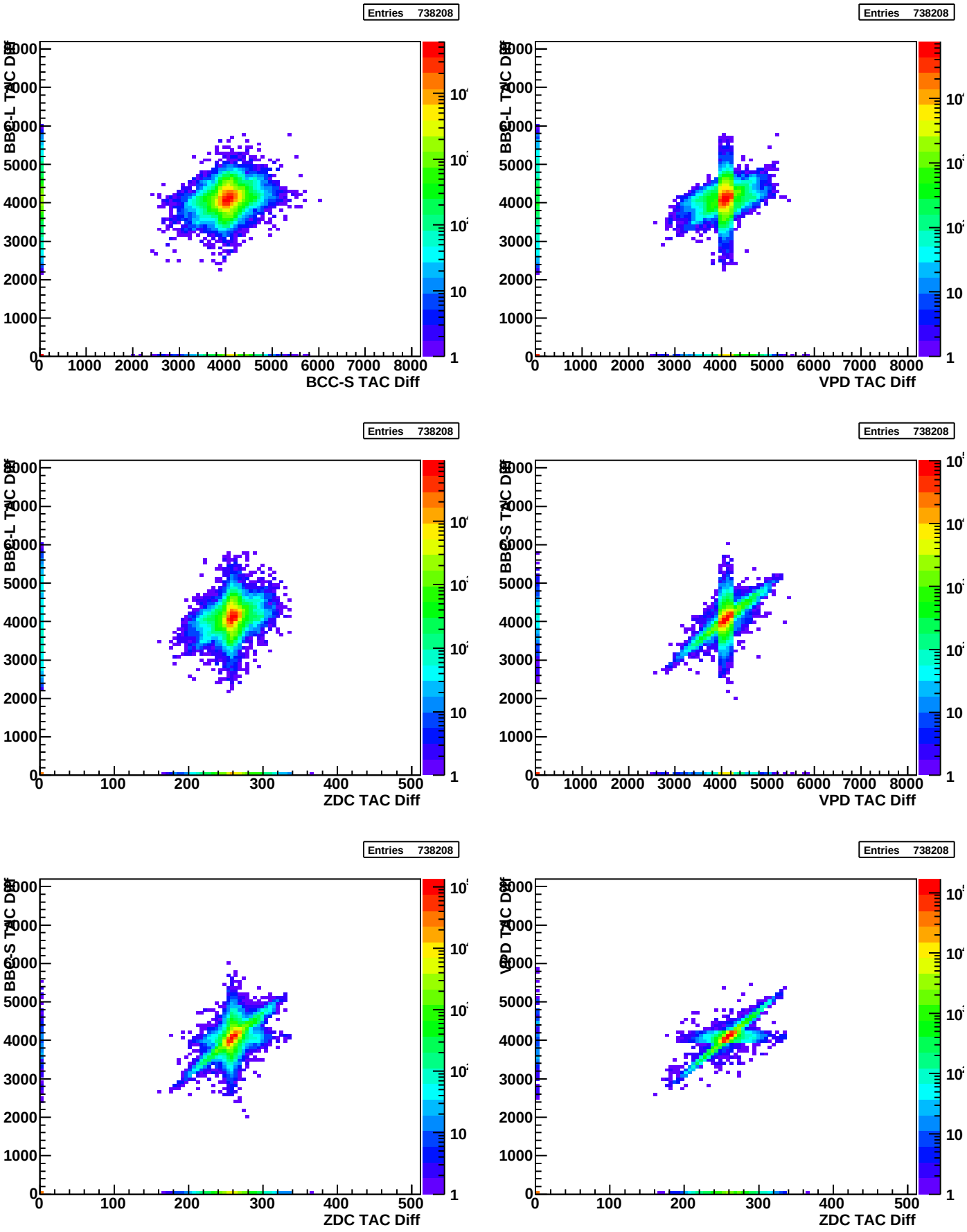

Entries 738208
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

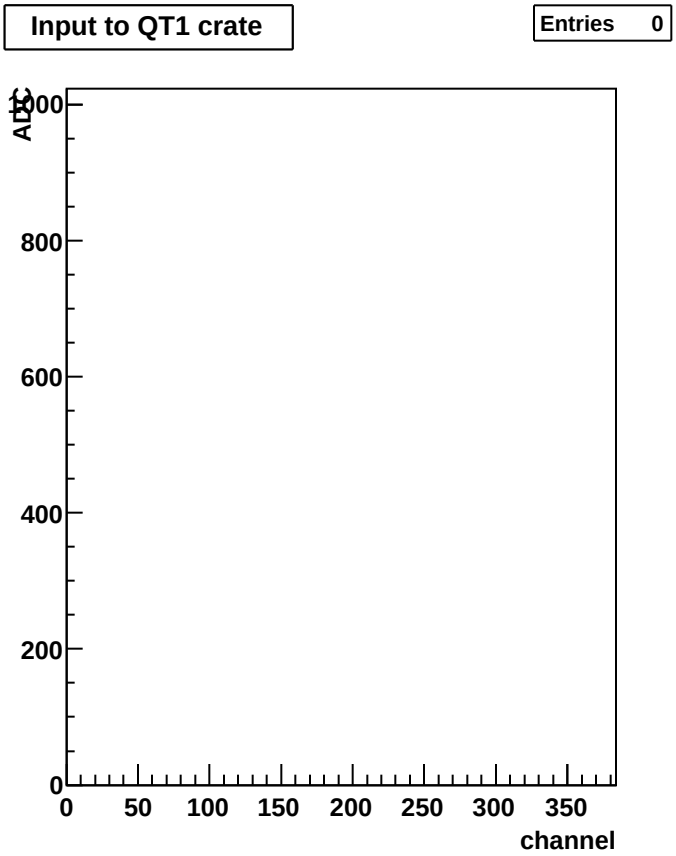

Entries 738208
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

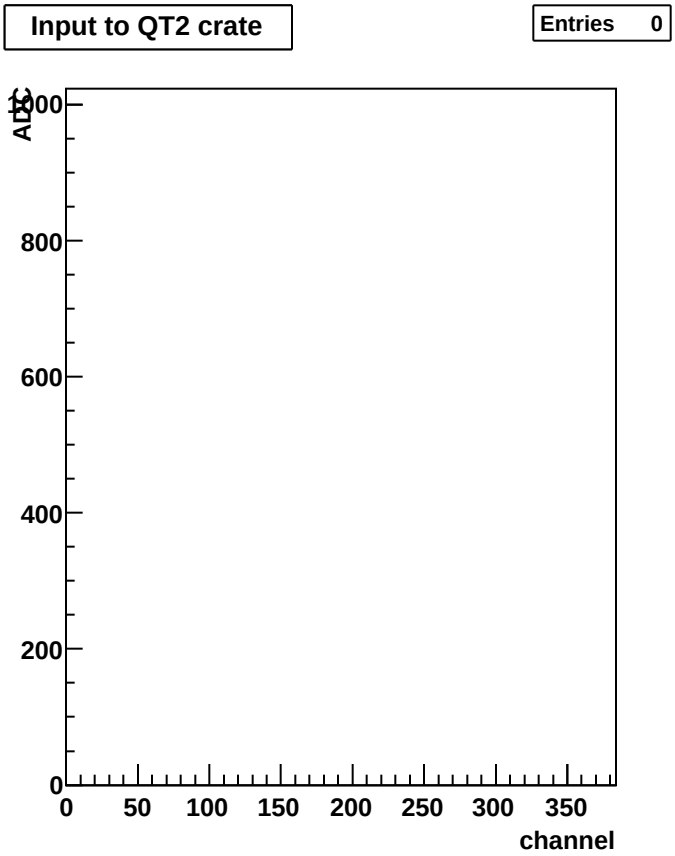

Entries 738208
BBC-L-East ADC Sum
BBC-L-West ADC Sum

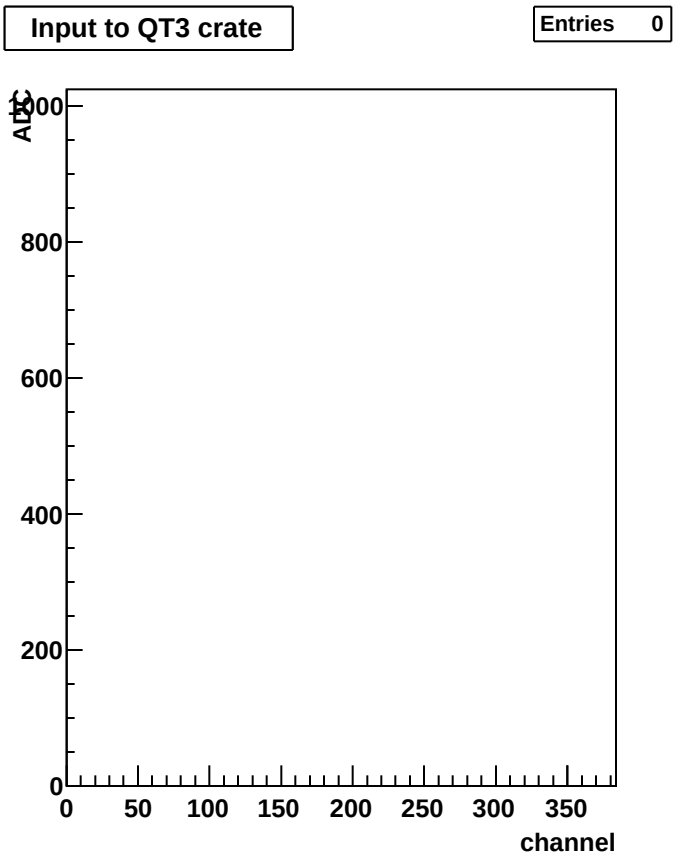

Entries 738208
BBC-S-East ADC Sum
BBC-S-West ADC Sum

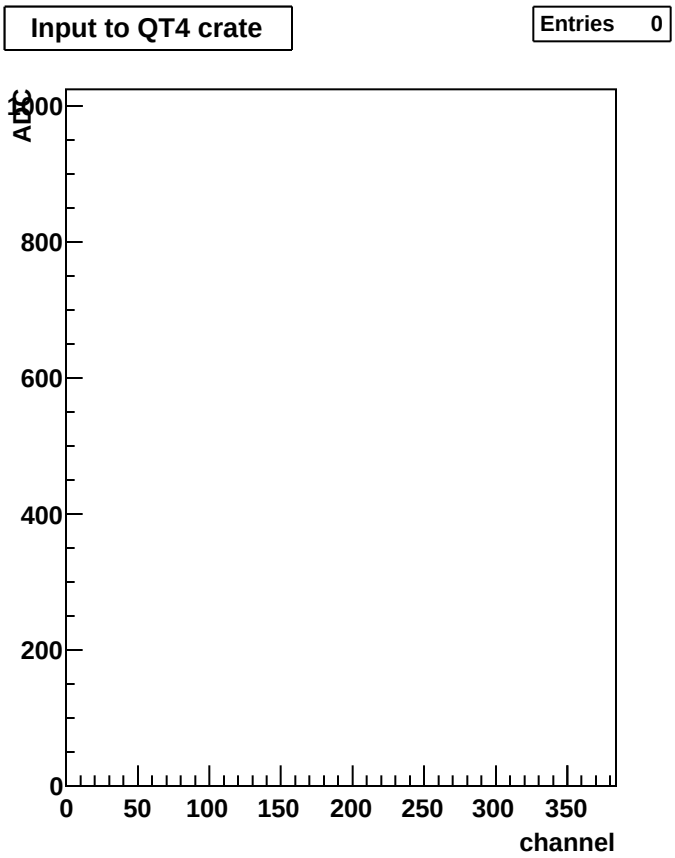

Entries 738208
ZDC East ADC Sum Att
ZDC West ADC Sum Att

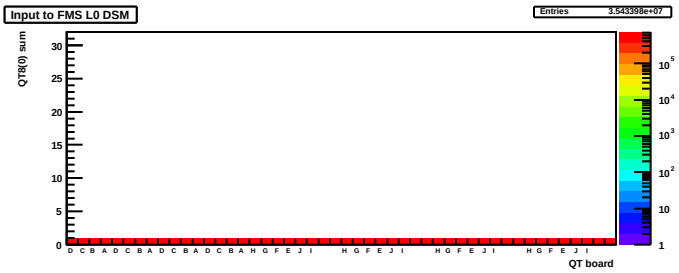

Entries 738208
BBC-L TAC Diff
BCC-S TAC Diff
Entries 738208
BBC-L TAC Diff
VPD TAC Diff
Entries 738208
BBC-L TAC Diff
ZDC TAC Diff
Entries 738208
BBC-S TAC Diff
VPD TAC Diff
Entries 738208
BBC-S TAC Diff
ZDC TAC Diff
Entries 738208
VPD TAC Diff
ZDC TAC Diff

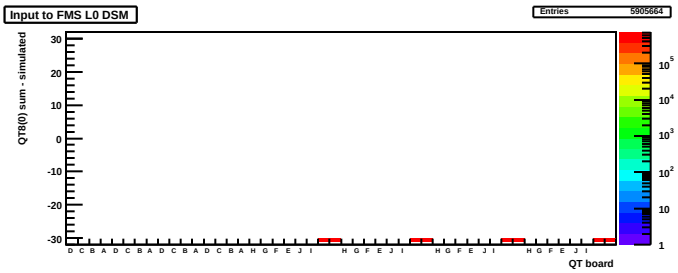

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

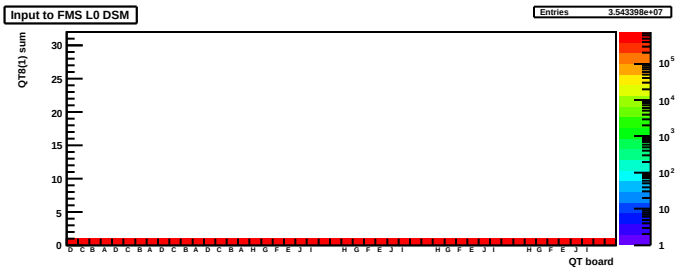

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

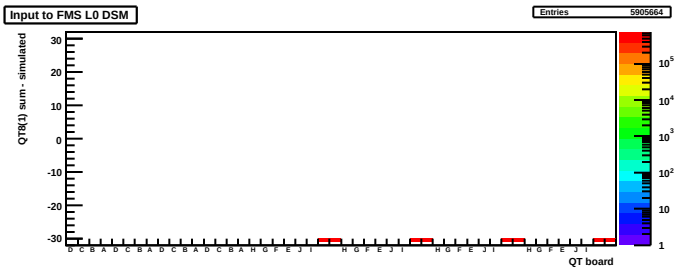

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

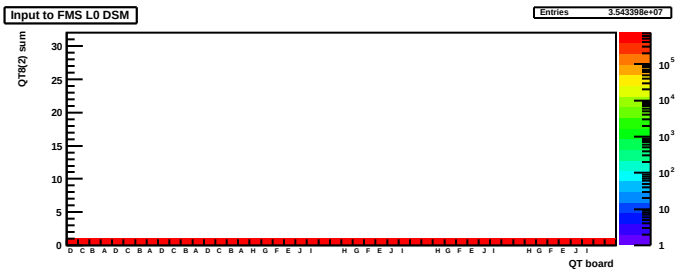

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

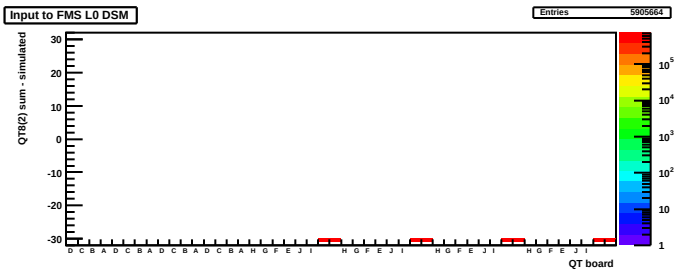

Input to FMS L0 DSM
Entries 3.543398e+07
QT8(0) sum
QT board

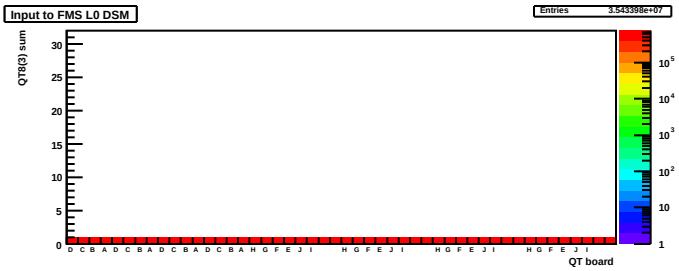

Input to FMS L0 DSM
Entries 5905664
QT8(0) sum - simulated
QT board

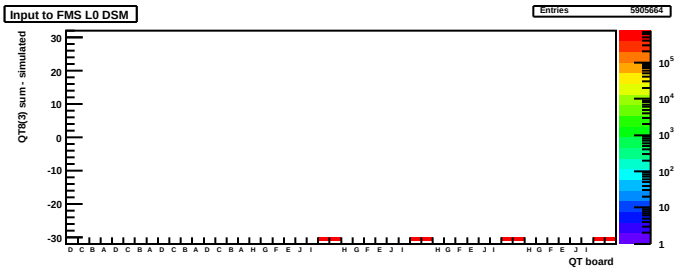

Input to FMS L0 DSM
Entries 3.543398e+07
QT8(1) sum
QT board

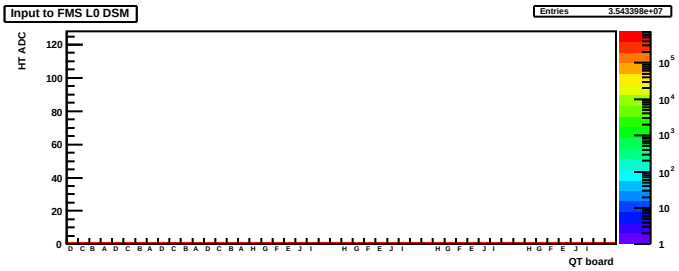

Input to FMS L0 DSM
Entries 5905664
QT8(1) sum - simulated
QT board

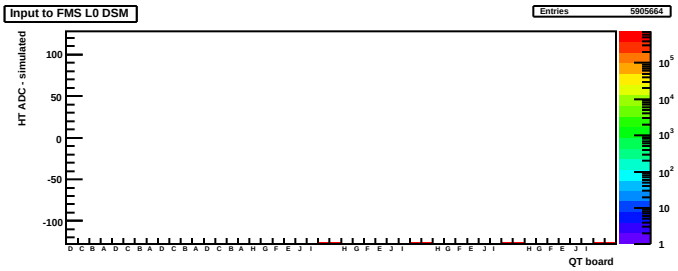

Input to FMS L0 DSM
Entries 3.543398e+07
QT8(2) sum
QT board

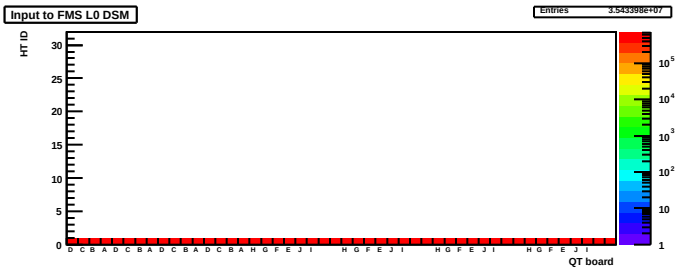

Input to FMS L0 DSM
Entries 5905664
QT8(2) sum - simulated
QT board

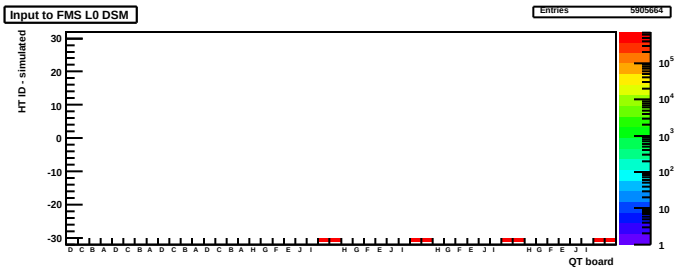

Input to FMS L0 DSM
Entries 3.543398e+07
QT8(3) sum
QT board

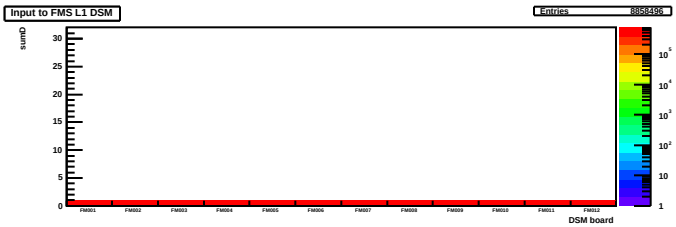

Input to FMS L0 DSM
Entries 5905664
QT8(3) sum - simulated
QT board

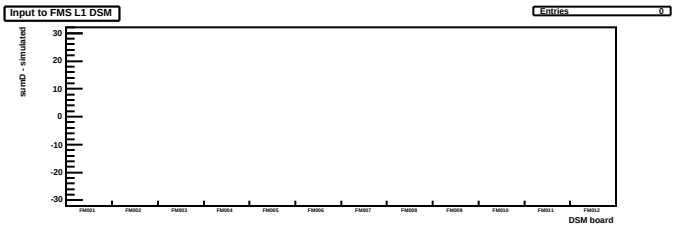

Input to FMS L0 DSM
Entries 3.543398e+07
HT ADC
QT board

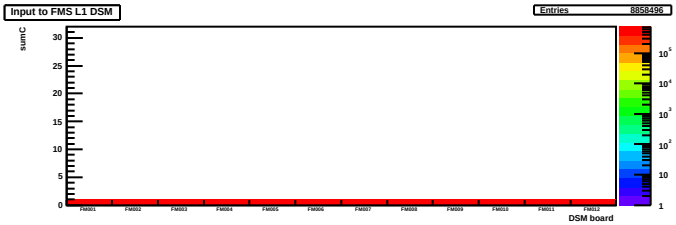

Input to FMS L0 DSM
Entries 5905664
HT ADC - simulated
QT board

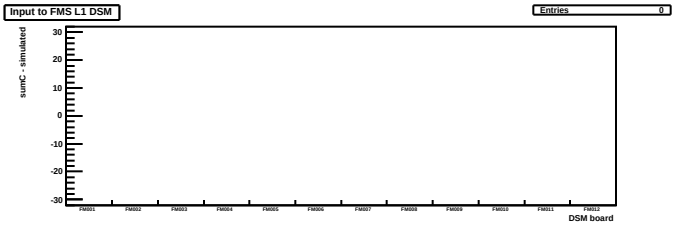

Input to FMS L0 DSM
Entries 3.543398e+07
HT ID
QT board

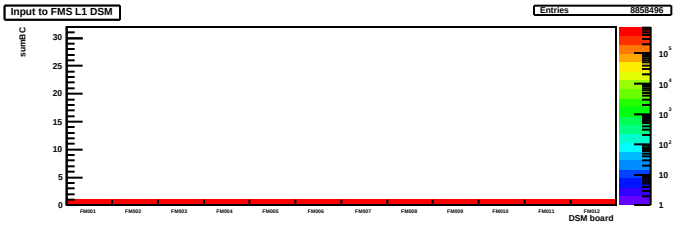

Input to FMS L0 DSM
Entries 5905664
HT ID - simulated
QT board

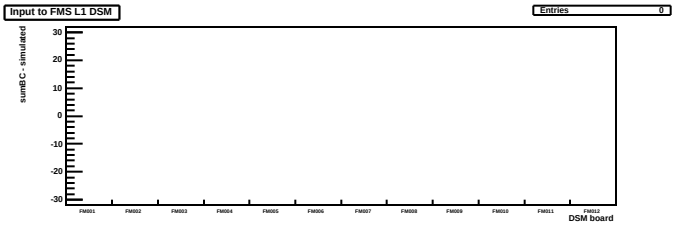

Input to FMS L1 DSM
Entries 8858496
sumD
DSM board

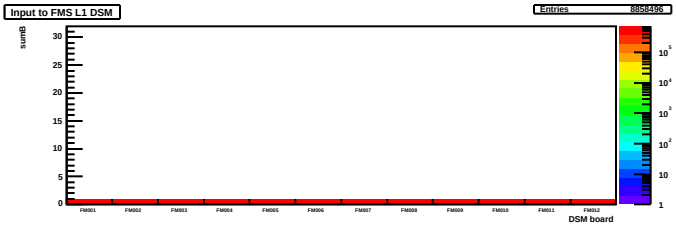

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

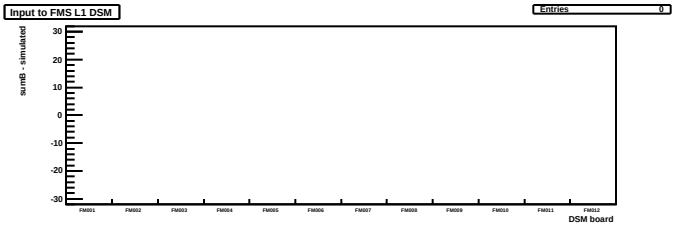

Input to FMS L1 DSM
Entries 8858496
sumC
DSM board

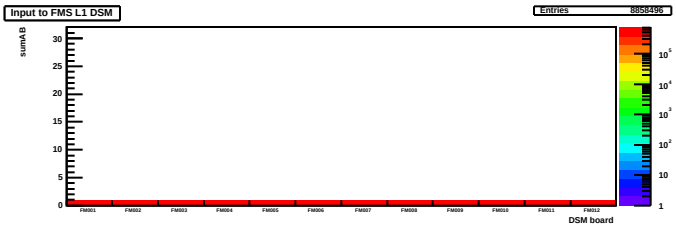

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

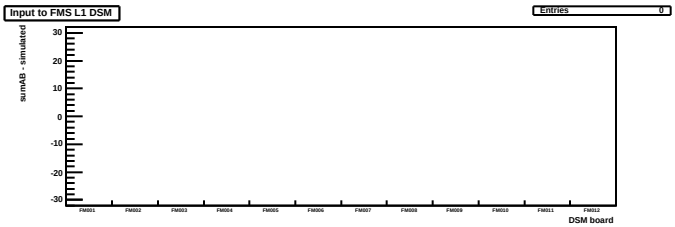

Input to FMS L1 DSM
Entries 8858496
sumBC
DSM board

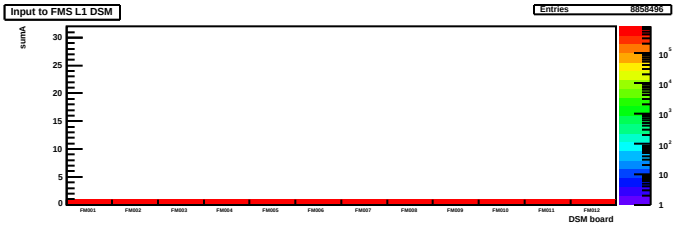

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

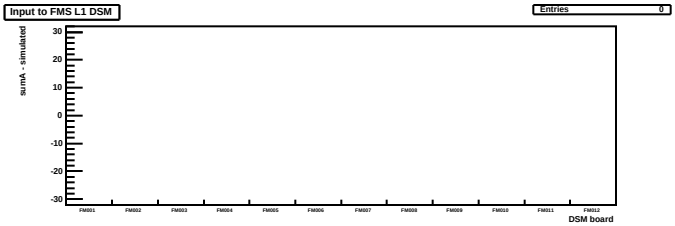

Input to FMS L1 DSM
Entries 8858496
sumB
DSM board

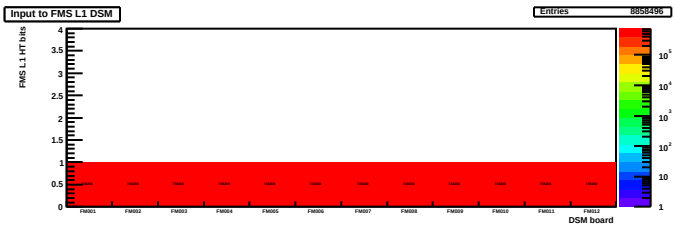

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

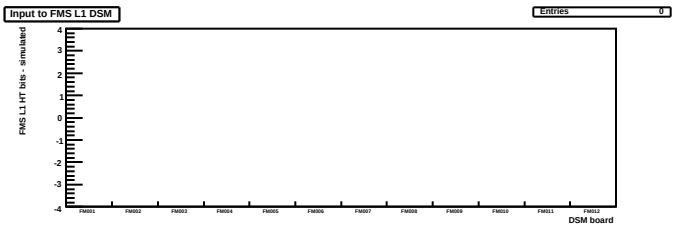

Input to FMS L1 DSM
Entries 8858496
sumAB
DSM board

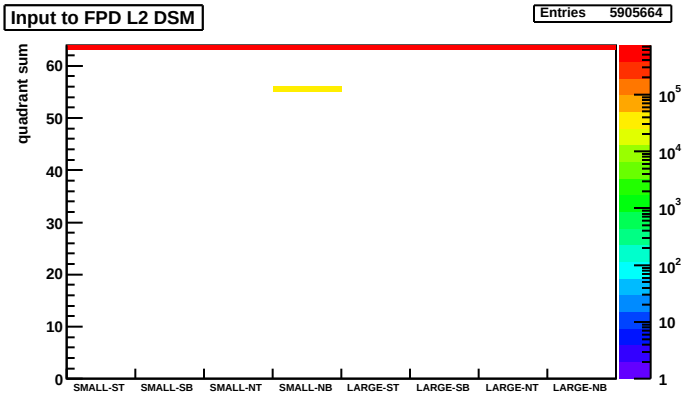

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

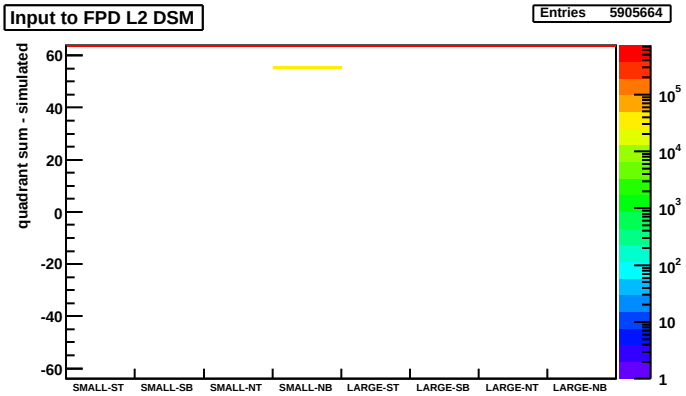

Input to FMS L1 DSM
Entries 8858496
sumA
DSM board

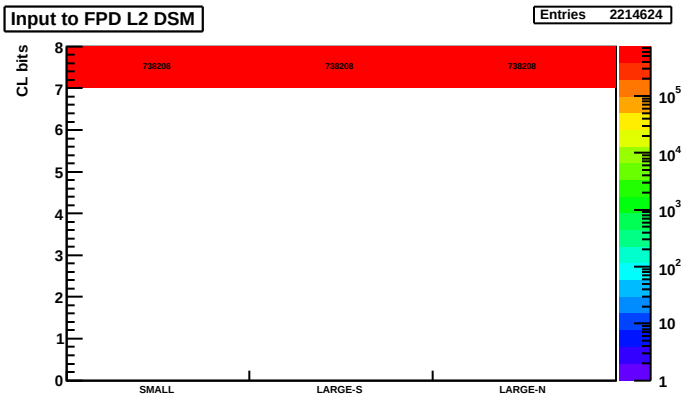

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

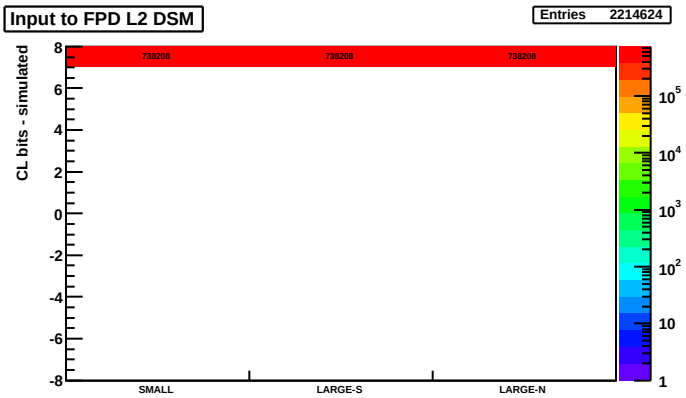

Input to FMS L1 DSM
Entries 8858496
FMS L1 HT bits
DSM board

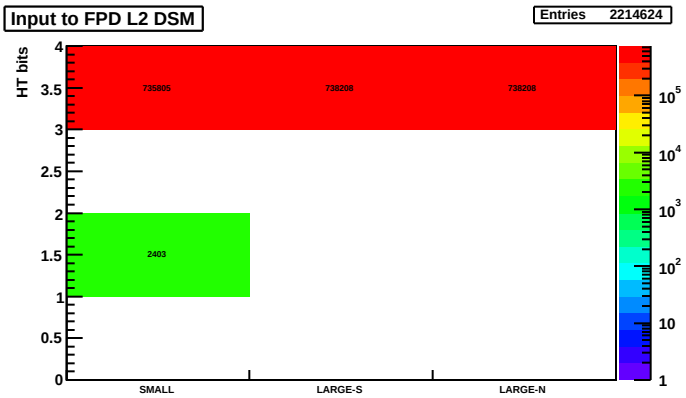

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

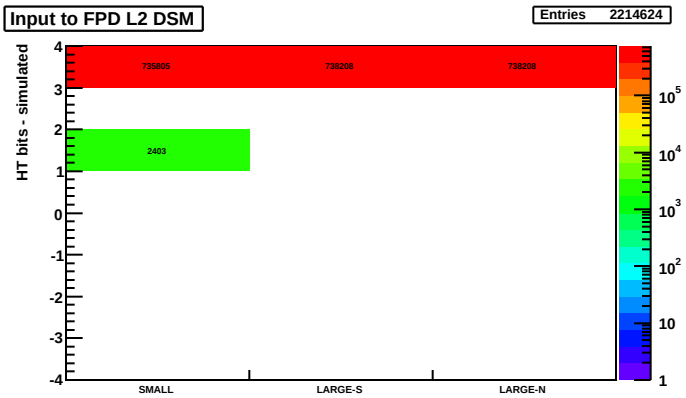

Input to FPD L2 DSM
Entries 5905664
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

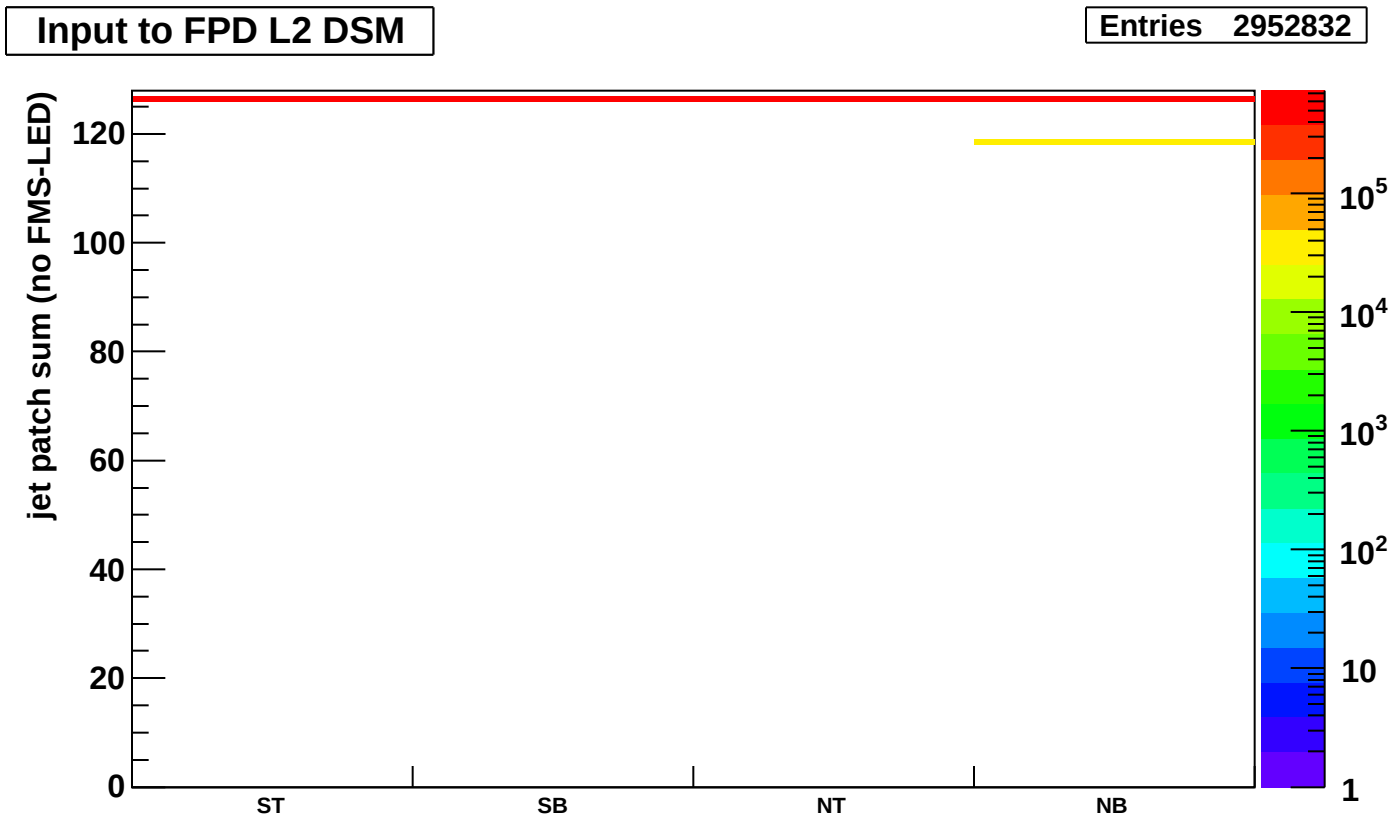

Input to FPD L2 DSM
Entries 5905664
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

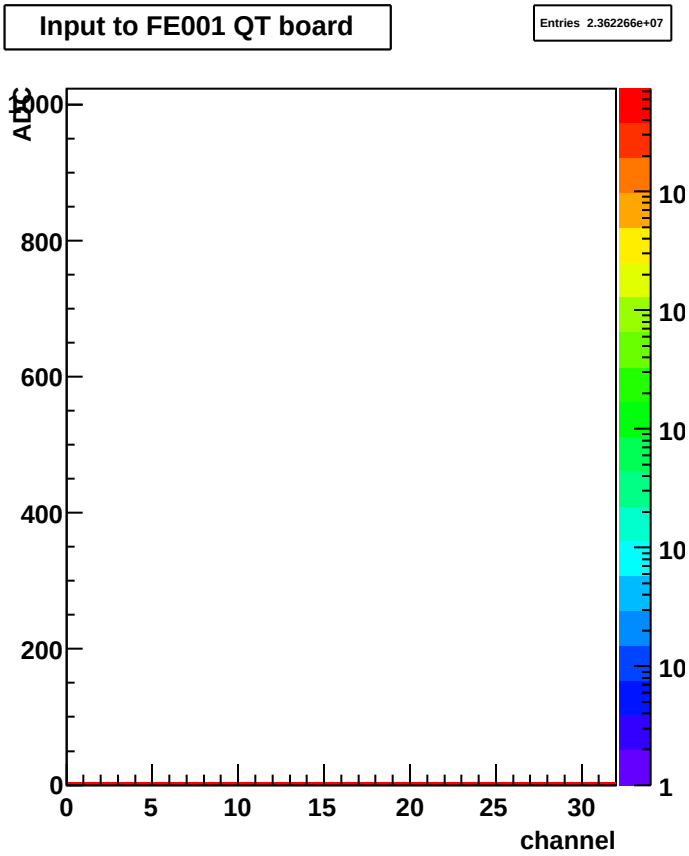

Input to FPD L2 DSM
Entries 2214624
CL bits
738208
738208
738208
SMALL
LARGE-S
LARGE-N

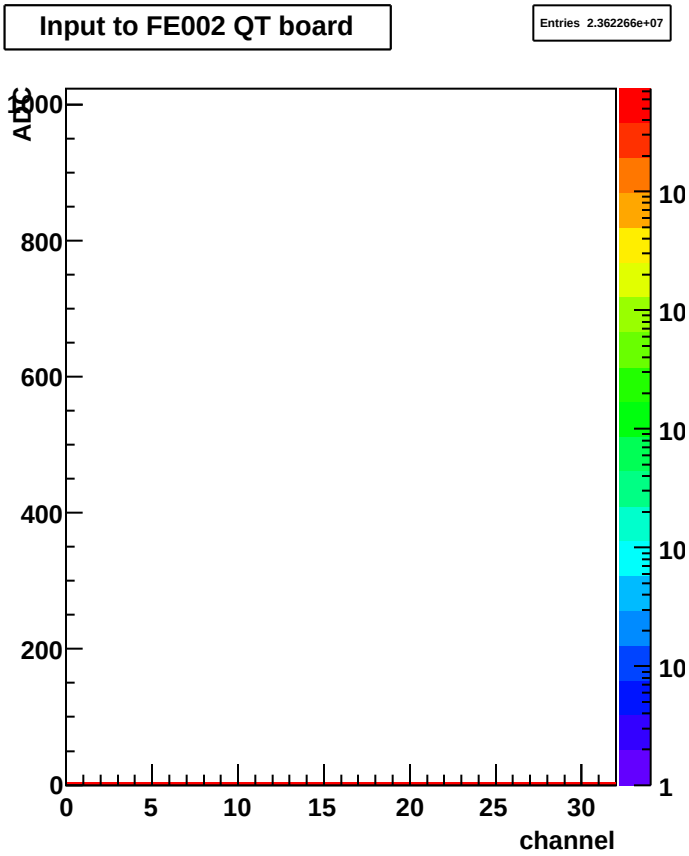

Input to FPD L2 DSM
Entries 2214624
CL bits - simulated
738208
738208
738208
SMALL
LARGE-S
LARGE-N

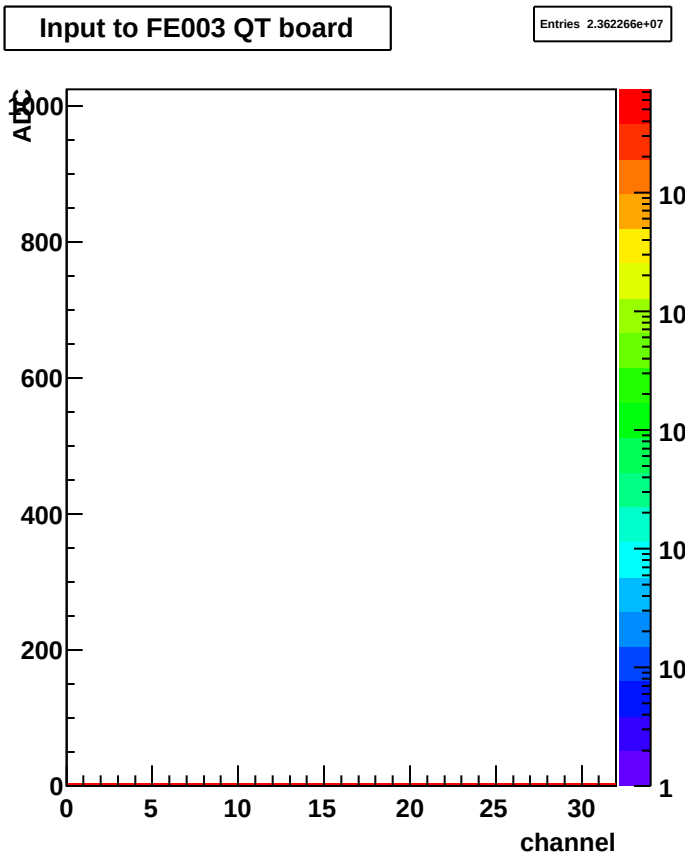

Input to FPD L2 DSM
Entries 2214624
HT bits
735805
738208
738208
2403
SMALL
LARGE-S
LARGE-N

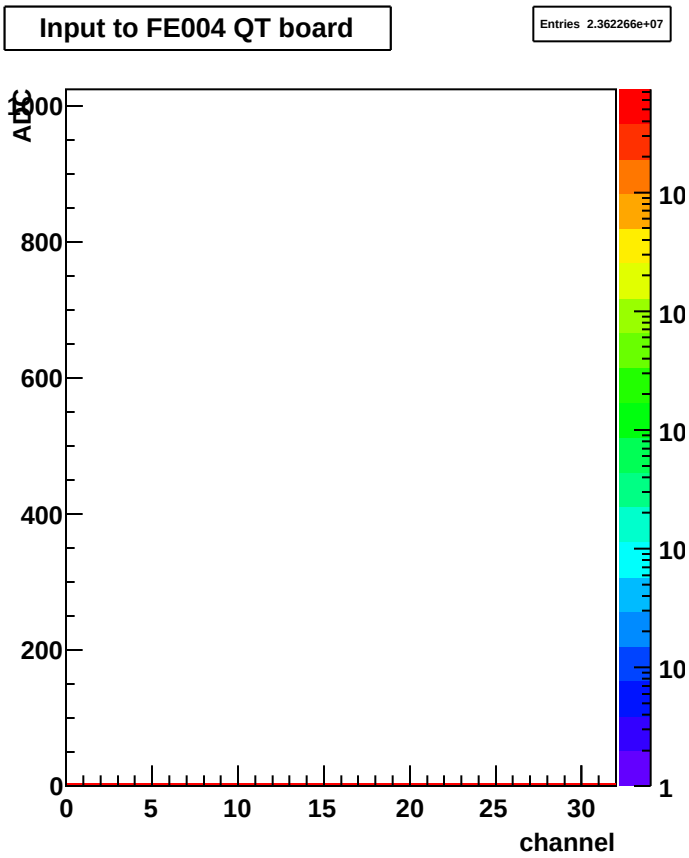

Input to FPD L2 DSM
Entries 2214624
HT bits - simulated
735805
738208
738208
2403
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2952832
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

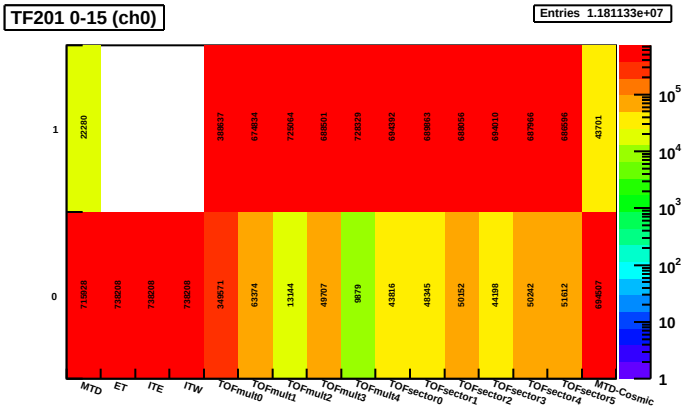

Input to FE001 QT board
Entries 2.362266e+07
ADC
channel


Input to FE002 QT board
Entries 2.362266e+07
ADC
channel

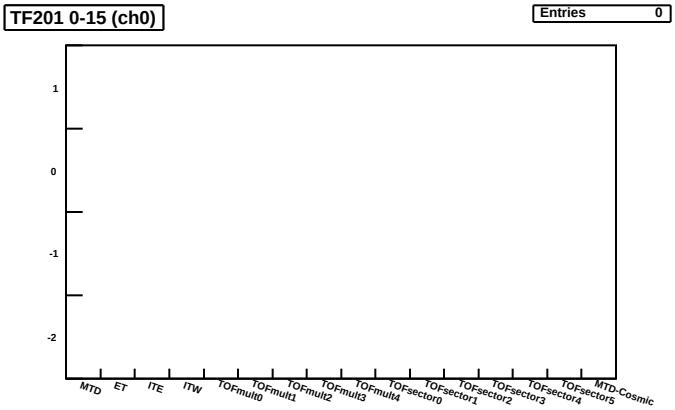

Input to FE003 QT board
Entries 2.362266e+07
ADC
channel


Input to FE004 QT board
Entries 2.362266e+07
ADC
channel

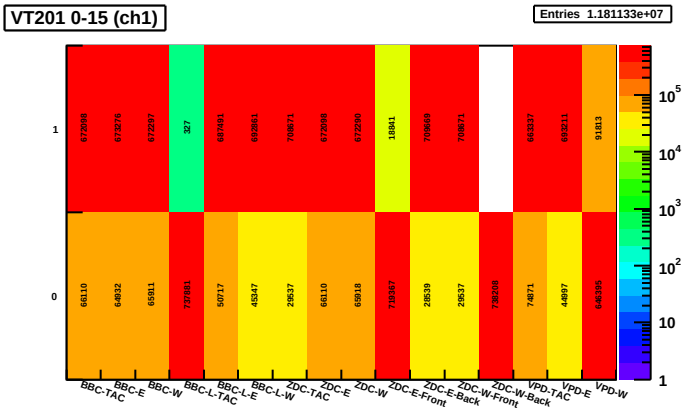
TF201 0-15 (ch0)



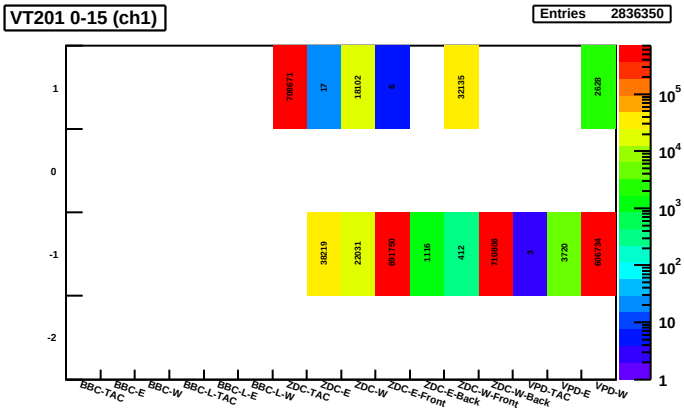
TF201 0-15 (ch0)



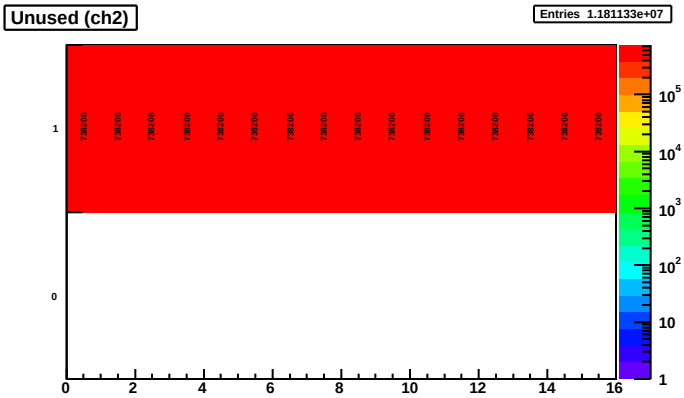
VT201 0-15 (ch1)

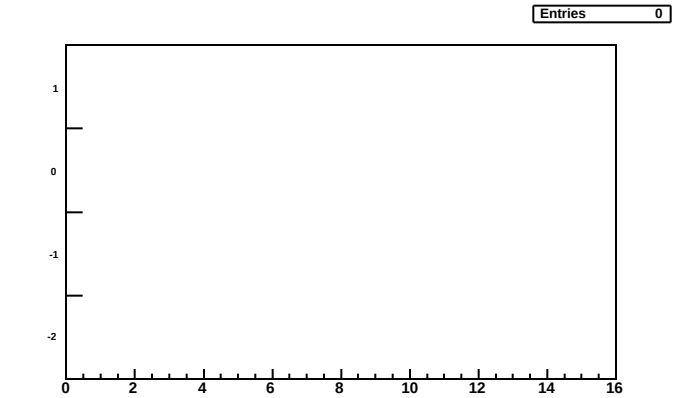


VT201 0-15 (ch1)

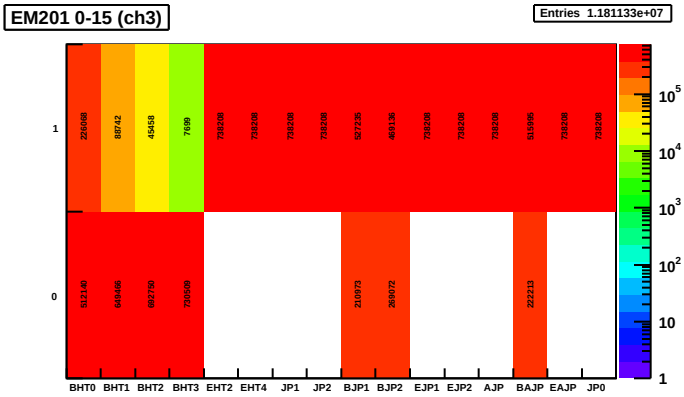


Unused (ch2)

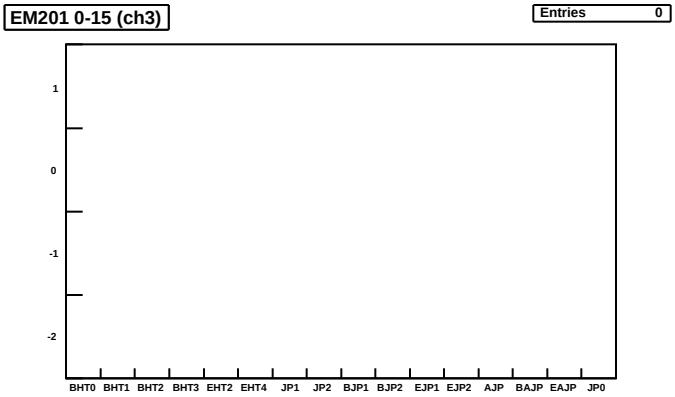




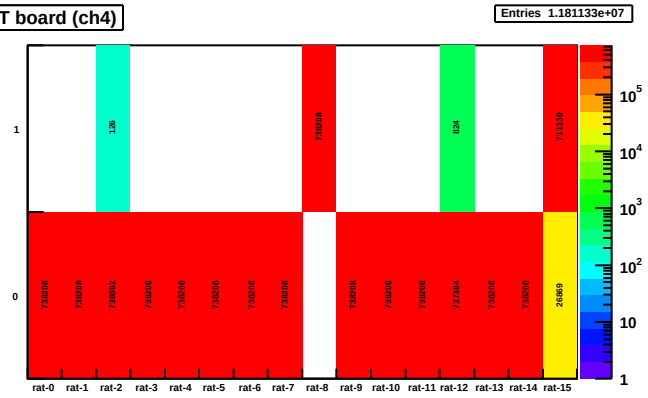
EM201 0-15 (ch3)

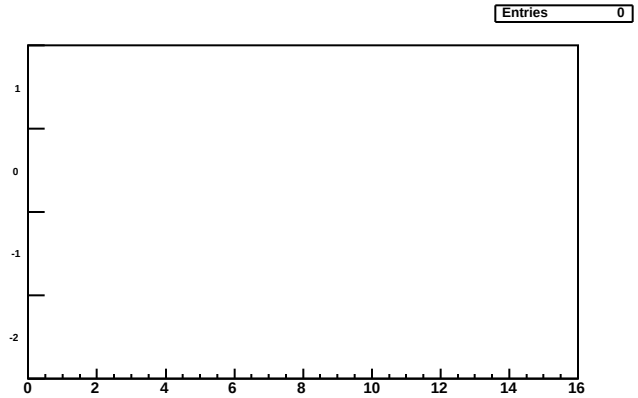


EM201 0-15 (ch3)

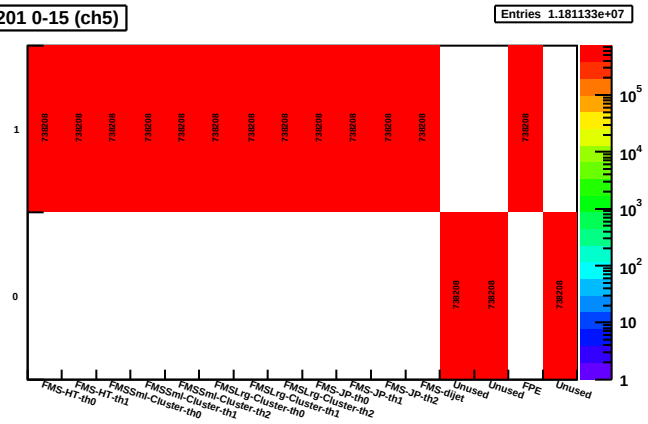


RAT board (ch4)

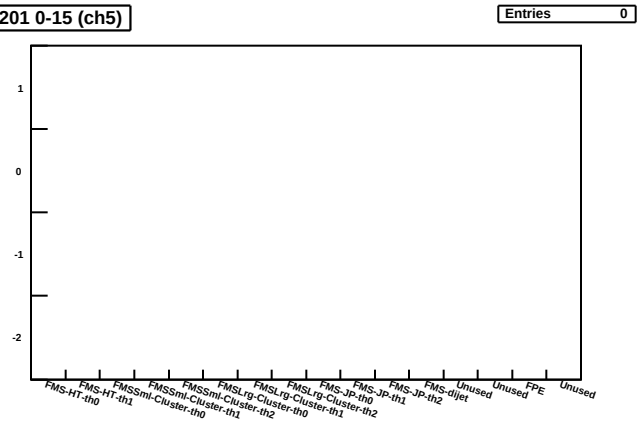




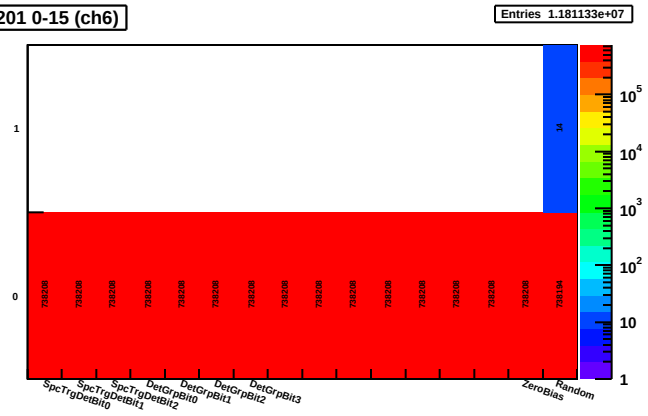
FP201 0-15 (ch5)

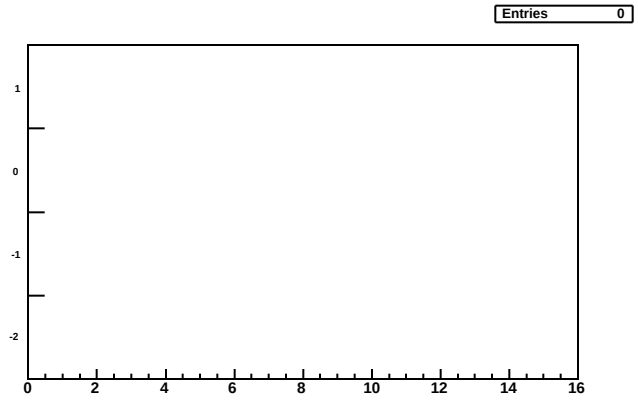
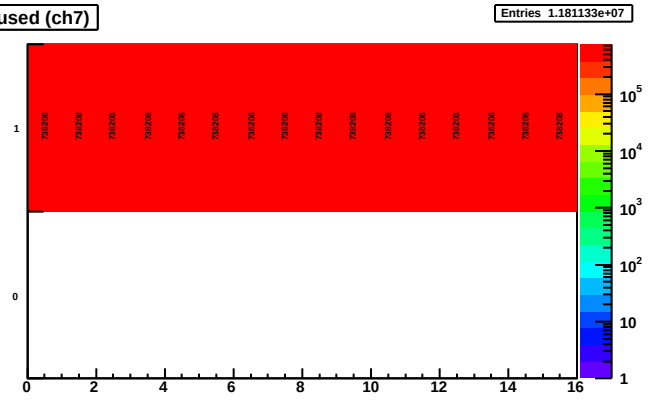
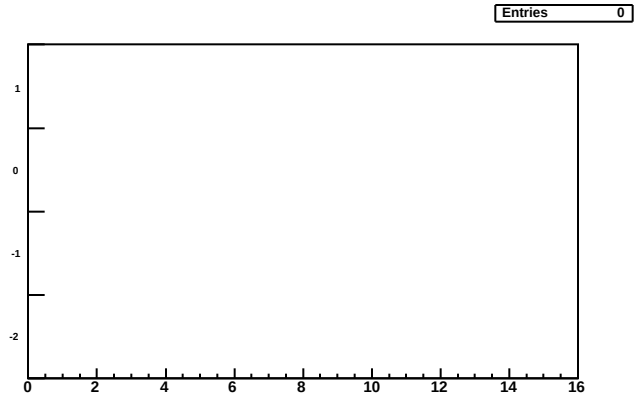


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)