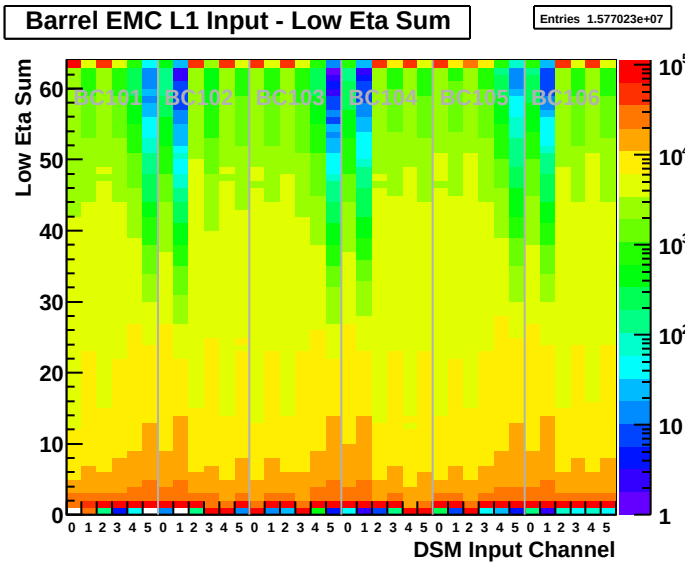
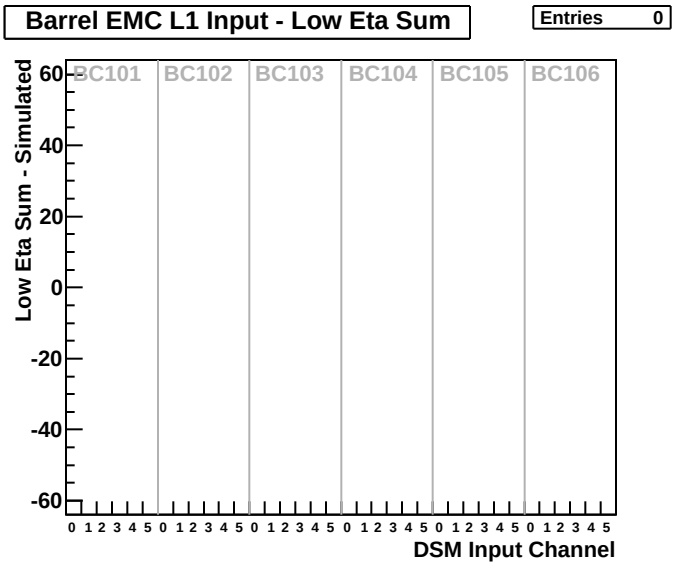
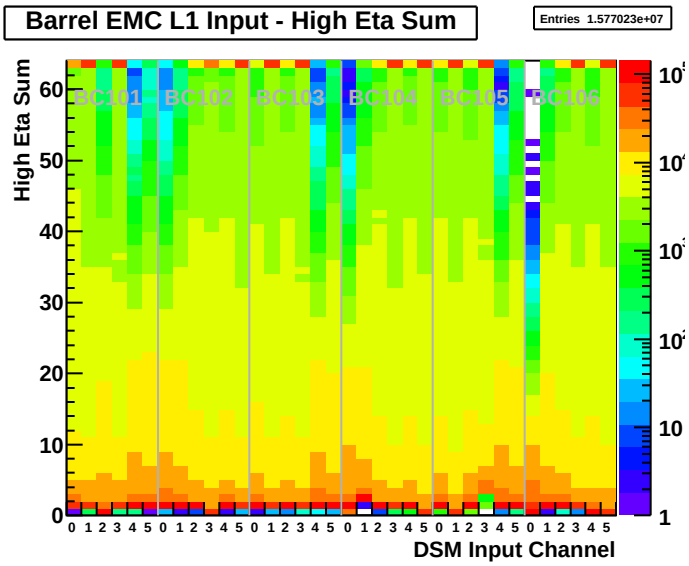
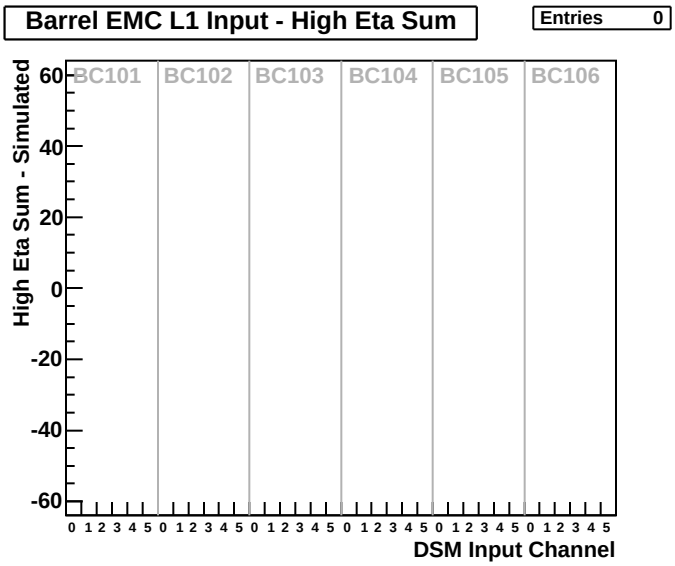
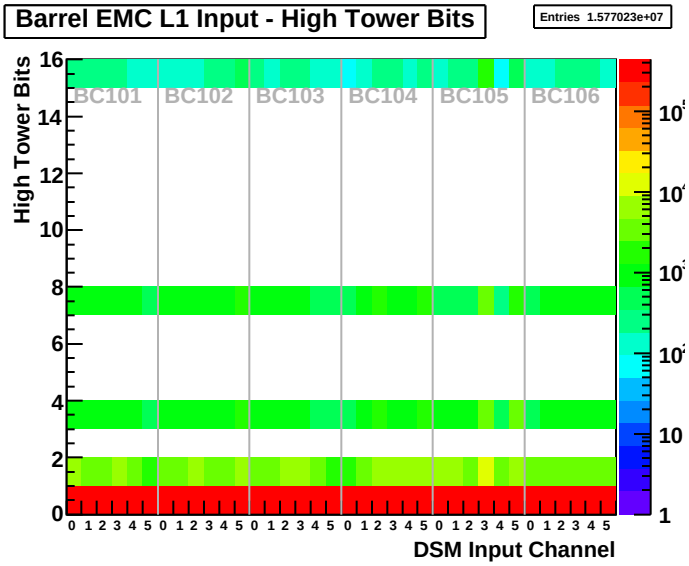
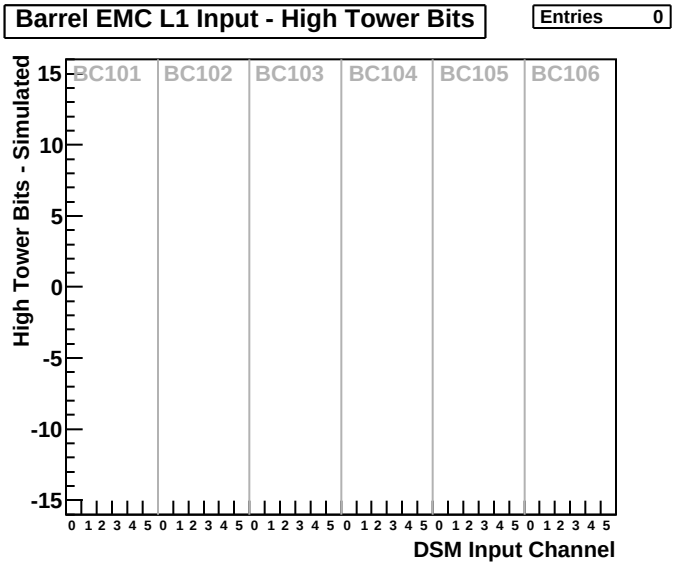

Barrel EMC L1 Input - Low Eta Sum
Entries 1.577023e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

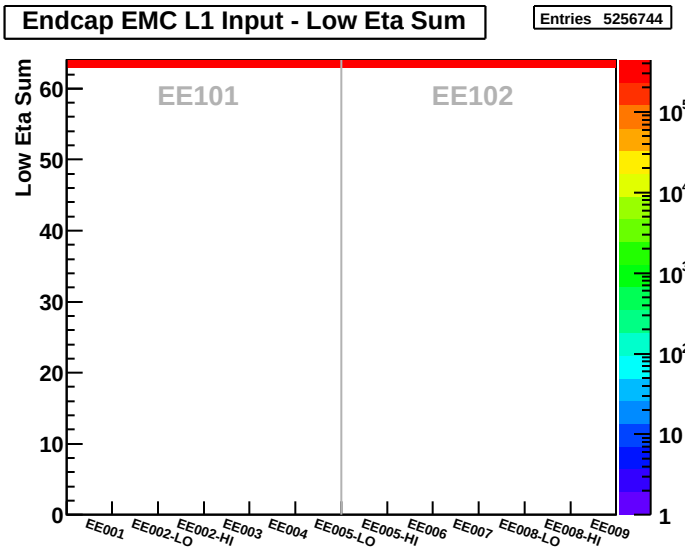

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

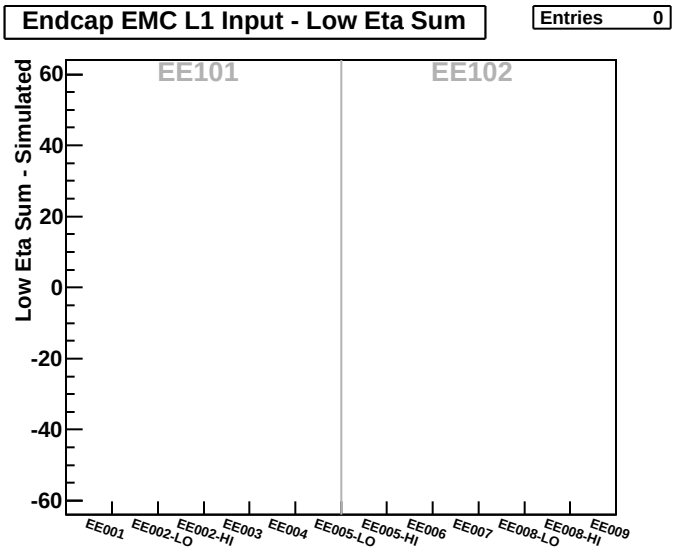

Barrel EMC L1 Input - High Eta Sum
Entries 1.577023e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

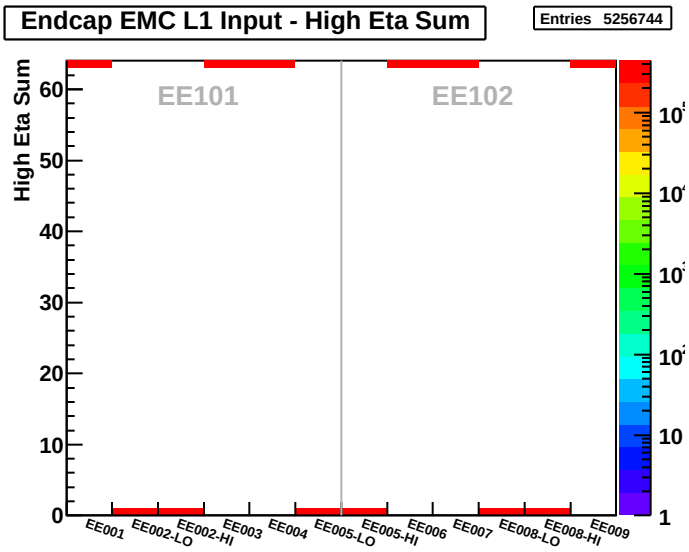

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

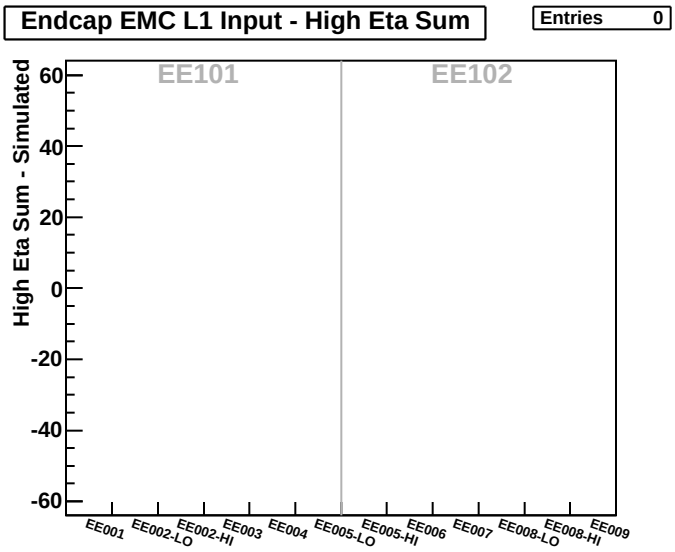

Barrel EMC L1 Input - High Tower Bits
Entries 1.577023e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

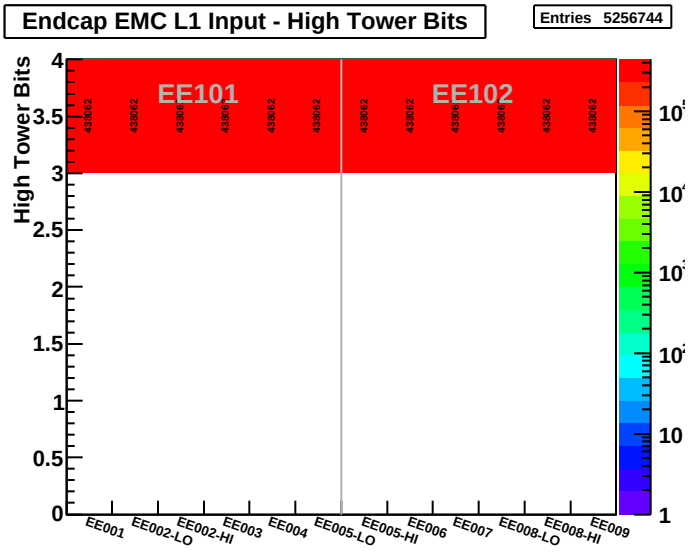

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

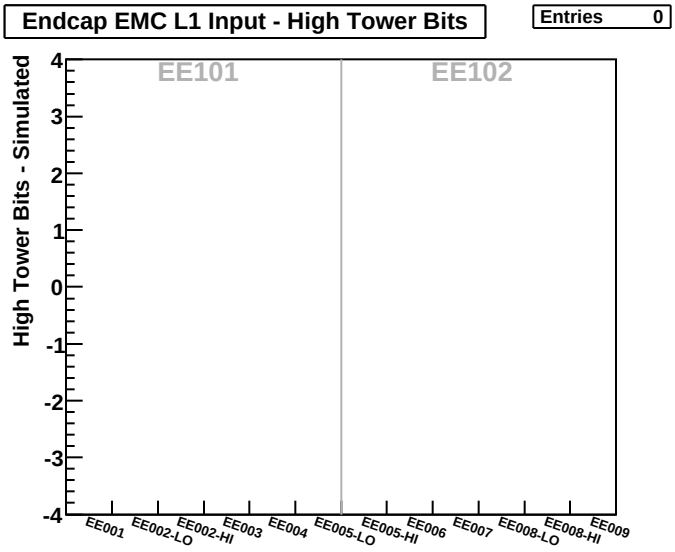

Endcap EMC L1 Input - Low Eta Sum
Entries 5256744
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

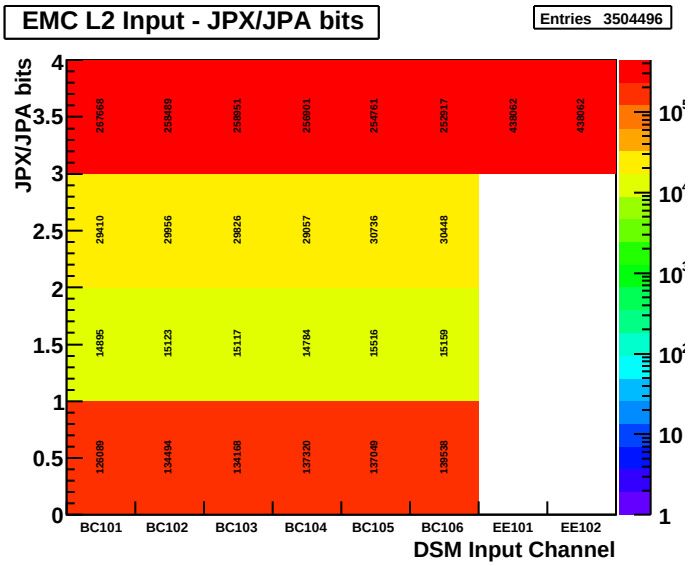

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

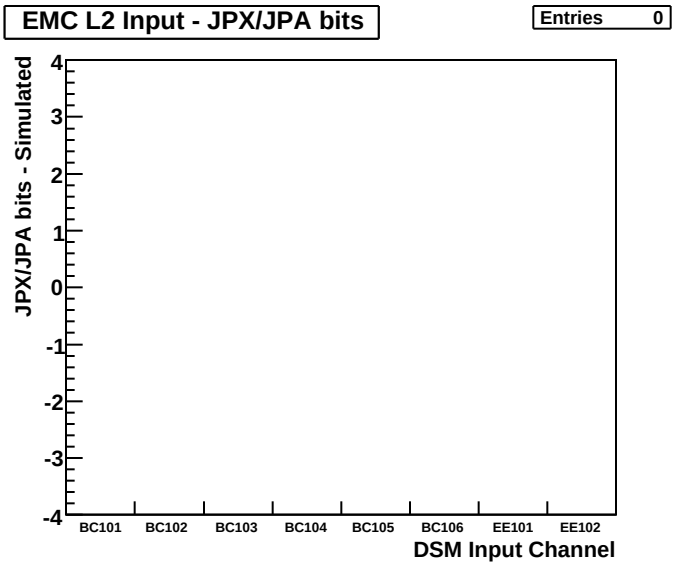

Endcap EMC L1 Input - High Eta Sum
Entries 5256744
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

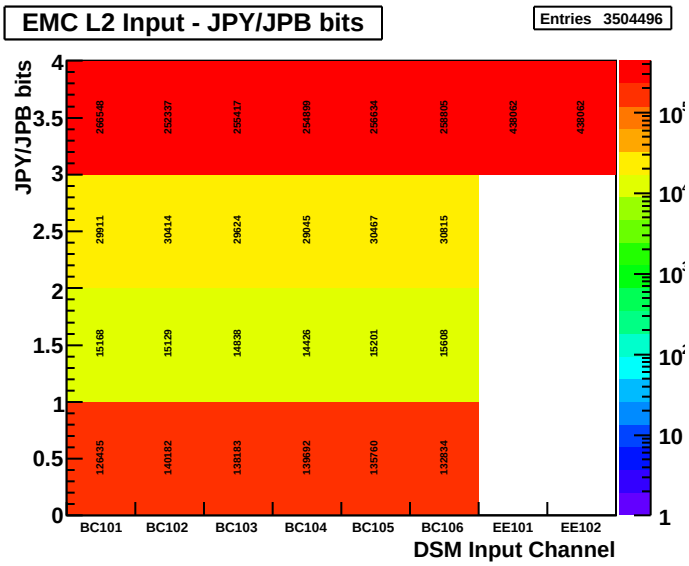

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

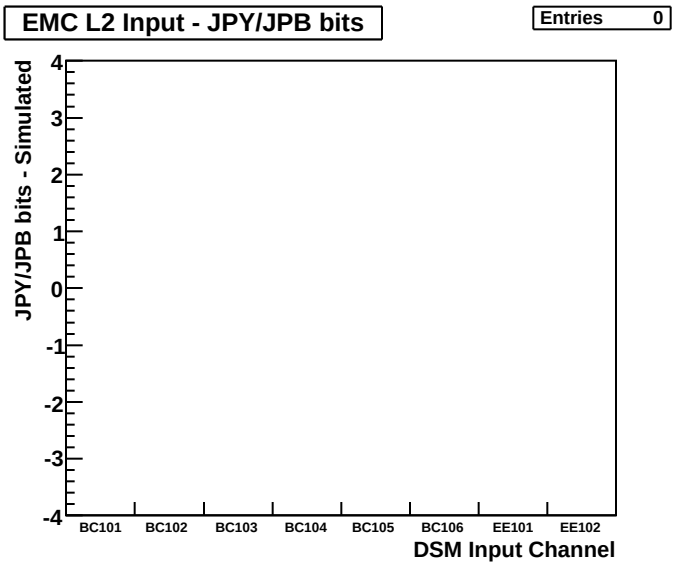

Endcap EMC L1 Input - High Tower Bits
Entries 5256744
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

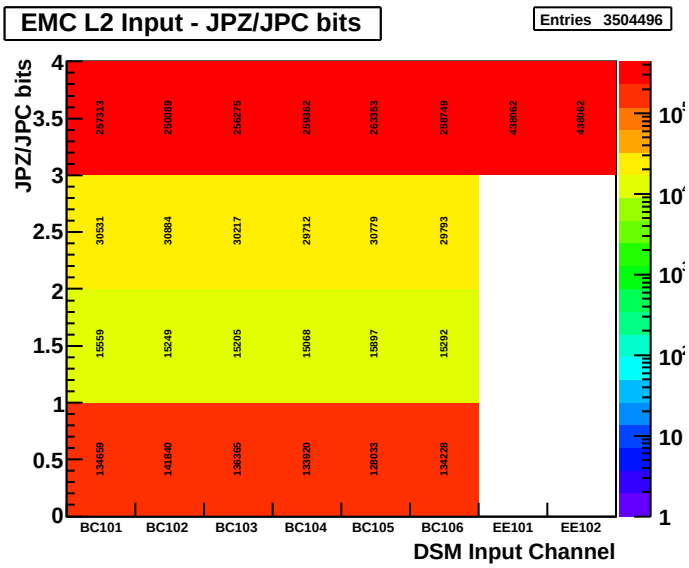

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

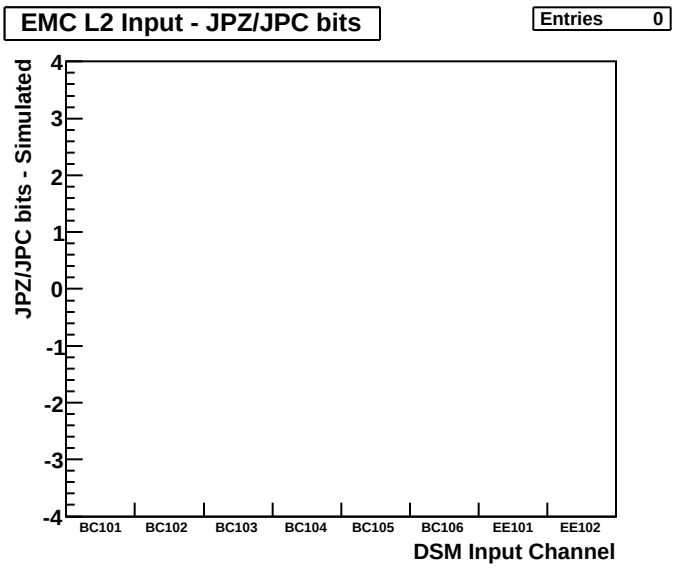

EMC L2 Input - JPX/JPA bits
Entries 3504496
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
257668 258489 258851 256801 254761 252917 438062 438062
29410 29956 29826 29057 30736 30448
14895 15123 15117 14784 15516 15159
126089 134494 134168 137320 137049 139538

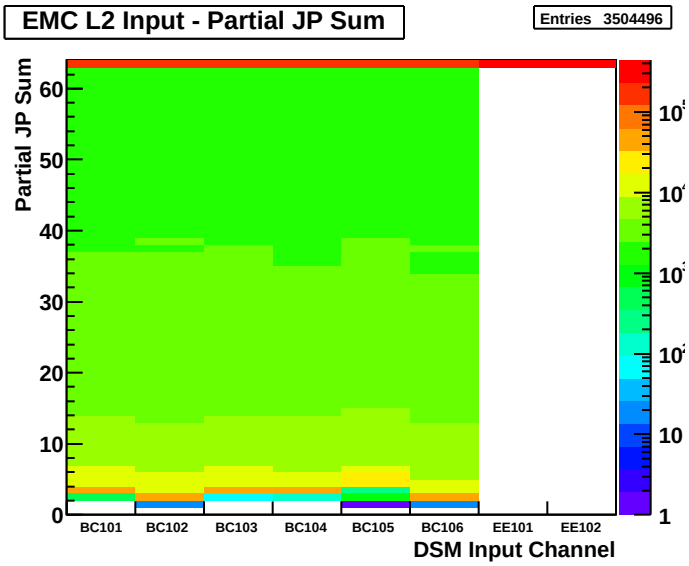

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

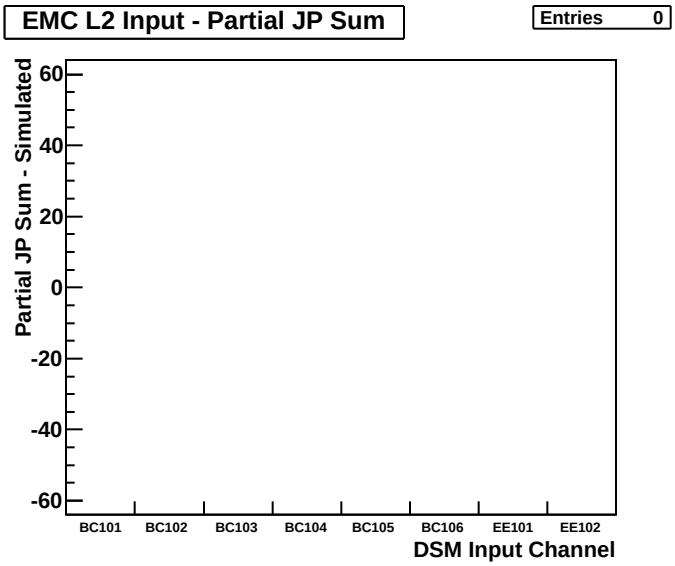

EMC L2 Input - JPY/JPB bits
Entries 3504496
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
266548 252337 255417 254899 256634 258805 438062 438062
29911 30414 29624 29045 30467 30815
15168 15129 14838 14426 15201 15608
126435 140182 138183 139692 135760 132834

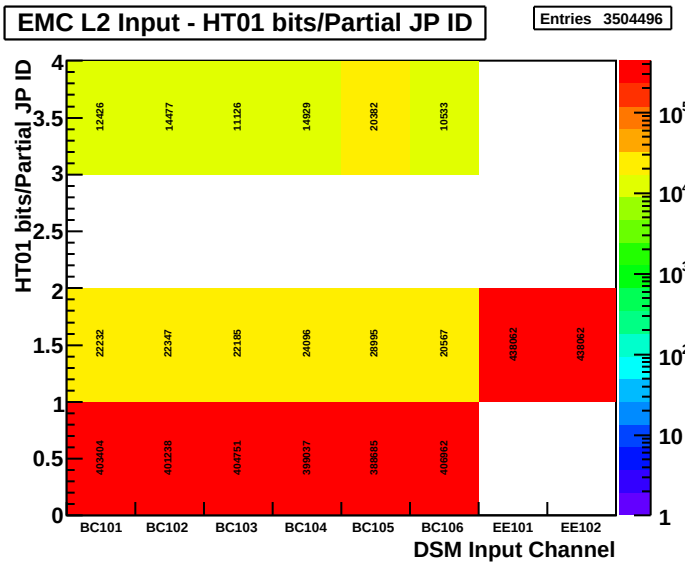

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

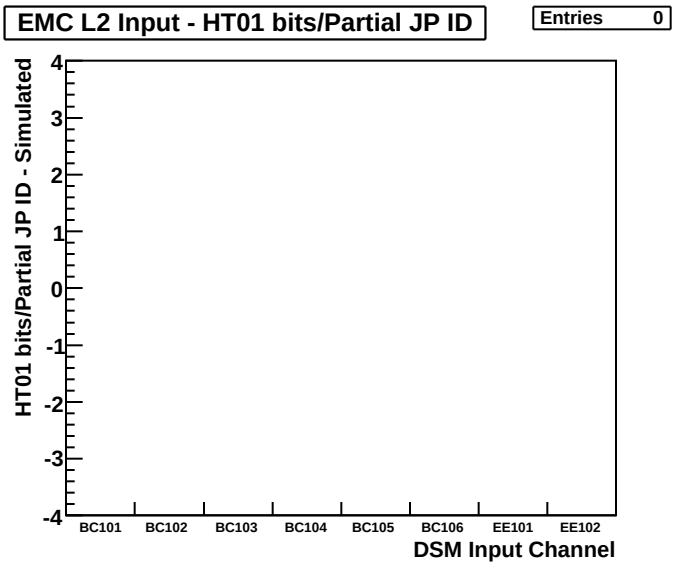

EMC L2 Input - JPZ/JPC bits
Entries 3504496
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
257313 250089 258275 253862 263353 259749 438062 438062
30531 30884 30217 29712 30779 29793
15559 15249 15205 15068 15897 15292
134559 141840 136366 133920 128033 134228

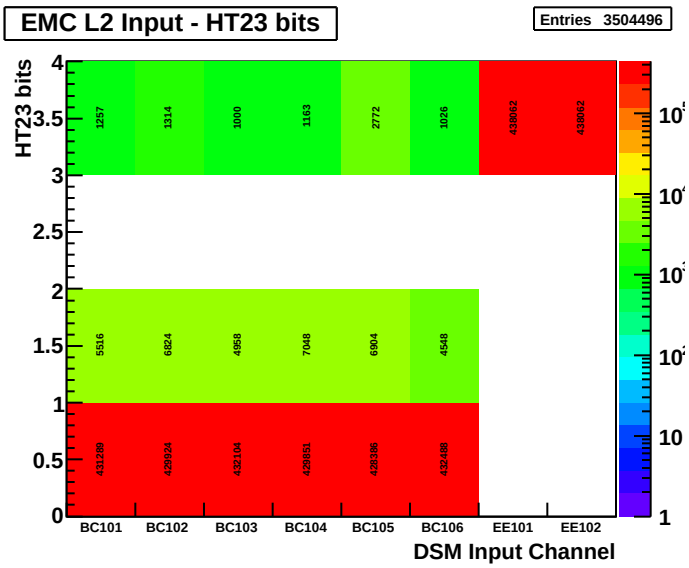

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

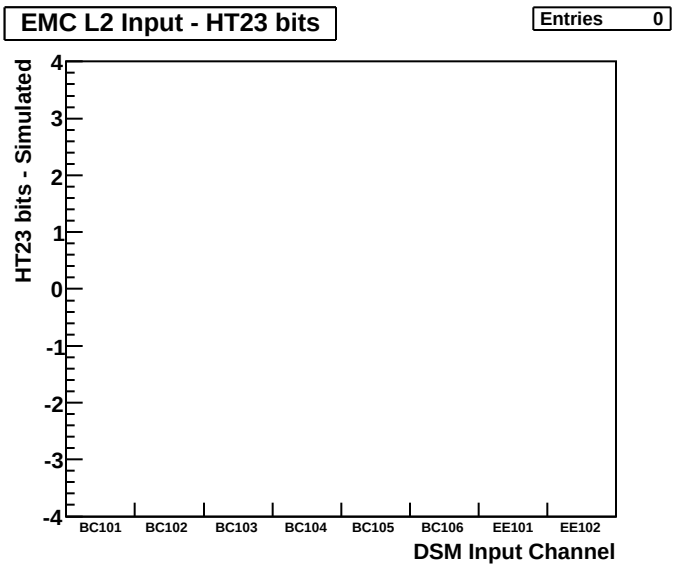

EMC L2 Input - Partial JP Sum
Entries 3504496
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

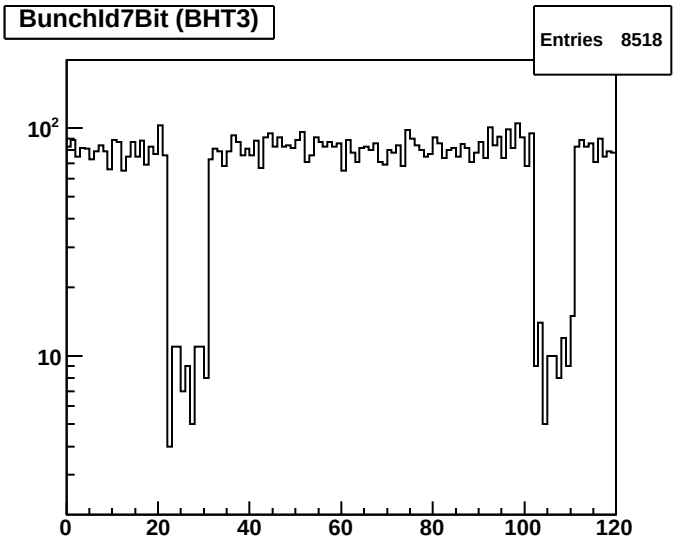

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

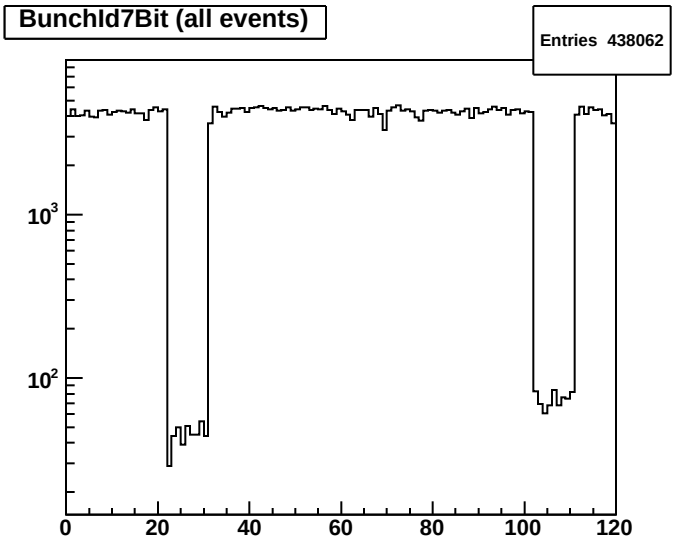

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3504496
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

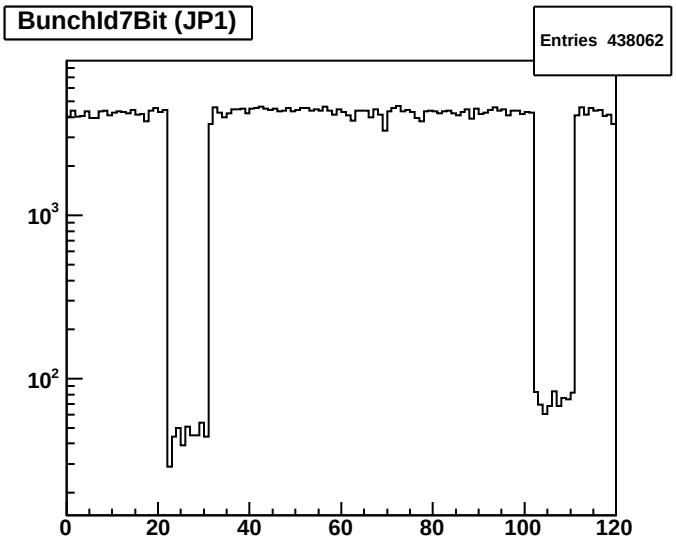

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

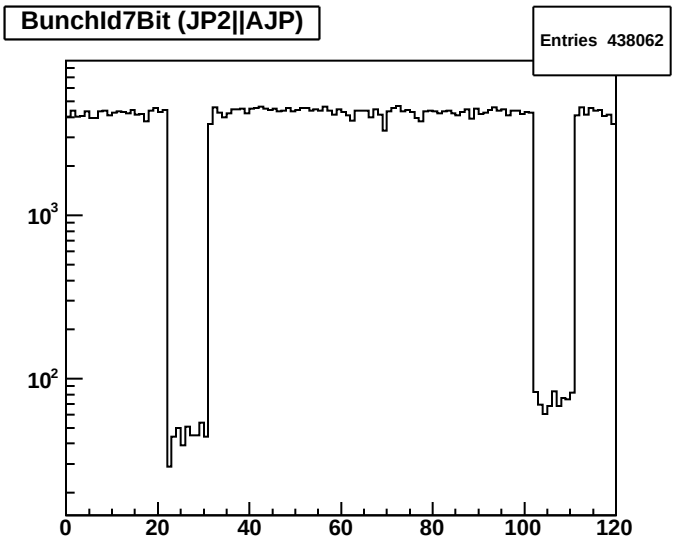

EMC L2 Input - HT23 bits
Entries 3504496
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

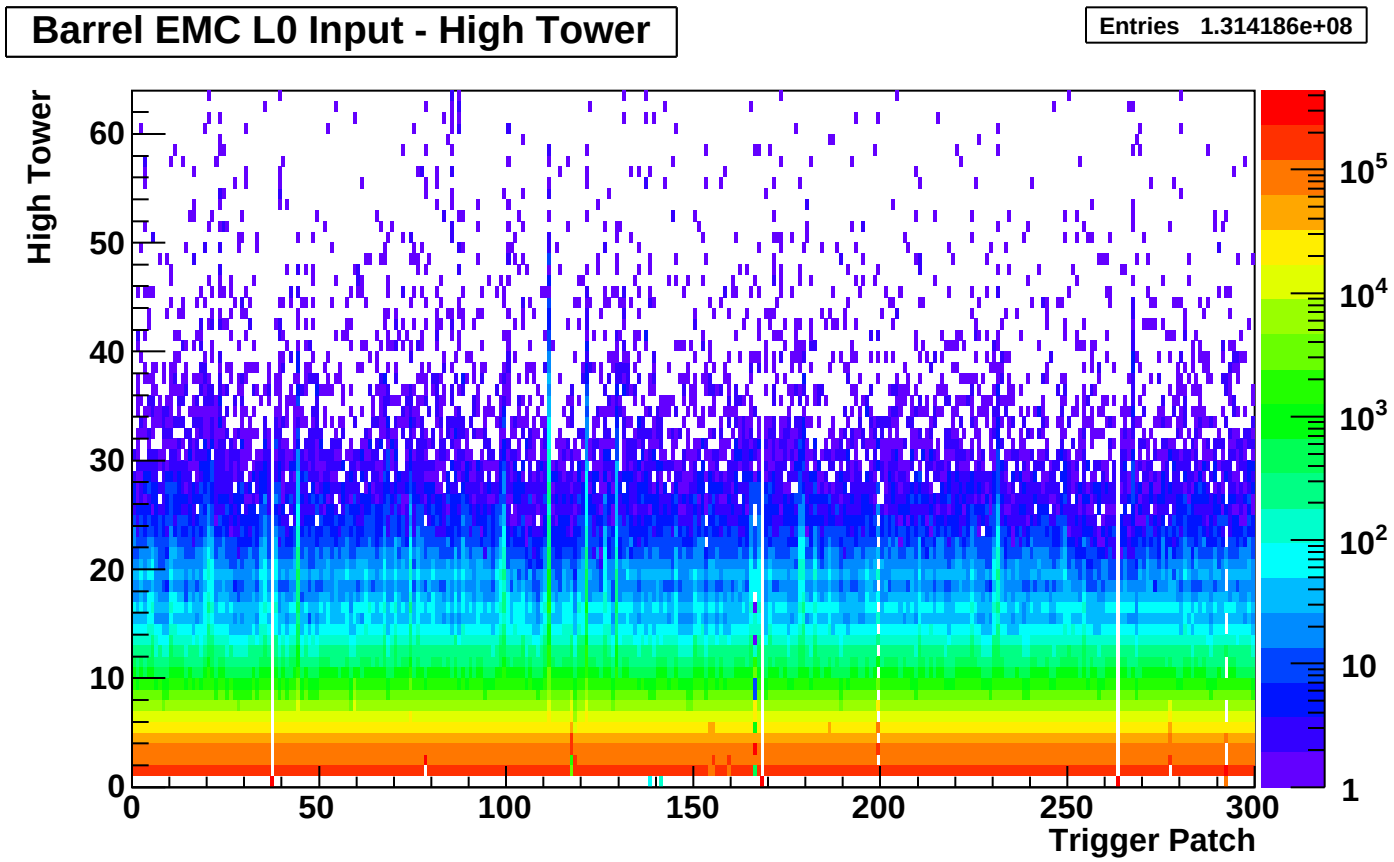

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

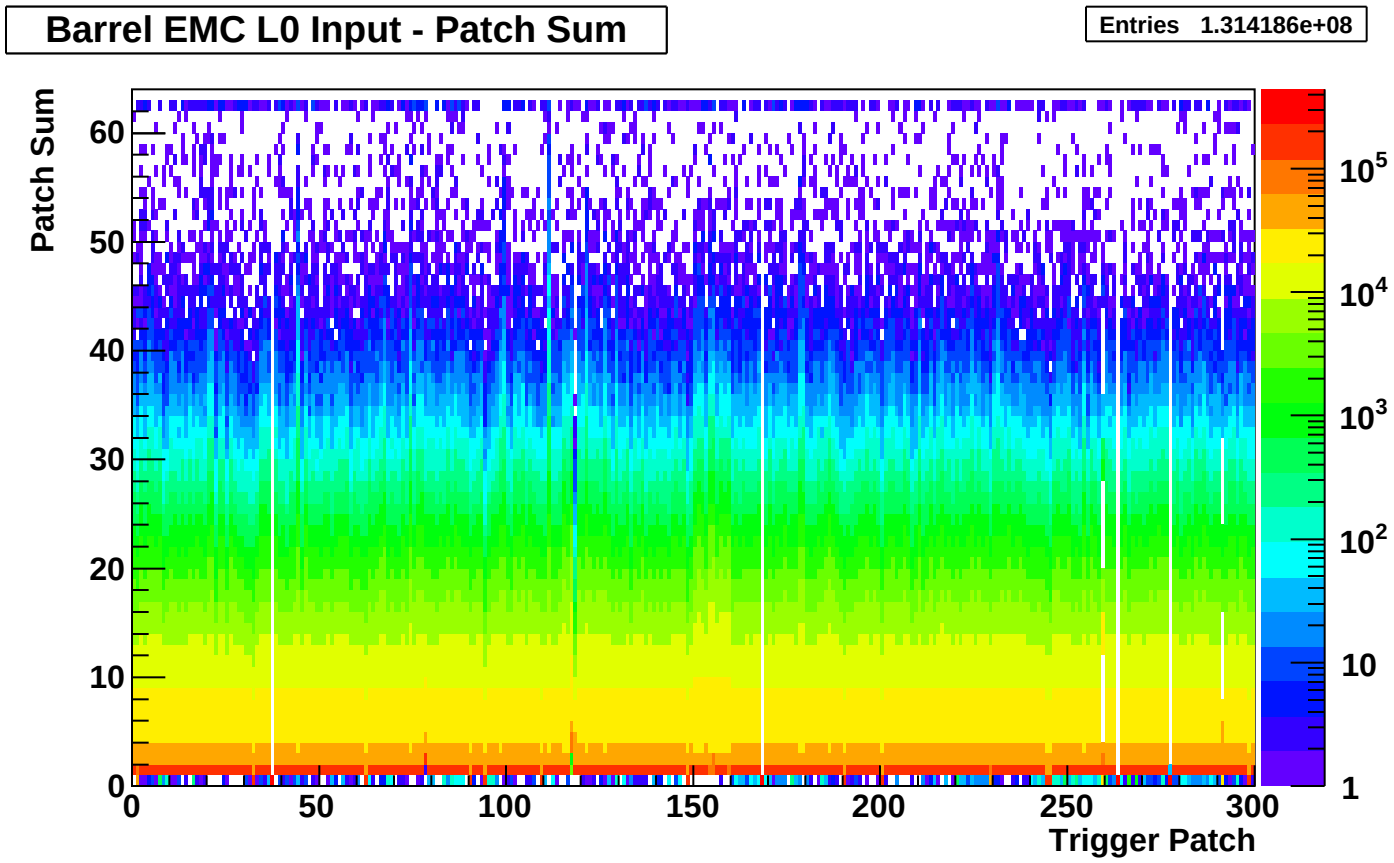

BunchId7Bit (BHT3)
Entries 8518
10^2
10
0
20
40
60
80
100
120

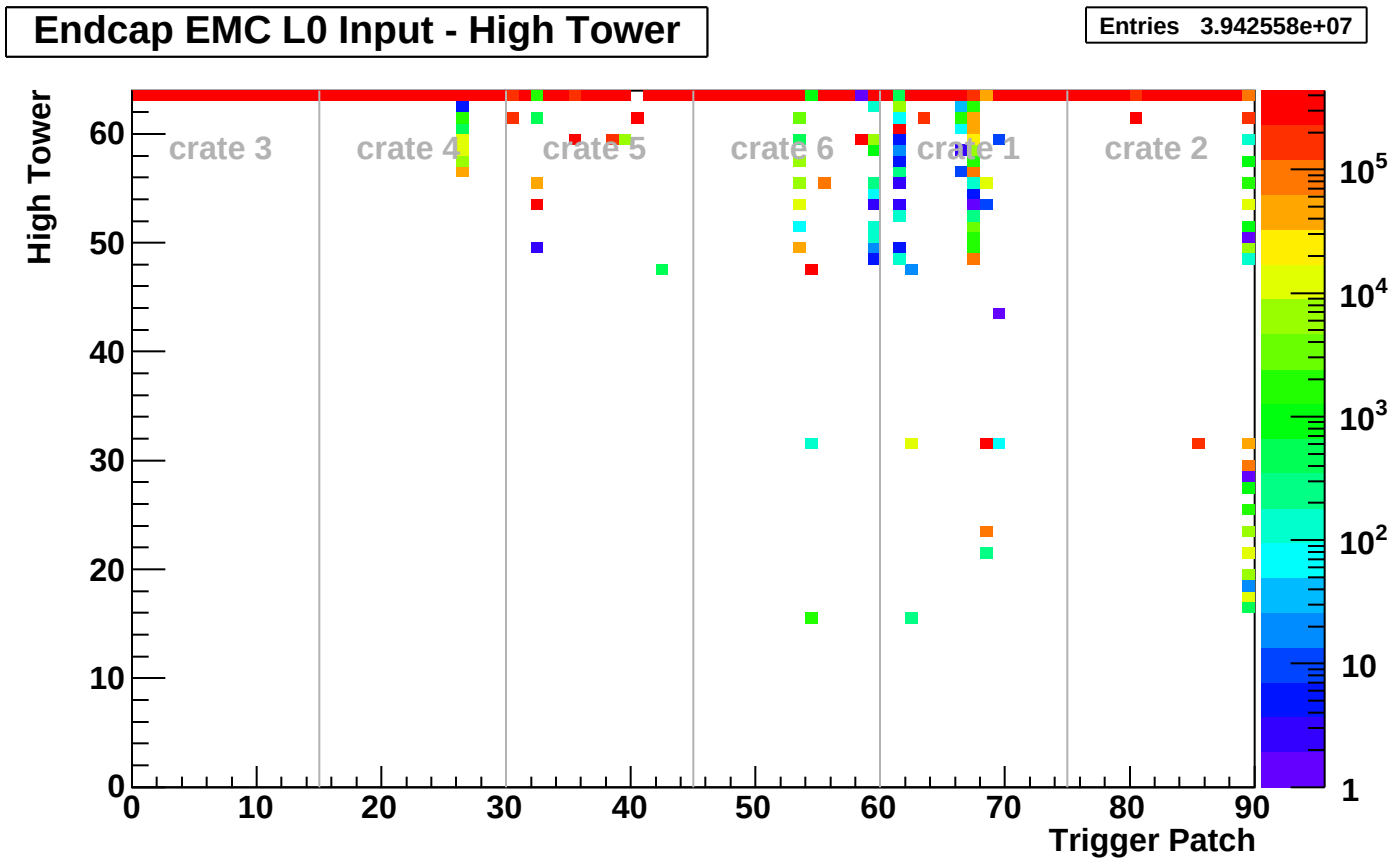

BunchId7Bit (all events)
Entries 438062
10^3
10^2
0
20
40
60
80
100
120

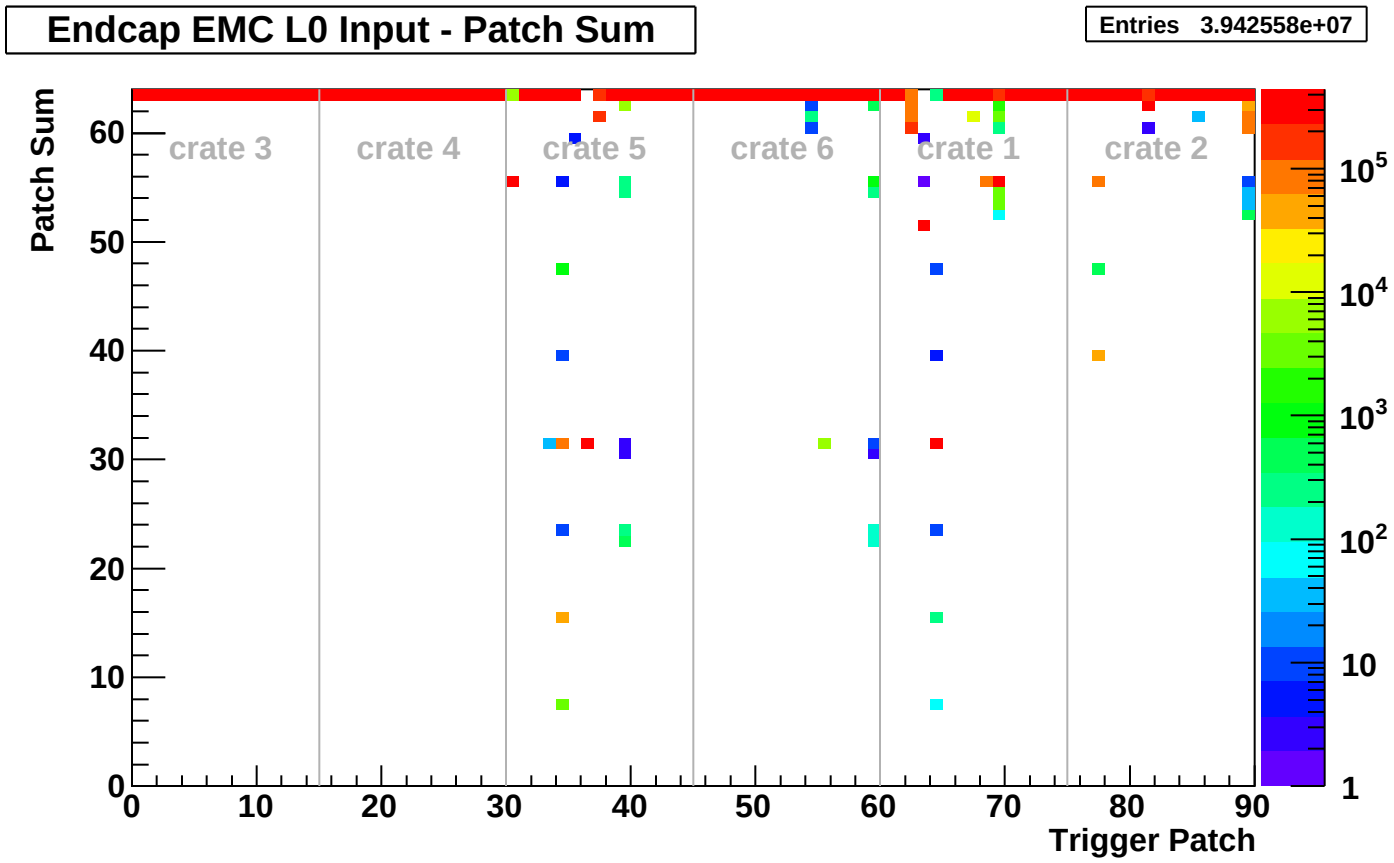

BunchId7Bit (JP1)
Entries 438062
10^3
10^2
0
20
40
60
80
100
120

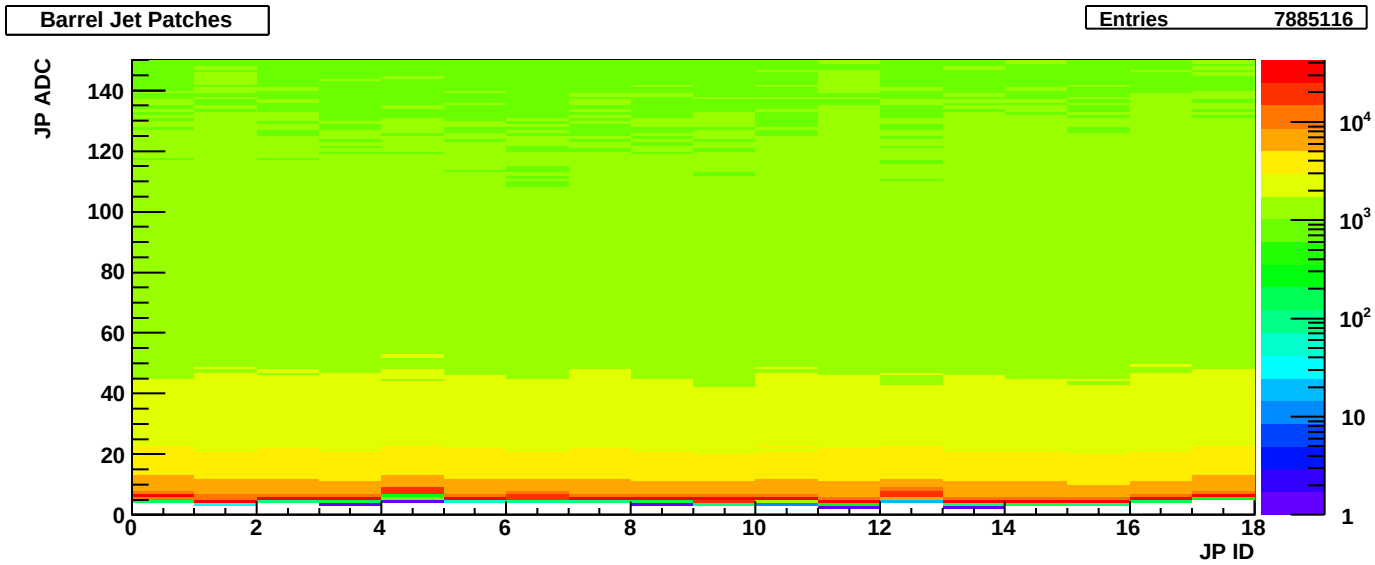

BunchId7Bit (JP2||AJP)
Entries 438062
10^3
10^2
0
20
40
60
80
100
120

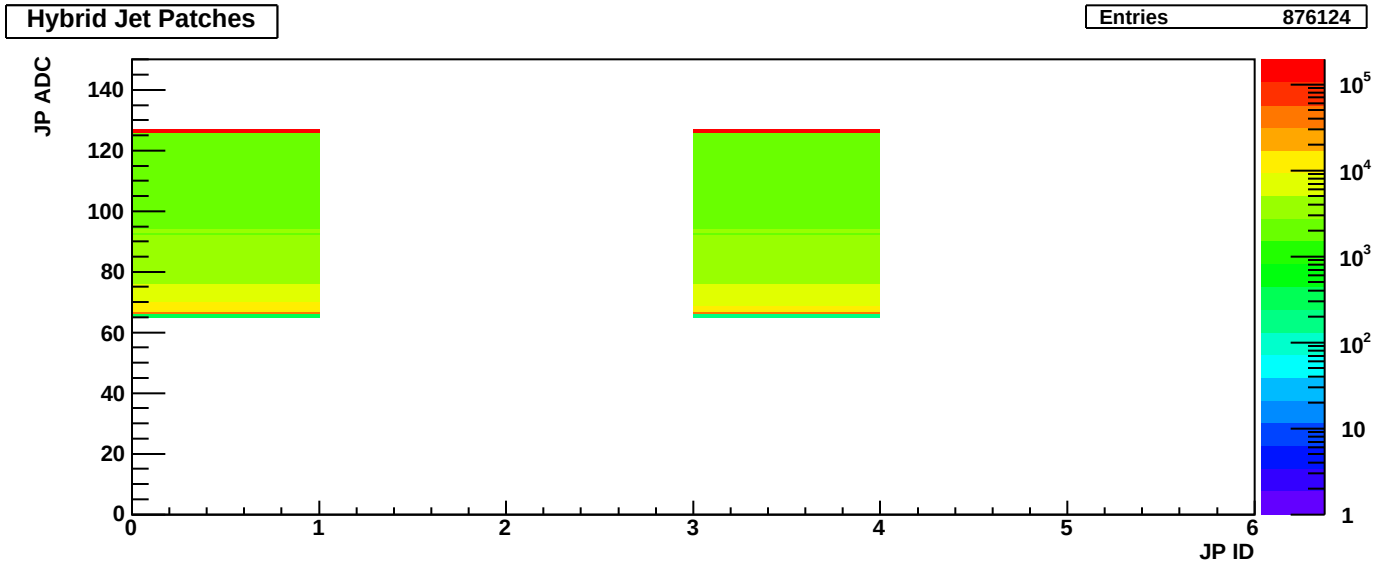

Barrel EMC L0 Input - High Tower
Entries 1.314186e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

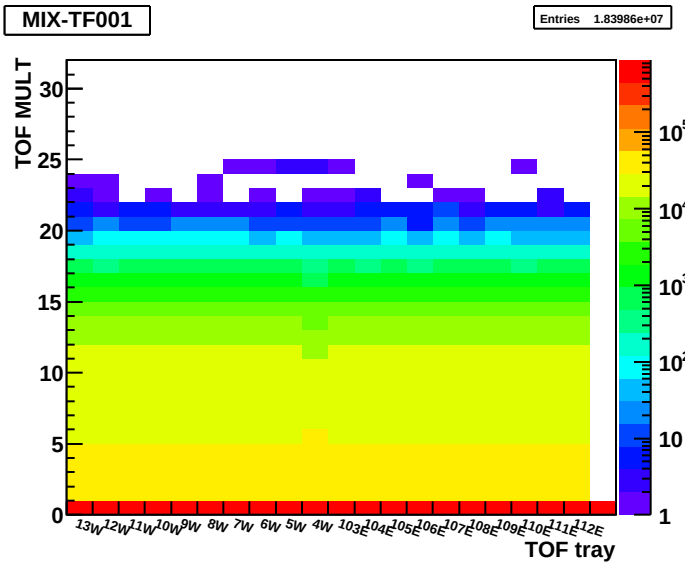

Barrel EMC L0 Input - Patch Sum
Entries 1.314186e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

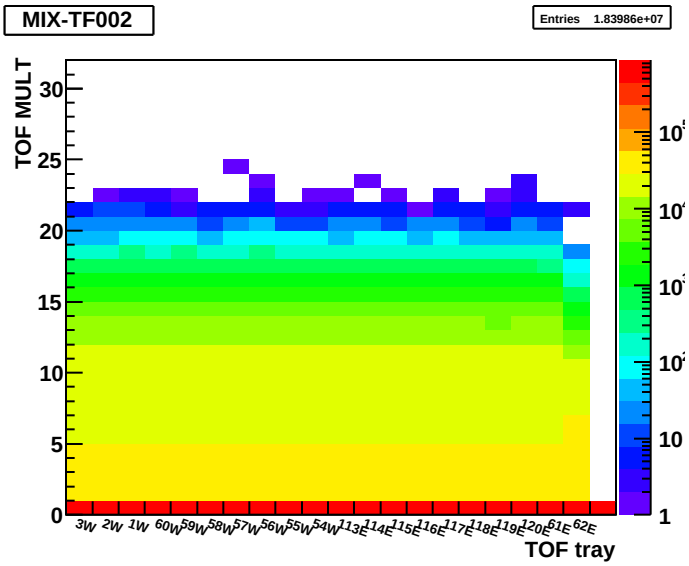

Endcap EMC L0 Input - High Tower
Entries 3.942558e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

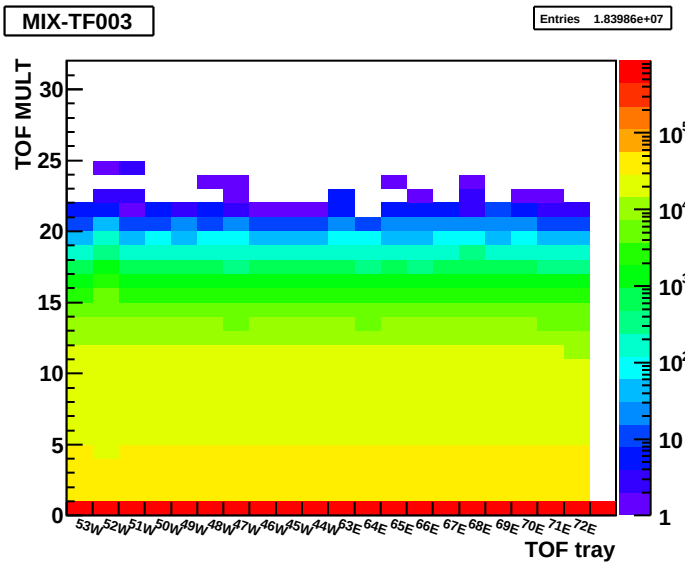

Endcap EMC L0 Input - Patch Sum
Entries 3.942558e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

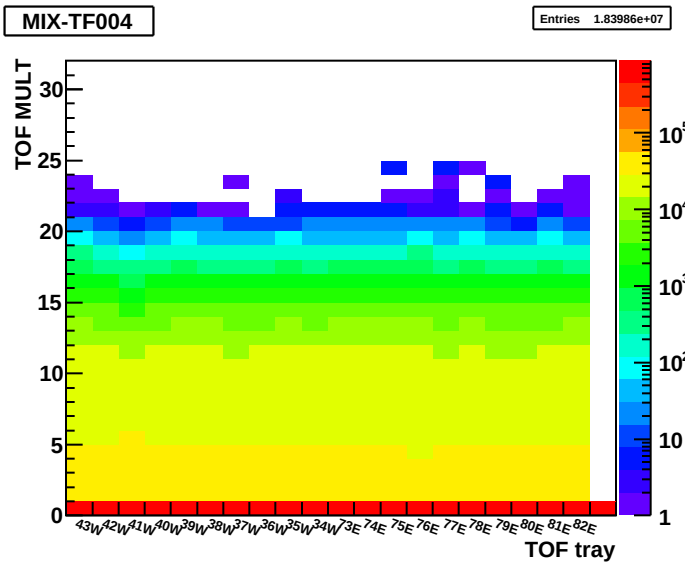

Barrel Jet Patches
Entries 7885116
JP ADC
JP ID

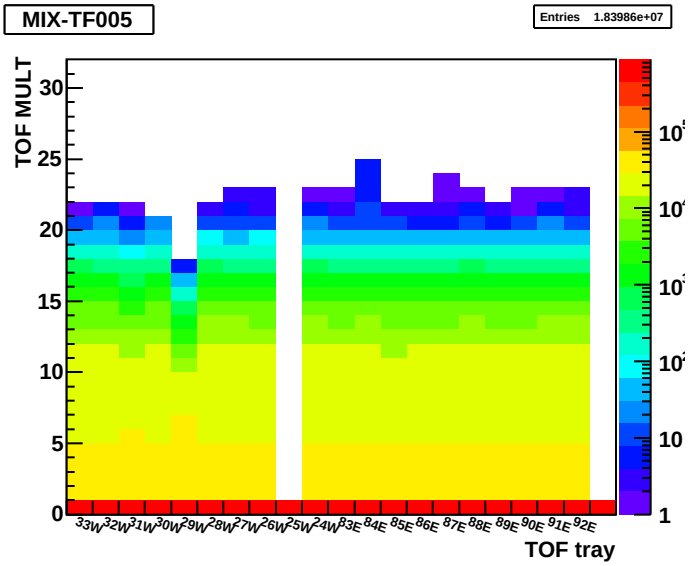

Hybrid Jet Patches
Entries 876124
JP ADC
JP ID

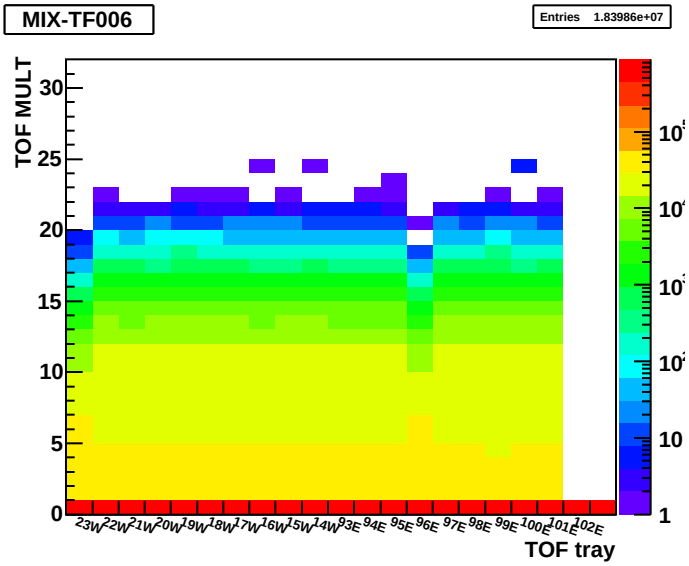

MIX-TF001
Entries 1.83986e+07
TOF MULT
TOF tray

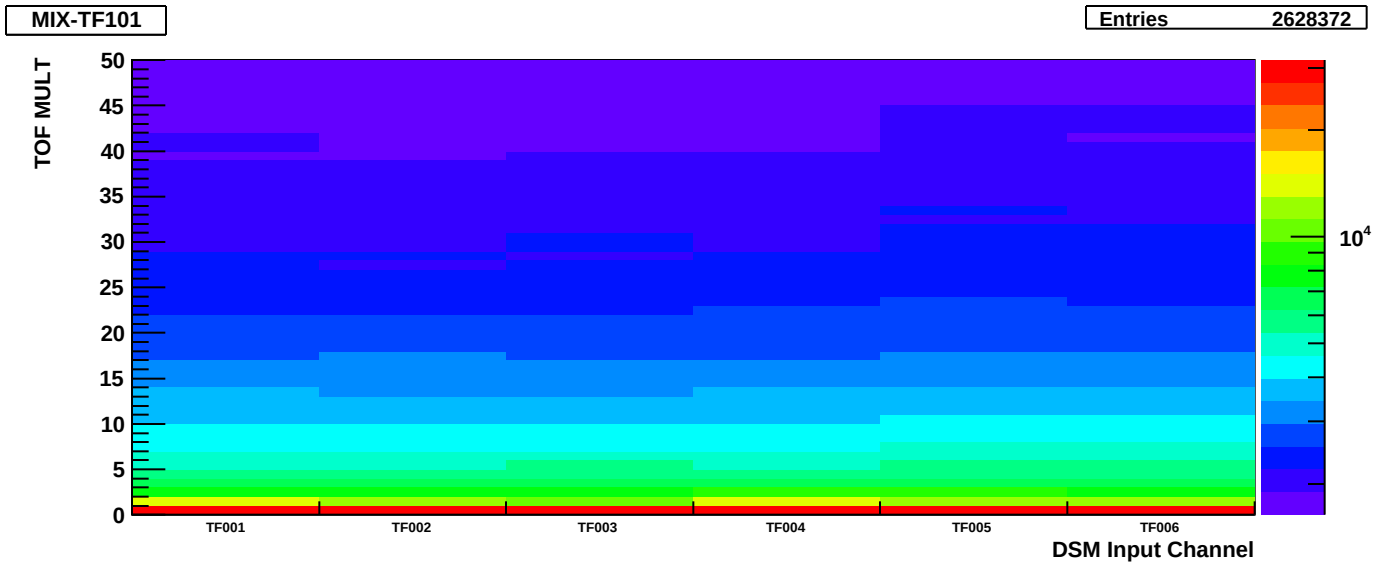

MIX-TF002
Entries 1.83986e+07
TOF MULT
TOF tray

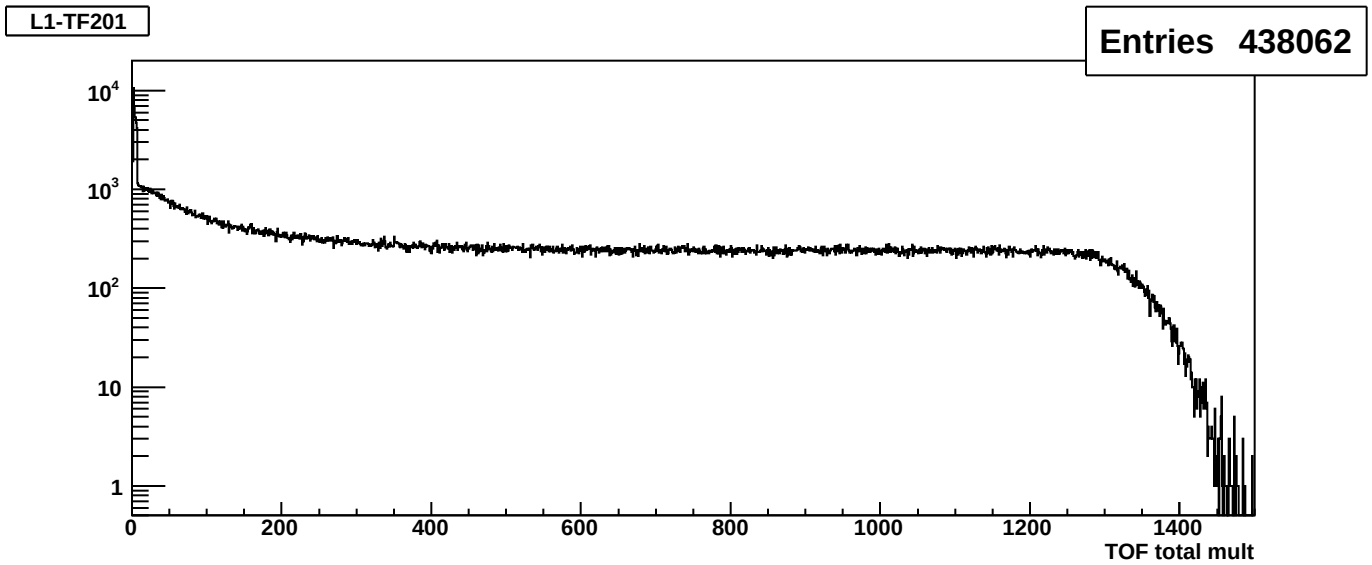

MIX-TF003
Entries 1.83986e+07
TOF MULT
TOF tray

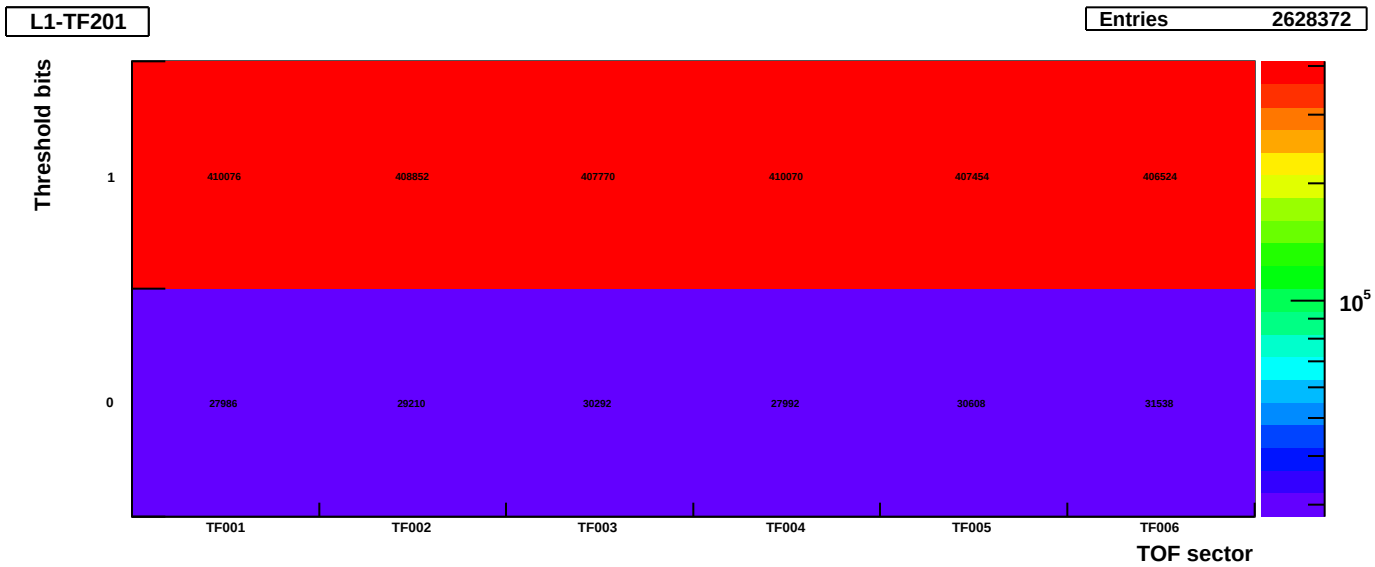

MIX-TF004
Entries 1.83986e+07
TOF MULT
TOF tray

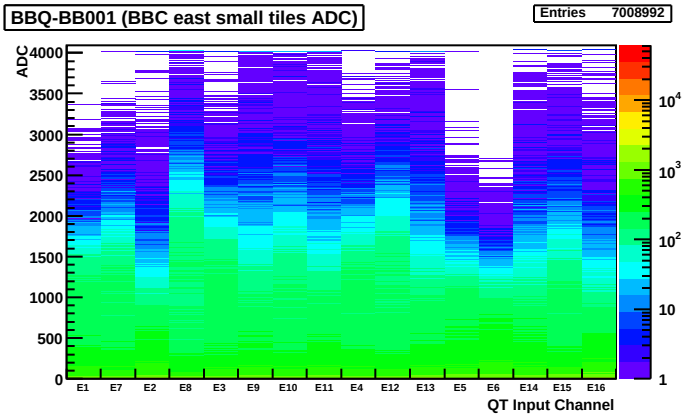

MIX-TF005
Entries 1.83986e+07
TOF MULT
TOF tray

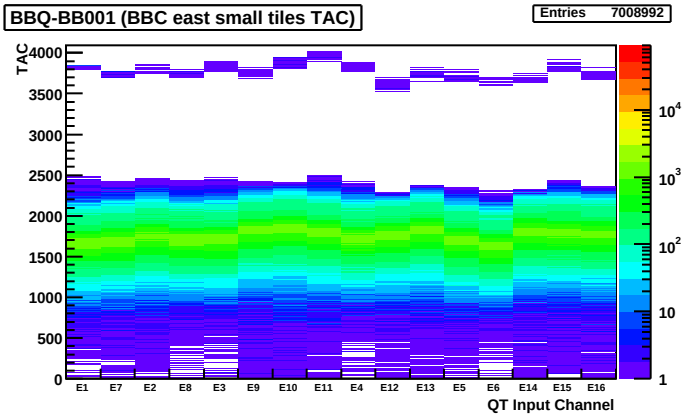

MIX-TF006
Entries 1.83986e+07
TOF MULT
TOF tray

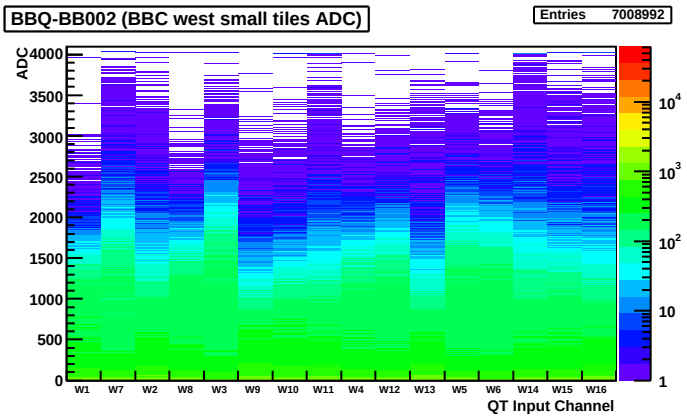

MIX-TF101
Entries 2628372
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

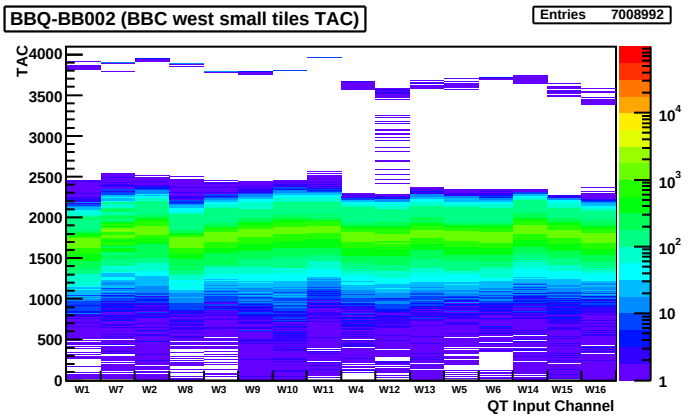

L1-TF201
Entries 438062
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

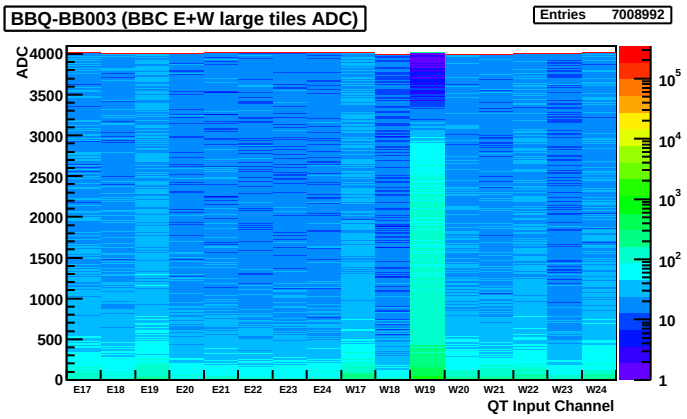

L1-TF201
Entries 2628372
Threshold bits
1
0
410076
408852
407770
410070
407454
406524
27986
29210
30292
27992
30608
31538
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

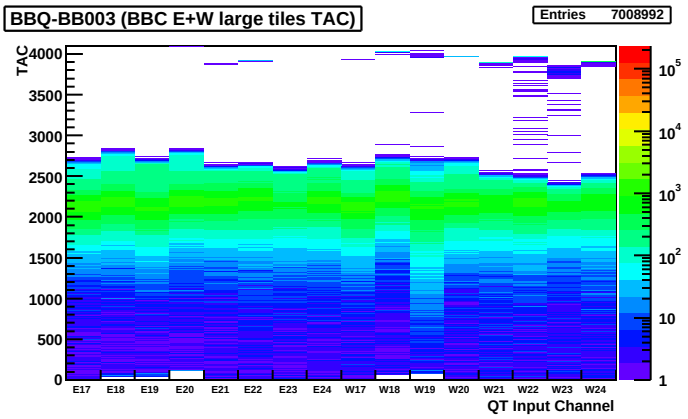

BBQ-BB001 (BBC east small tiles ADC)
Entries 7008992
ADC
QT Input Channel

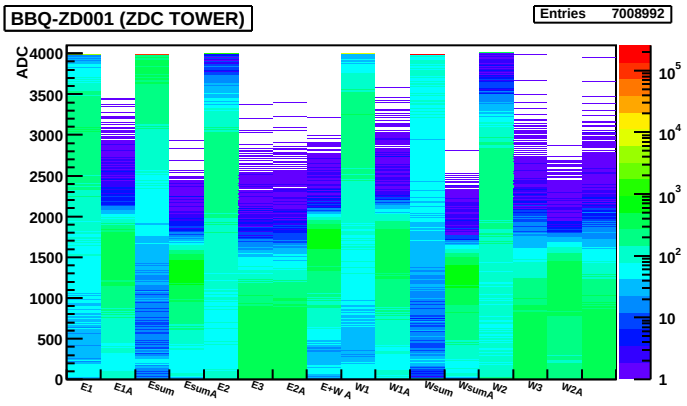

BBQ-BB001 (BBC east small tiles TAC)
Entries 7008992
TAC
QT Input Channel

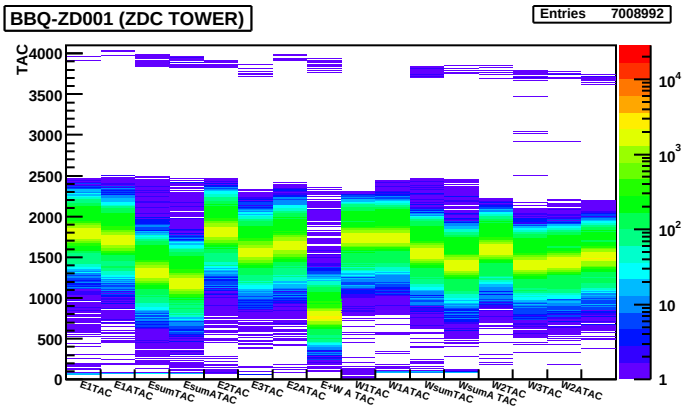

BBQ-BB002 (BBC west small tiles ADC)
Entries 7008992
ADC
QT Input Channel

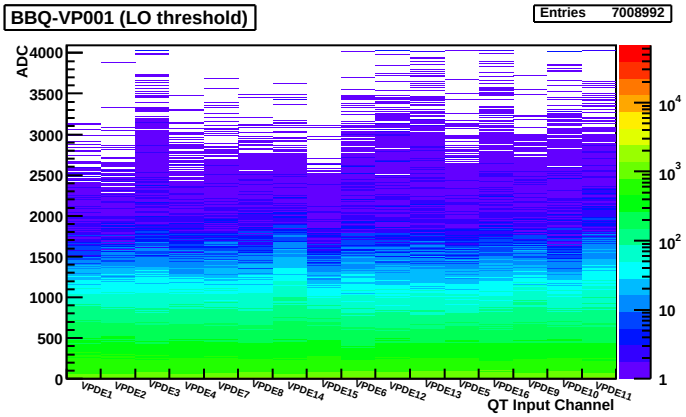

BBQ-BB002 (BBC west small tiles TAC)
Entries 7008992
TAC
QT Input Channel

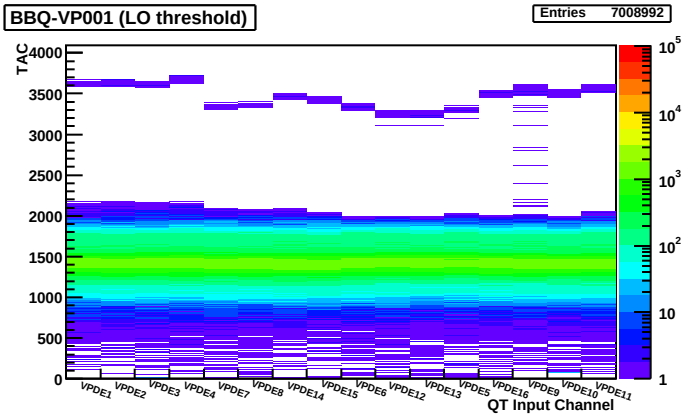

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7008992
ADC
QT Input Channel

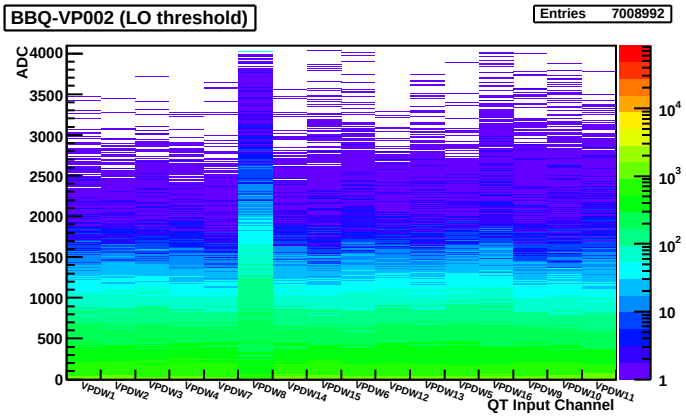

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7008992
TAC
QT Input Channel

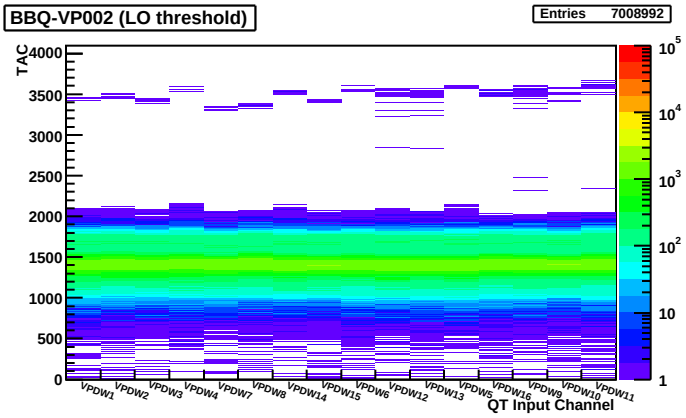

BBQ-ZD001 (ZDC TOWER)
Entries 7008992
ADC

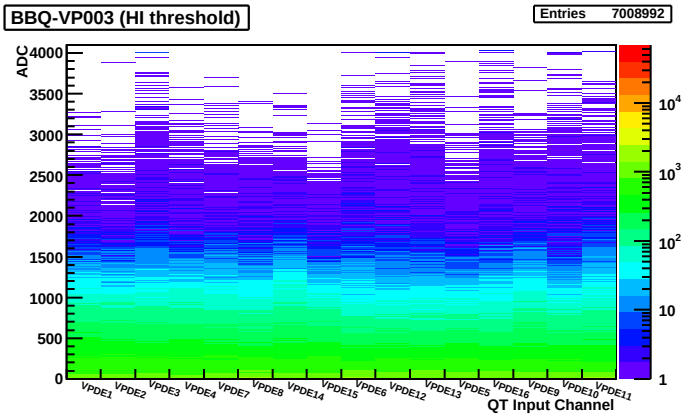

BBQ-ZD001 (ZDC TOWER)
Entries 7008992
TAC

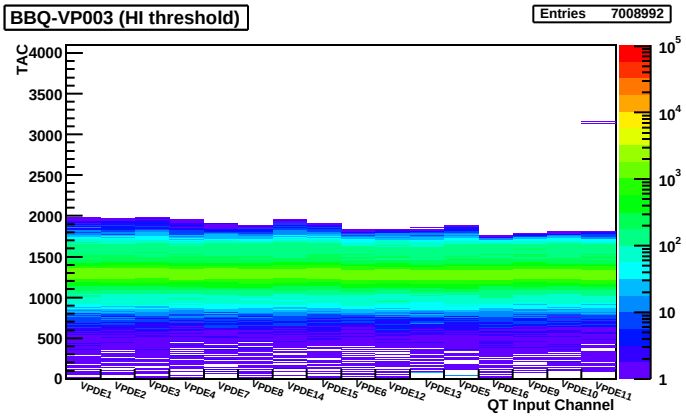

BBQ-VP001 (LO threshold)
Entries 7008992
ADC
QT Input Channel

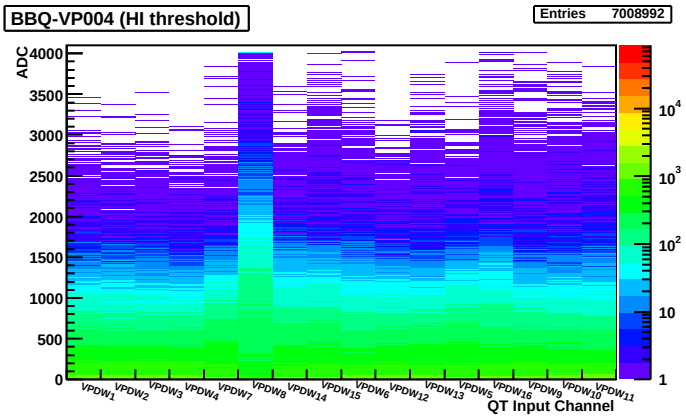

BBQ-VP001 (LO threshold)
Entries 7008992
TAC
QT Input Channel

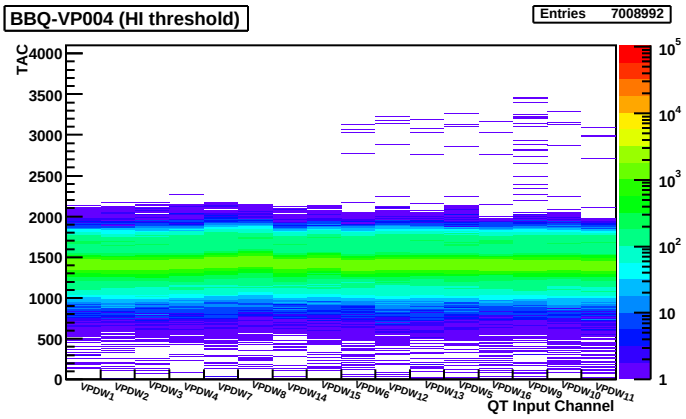

BBQ-VP002 (LO threshold)
Entries 7008992
ADC
QT Input Channel

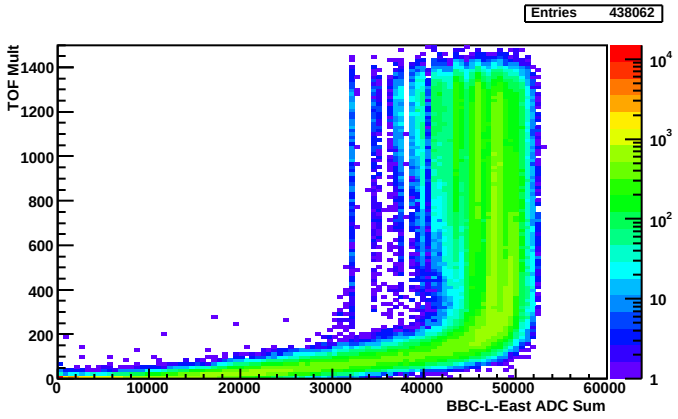

BBQ-VP002 (LO threshold)
Entries 7008992
TAC
QT Input Channel

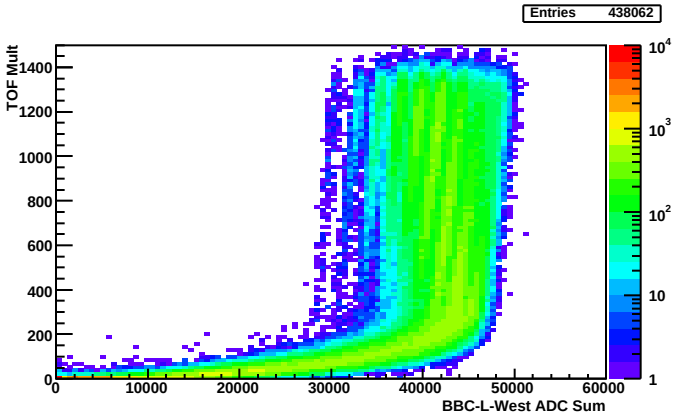

BBQ-VP003 (HI threshold)
Entries 7008992
ADC
QT Input Channel

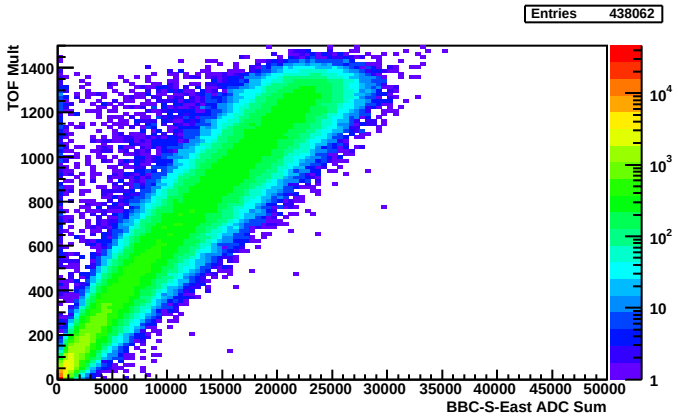

BBQ-VP003 (HI threshold)
Entries 7008992
TAC
QT Input Channel

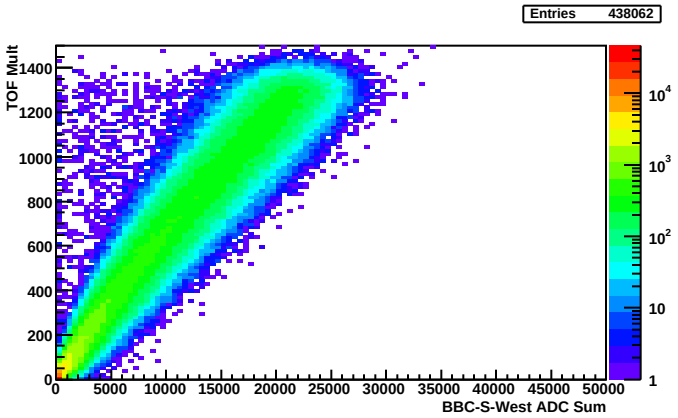

BBQ-VP004 (HI threshold)
Entries 7008992
ADC
QT Input Channel

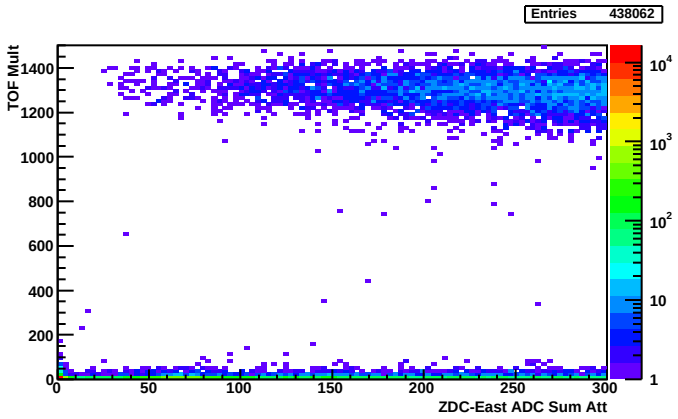

BBQ-VP004 (HI threshold)
Entries 7008992
TAC
QT Input Channel

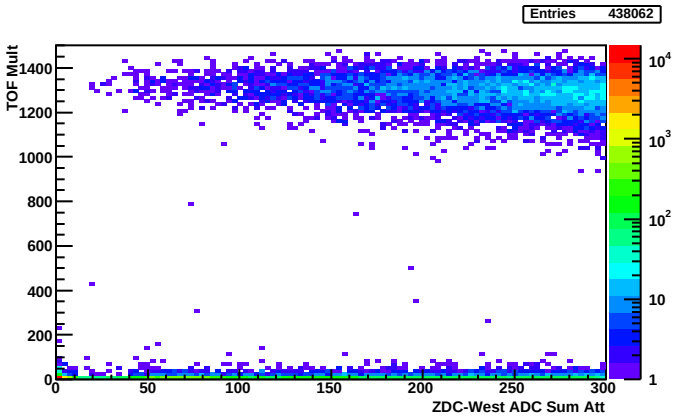

Entries 438062
TOF Mult
BBC-L-East ADC Sum

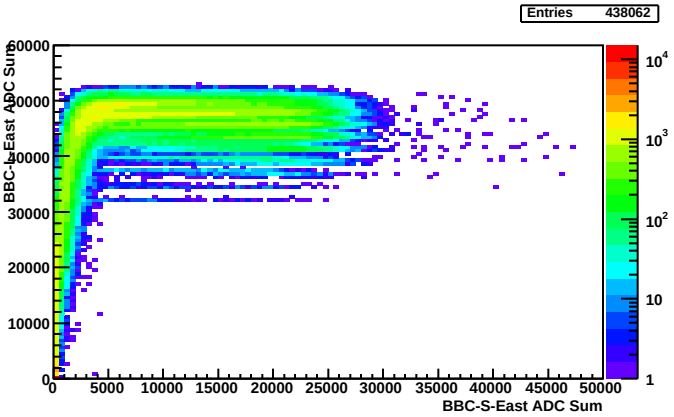

Entries 438062
TOF Mult
BBC-L-West ADC Sum

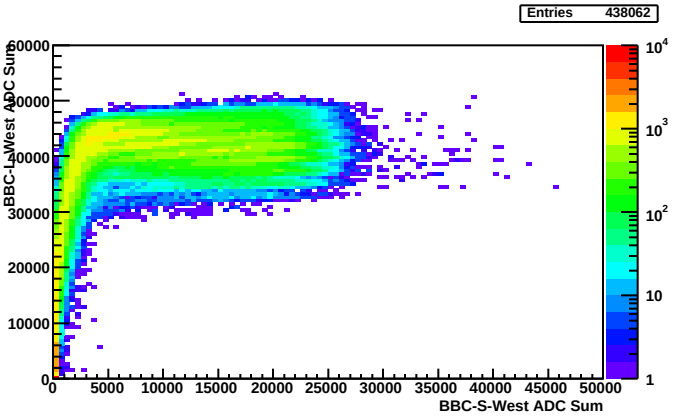

Entries 438062
TOF Mult
BBC-S-East ADC Sum

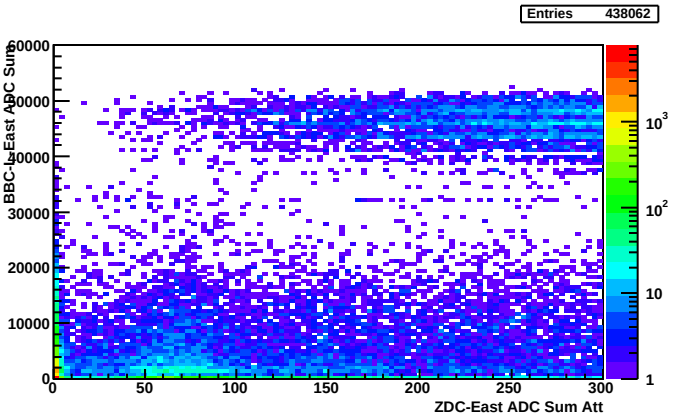

Entries 438062
TOF Mult
BBC-S-West ADC Sum

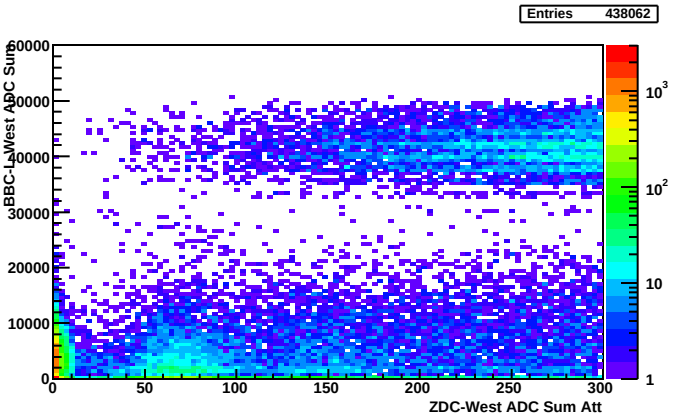

Entries 438062
TOF Mult
ZDC-East ADC Sum Att

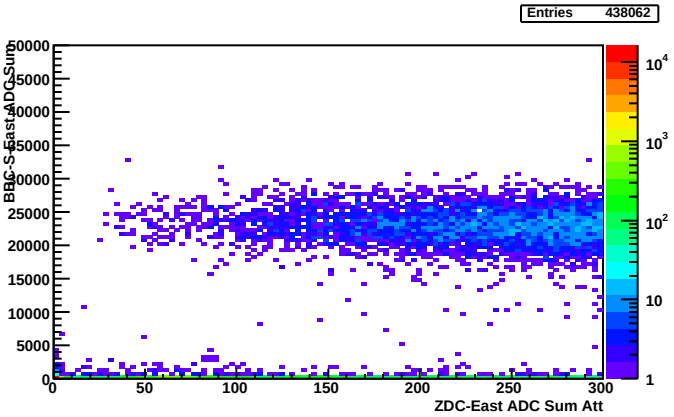

Entries 438062
TOF Mult
ZDC-West ADC Sum Att


Entries 438062
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 438062
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 438062
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 438062
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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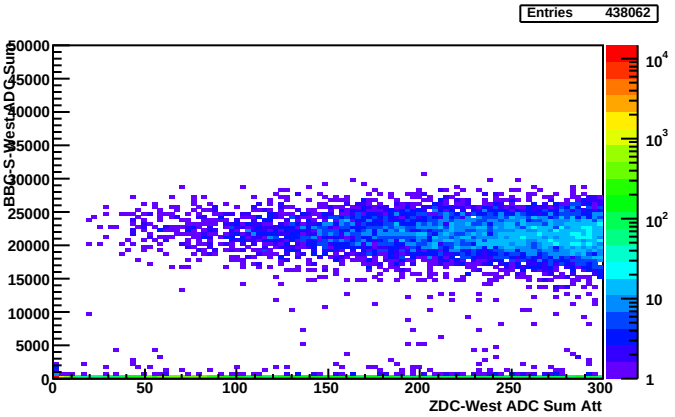
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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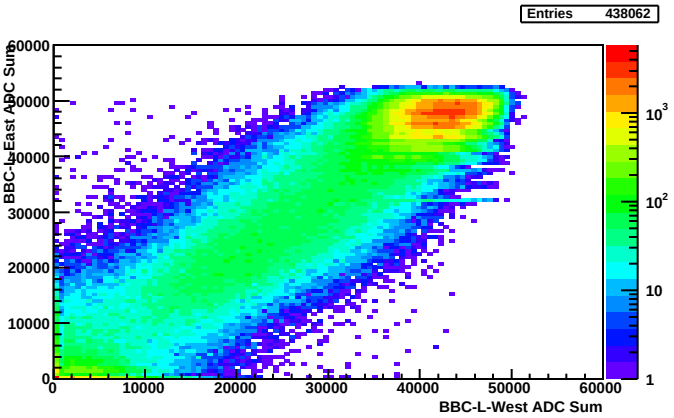
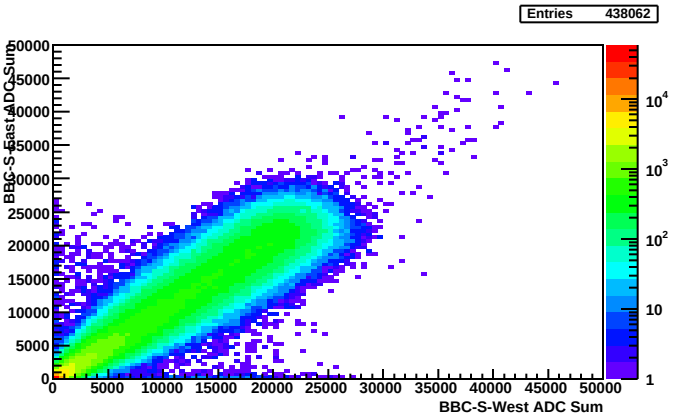
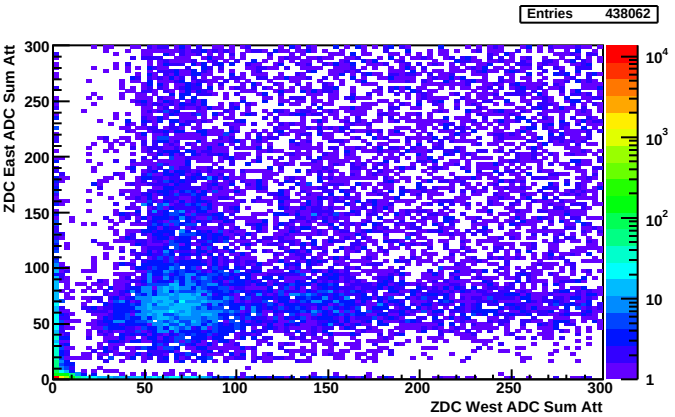
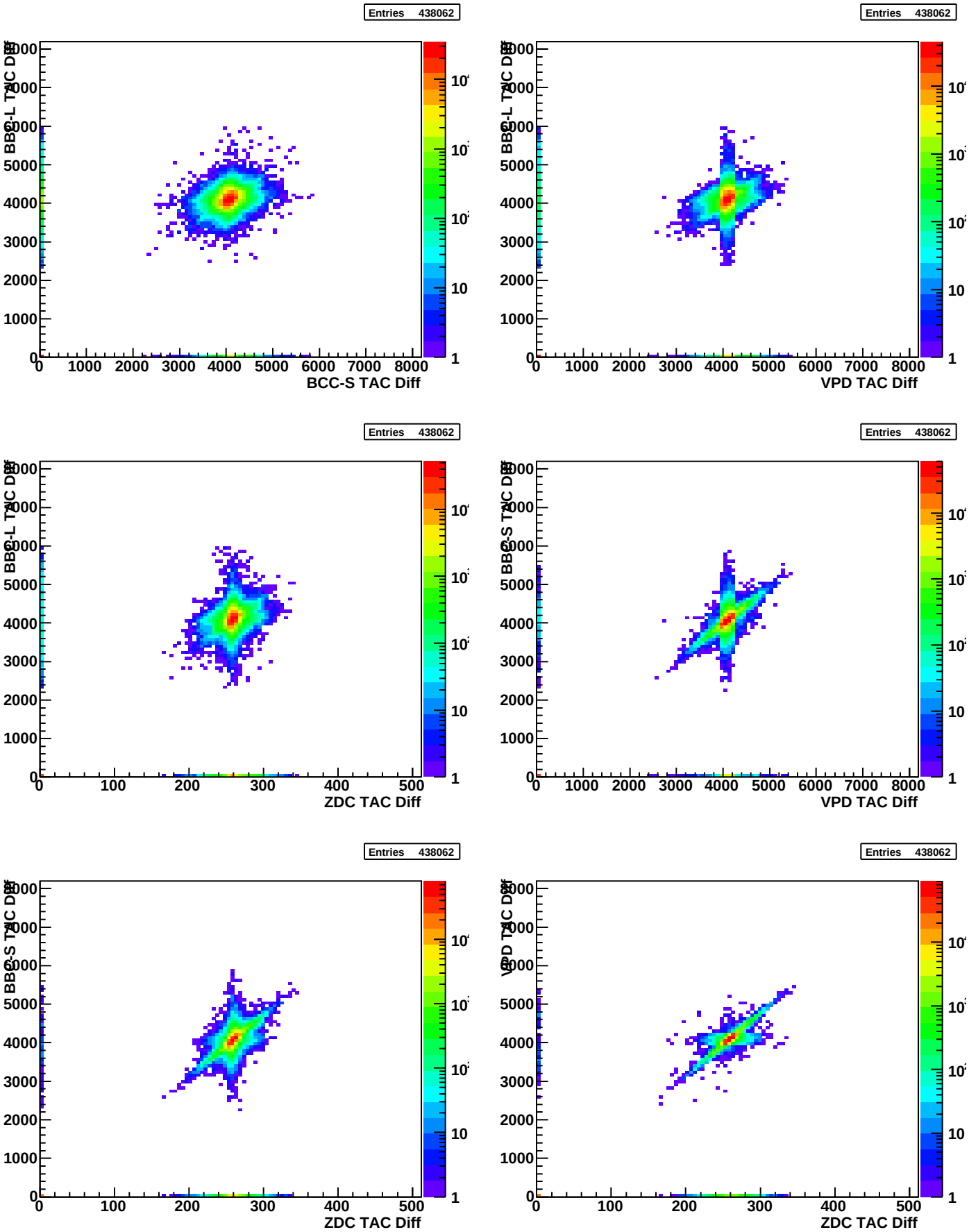
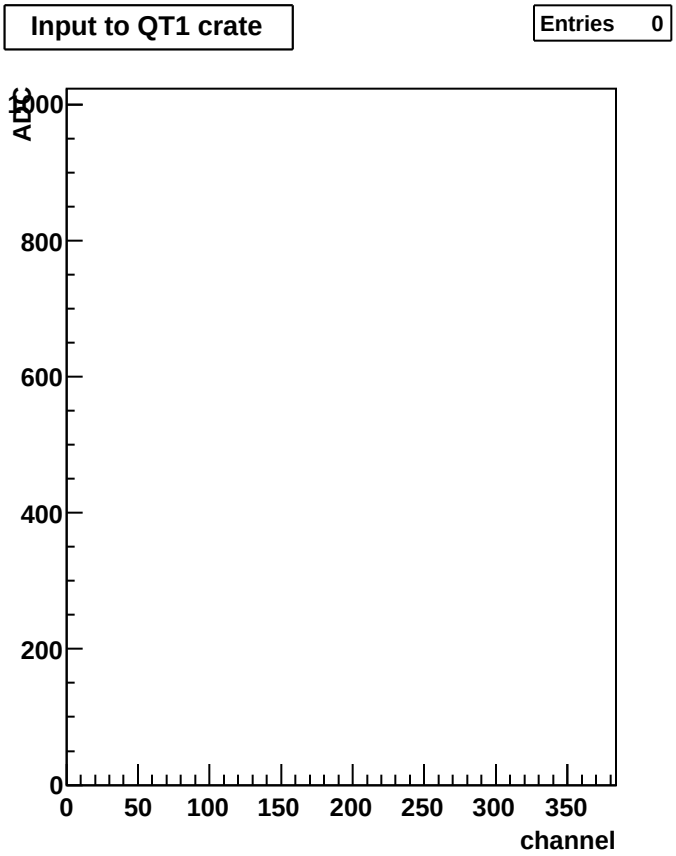
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

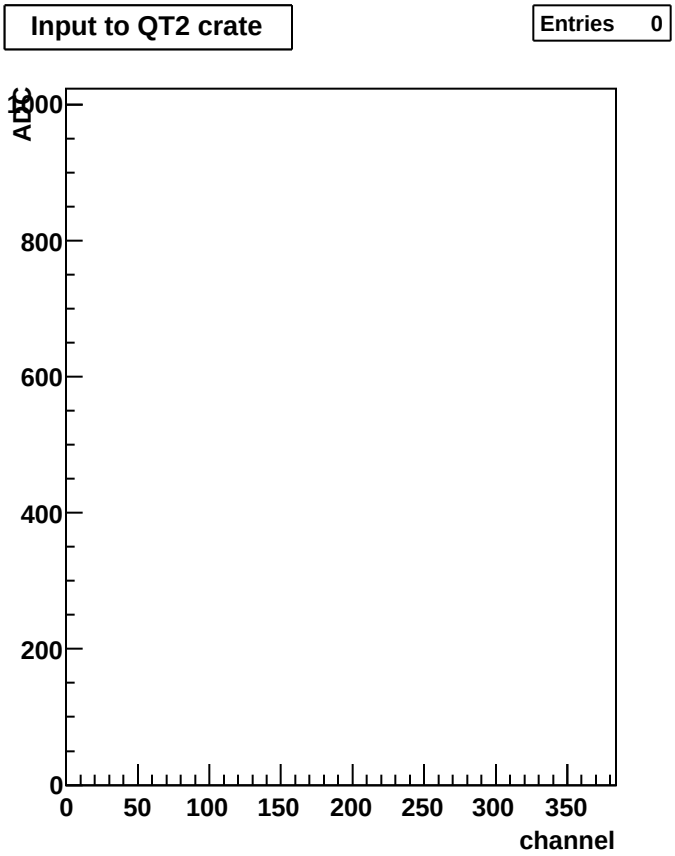

Entries 438062
BBC-L-East ADC Sum
BBC-L-West ADC Sum

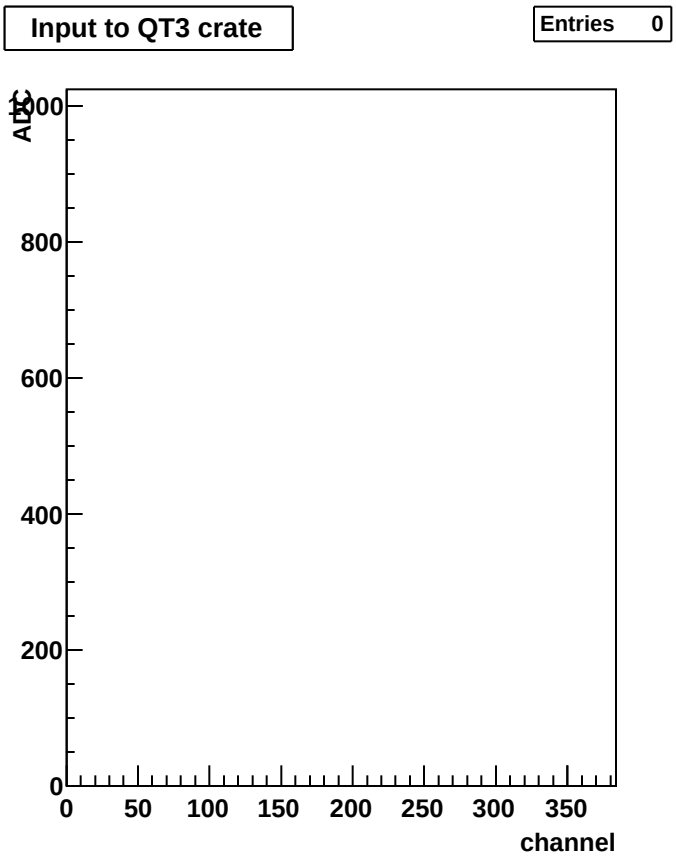

Entries 438062
BBC-S-East ADC Sum
BBC-S-West ADC Sum

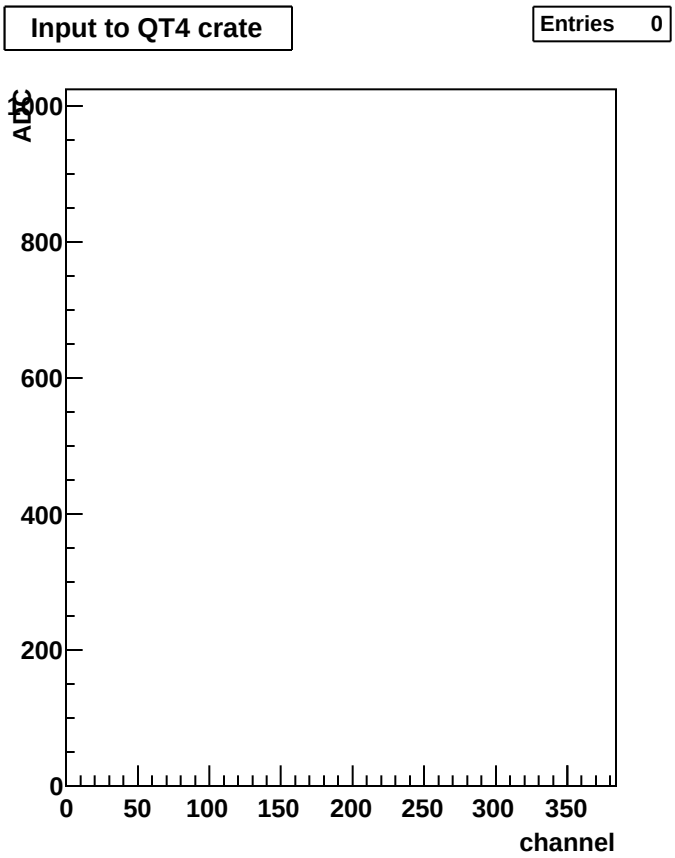

Entries 438062
ZDC East ADC Sum Att
ZDC West ADC Sum Att

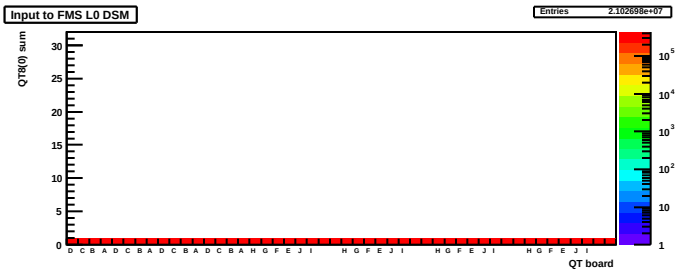

Entries 438062
BBC-L TAC Diff
BCC-S TAC Diff
Entries 438062
BBC-L TAC Diff
VPD TAC Diff
Entries 438062
BBC-L TAC Diff
ZDC TAC Diff
Entries 438062
BBC-S TAC Diff
VPD TAC Diff
Entries 438062
BBC-S TAC Diff
ZDC TAC Diff
Entries 438062
VPD TAC Diff
ZDC TAC Diff

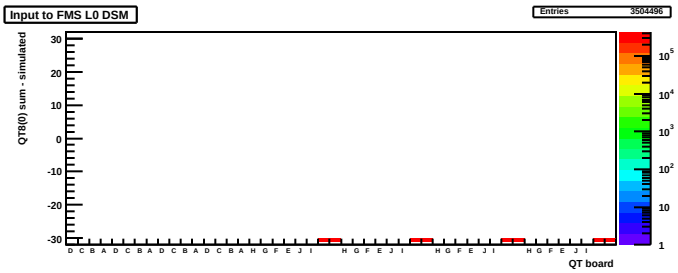

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

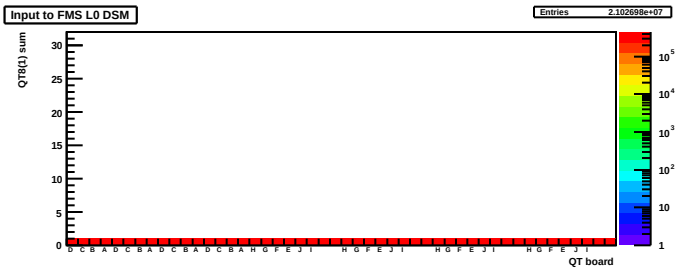

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

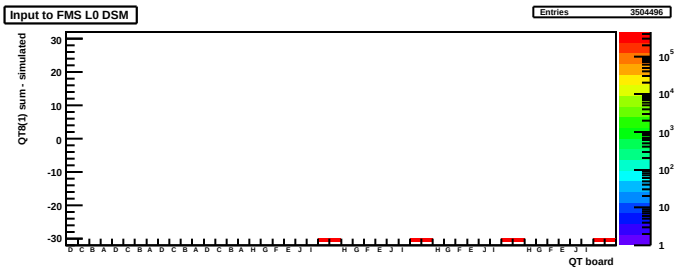

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

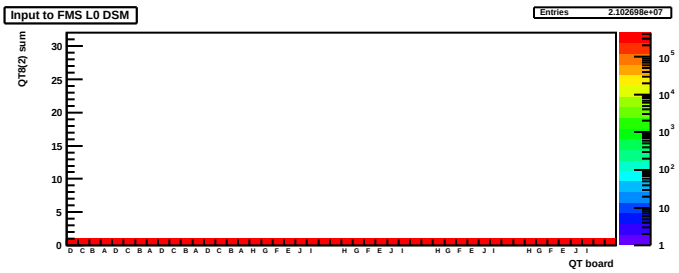

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

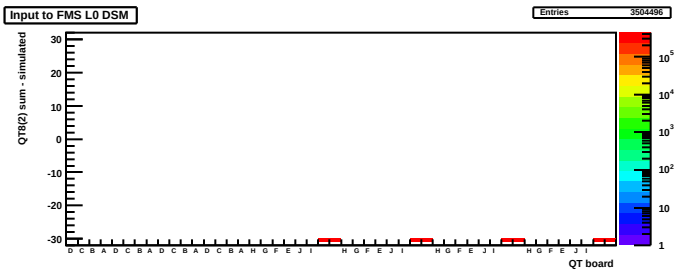

Input to FMS L0 DSM
Entries 2.102698e+07
QT8(0) sum
QT board

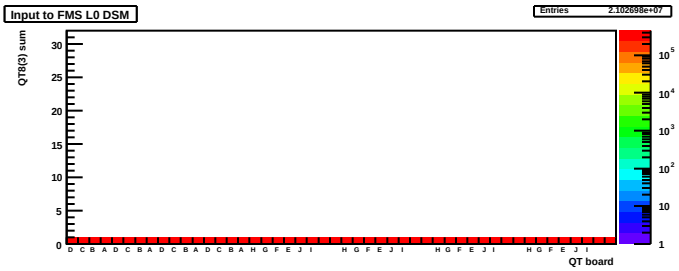

Input to FMS L0 DSM
Entries 3504496
QT8(0) sum - simulated
QT board

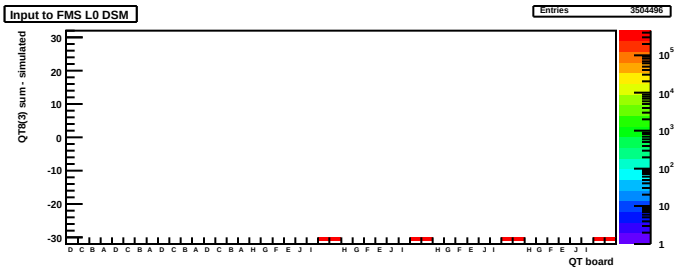

Input to FMS L0 DSM
Entries 2.102698e+07
QT8(1) sum
QT board

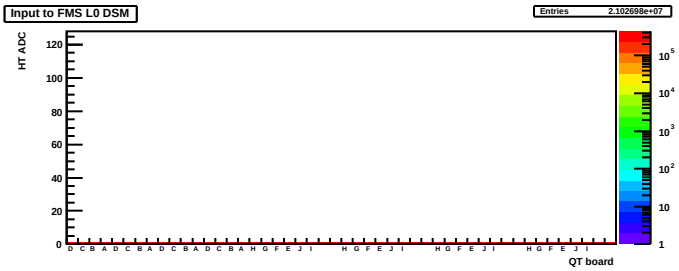

Input to FMS L0 DSM
Entries 3504496
QT8(1) sum - simulated
QT board

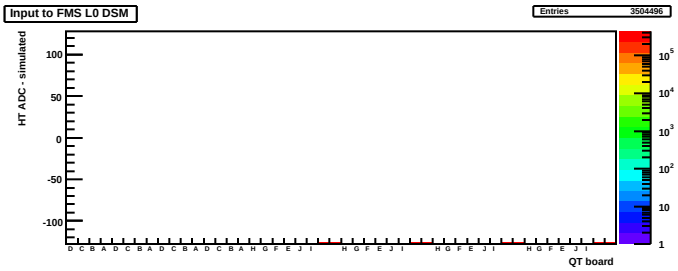

Input to FMS L0 DSM
Entries 2.102698e+07
QT8(2) sum
QT board

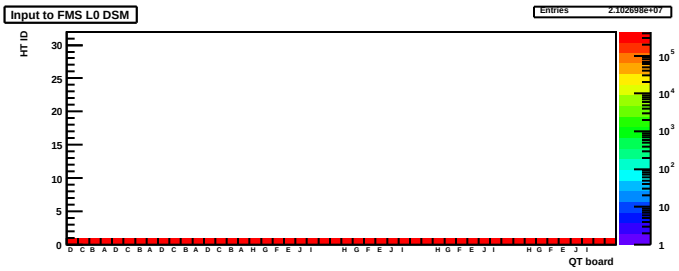

Input to FMS L0 DSM
Entries 3504496
QT8(2) sum - simulated
QT board

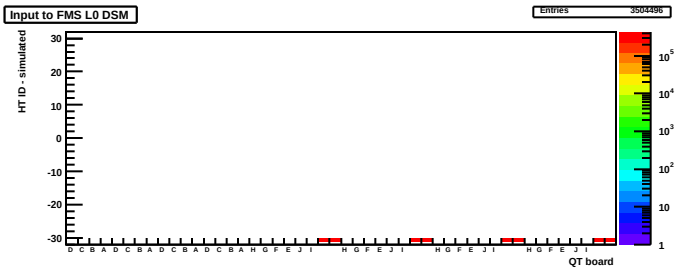

Input to FMS L0 DSM
Entries 2.102698e+07
QT8(3) sum
QT board

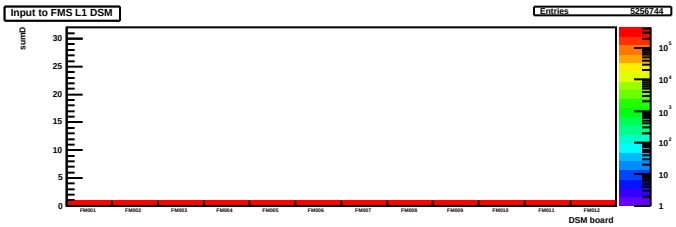

Input to FMS L0 DSM
Entries 3504496
QT8(3) sum - simulated
QT board

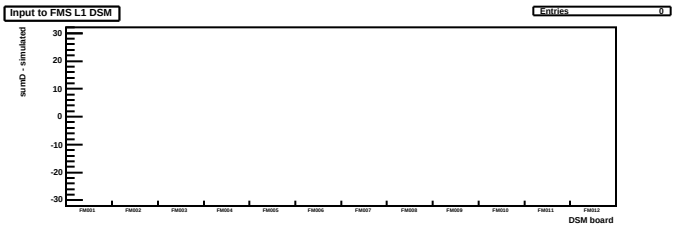

Input to FMS L0 DSM
Entries 2.102698e+07
HT ADC
QT board

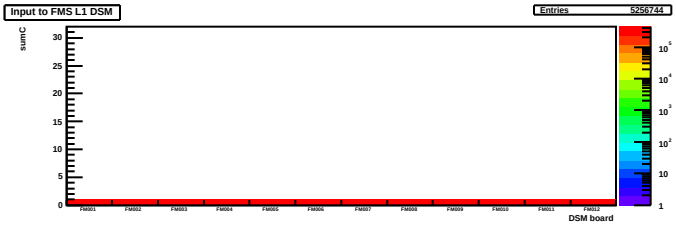

Input to FMS L0 DSM
Entries 3504496
HT ADC - simulated
QT board

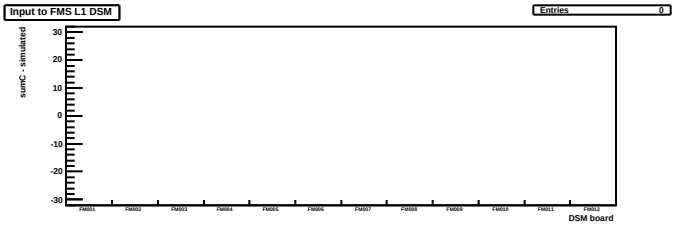

Input to FMS L0 DSM
Entries 2.102698e+07
HT ID
QT board

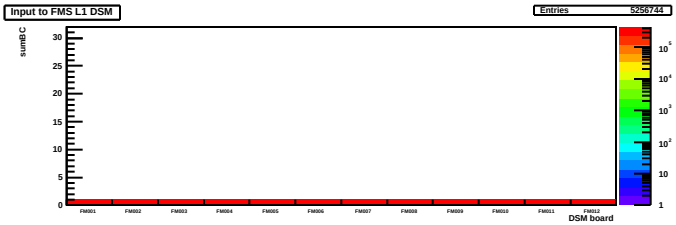

Input to FMS L0 DSM
Entries 3504496
HT ID - simulated
QT board

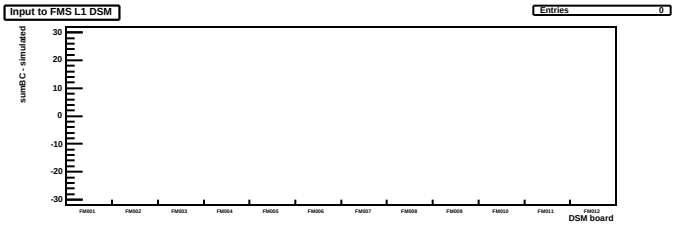

Input to FMS L1 DSM
Entries 5256744
sumD
DSM board

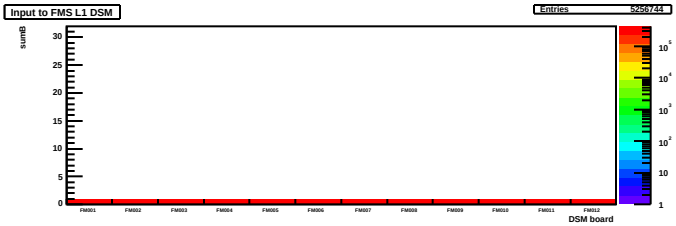

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

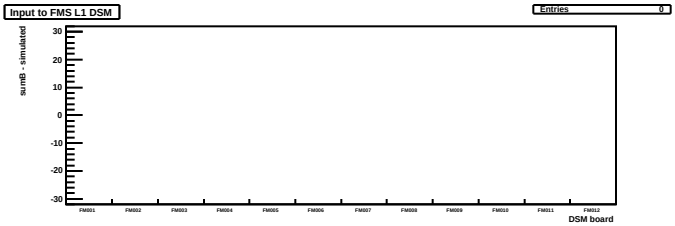

Input to FMS L1 DSM
Entries 5256744
sumC
DSM board

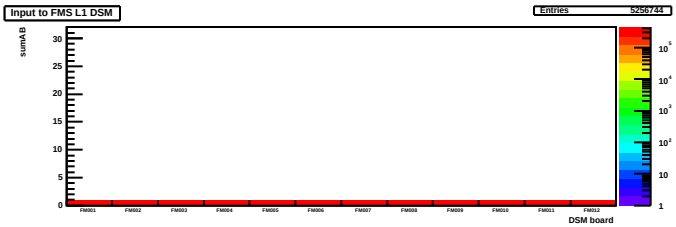

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

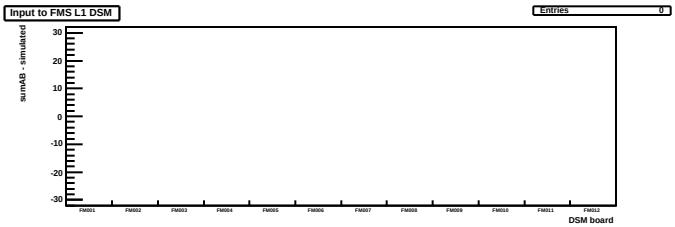

Input to FMS L1 DSM
Entries 5256744
sumBC
DSM board

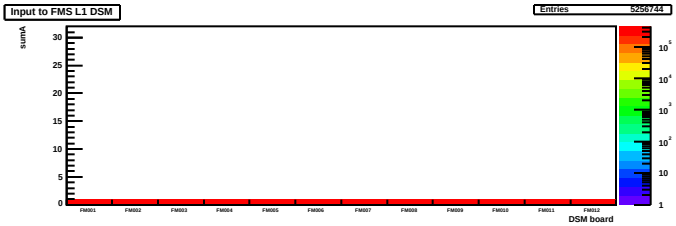

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

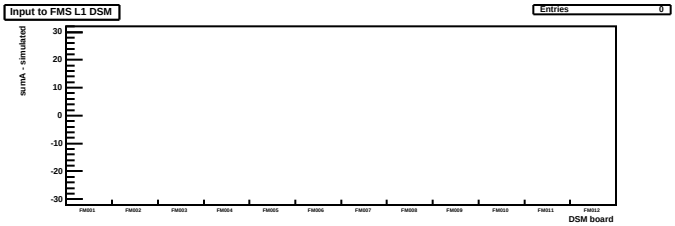

Input to FMS L1 DSM
Entries 5256744
sumB
DSM board

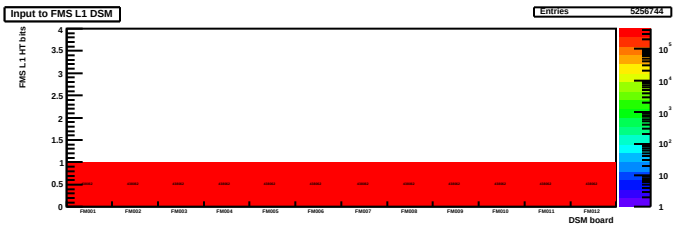

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

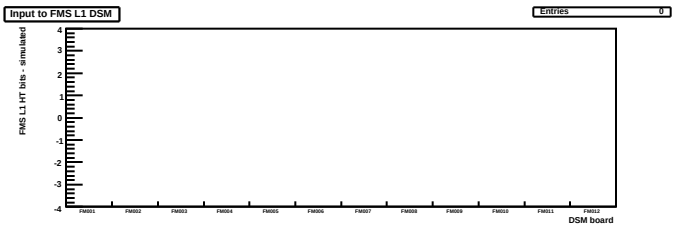

Input to FMS L1 DSM
Entries 5256744
sumAB
DSM board

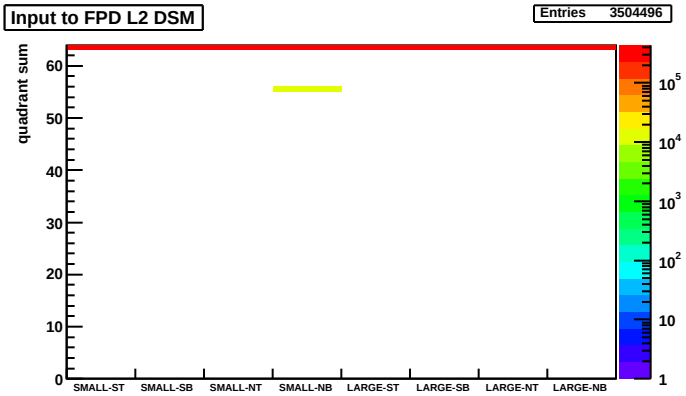

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

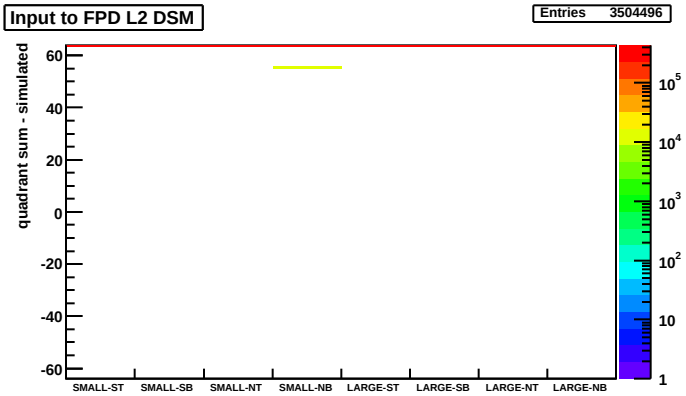

Input to FMS L1 DSM
Entries 5256744
sumA
DSM board

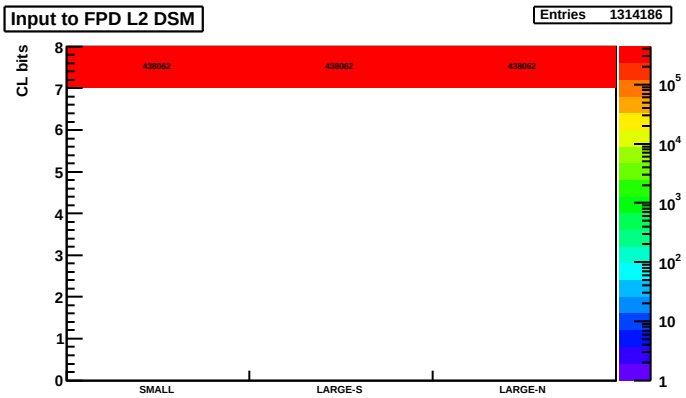

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

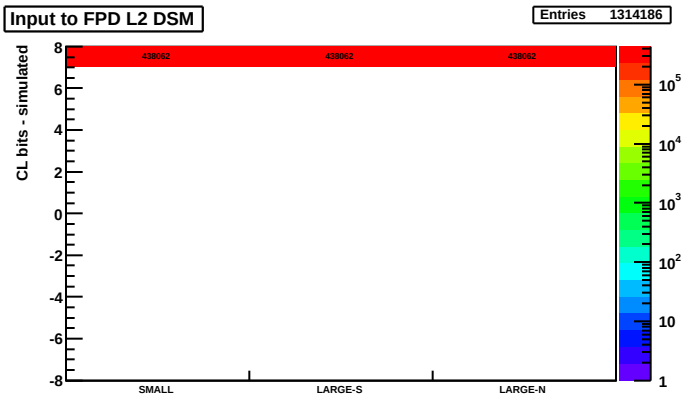

Input to FMS L1 DSM
Entries 5256744
FMS L1 HT bits
DSM board

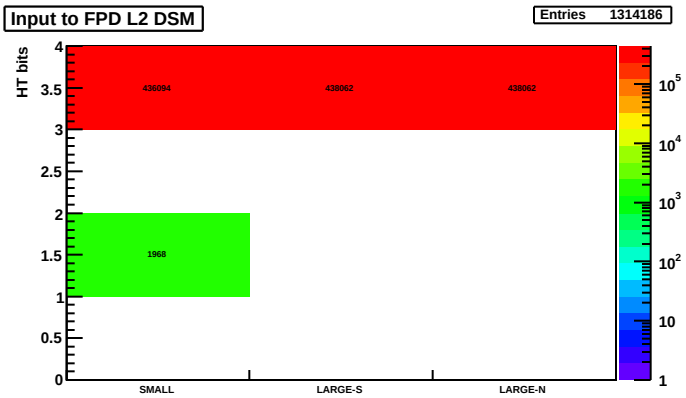

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

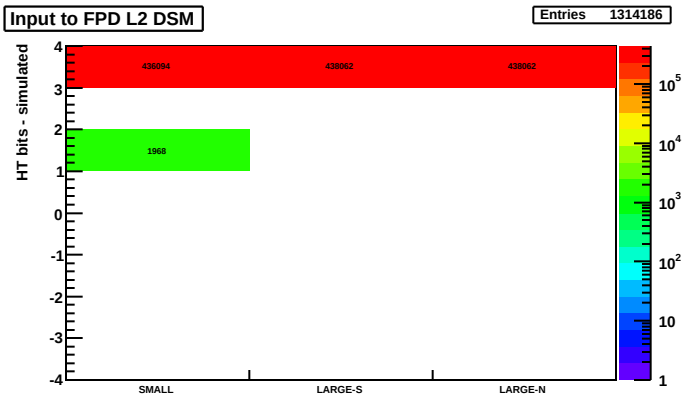

Input to FPD L2 DSM
Entries 3504496
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

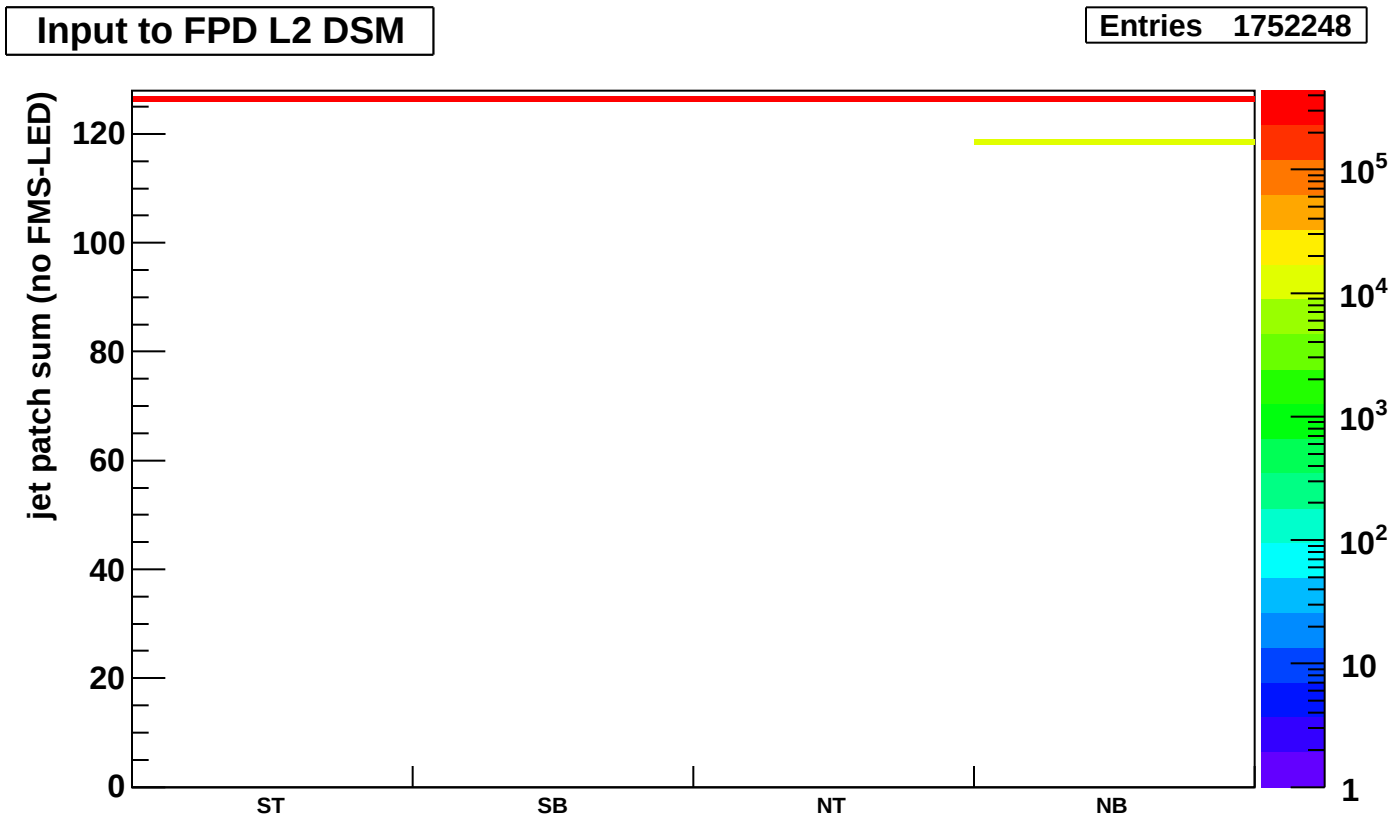

Input to FPD L2 DSM
Entries 3504496
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

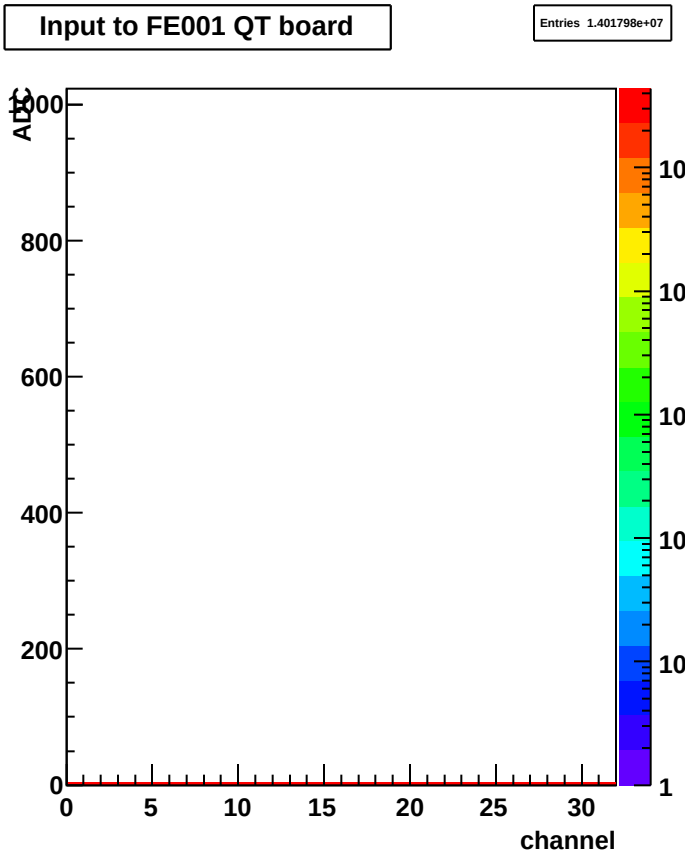

Input to FPD L2 DSM
Entries 1314186
CL bits
438062
438062
438062
SMALL
LARGE-S
LARGE-N

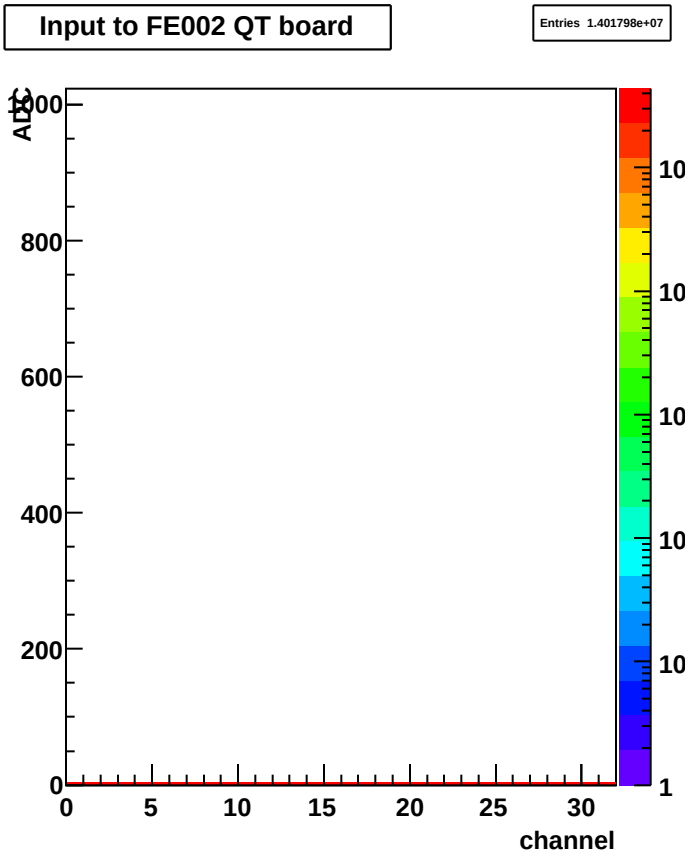

Input to FPD L2 DSM
Entries 1314186
CL bits - simulated
438062
438062
438062
SMALL
LARGE-S
LARGE-N

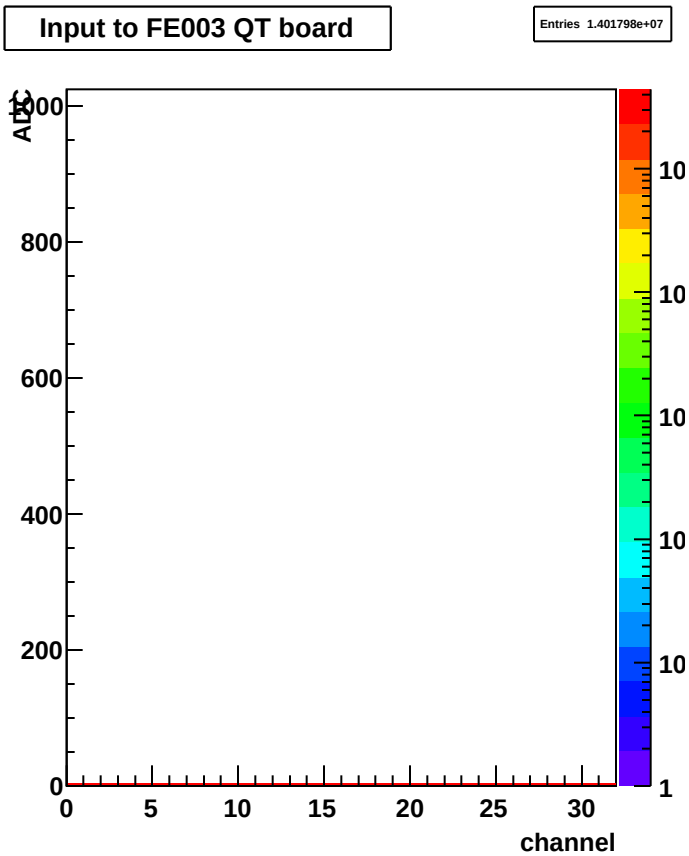

Input to FPD L2 DSM
Entries 1314186
HT bits
436094
438062
438062
1968
SMALL
LARGE-S
LARGE-N

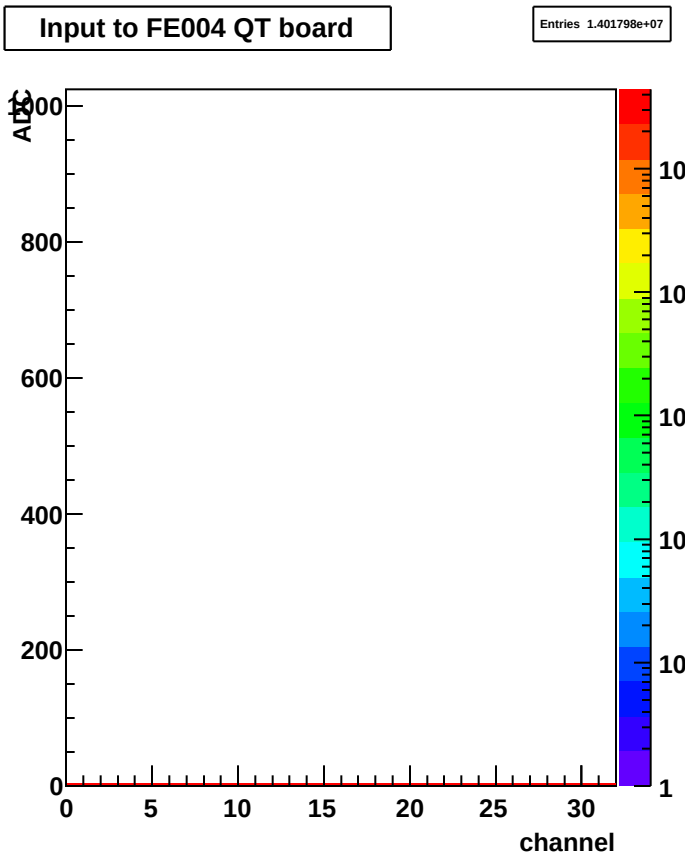

Input to FPD L2 DSM
Entries 1314186
HT bits - simulated
436094
438062
438062
1968
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1752248
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

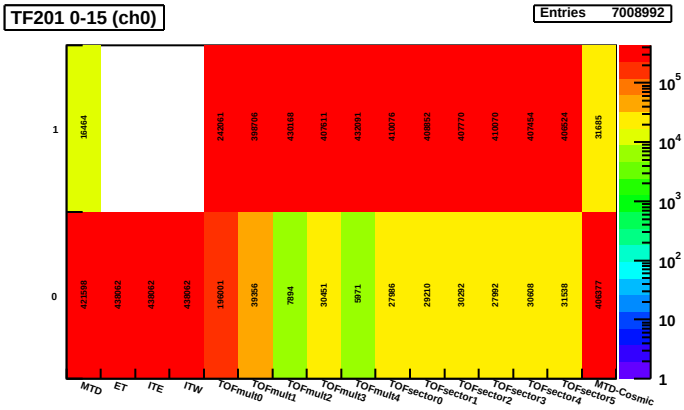

Input to FE001 QT board
Entries 1.401798e+07
ADC
channel


Input to FE002 QT board
Entries 1.401798e+07
ADC
channel

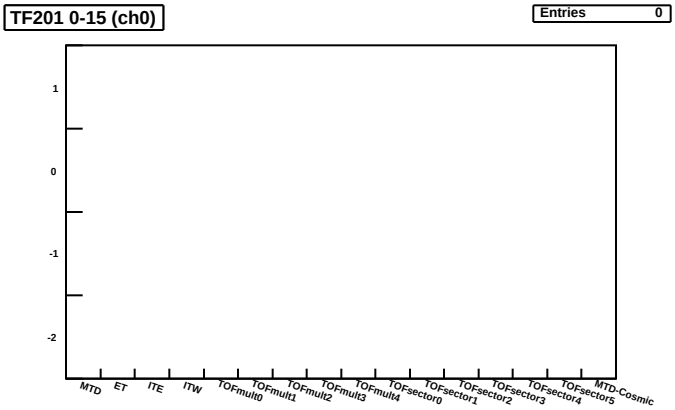

Input to FE003 QT board
Entries 1.401798e+07
ADC
channel


Input to FE004 QT board
Entries 1.401798e+07
ADC
channel

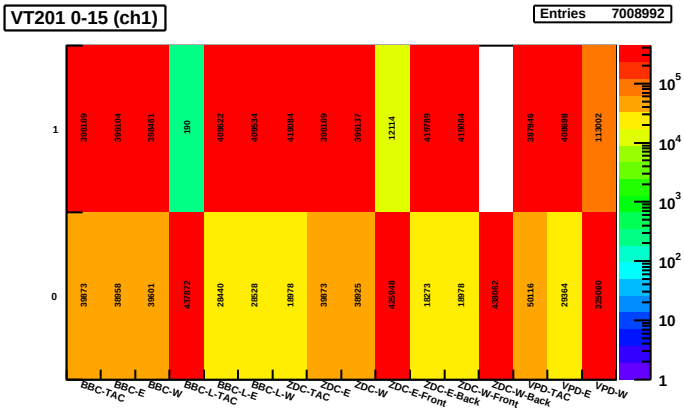
TF201 0-15 (ch0)



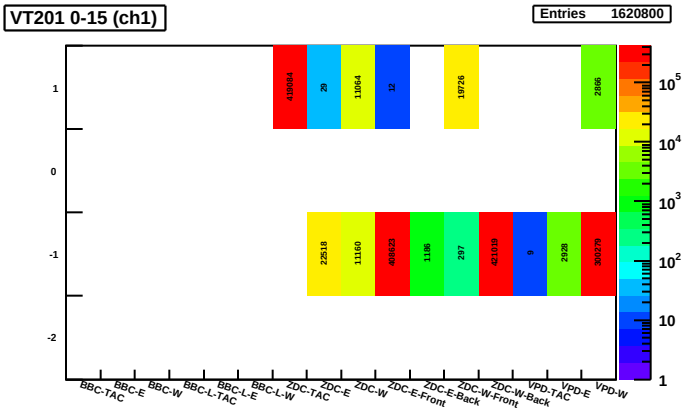
TF201 0-15 (ch0)



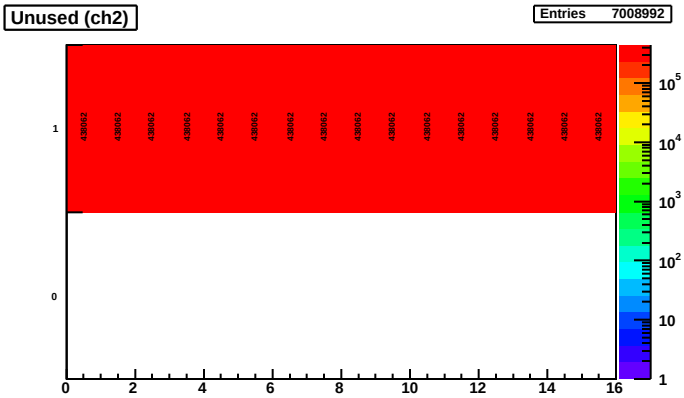
VT201 0-15 (ch1)

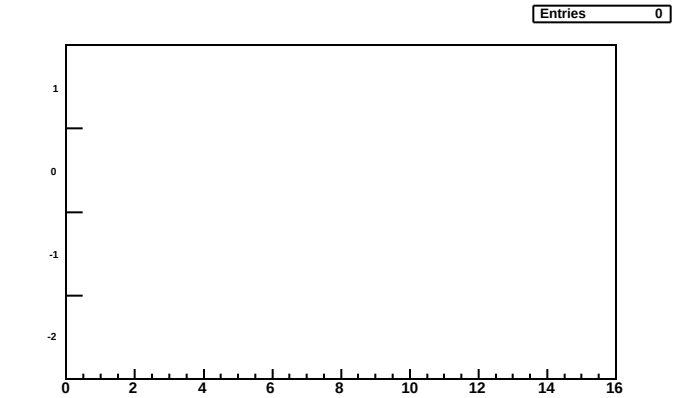


VT201 0-15 (ch1)

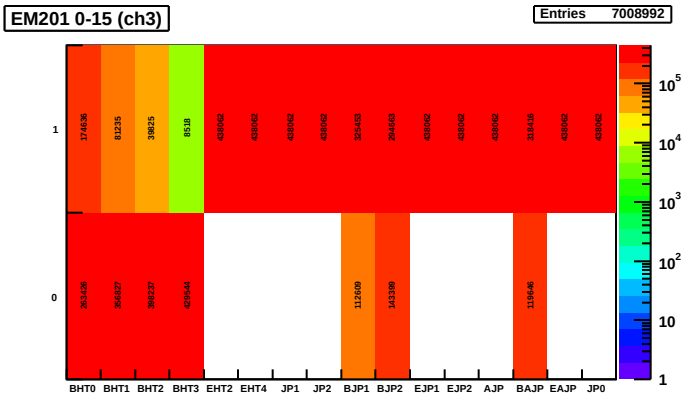


Unused (ch2)

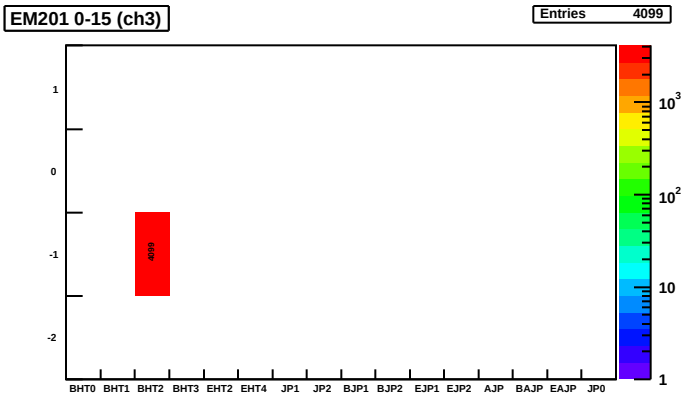




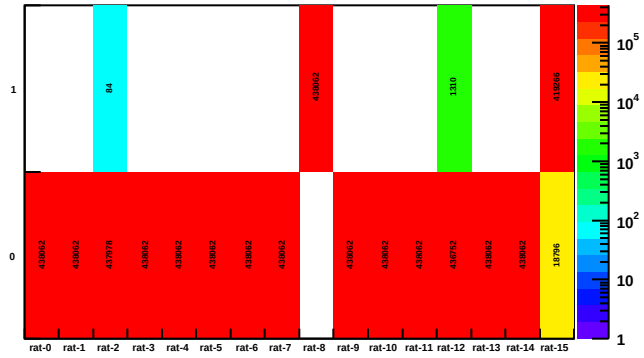
EM201 0-15 (ch3)

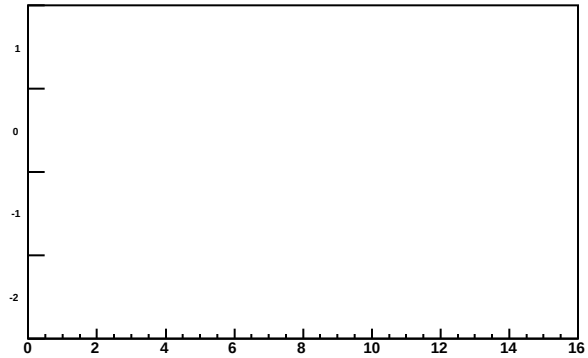


EM201 0-15 (ch3)

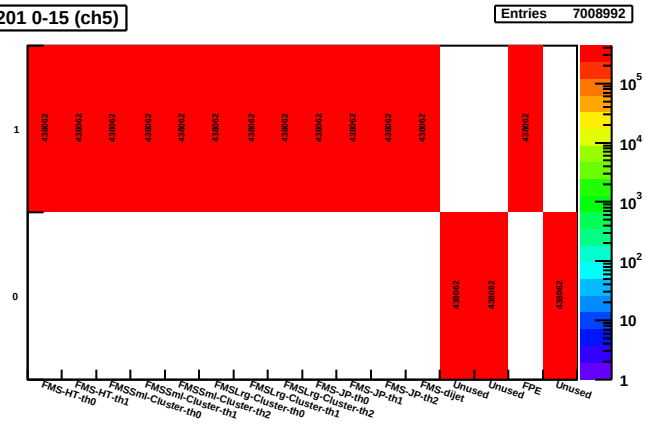


RAT board (ch4)

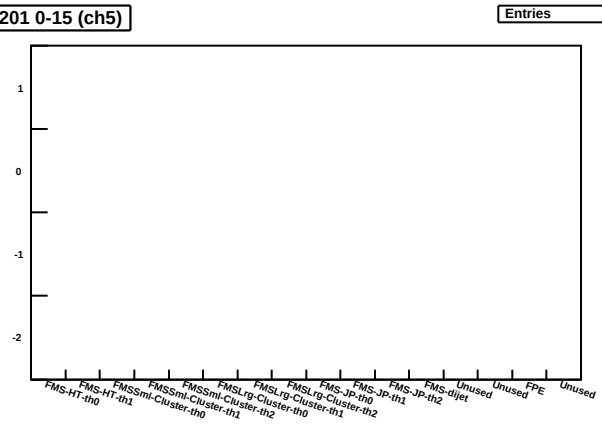




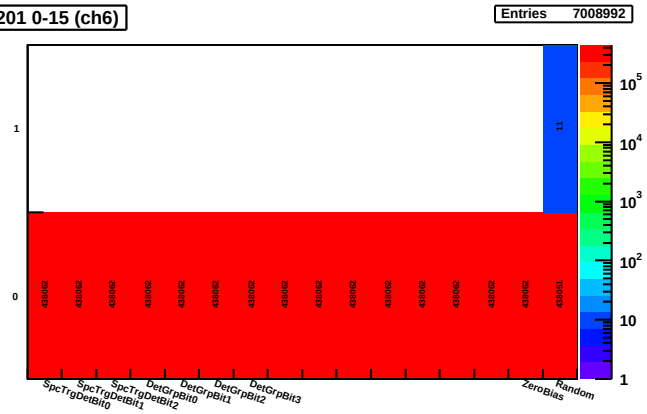
FP201 0-15 (ch5)

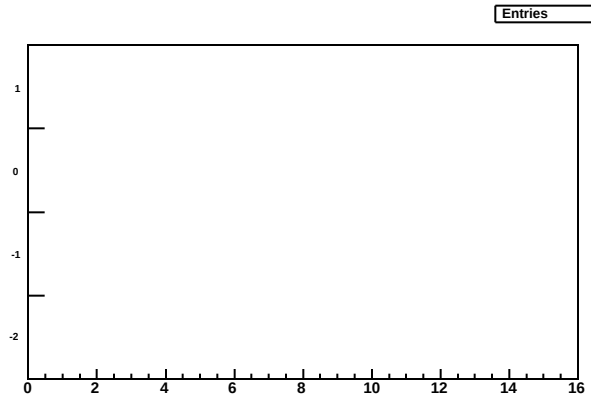
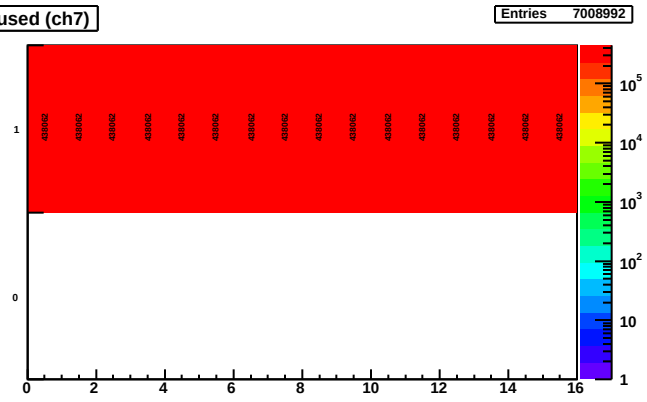
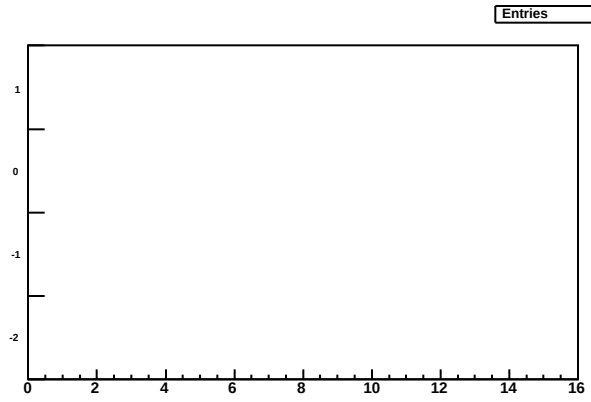


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)