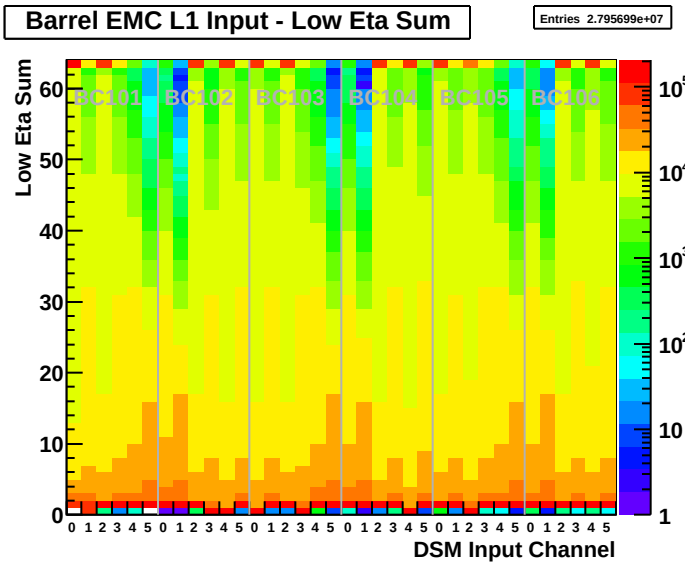
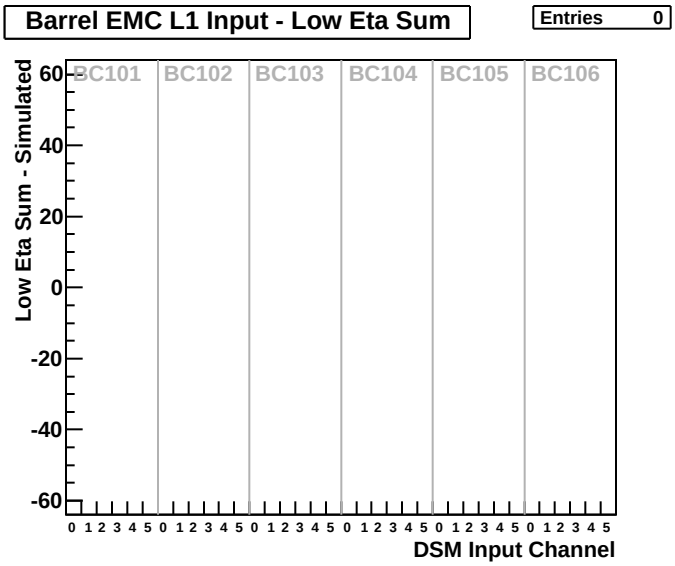
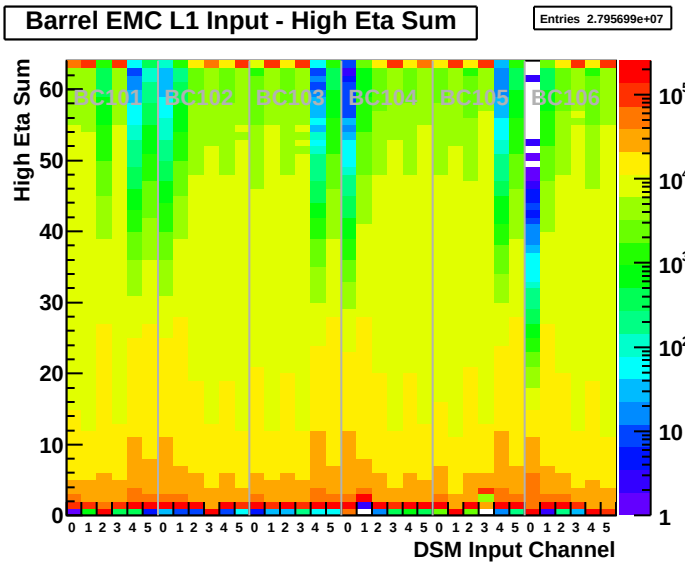
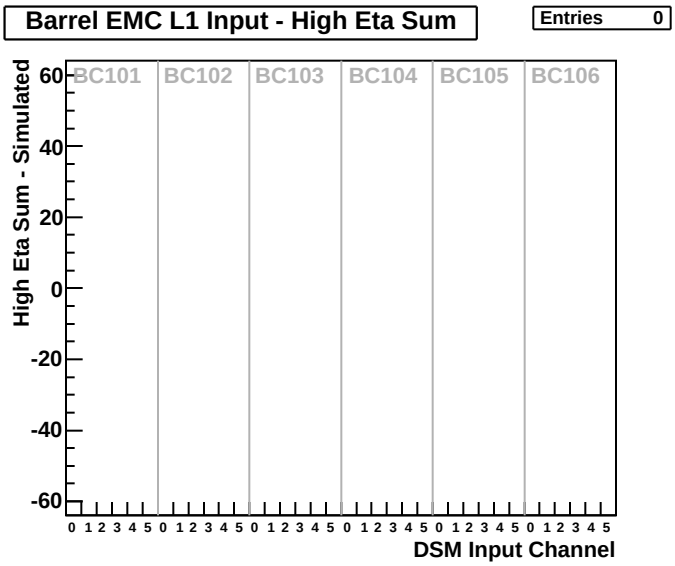
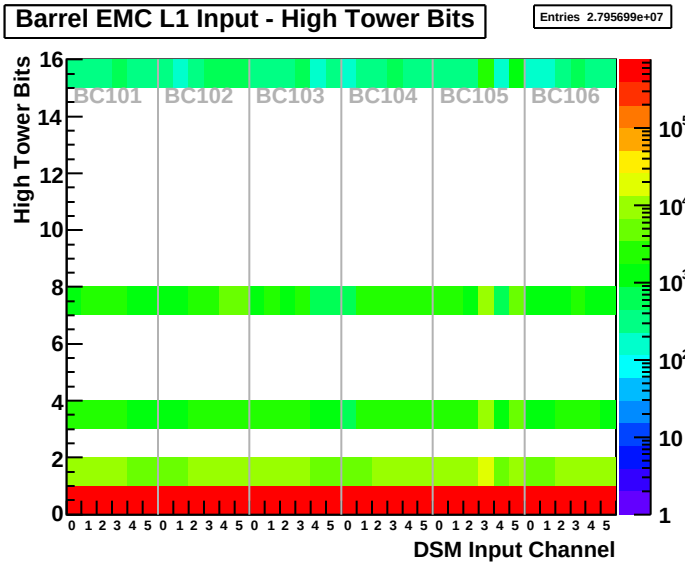
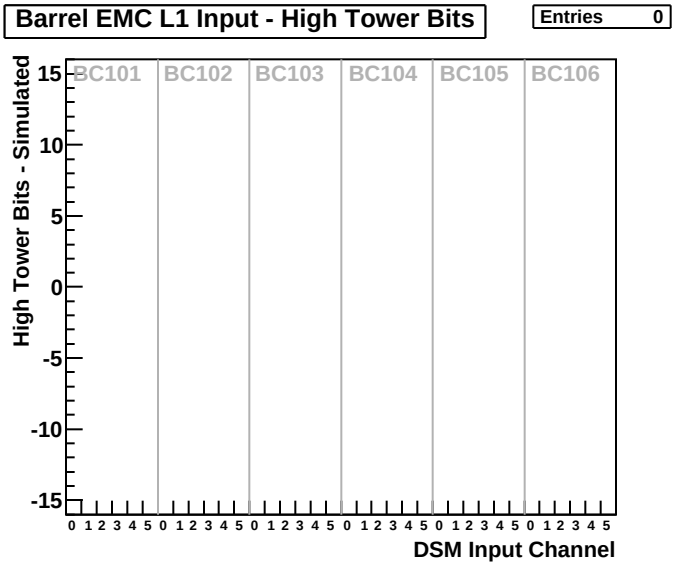

Barrel EMC L1 Input - Low Eta Sum
Entries 2.795699e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

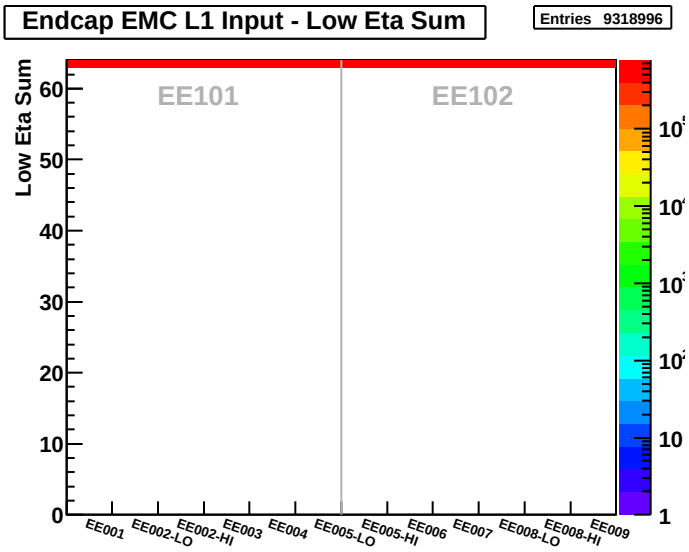

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

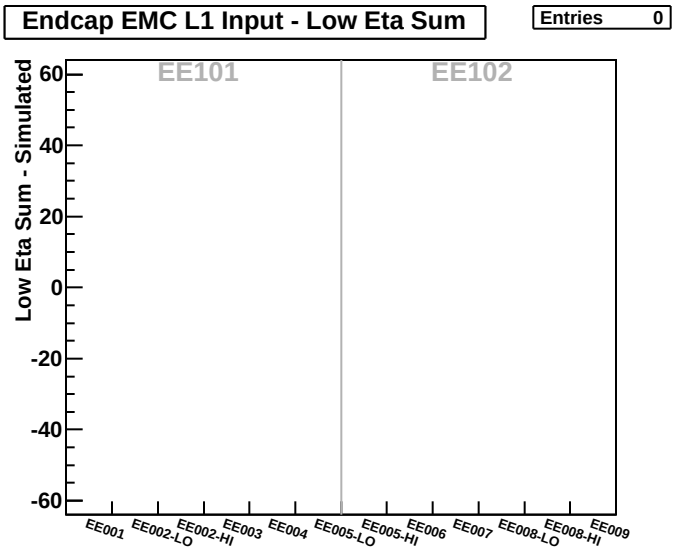

Barrel EMC L1 Input - High Eta Sum
Entries 2.795699e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

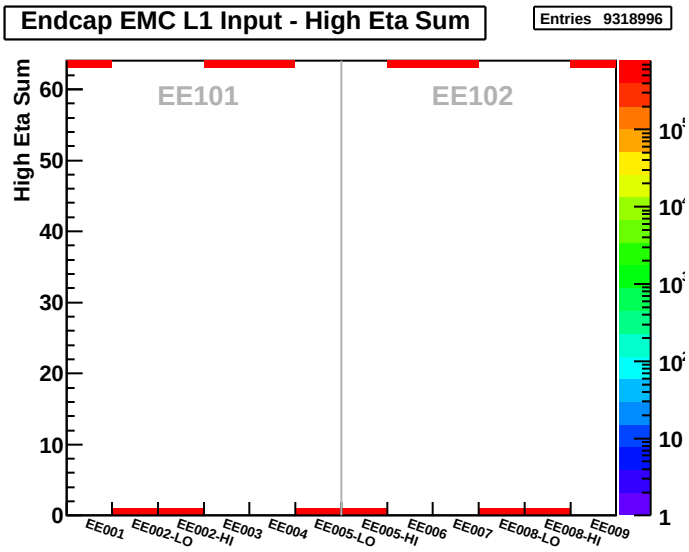

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

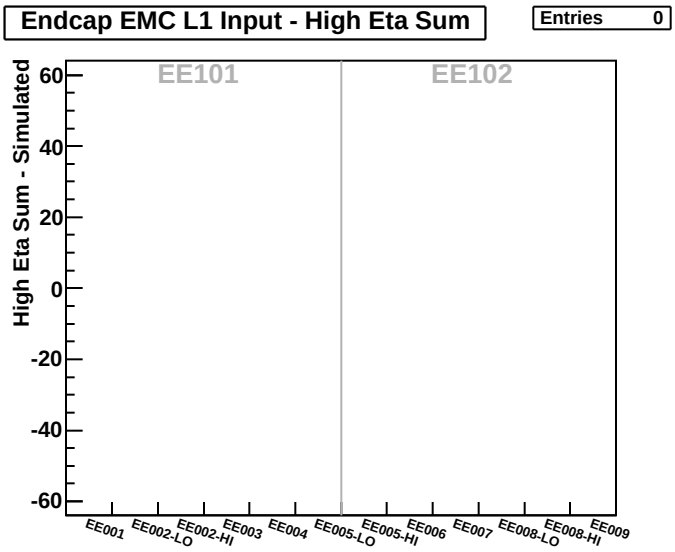

Barrel EMC L1 Input - High Tower Bits
Entries 2.795699e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

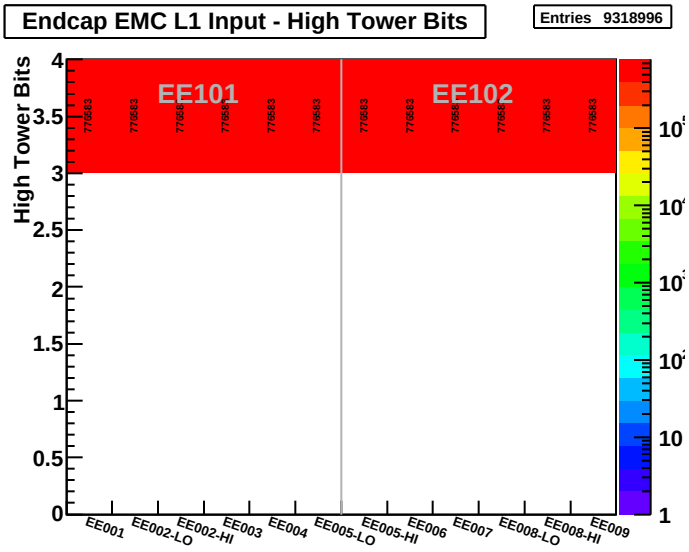

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

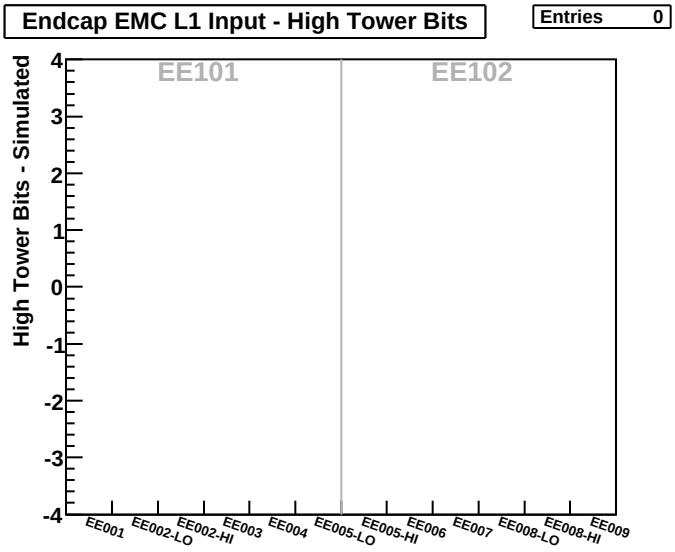

Endcap EMC L1 Input - Low Eta Sum
Entries 9318996
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

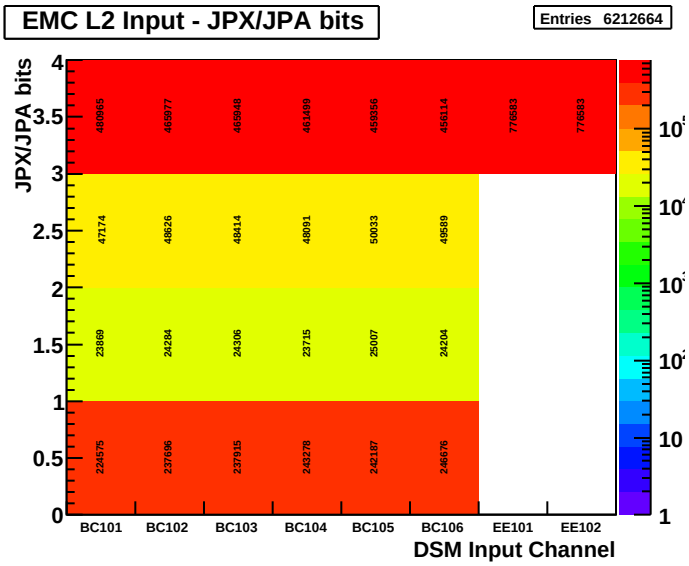

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

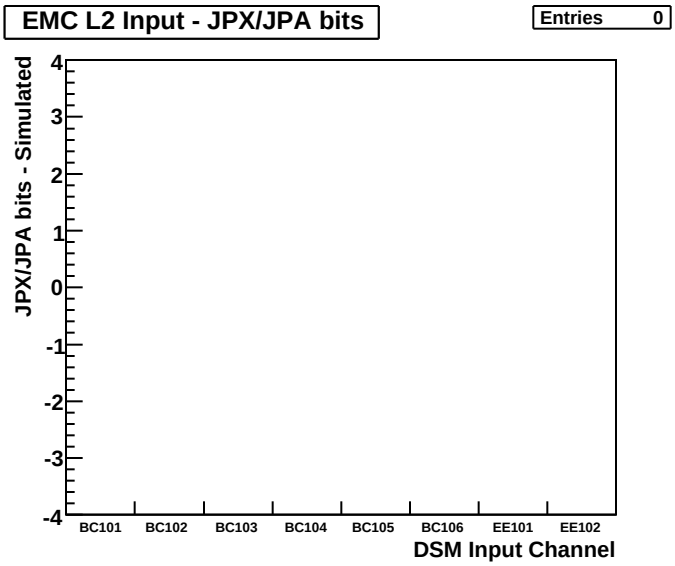

Endcap EMC L1 Input - High Eta Sum
Entries 9318996
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

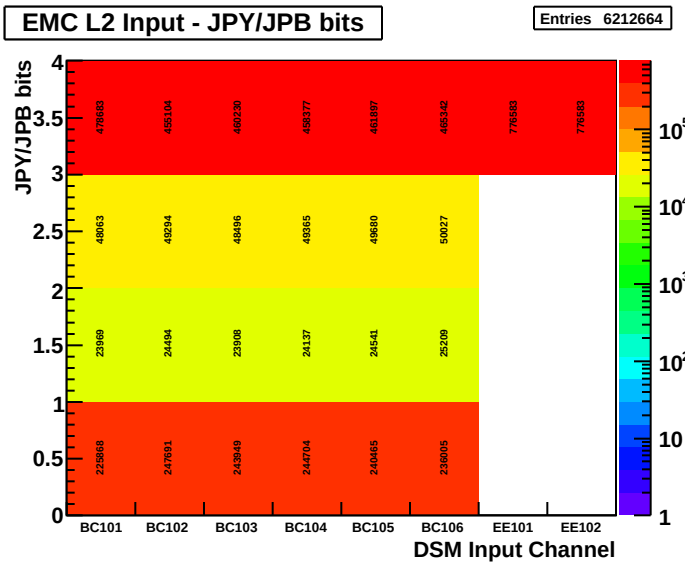

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

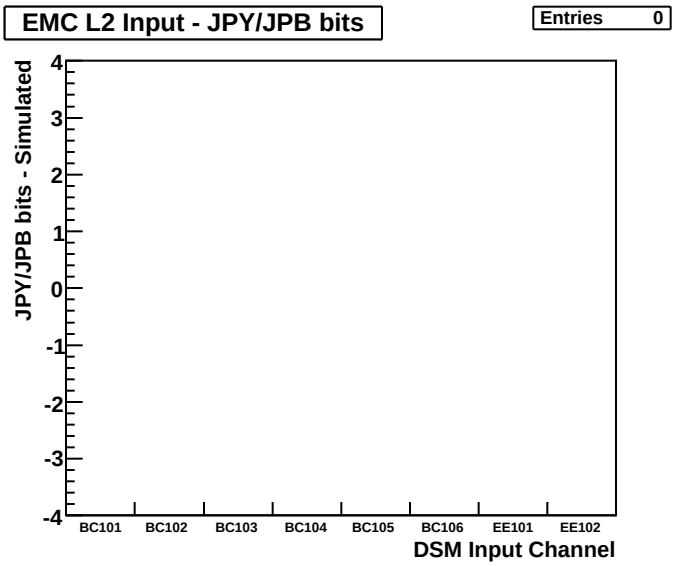

Endcap EMC L1 Input - High Tower Bits
Entries 9318996
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

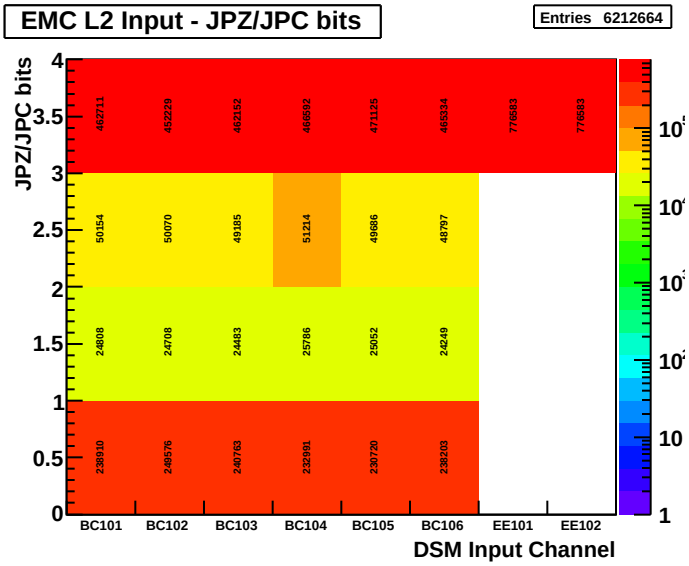

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

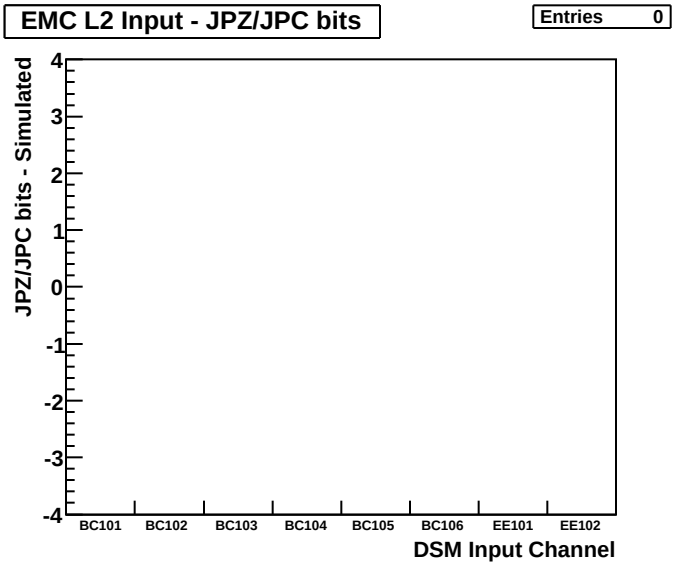

EMC L2 Input - JPX/JPA bits
Entries 6212664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
450965 459977 465948 461499 459356 456114 776583 776583
47174 48626 48414 48091 50033 49589
23869 24284 24306 23715 25007 24204
224575 237686 237915 243278 242187 246676

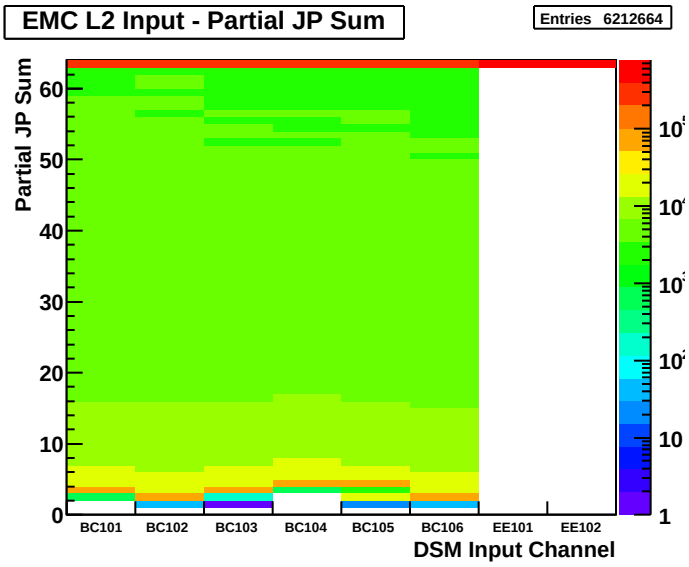

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

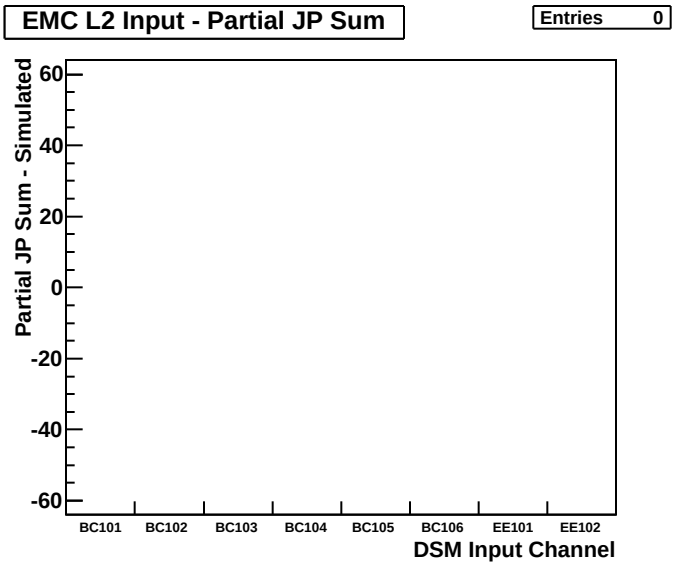

EMC L2 Input - JPY/JPB bits
Entries 6212664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
476683 455104 460230 458377 461897 465342 776583 776583
48063 48294 48496 48365 48680 50027
23969 24494 23908 24137 24541 25209
223868 247691 243949 244704 240465 236005

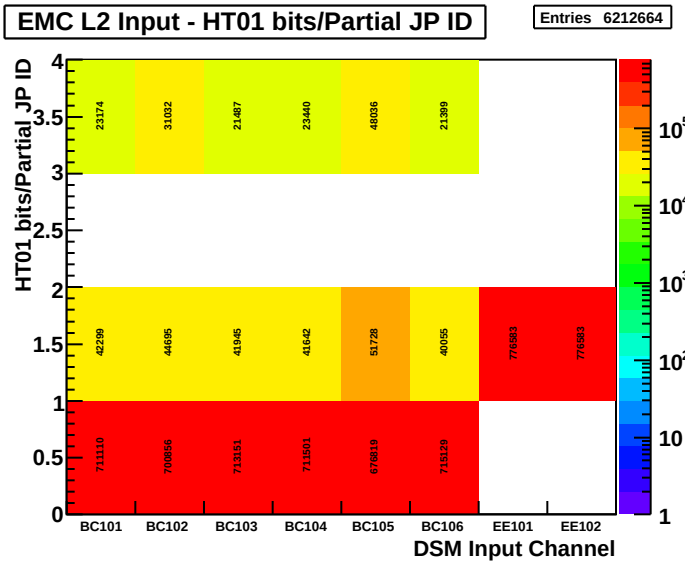

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

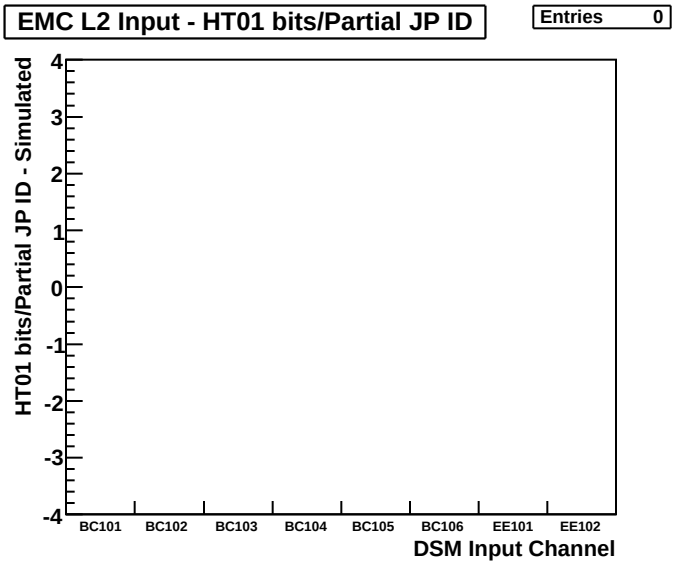

EMC L2 Input - JPZ/JPC bits
Entries 6212664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
462711 452229 462152 466592 471125 465334 776583 776583
50154 50070 49185 51214 49686 48797
24808 24708 24483 25786 25952 24249
238910 249576 240763 232991 230720 238203

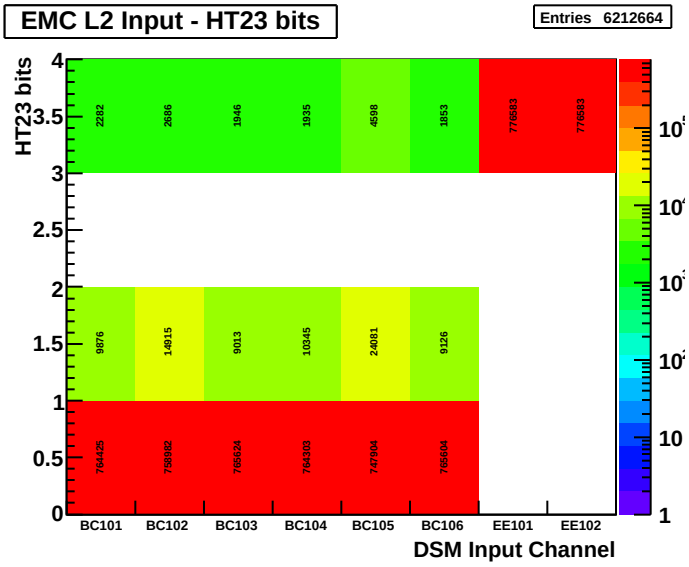

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

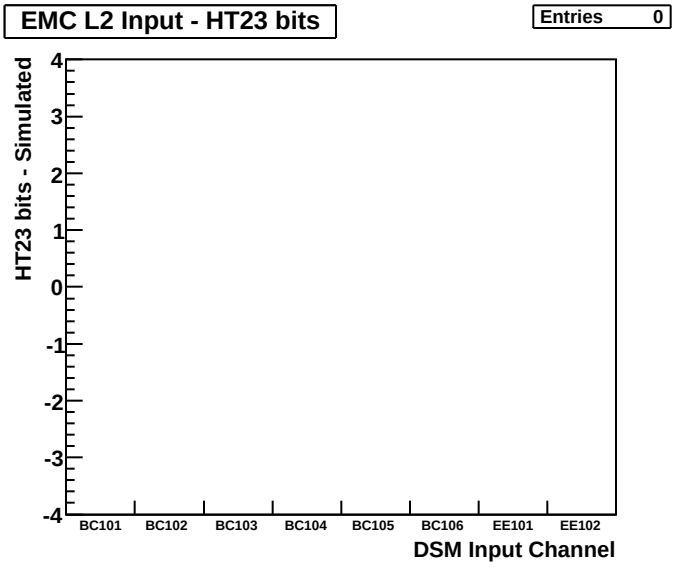

EMC L2 Input - Partial JP Sum
Entries 6212664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

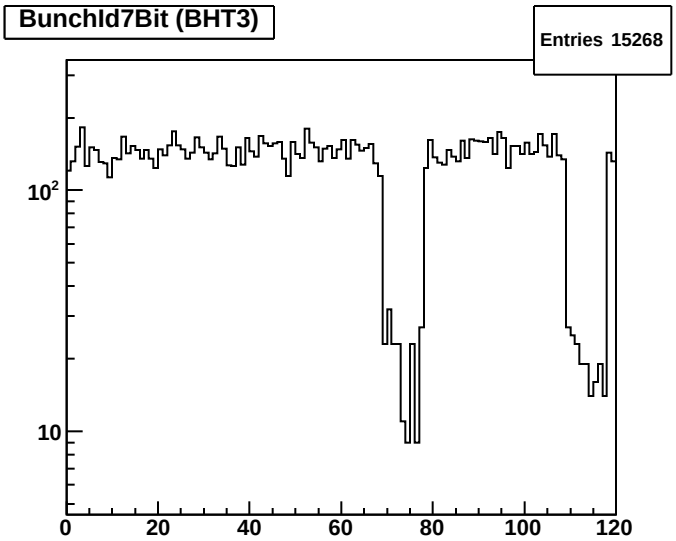

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

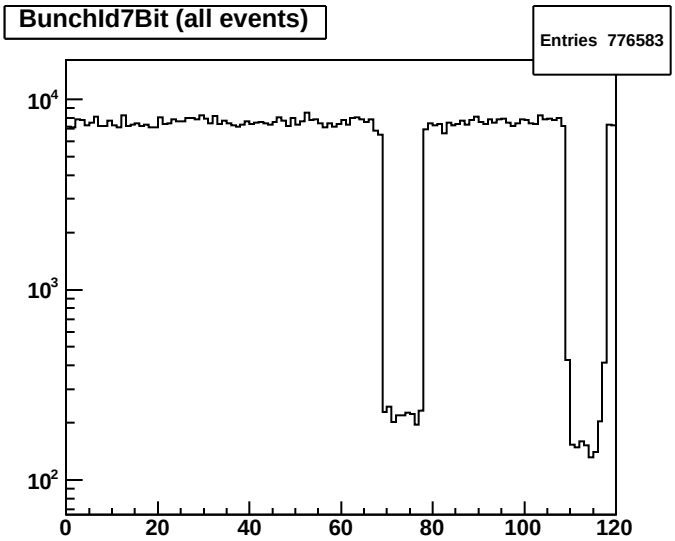

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6212664
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

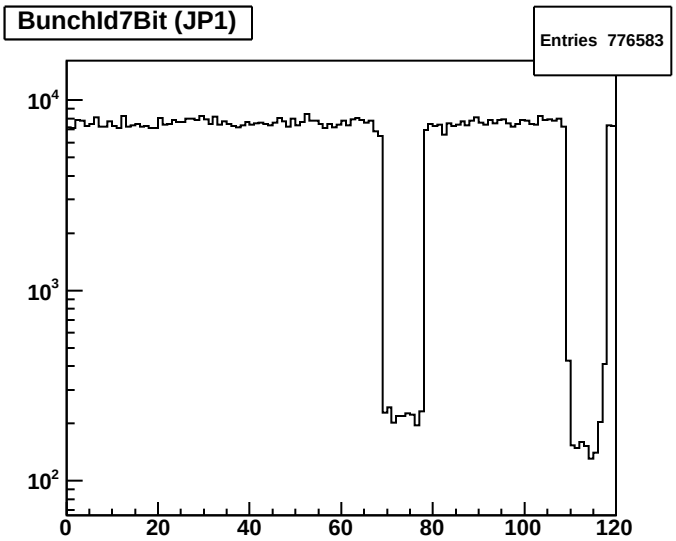

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

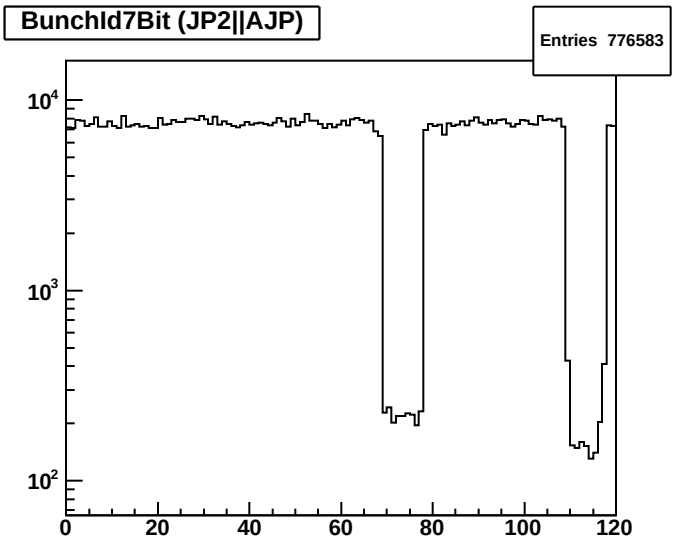

EMC L2 Input - HT23 bits
Entries 6212664
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

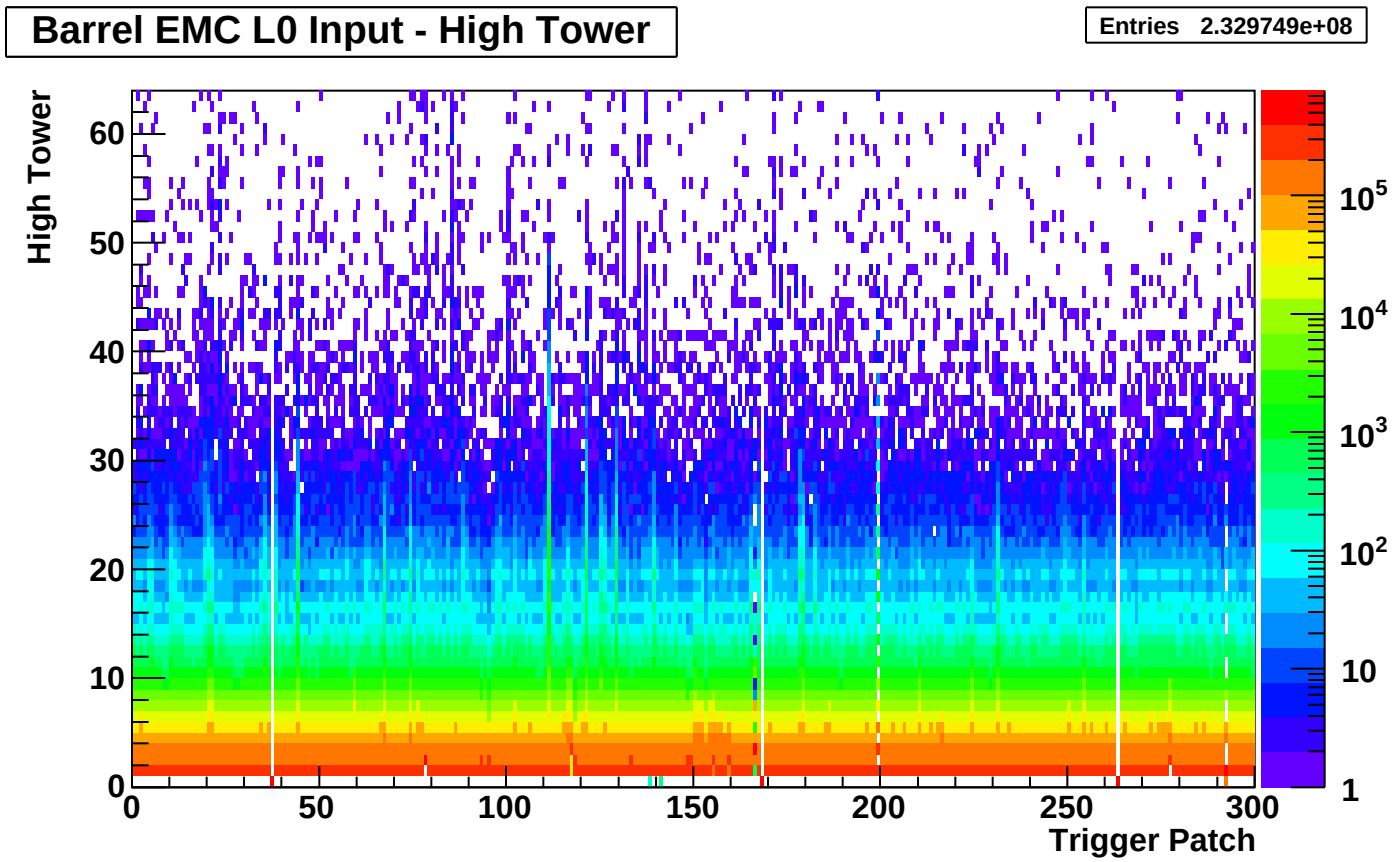

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

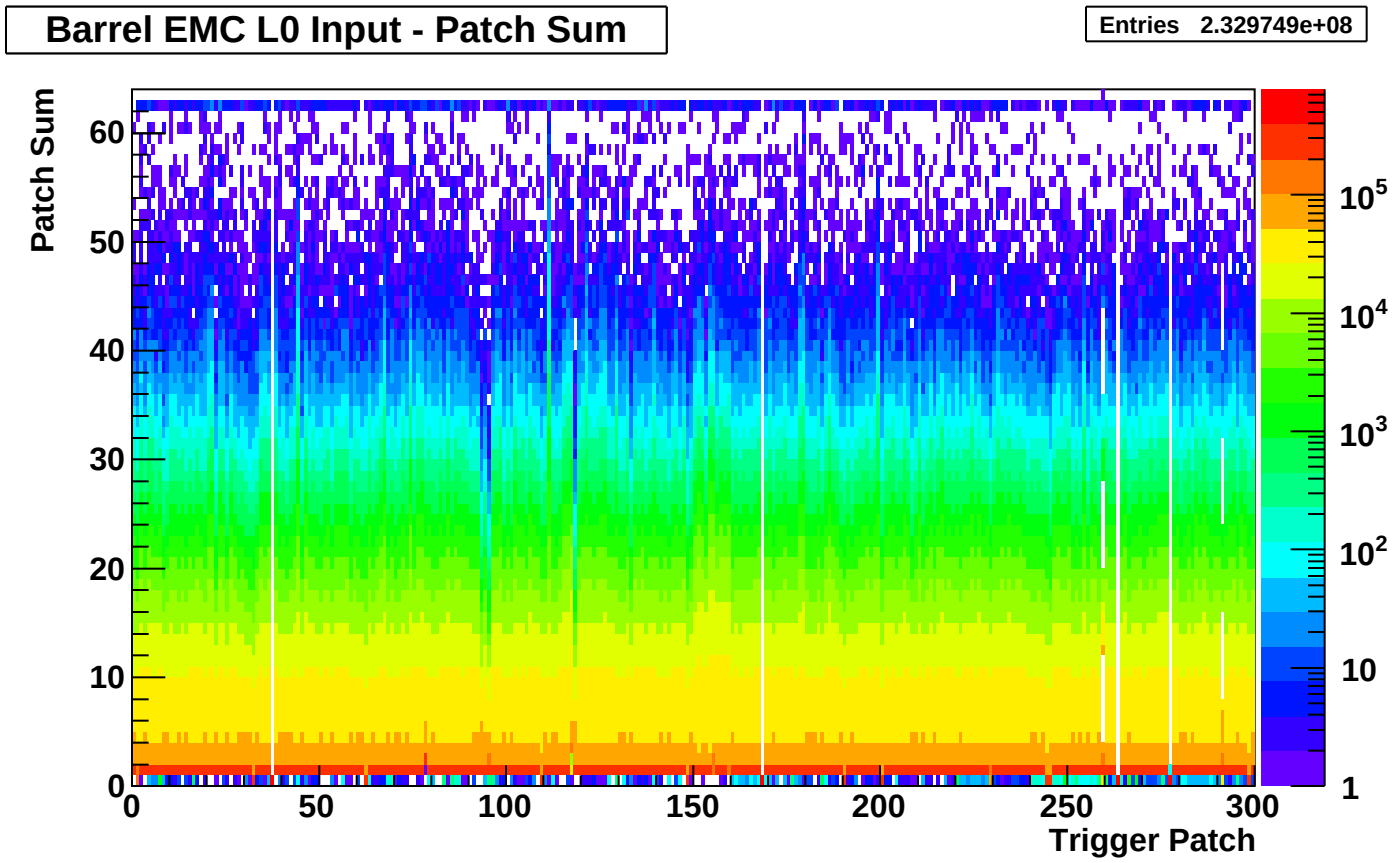

BunchId7Bit (BHT3)
Entries 15268
10^2
10
0
20
40
60
80
100
120

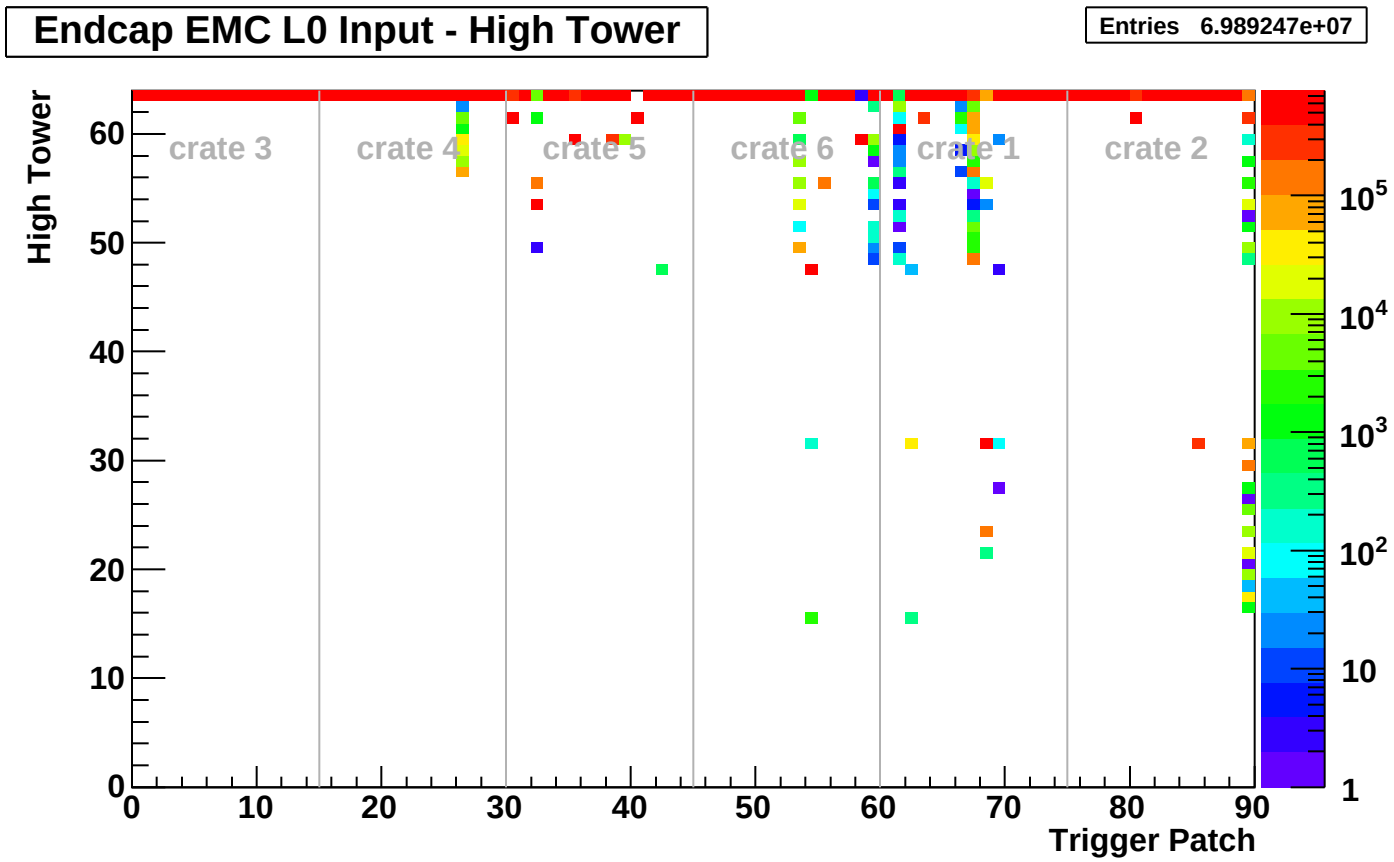

BunchId7Bit (all events)
Entries 776583
10^4
10^3
10^2
0
20
40
60
80
100
120

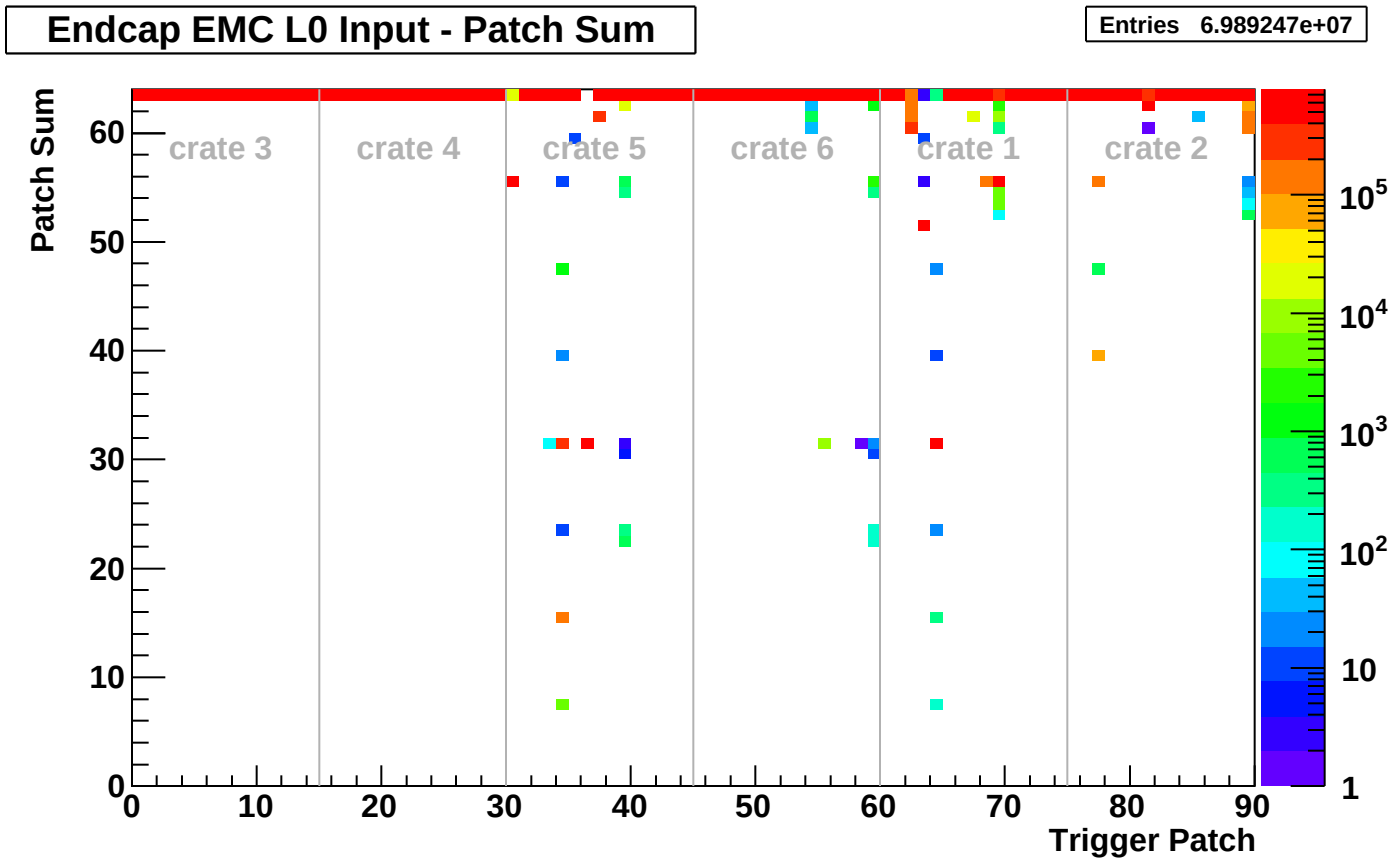

BunchId7Bit (JP1)
Entries 776583
10^4
10^3
10^2
0
20
40
60
80
100
120

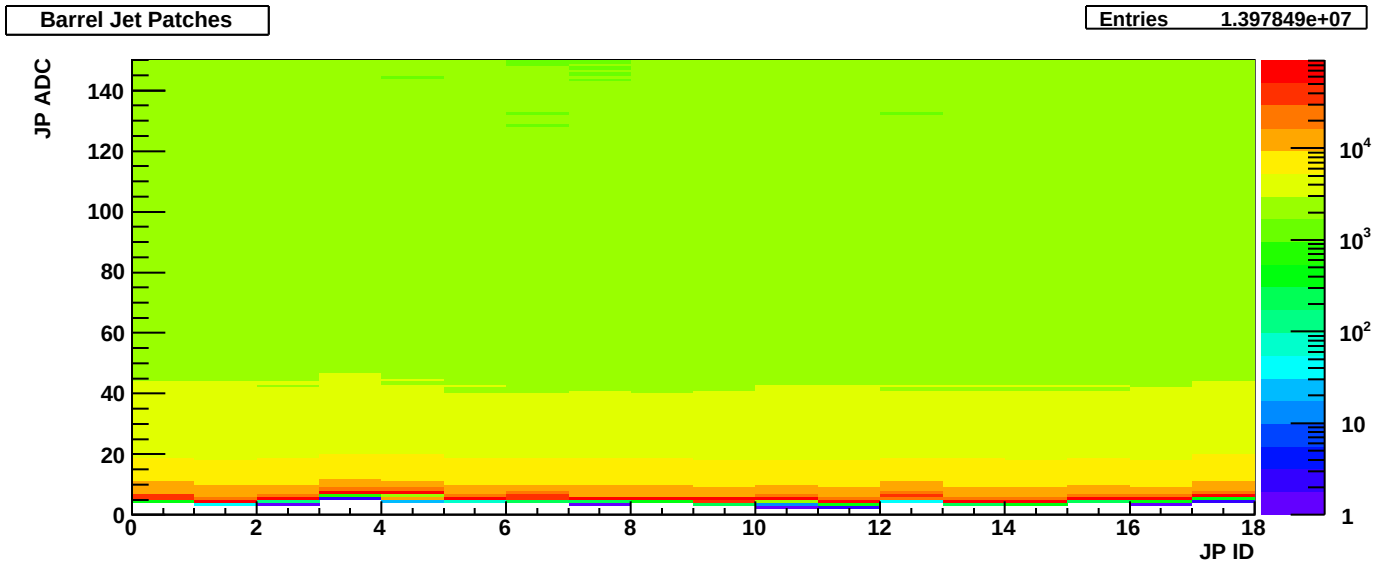

BunchId7Bit (JP2||AJP)
Entries 776583
10^4
10^3
10^2
0
20
40
60
80
100
120

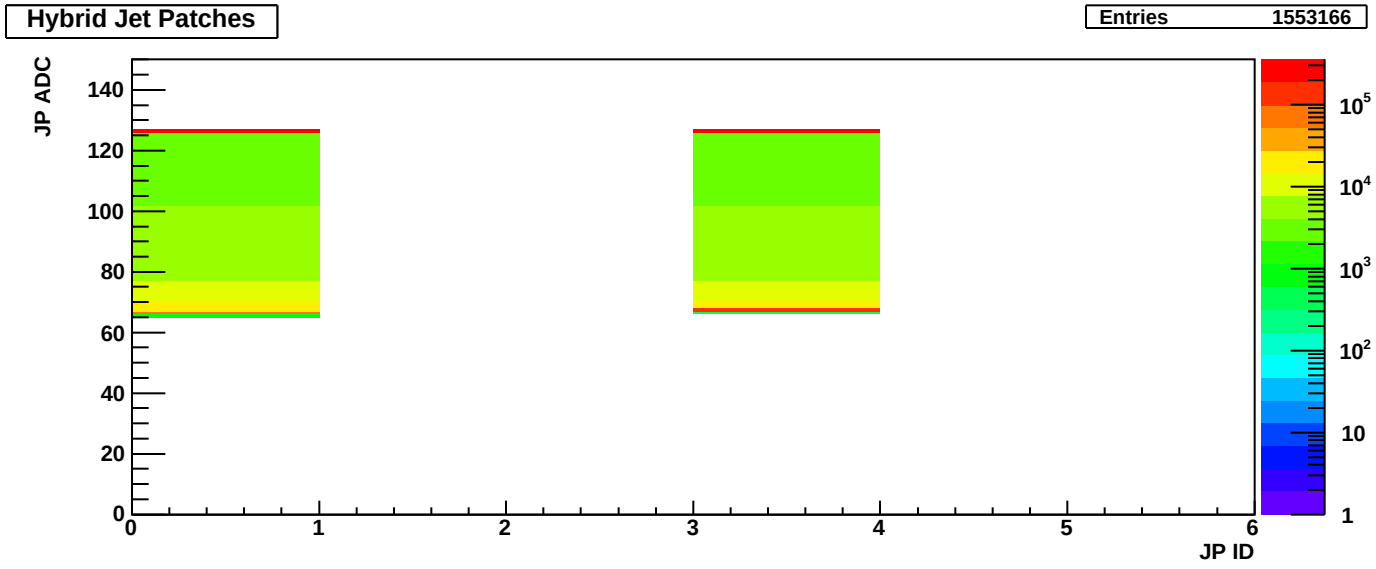

Barrel EMC L0 Input - High Tower
Entries 2.329749e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

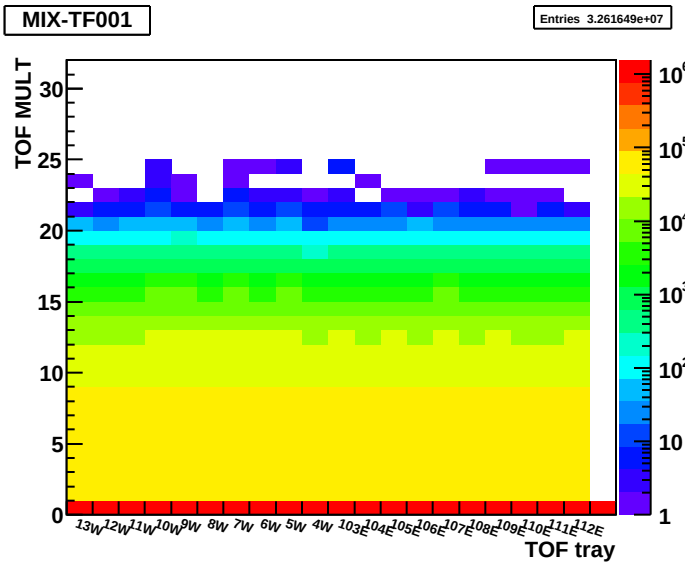

Barrel EMC L0 Input - Patch Sum
Entries 2.329749e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

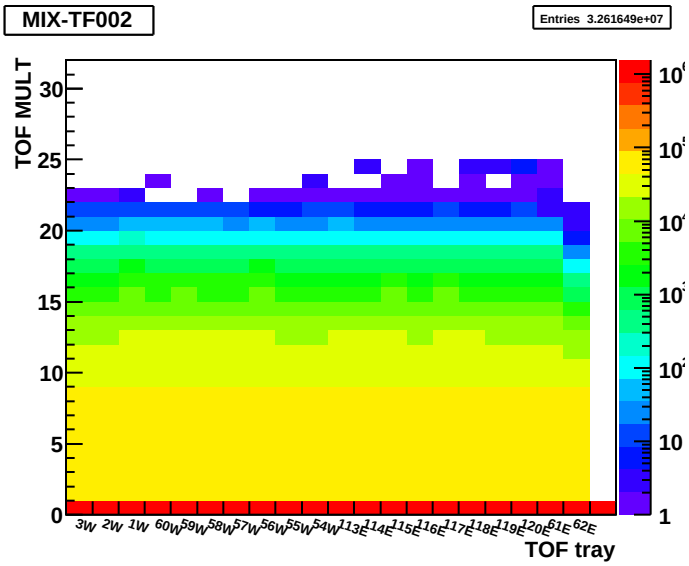

Endcap EMC L0 Input - High Tower
Entries 6.989247e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

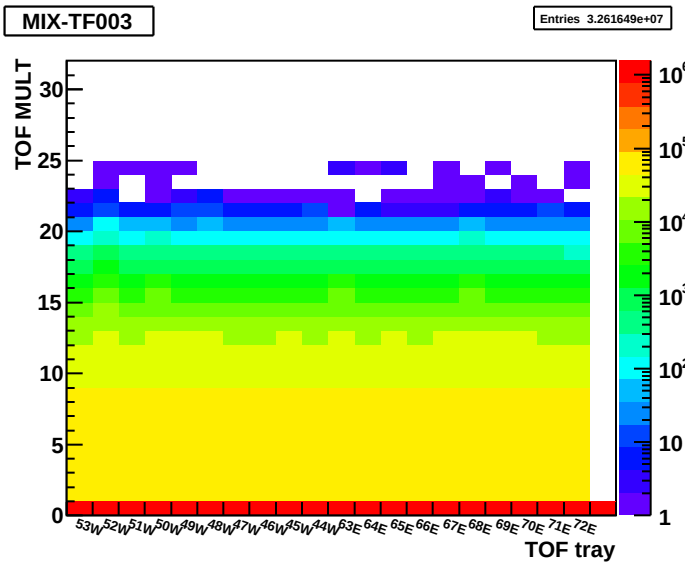

Endcap EMC L0 Input - Patch Sum
Entries 6.989247e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

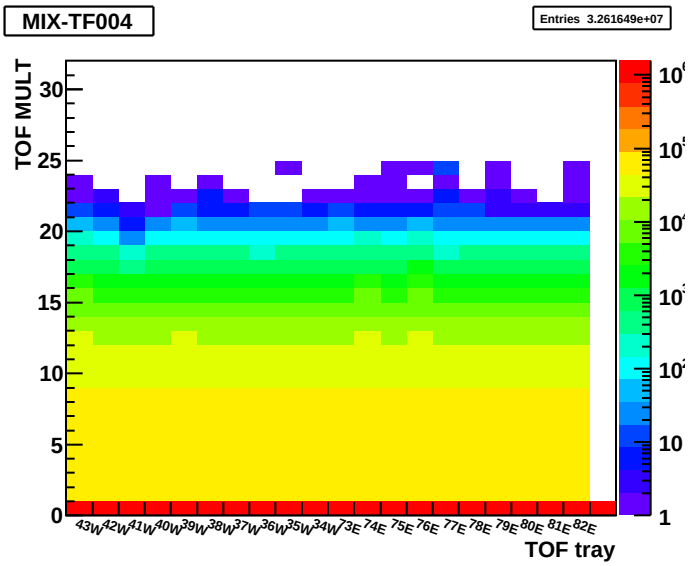

Barrel Jet Patches
Entries 1.397849e+07
JP ADC
JP ID

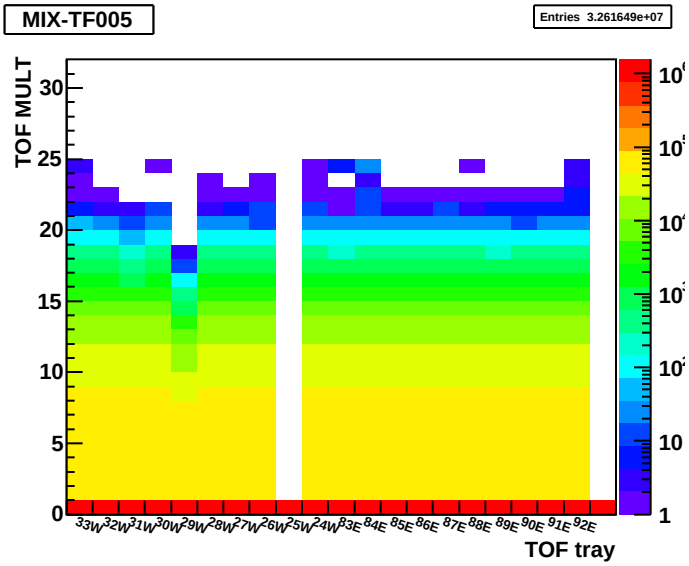

Hybrid Jet Patches
Entries 1553166
JP ADC
JP ID

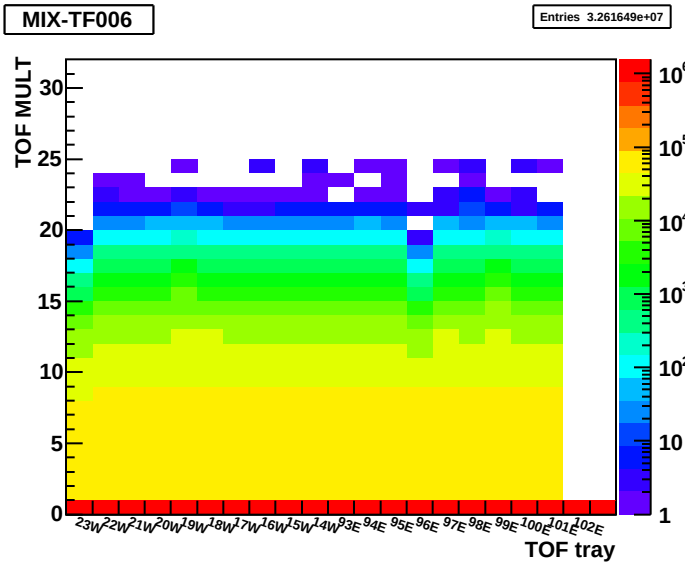

MIX-TF001
Entries 3.261649e+07
TOF MULT
TOF tray

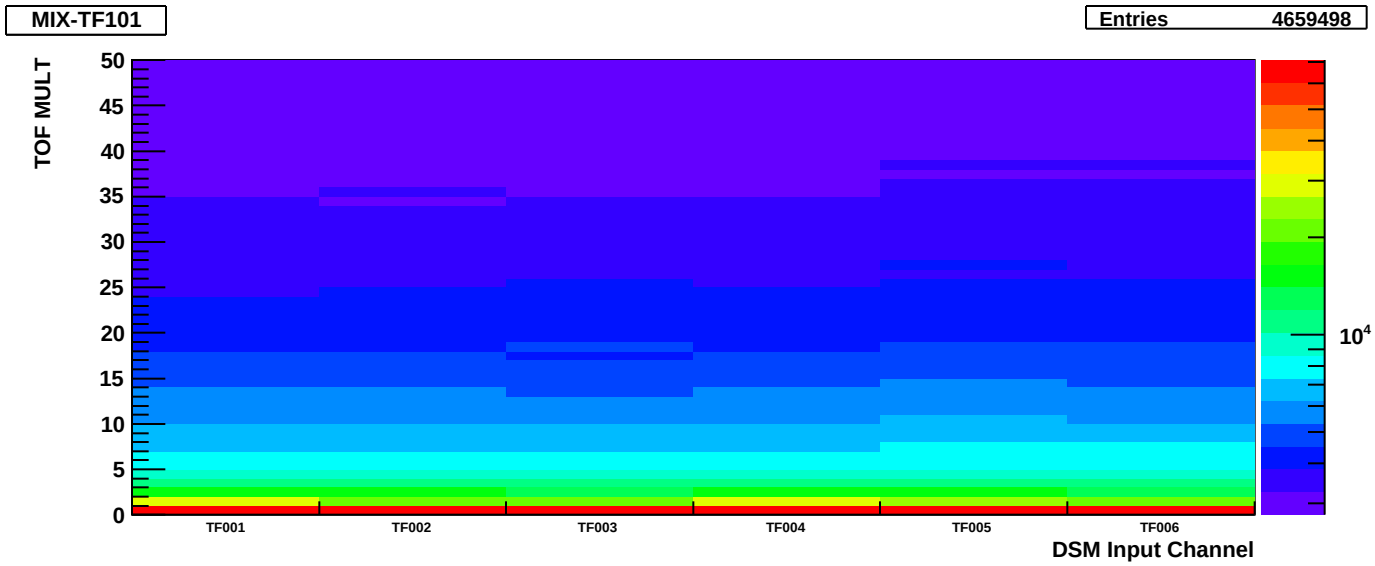

MIX-TF002
Entries 3.261649e+07
TOF MULT
TOF tray

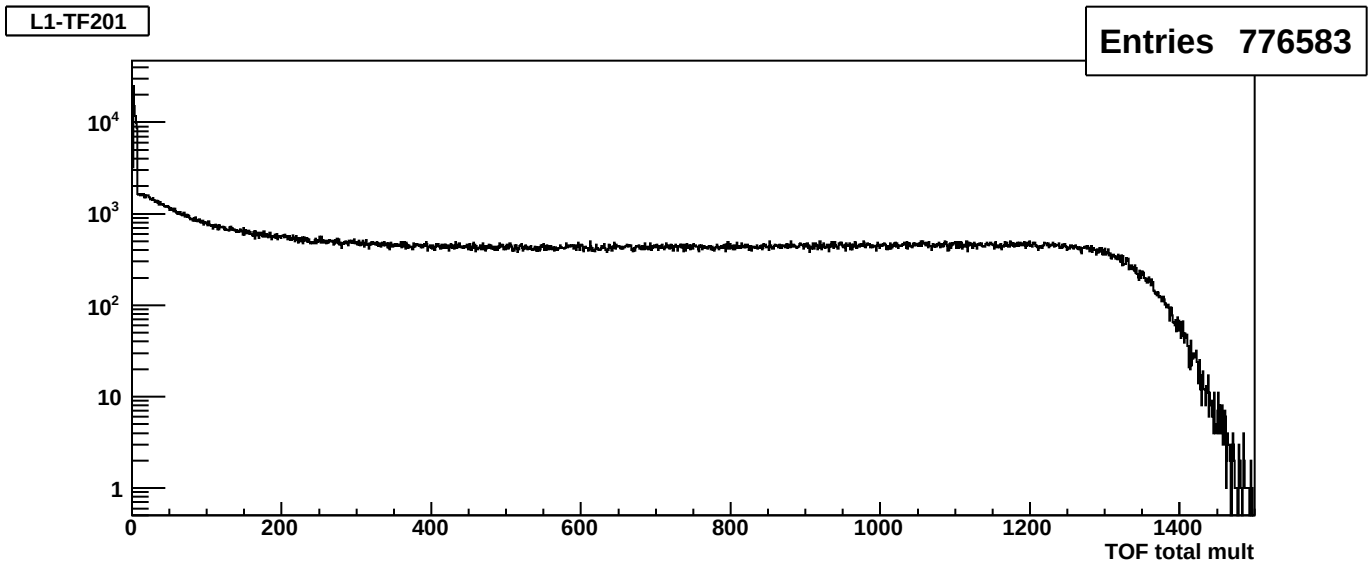

MIX-TF003
Entries 3.261649e+07
TOF MULT
TOF tray

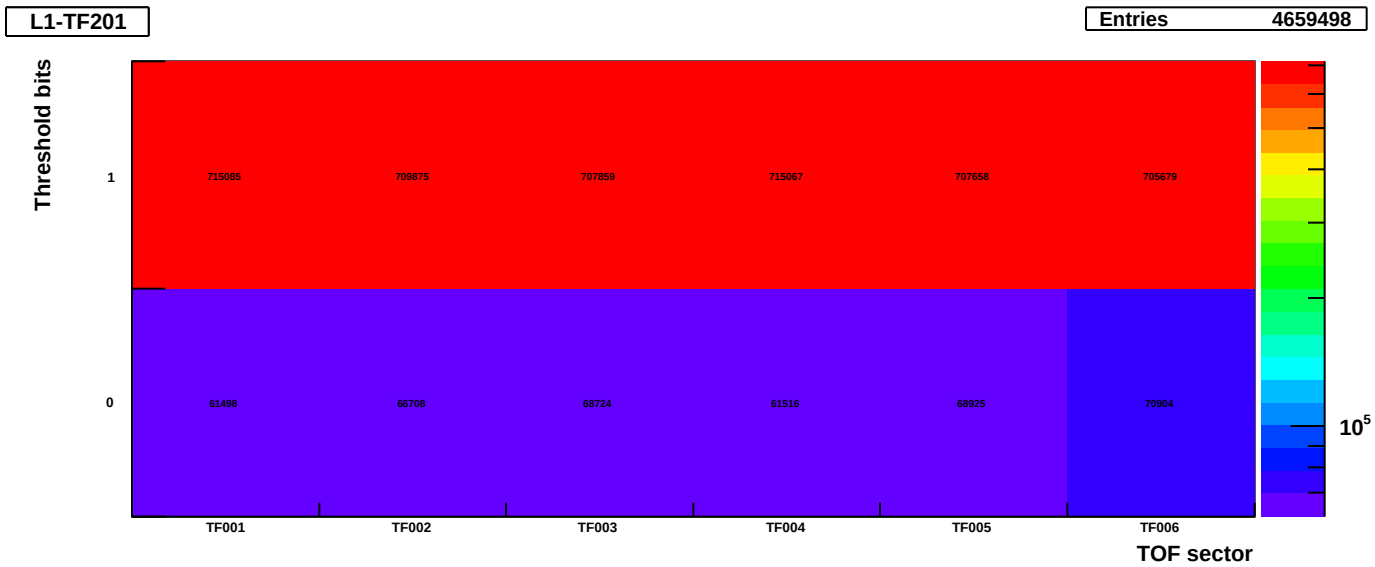

MIX-TF004
Entries 3.261649e+07
TOF MULT
TOF tray

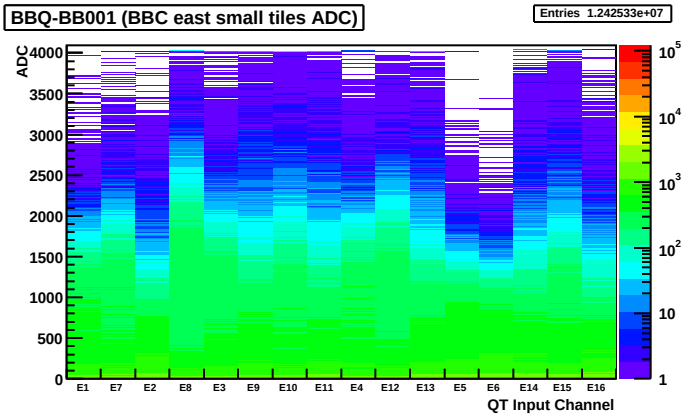

MIX-TF005
Entries 3.261649e+07
TOF MULT
TOF tray

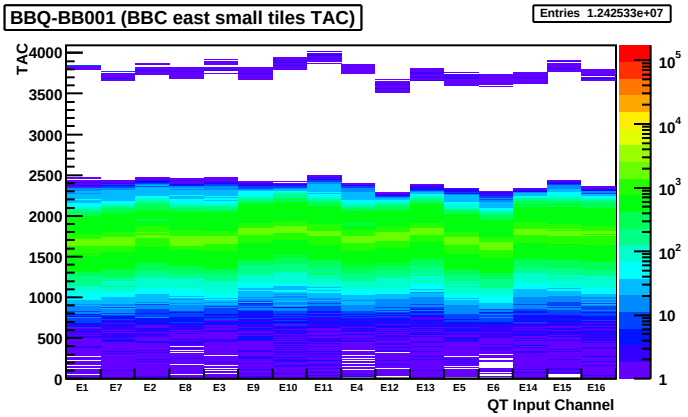

MIX-TF006
Entries 3.261649e+07
TOF MULT
TOF tray

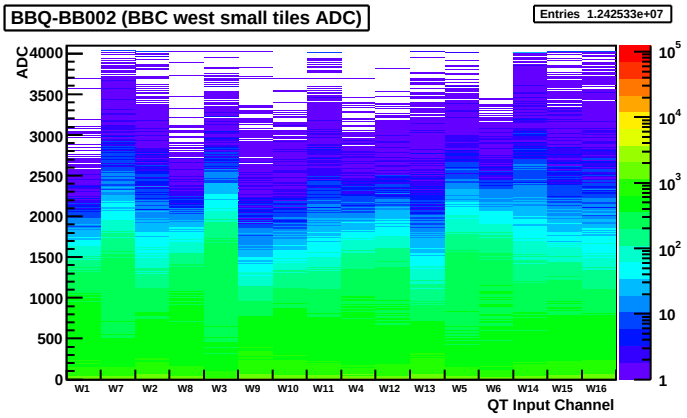

MIX-TF101
Entries 4659498
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

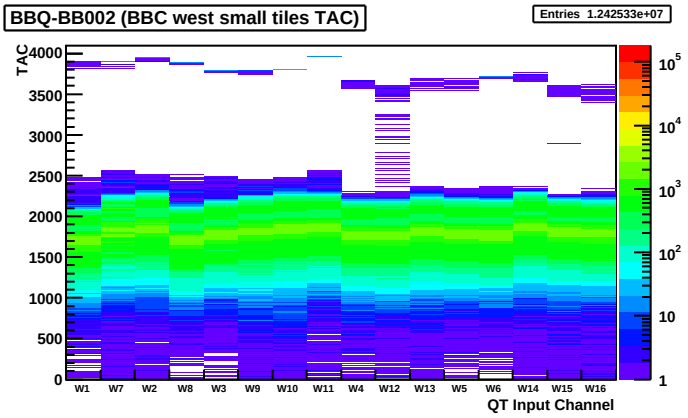

L1-TF201
Entries 776583
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

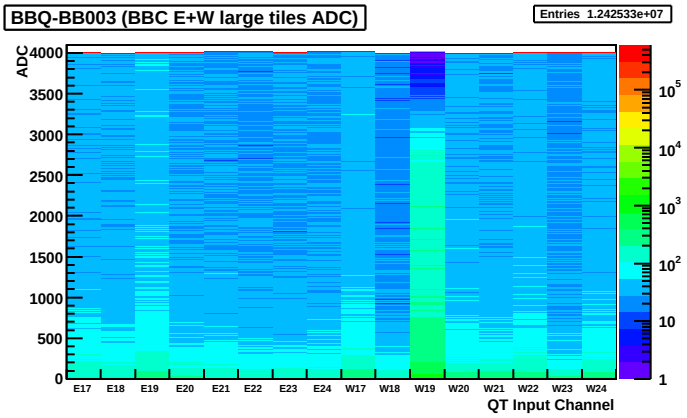

L1-TF201
Entries 4659498
Threshold bits
1
0
715085
709875
707859
715067
707658
705679
61498
66708
68724
61516
68925
70904
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

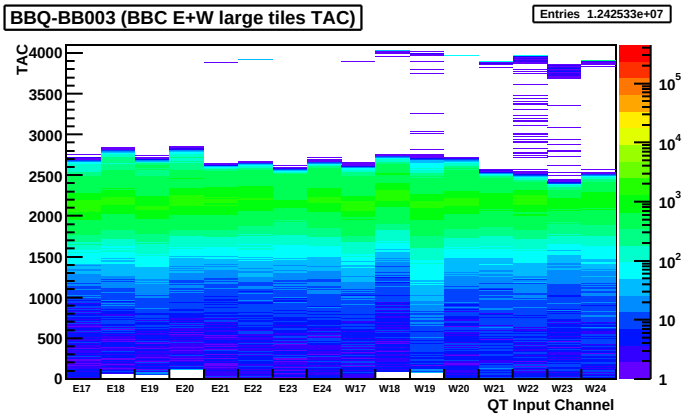

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.242533e+07
ADC
QT Input Channel

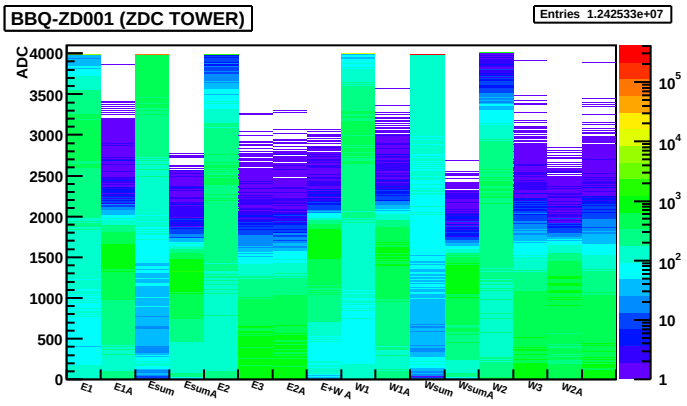

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.242533e+07
TAC
QT Input Channel

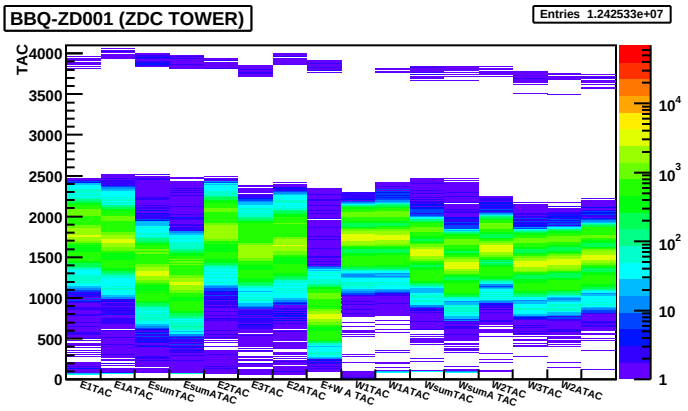

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.242533e+07
ADC
QT Input Channel

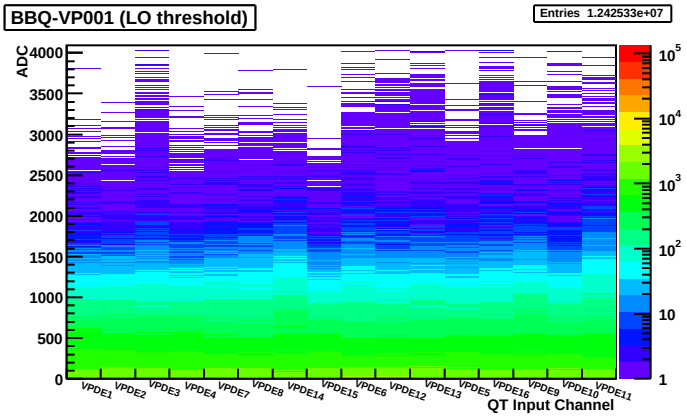

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.242533e+07
TAC
QT Input Channel

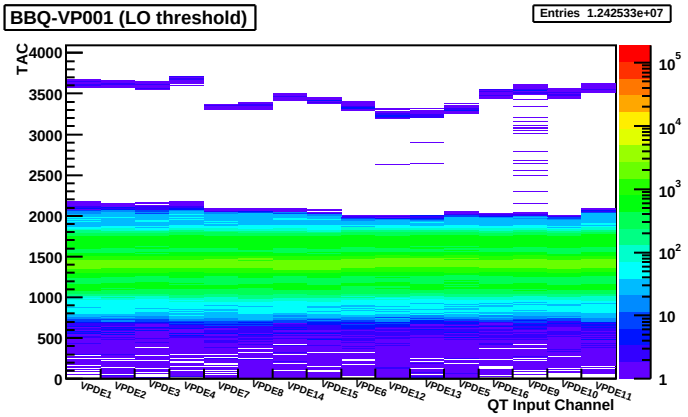

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.242533e+07
ADC
QT Input Channel

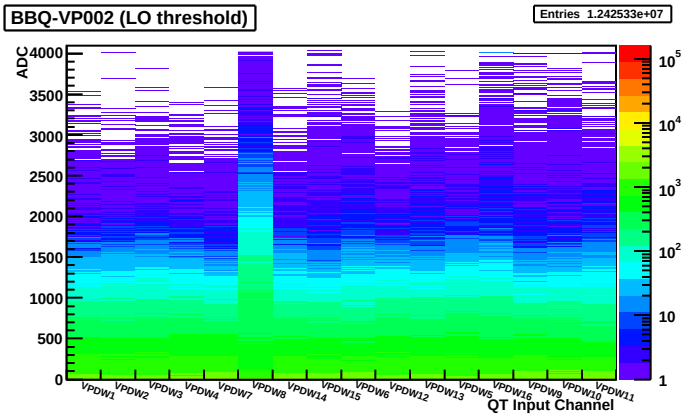

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.242533e+07
TAC
QT Input Channel

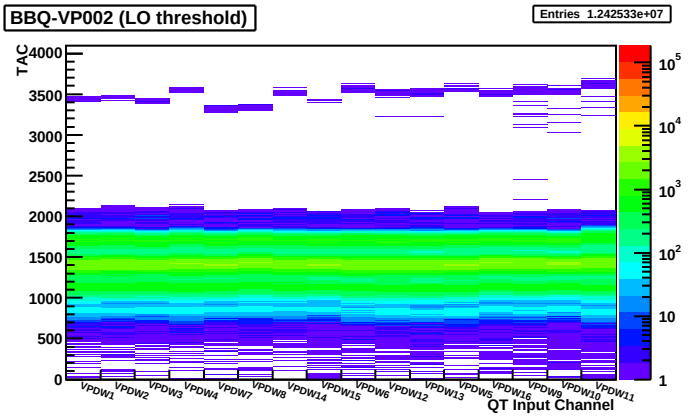

BBQ-ZD001 (ZDC TOWER)
Entries 1.242533e+07
ADC

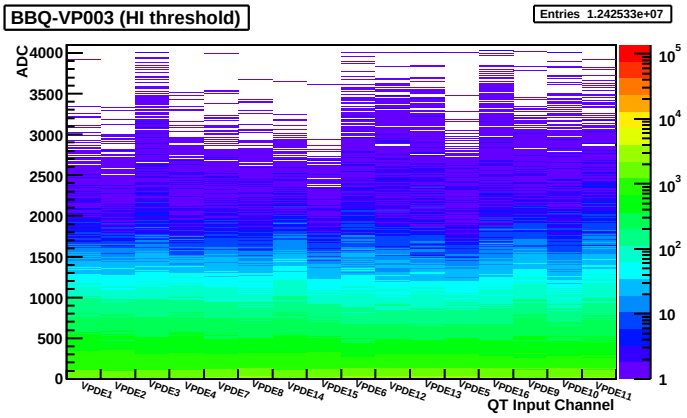

BBQ-ZD001 (ZDC TOWER)
Entries 1.242533e+07
TAC

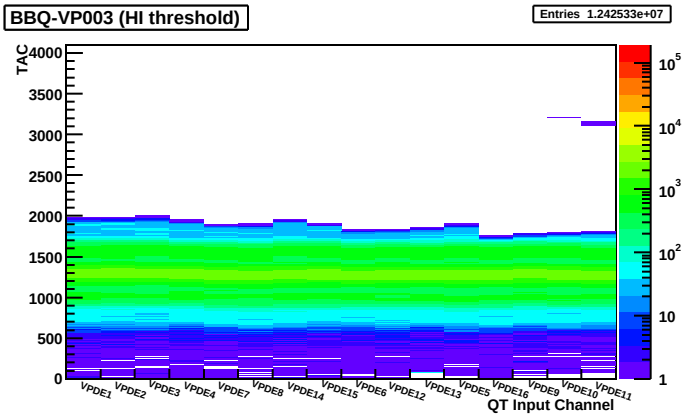

BBQ-VP001 (LO threshold)
Entries 1.242533e+07
ADC
QT Input Channel

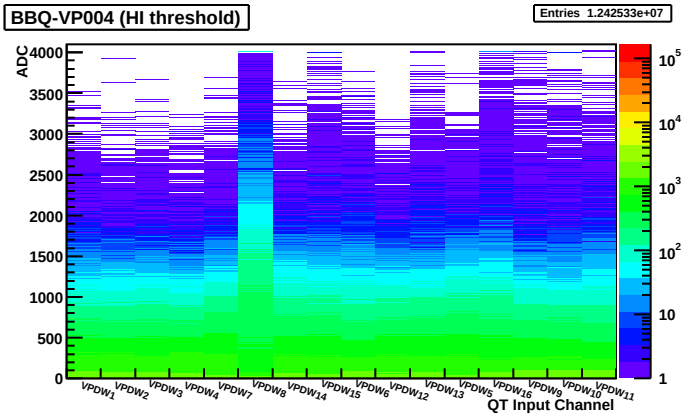

BBQ-VP001 (LO threshold)
Entries 1.242533e+07
TAC
QT Input Channel

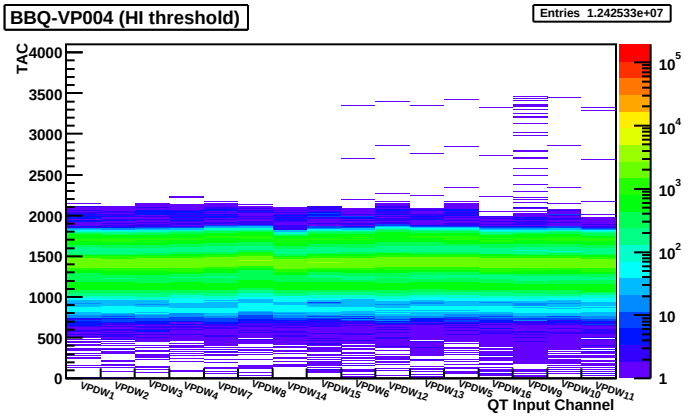

BBQ-VP002 (LO threshold)
Entries 1.242533e+07
ADC
QT Input Channel

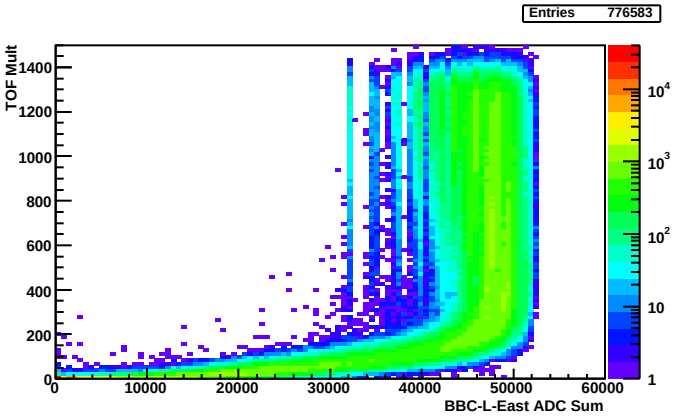

BBQ-VP002 (LO threshold)
Entries 1.242533e+07
TAC
QT Input Channel

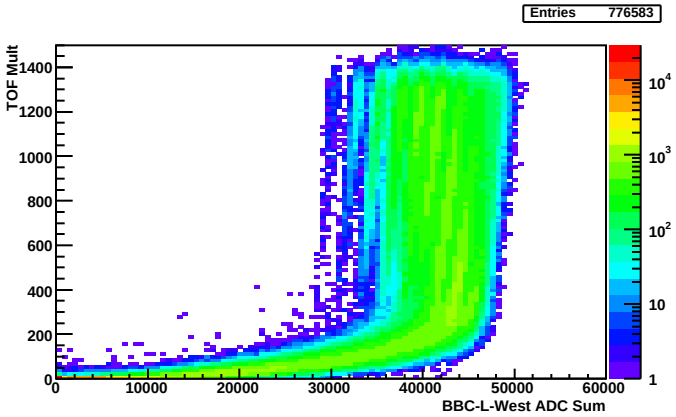

BBQ-VP003 (HI threshold)
Entries 1.242533e+07
ADC
QT Input Channel

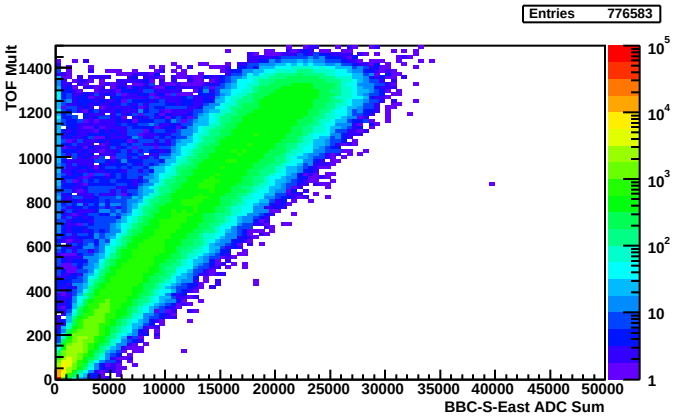

BBQ-VP003 (HI threshold)
Entries 1.242533e+07
TAC
QT Input Channel

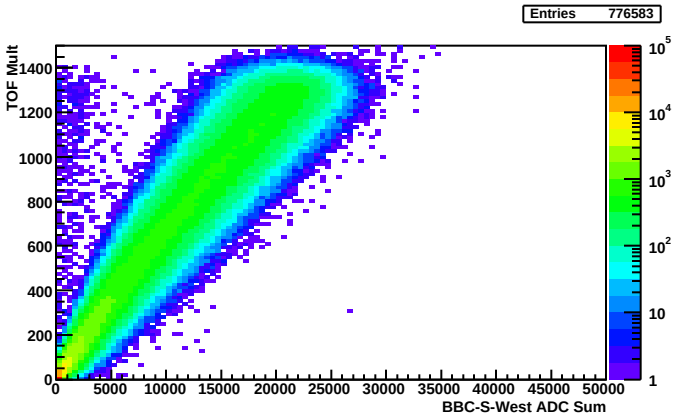

BBQ-VP004 (HI threshold)
Entries 1.242533e+07
ADC
QT Input Channel

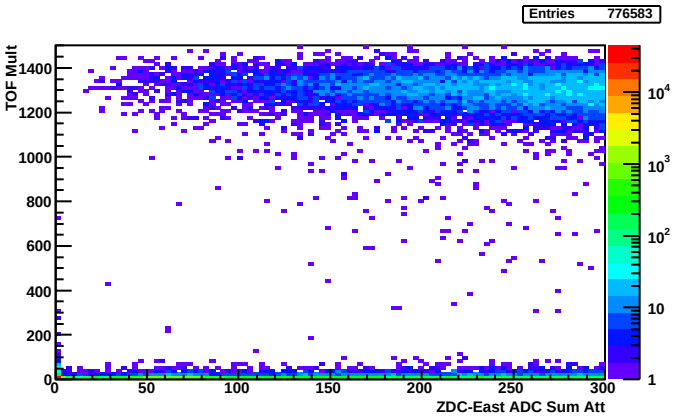

BBQ-VP004 (HI threshold)
Entries 1.242533e+07
TAC
QT Input Channel

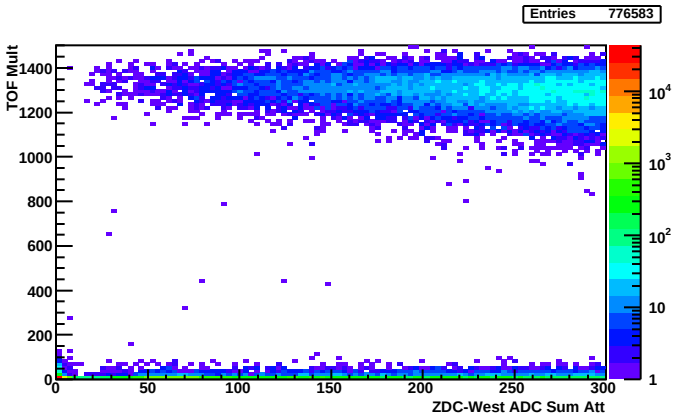

Entries 776583
TOF Mult
BBC-L-East ADC Sum

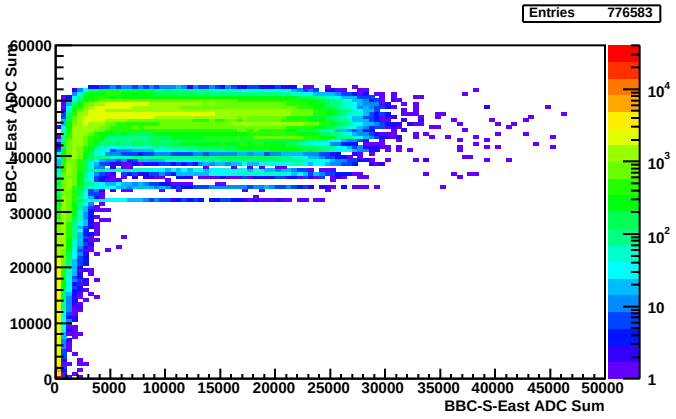

Entries 776583
TOF Mult
BBC-L-West ADC Sum

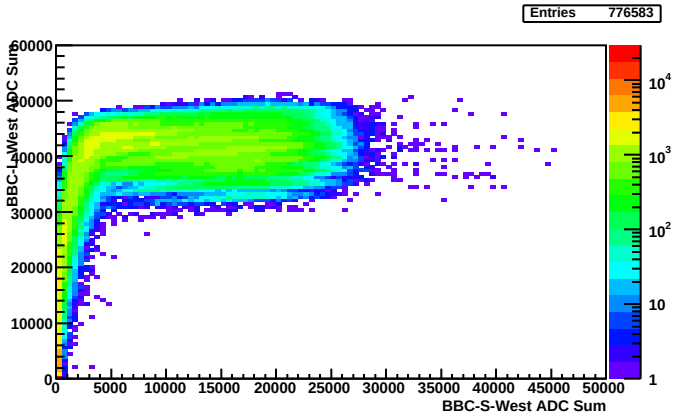

Entries 776583
TOF Mult
BBC-S-East ADC Sum

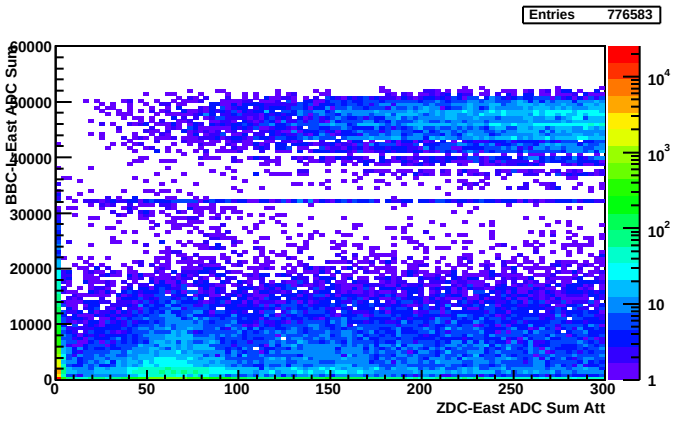

Entries 776583
TOF Mult
BBC-S-West ADC Sum

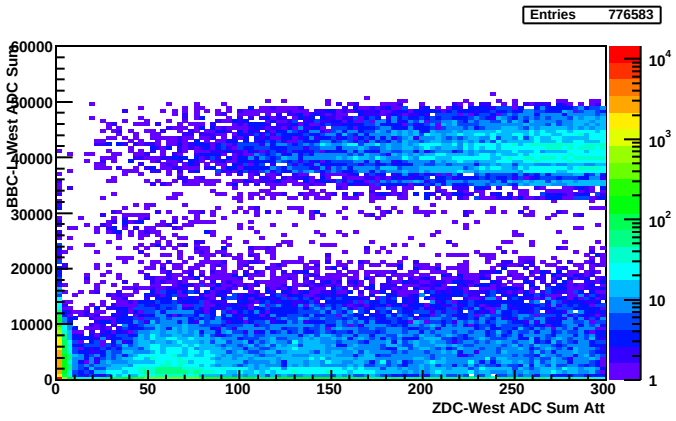

Entries 776583
TOF Mult
ZDC-East ADC Sum Att


Entries 776583
TOF Mult
ZDC-West ADC Sum Att


Entries 776583
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 776583
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 776583
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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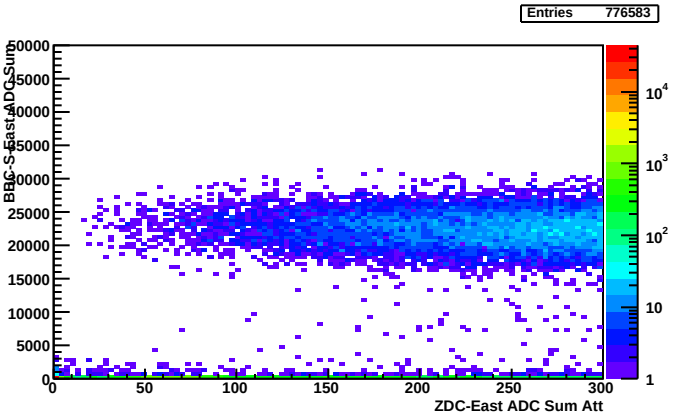
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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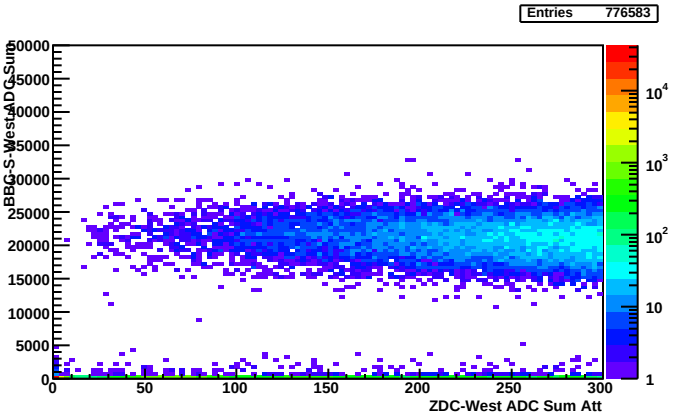
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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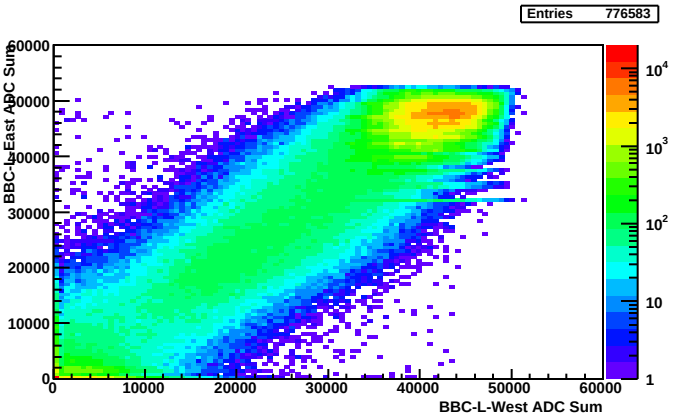
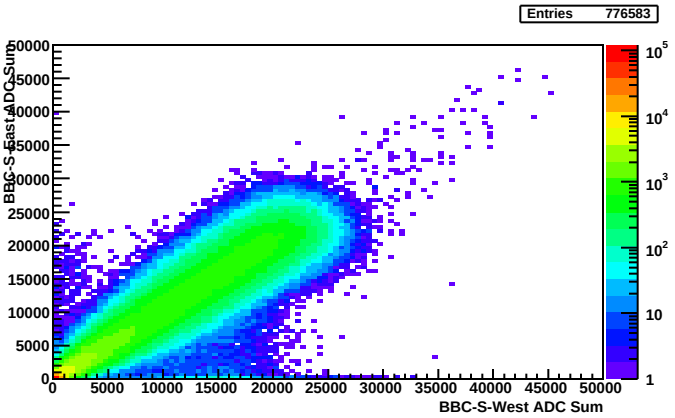
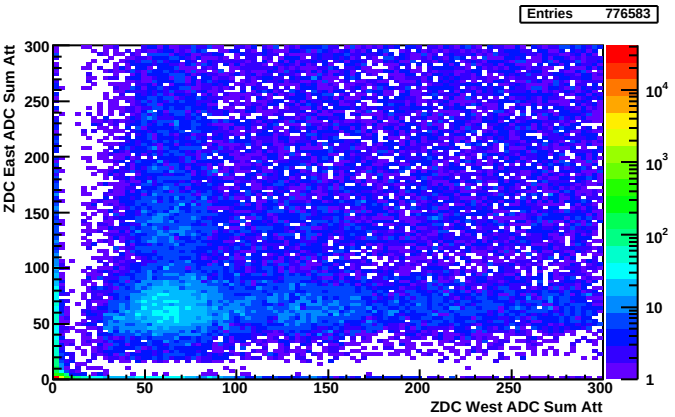
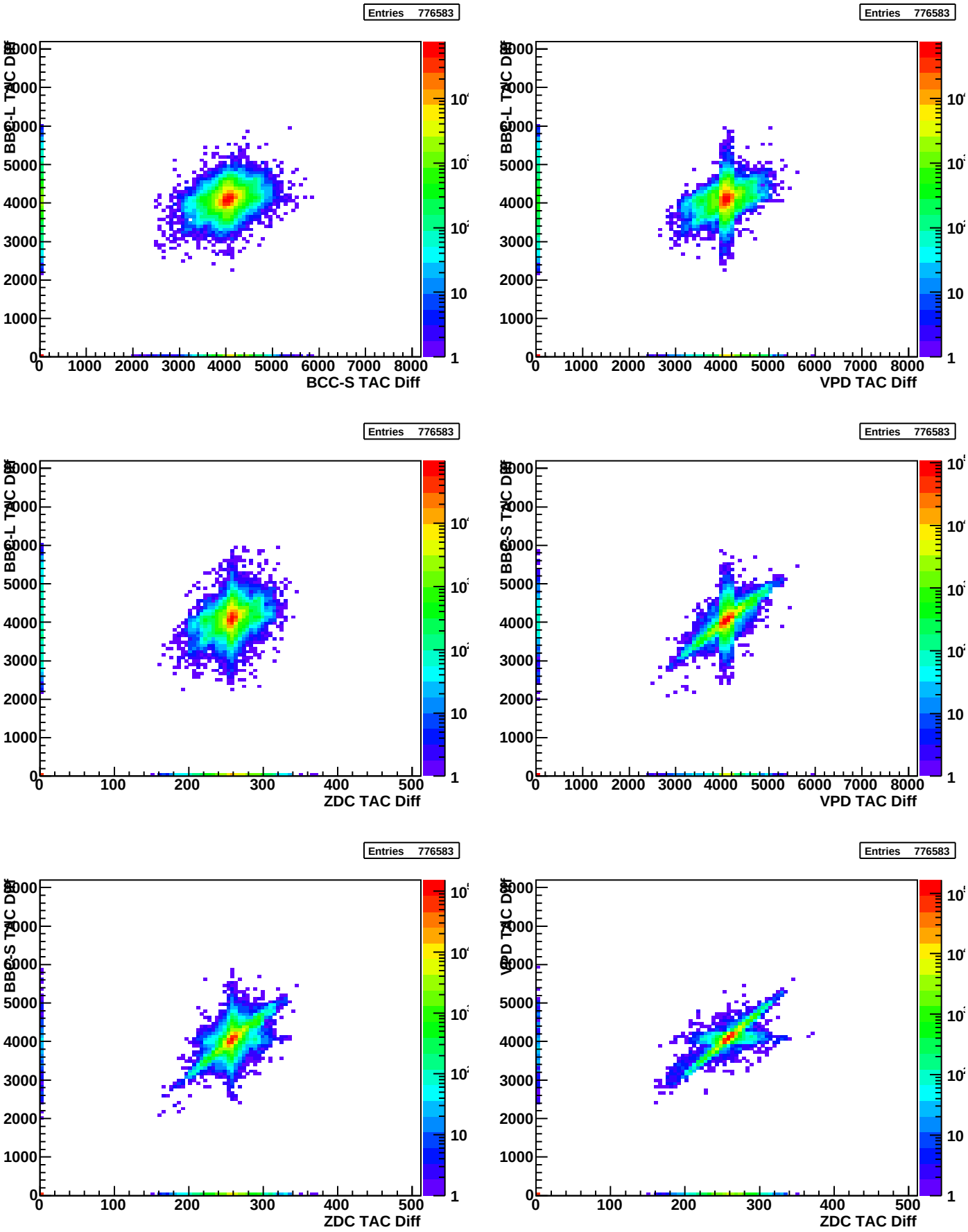
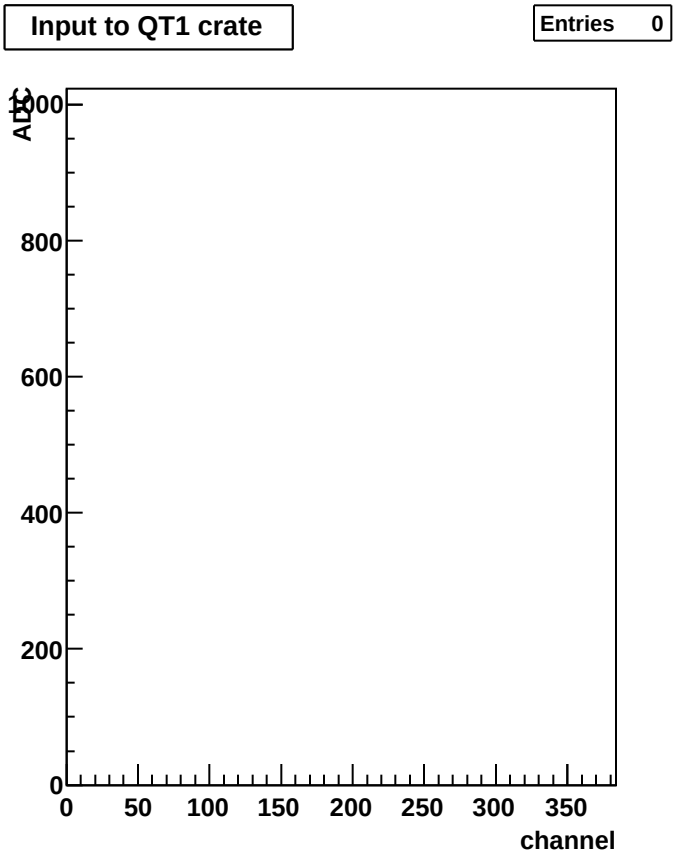
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

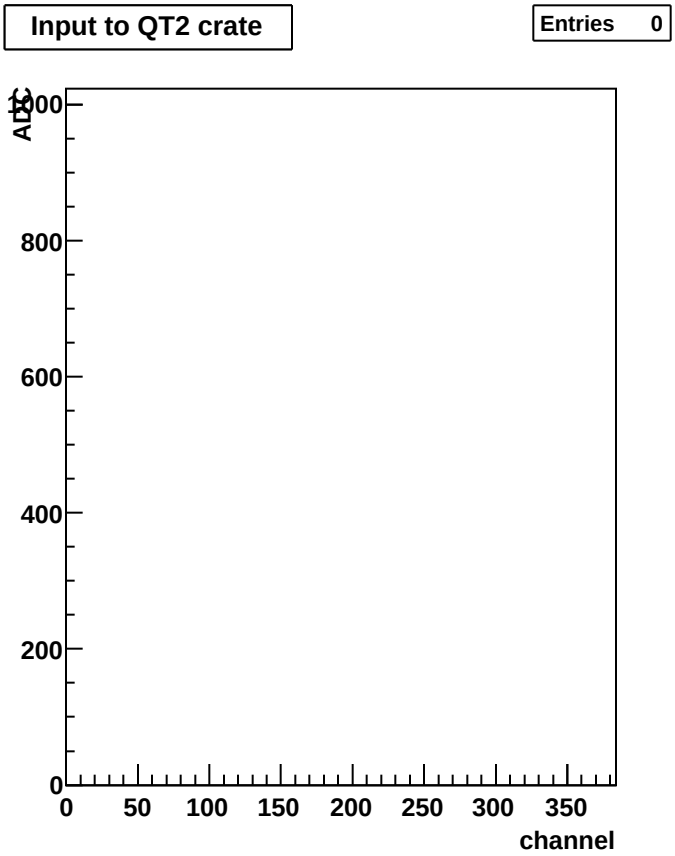

Entries 776583
BBC-L-East ADC Sum
BBC-L-West ADC Sum

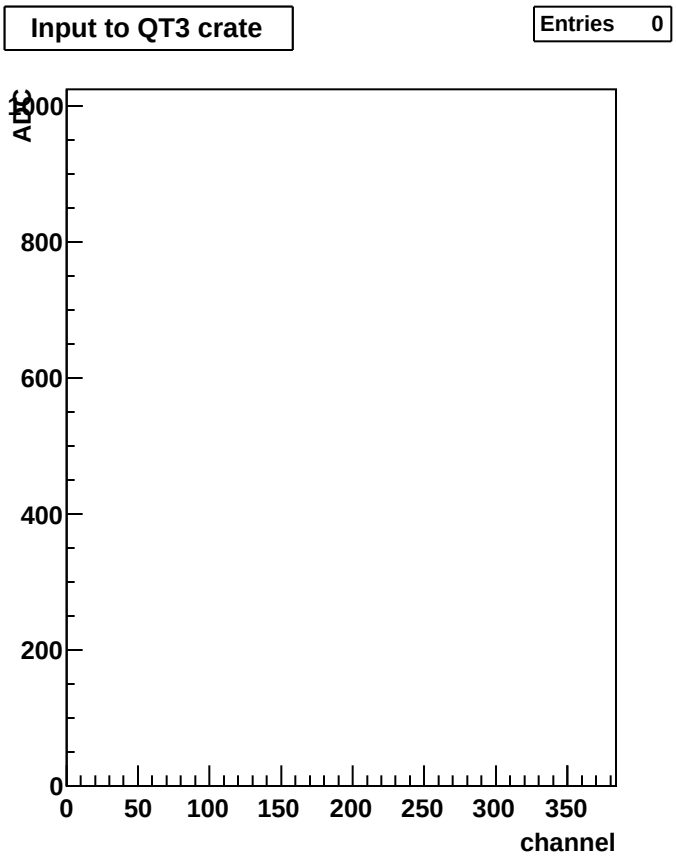

Entries 776583
BBC-S-East ADC Sum
BBC-S-West ADC Sum

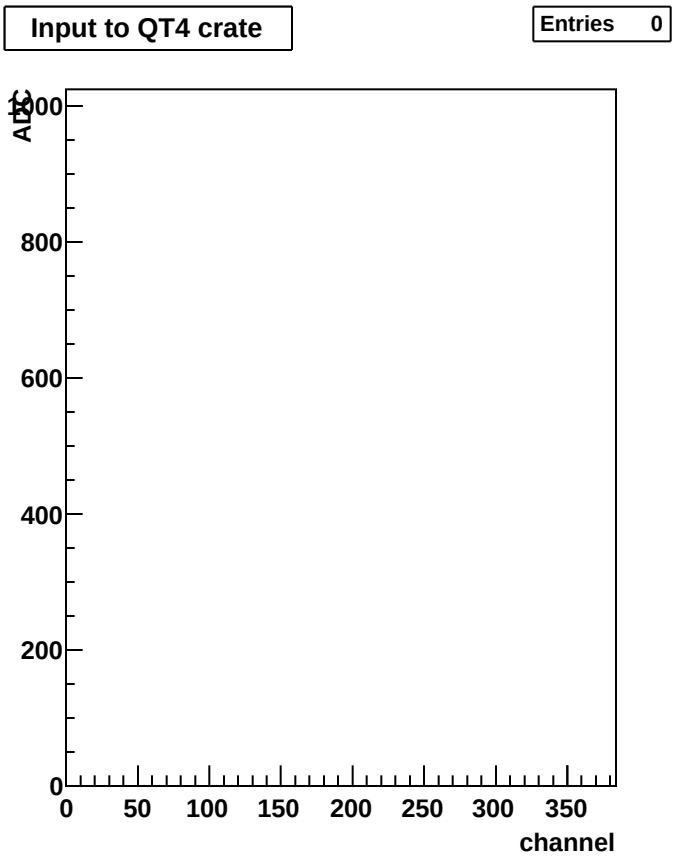

Entries 776583
ZDC East ADC Sum Att
ZDC West ADC Sum Att

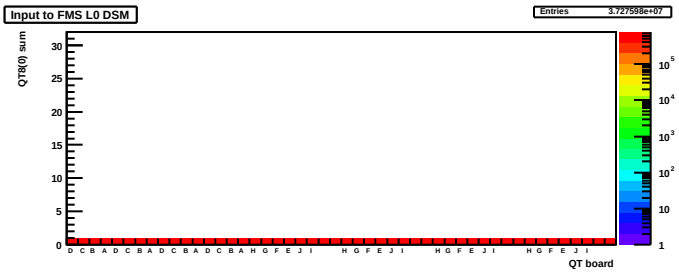

Entries 776583
BBC-L TAC Diff
BCC-S TAC Diff
Entries 776583
BBC-L TAC Diff
VPD TAC Diff
Entries 776583
BBC-L TAC Diff
ZDC TAC Diff
Entries 776583
BBC-S TAC Diff
VPD TAC Diff
Entries 776583
BBC-S TAC Diff
ZDC TAC Diff
Entries 776583
VPD TAC Diff
ZDC TAC Diff

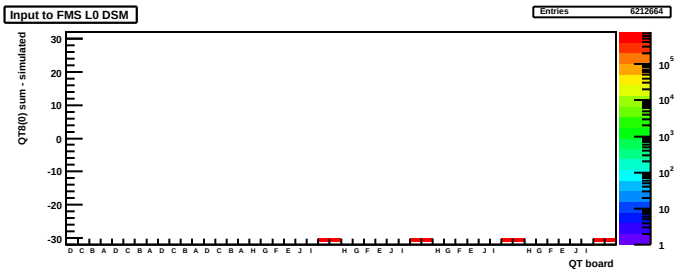

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

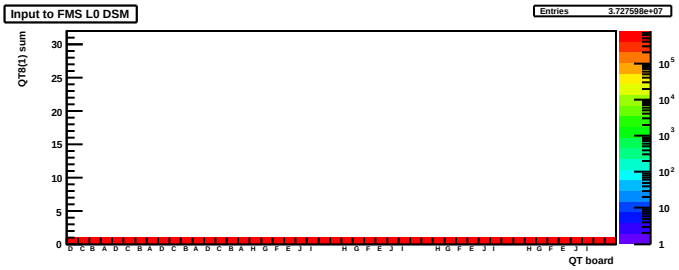

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

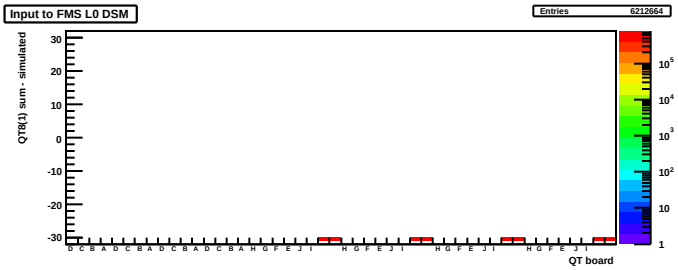

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

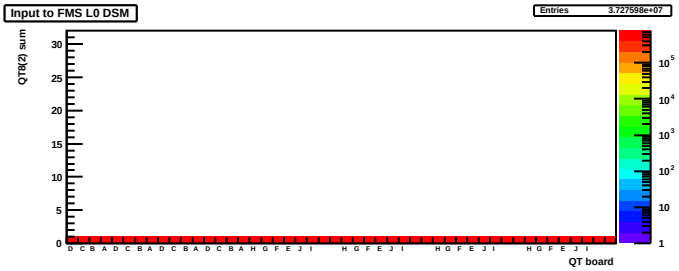

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

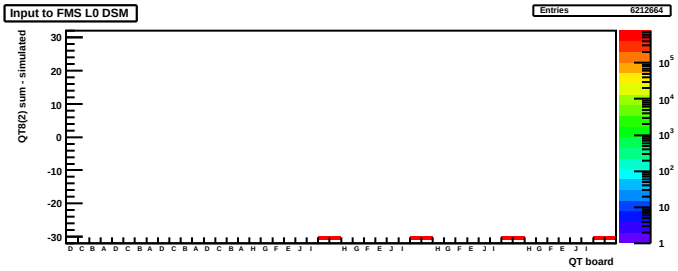

Input to FMS L0 DSM
Entries 3.727598e+07
QT8(0) sum
QT board

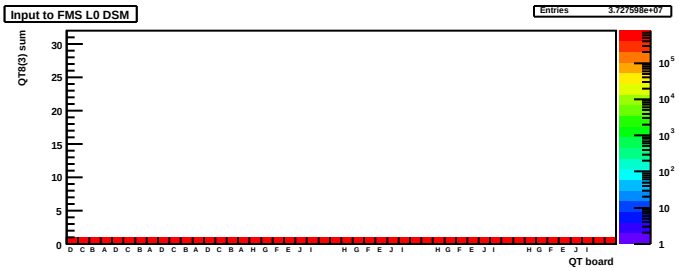

Input to FMS L0 DSM
Entries 6212664
QT8(0) sum - simulated
QT board

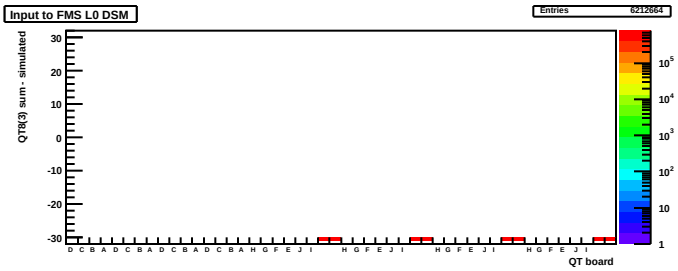

Input to FMS L0 DSM
Entries 3.727598e+07
QT8(1) sum
QT board

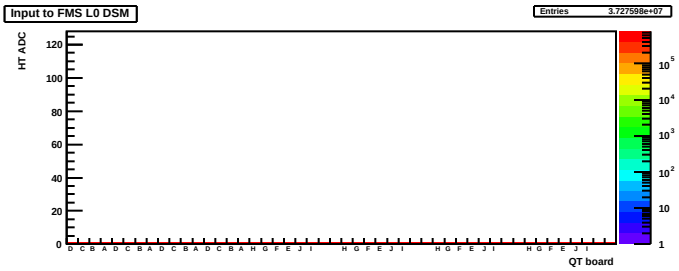

Input to FMS L0 DSM
Entries 6212664
QT8(1) sum - simulated
QT board

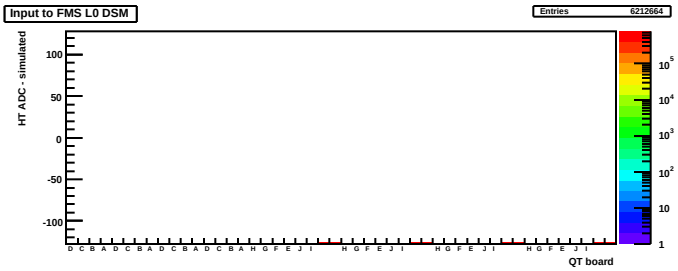

Input to FMS L0 DSM
Entries 3.727598e+07
QT8(2) sum
QT board

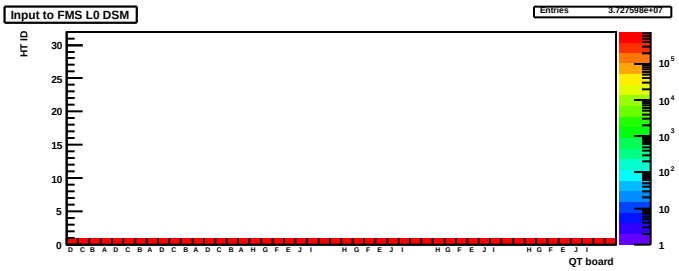

Input to FMS L0 DSM
Entries 6212664
QT8(2) sum - simulated
QT board

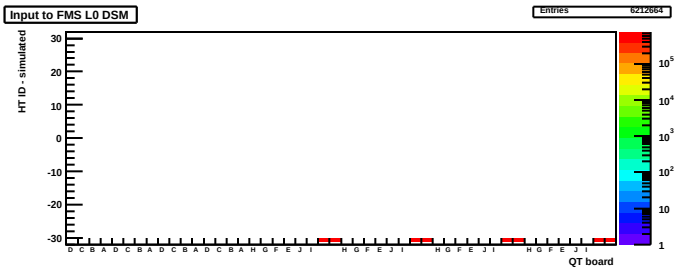

Input to FMS L0 DSM
Entries 3.727598e+07
QT8(3) sum
QT board

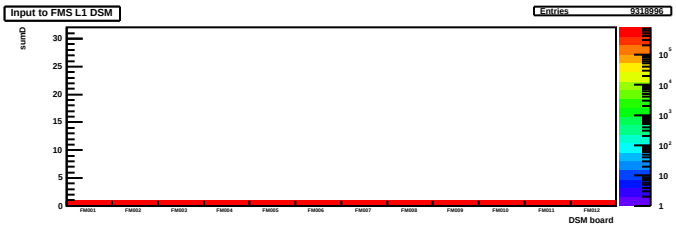

Input to FMS L0 DSM
Entries 6212664
QT8(3) sum - simulated
QT board

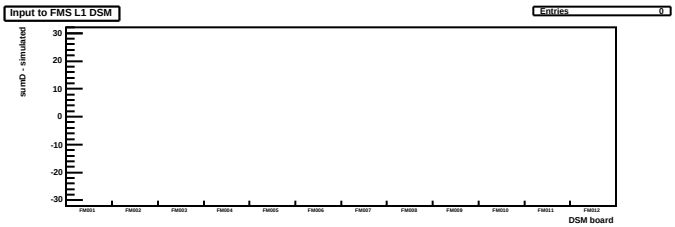

Input to FMS L0 DSM
Entries 3.727598e+07
HT ADC
QT board

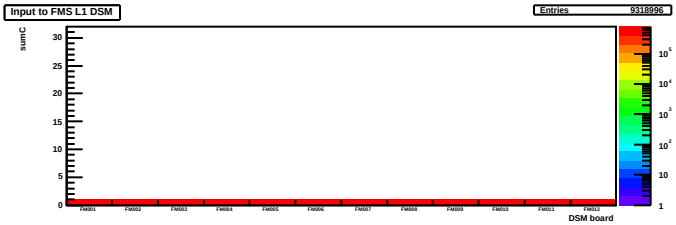

Input to FMS L0 DSM
Entries 6212664
HT ADC - simulated
QT board

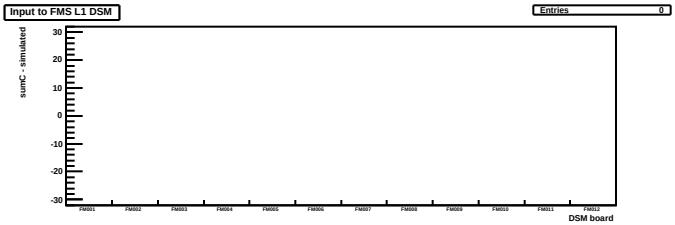

Input to FMS L0 DSM
Entries 3.727598e+07
HT ID
QT board

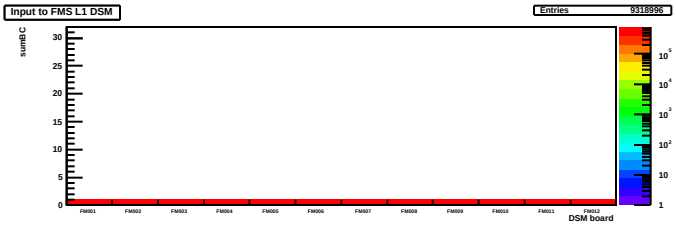

Input to FMS L0 DSM
Entries 6212664
HT ID - simulated
QT board

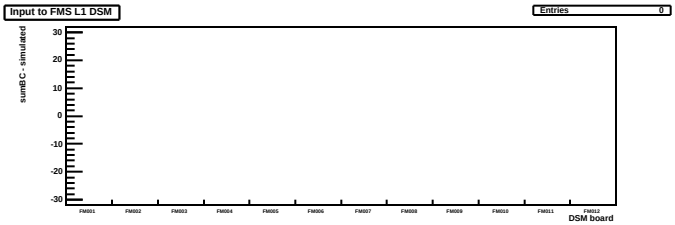

Input to FMS L1 DSM
Entries 9318996
sumD
DSM board

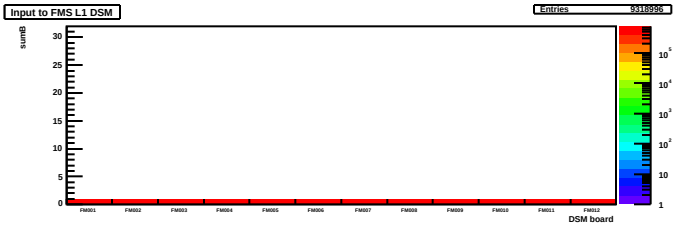

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

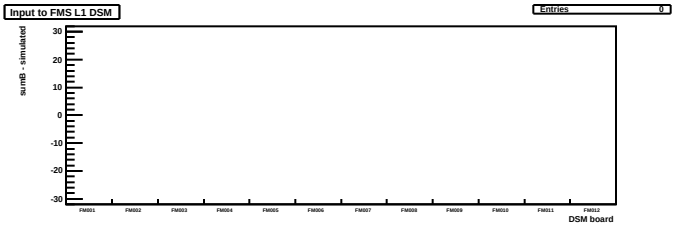

Input to FMS L1 DSM
Entries 9318996
sumC
DSM board

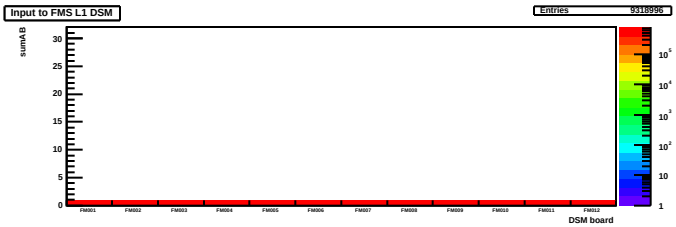

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

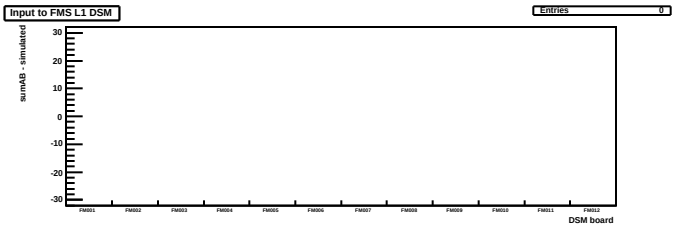

Input to FMS L1 DSM
Entries 9318996
sumBC
DSM board

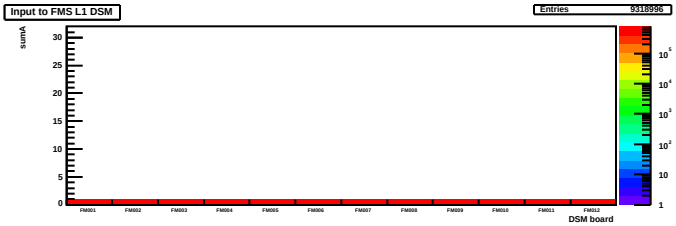

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

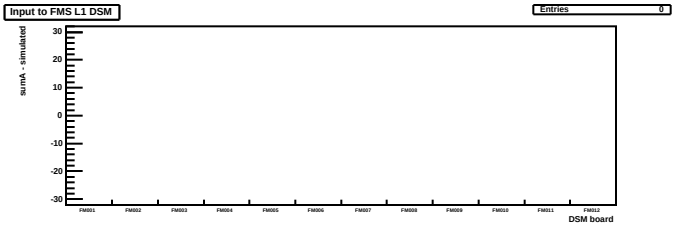

Input to FMS L1 DSM
Entries 9318996
sumB
DSM board

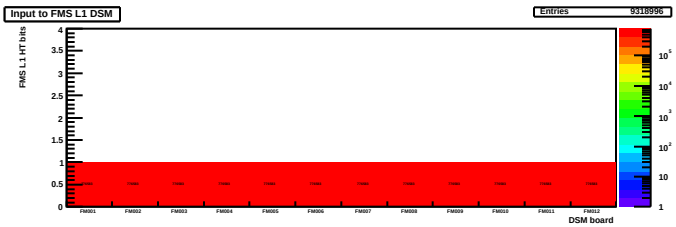

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

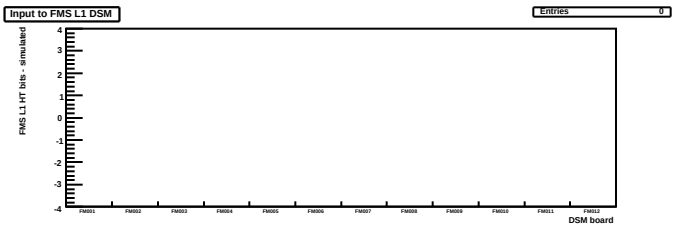

Input to FMS L1 DSM
Entries 9318996
sumAB
DSM board

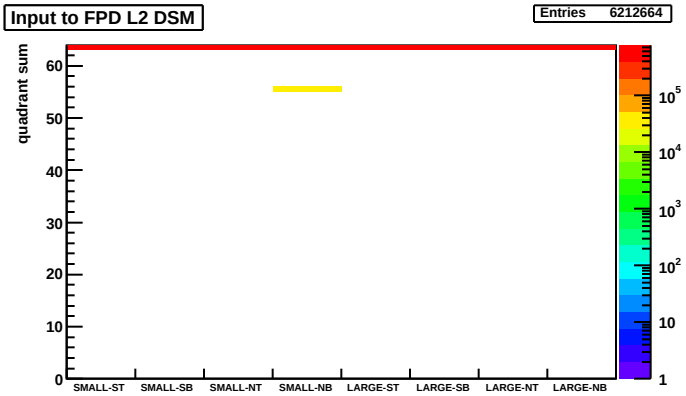

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

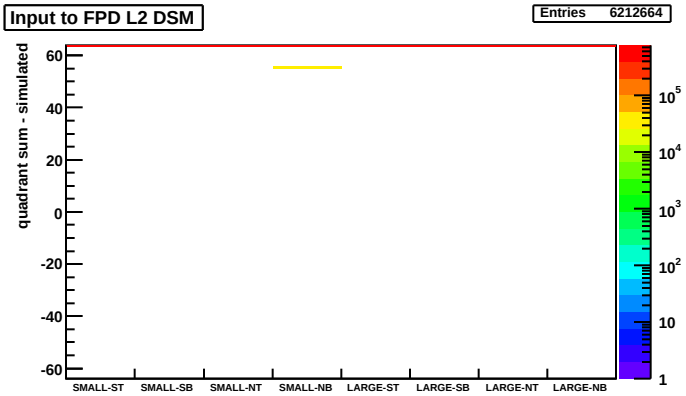

Input to FMS L1 DSM
Entries 9318996
sumA
DSM board

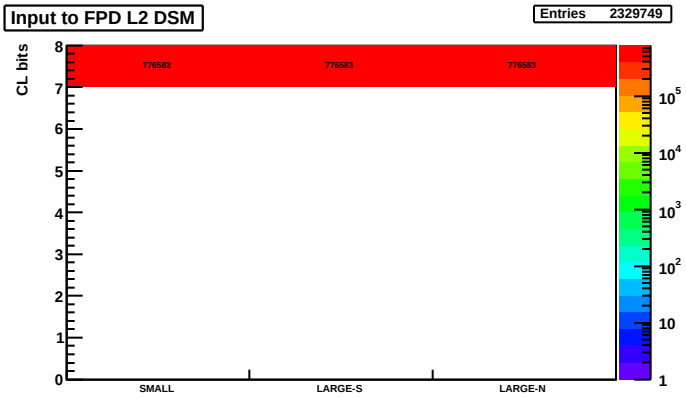

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

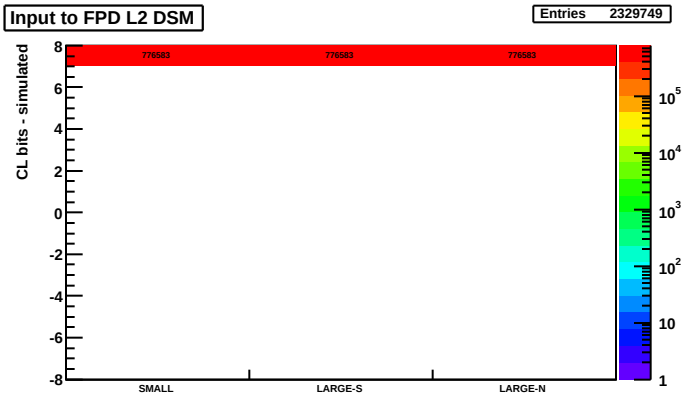

Input to FMS L1 DSM
Entries 9318996
FMS L1 HT bits
DSM board

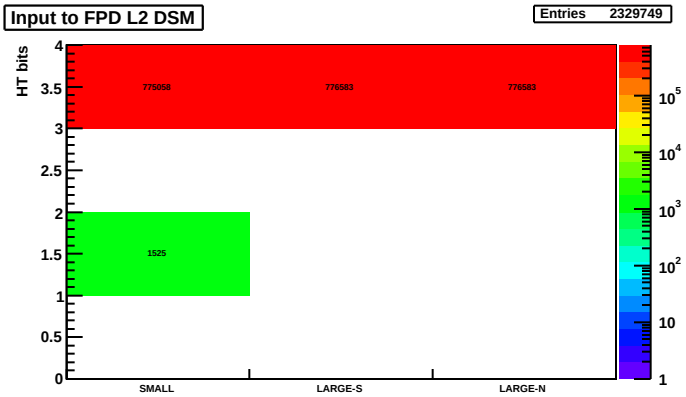

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

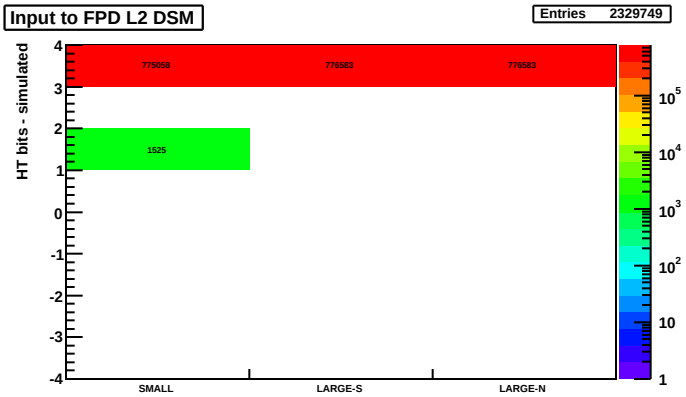

Input to FPD L2 DSM
Entries 6212664
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

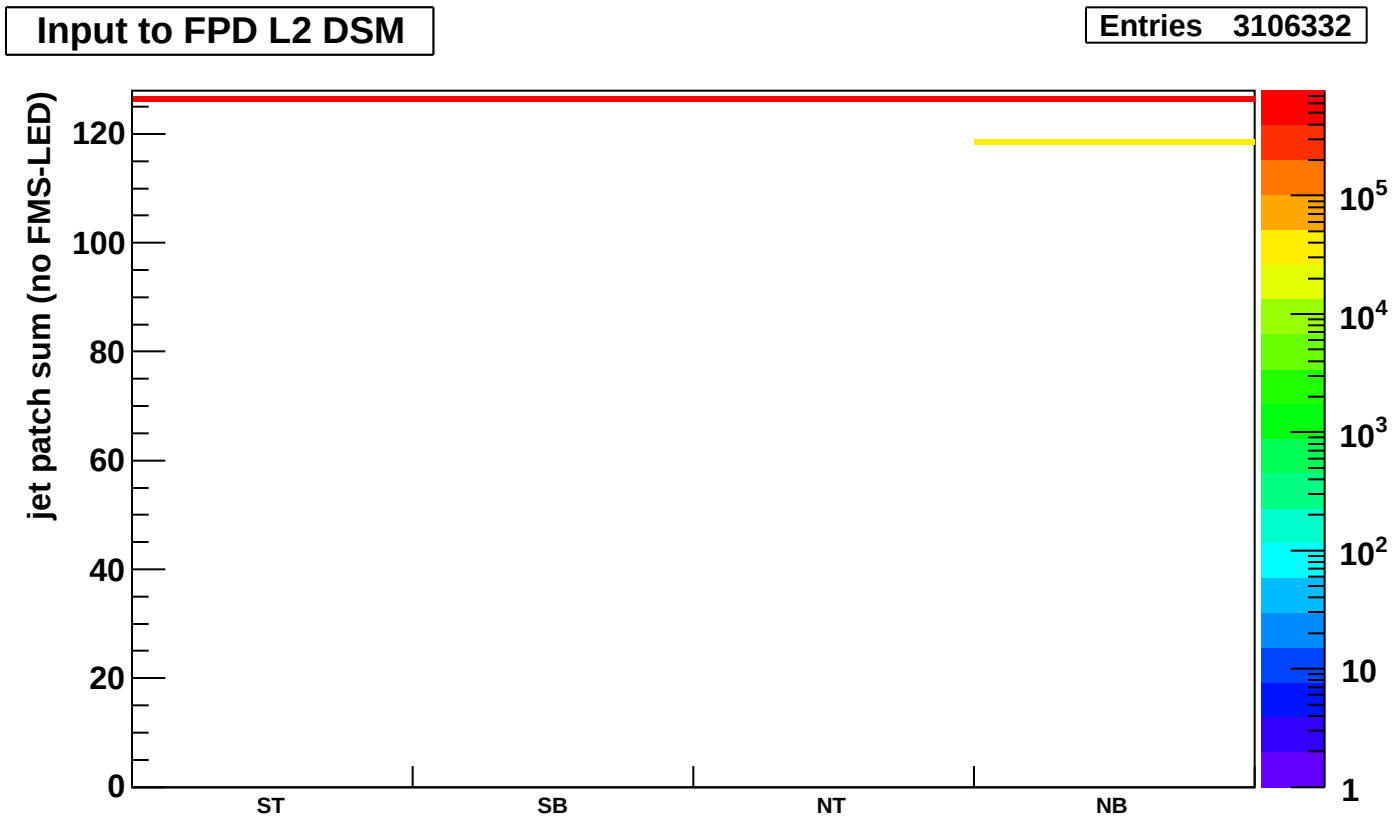

Input to FPD L2 DSM
Entries 6212664
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

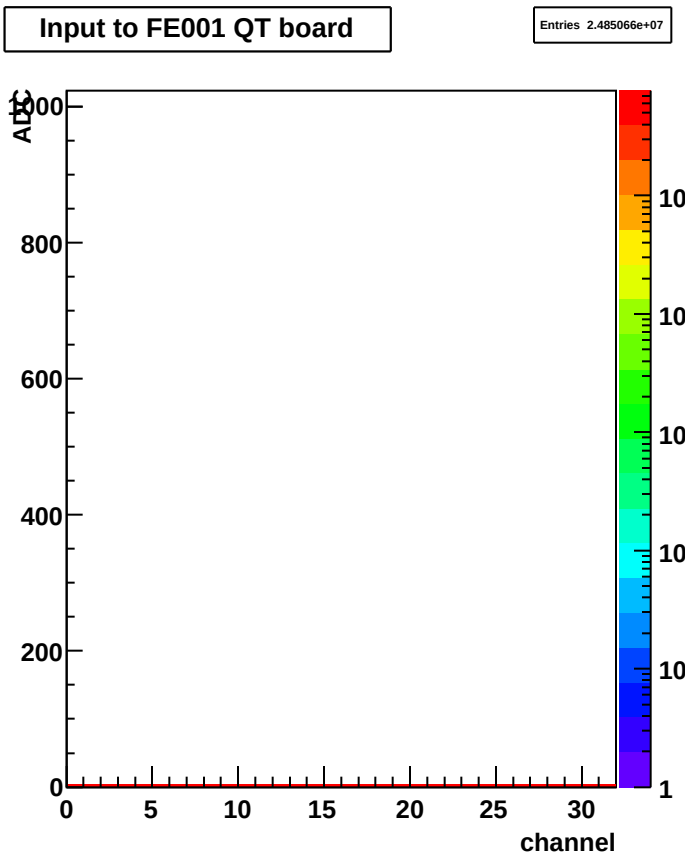

Input to FPD L2 DSM
Entries 2329749
CL bits
776583
776583
776583
SMALL
LARGE-S
LARGE-N

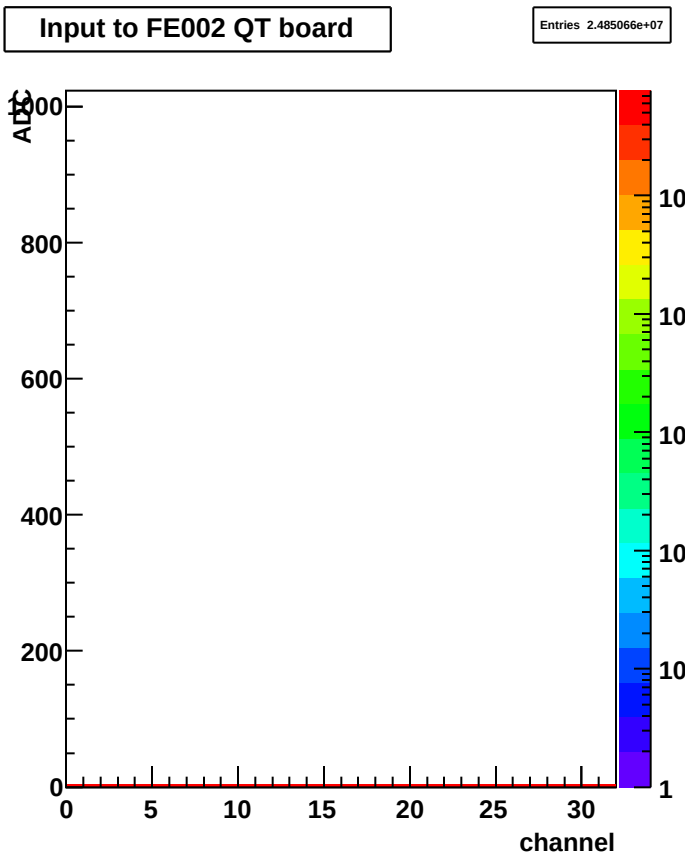

Input to FPD L2 DSM
Entries 2329749
CL bits - simulated
776583
776583
776583
SMALL
LARGE-S
LARGE-N

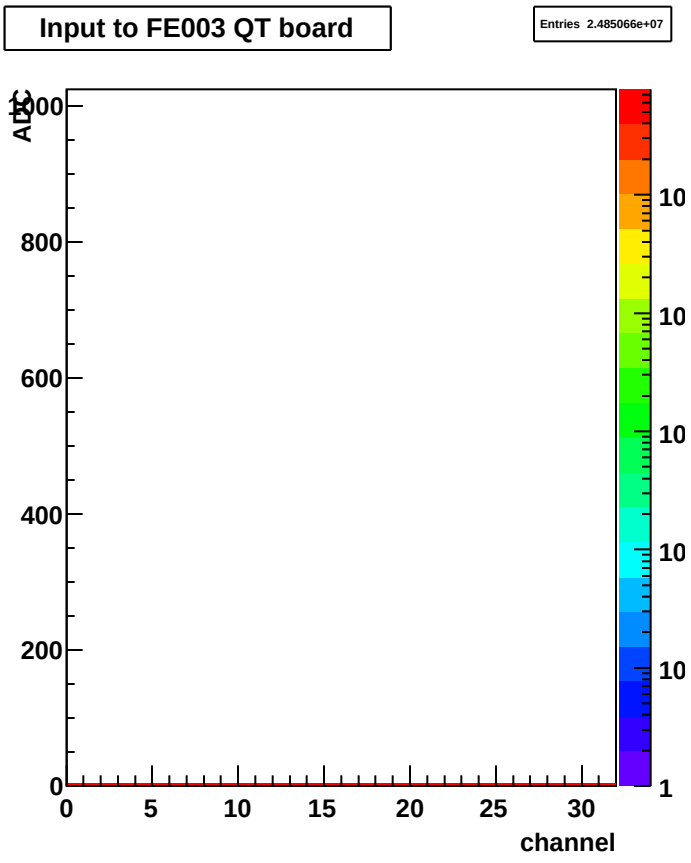

Input to FPD L2 DSM
Entries 2329749
HT bits
775058
776583
776583
1525
SMALL
LARGE-S
LARGE-N

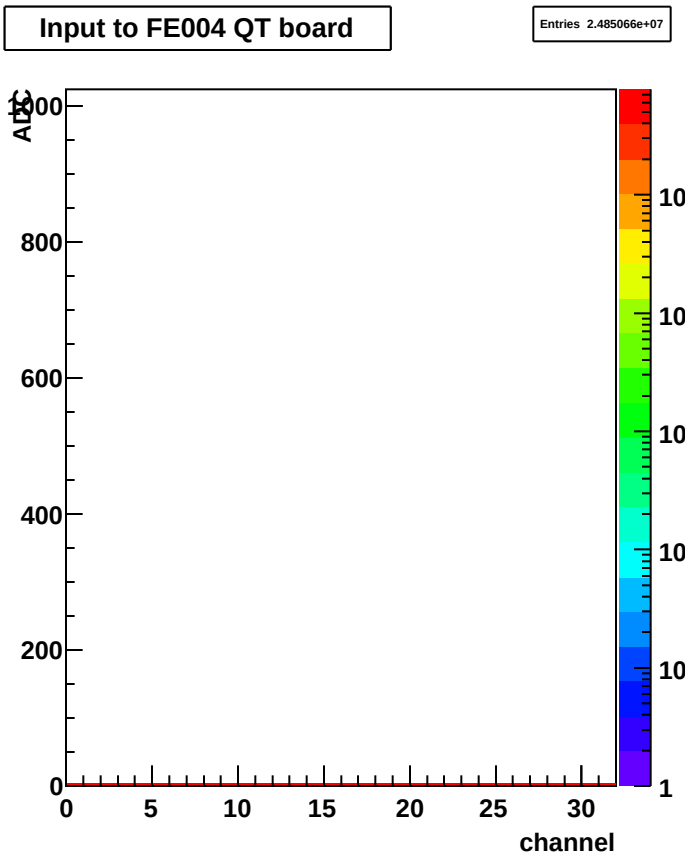

Input to FPD L2 DSM
Entries 2329749
HT bits - simulated
775058
776583
776583
1525
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3106332
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

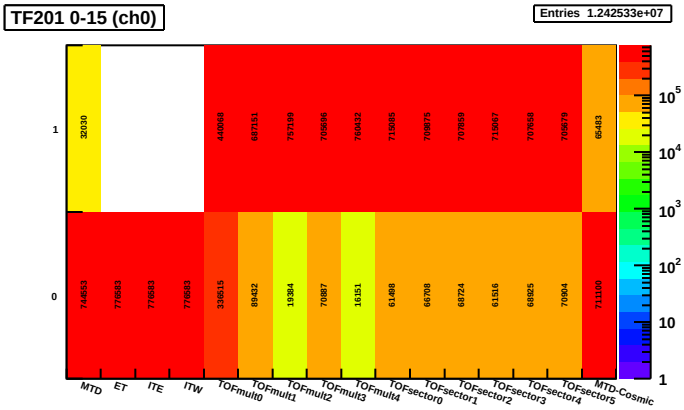

Input to FE001 QT board
Entries 2.485066e+07
ADC
channel


Input to FE002 QT board
Entries 2.485066e+07
ADC
channel

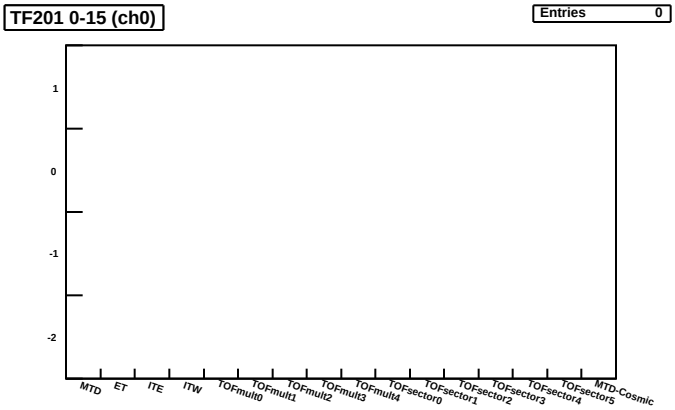

Input to FE003 QT board
Entries 2.485066e+07
ADC
channel


Input to FE004 QT board
Entries 2.485066e+07
ADC
channel

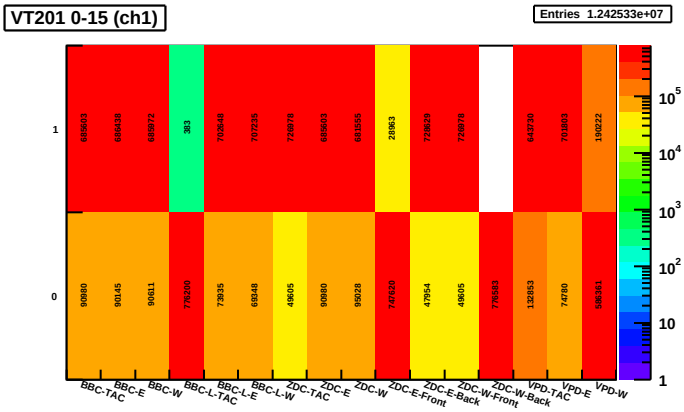
TF201 0-15 (ch0)



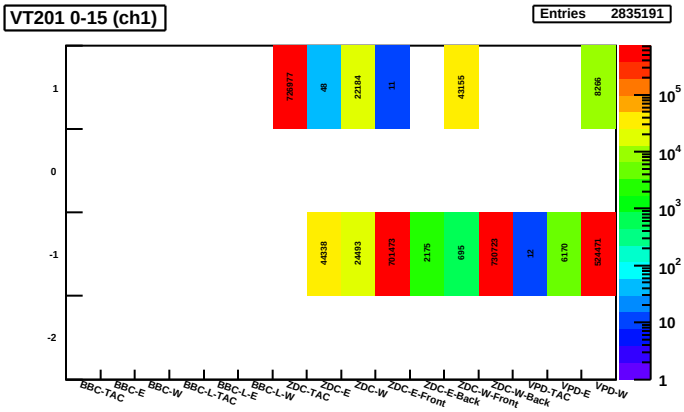
TF201 0-15 (ch0)



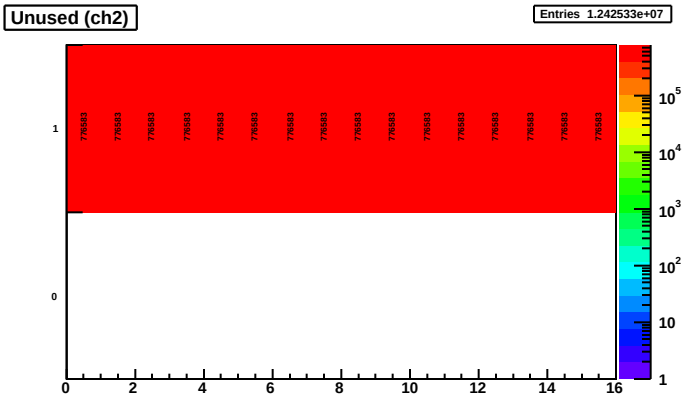
VT201 0-15 (ch1)

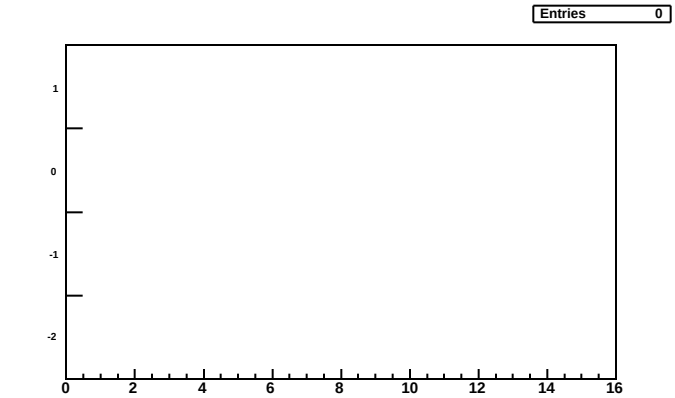


VT201 0-15 (ch1)

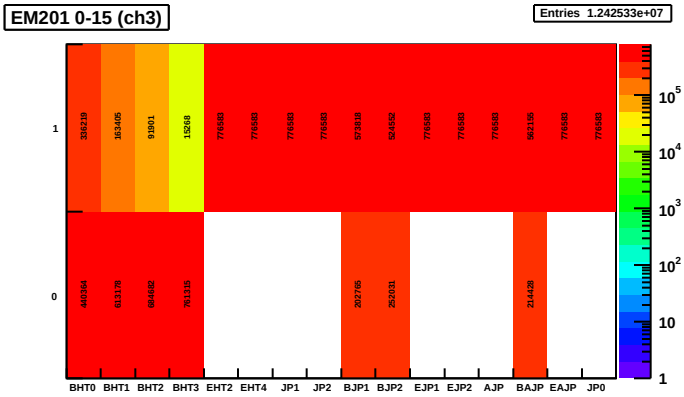


Unused (ch2)

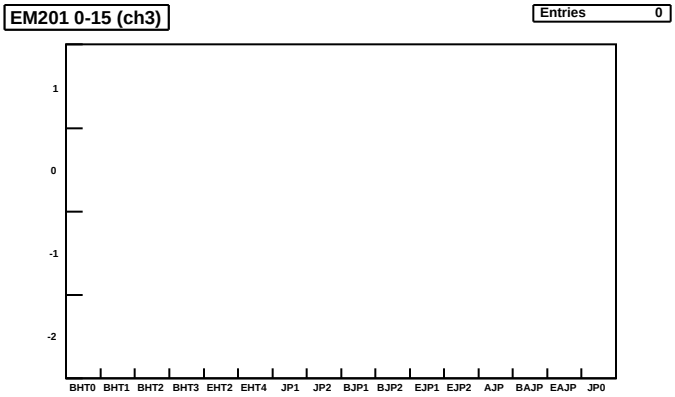




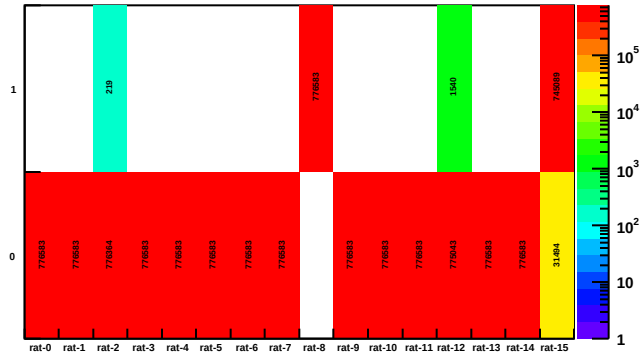
EM201 0-15 (ch3)

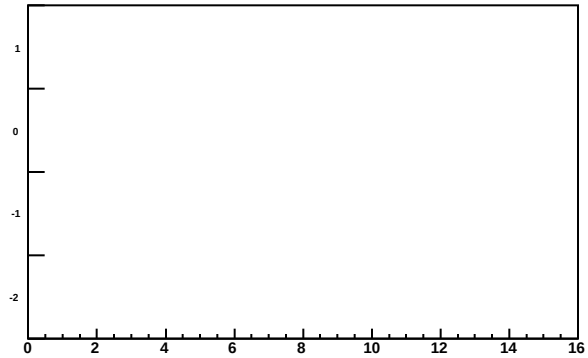


EM201 0-15 (ch3)

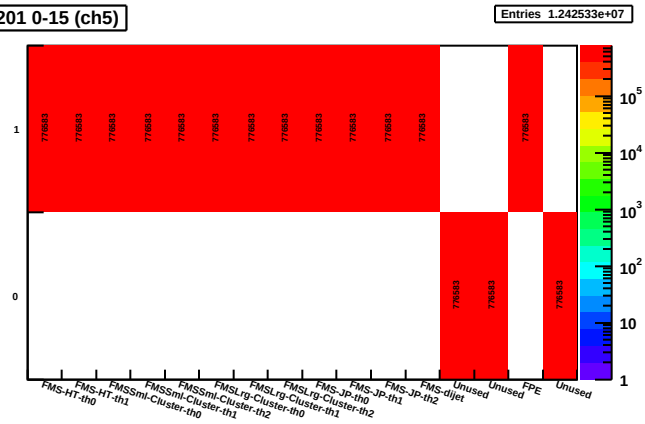


RAT board (ch4)

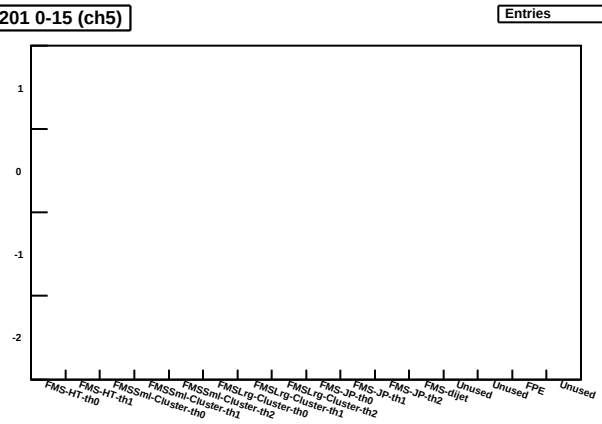




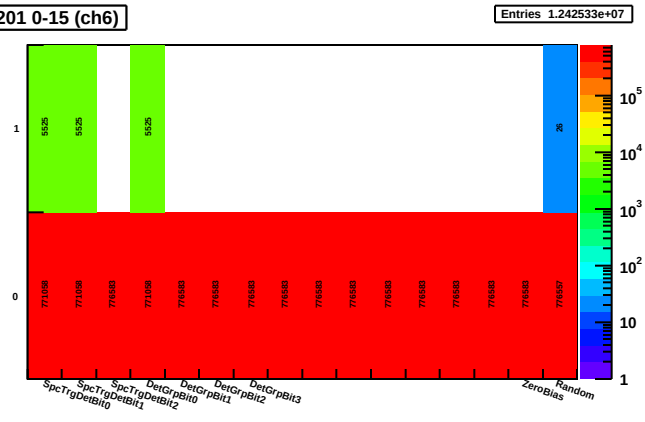
FP201 0-15 (ch5)

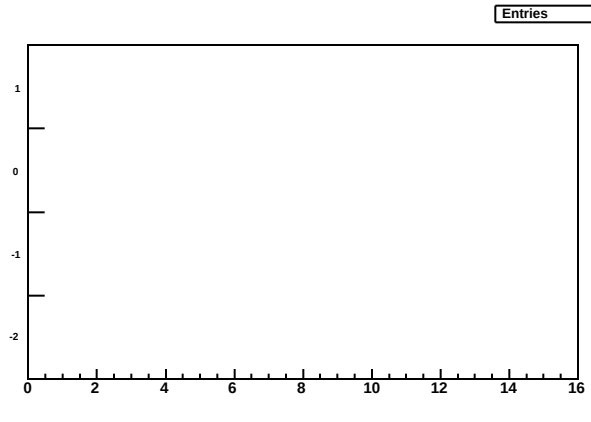
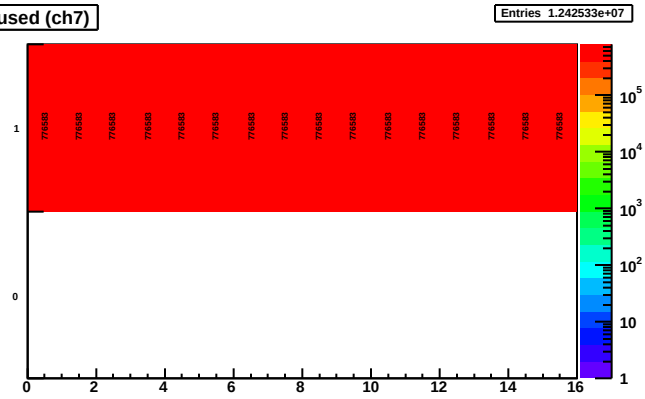
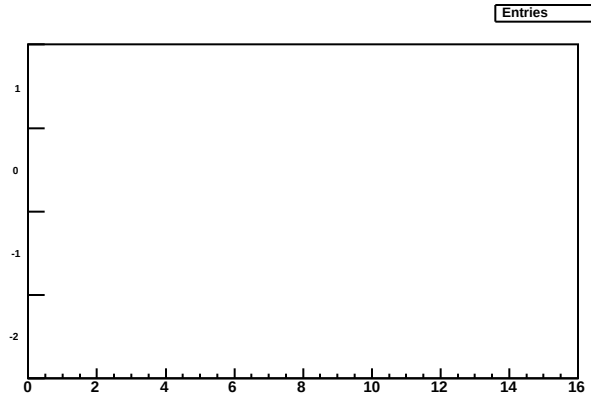


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)