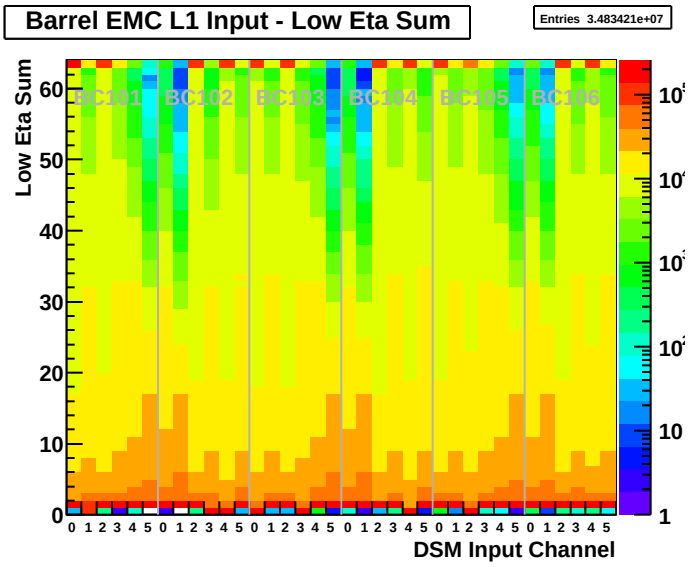
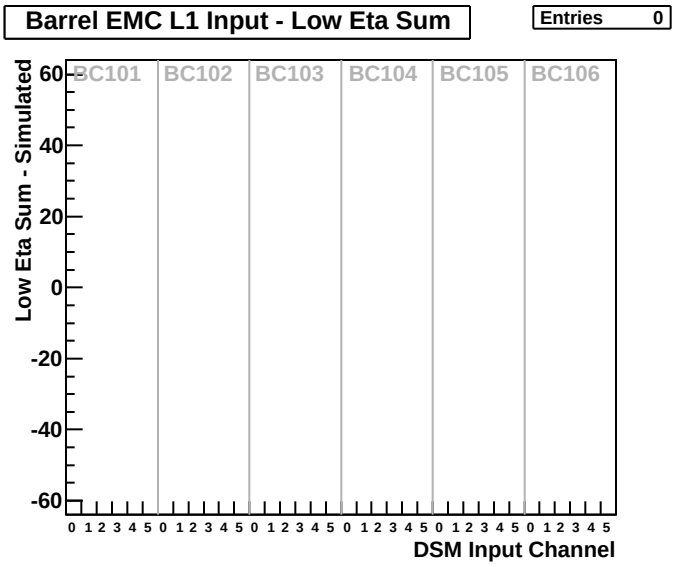
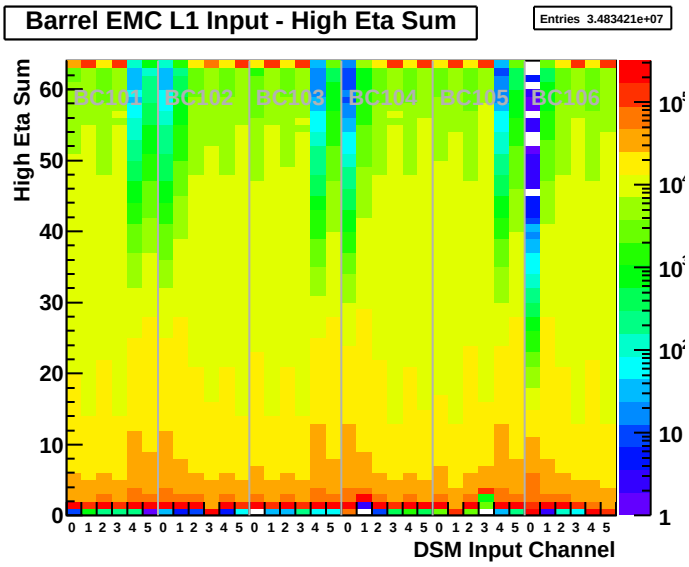
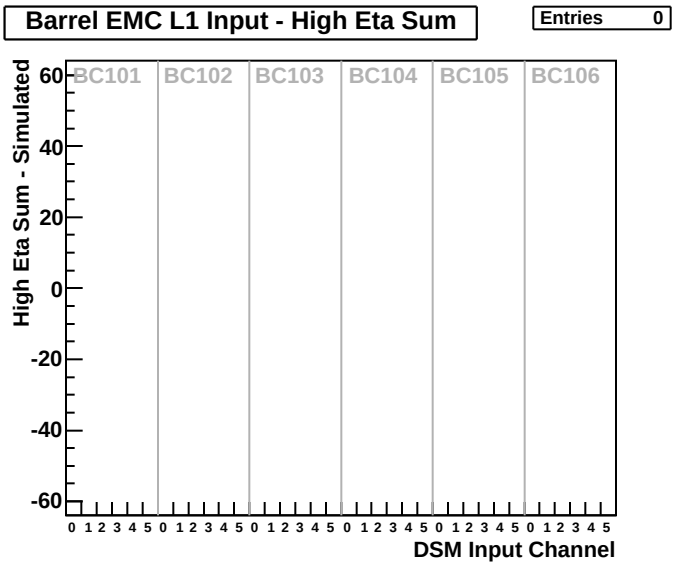
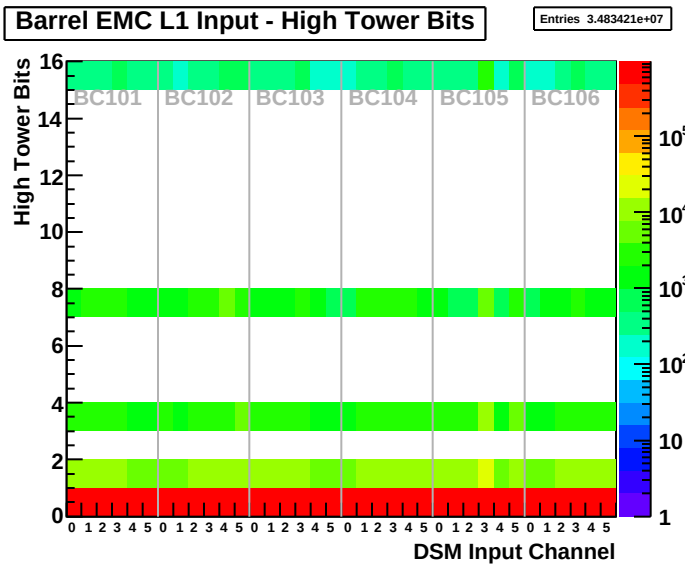
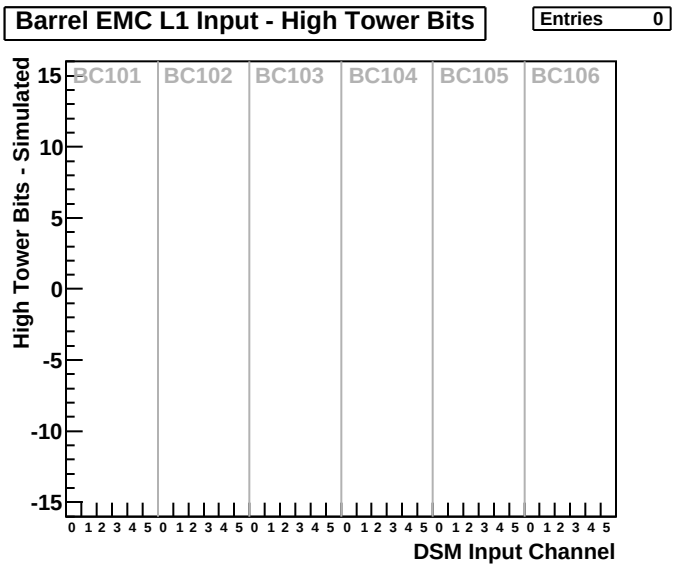

Barrel EMC L1 Input - Low Eta Sum
Entries 3.483421e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

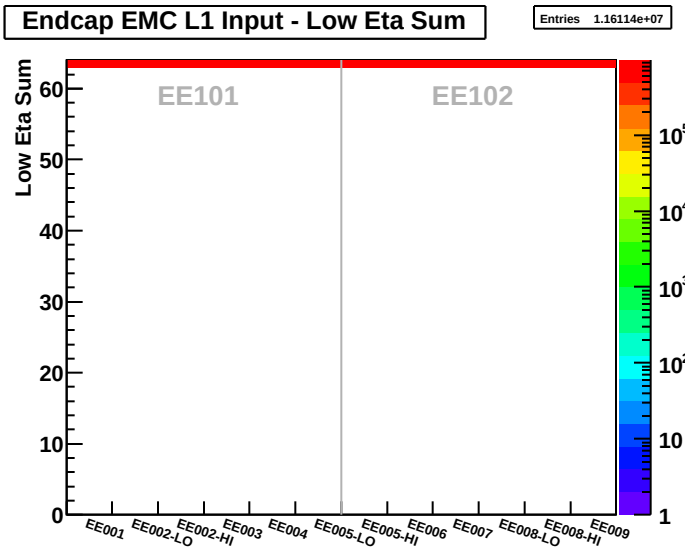

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

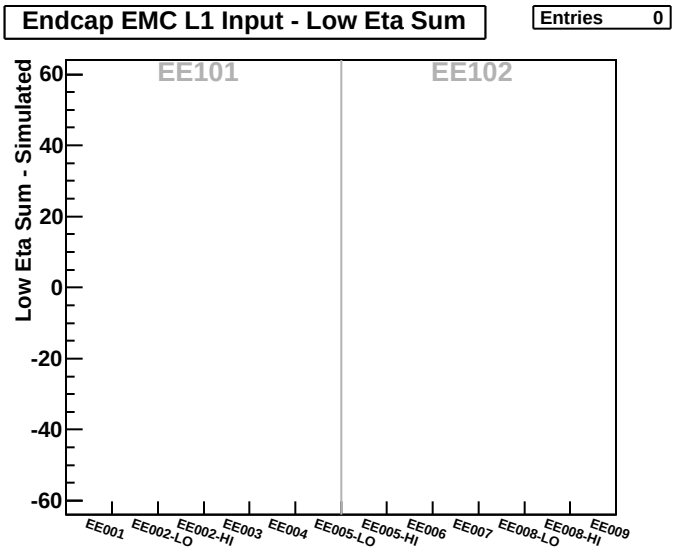

Barrel EMC L1 Input - High Eta Sum
Entries 3.483421e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

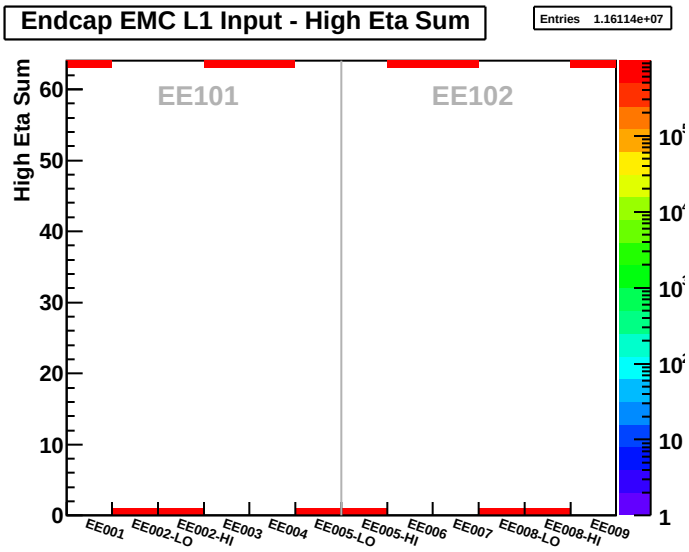

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

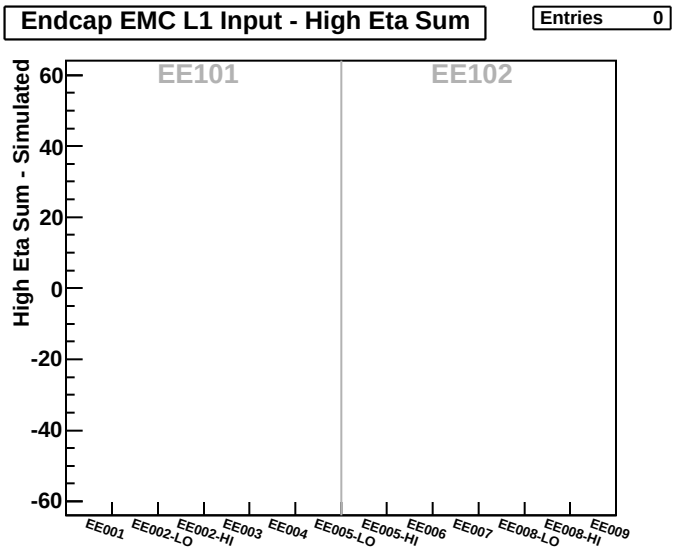

Barrel EMC L1 Input - High Tower Bits
Entries 3.483421e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

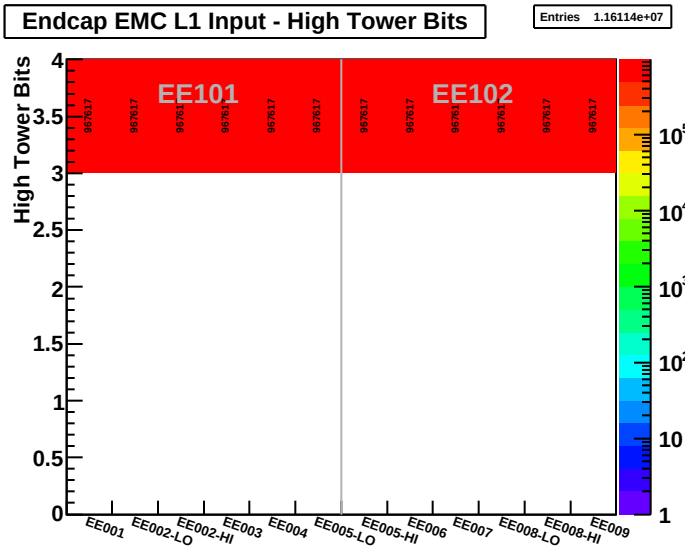

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

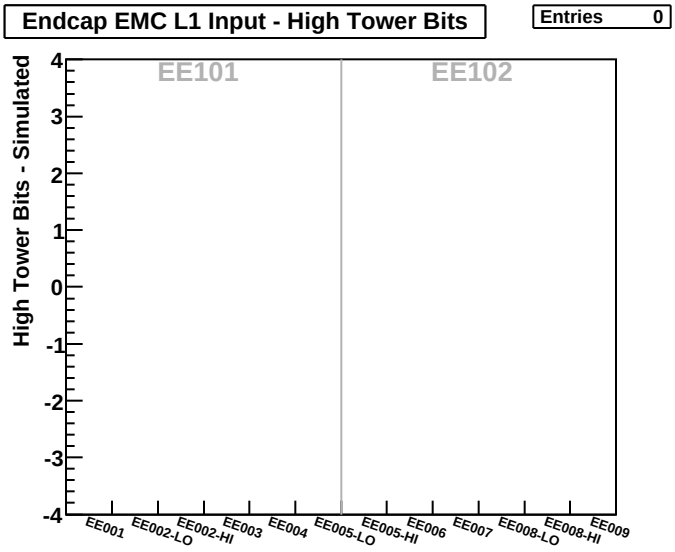

Endcap EMC L1 Input - Low Eta Sum
Entries 1.16114e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

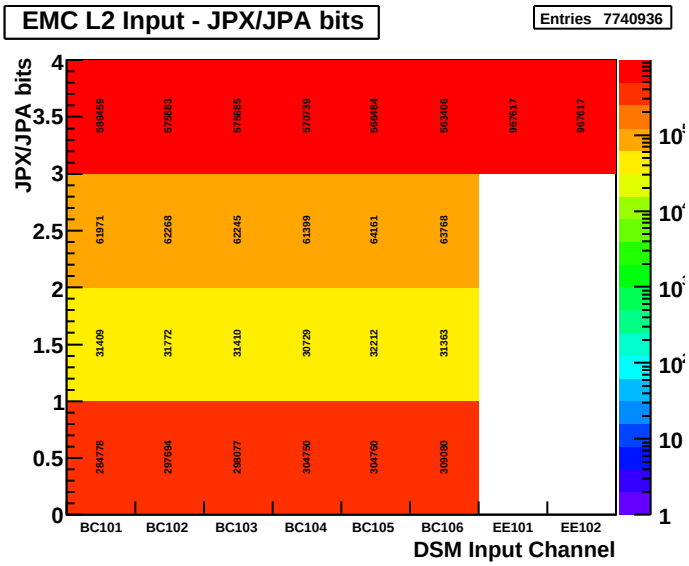

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

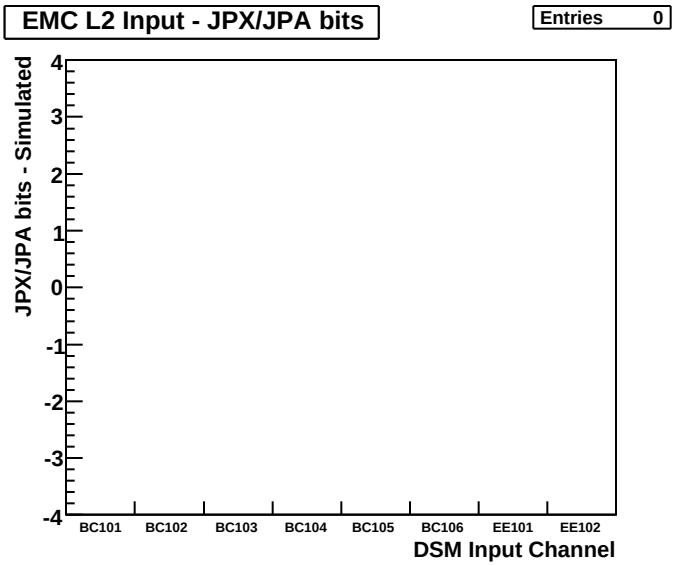

Endcap EMC L1 Input - High Eta Sum
Entries 1.16114e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

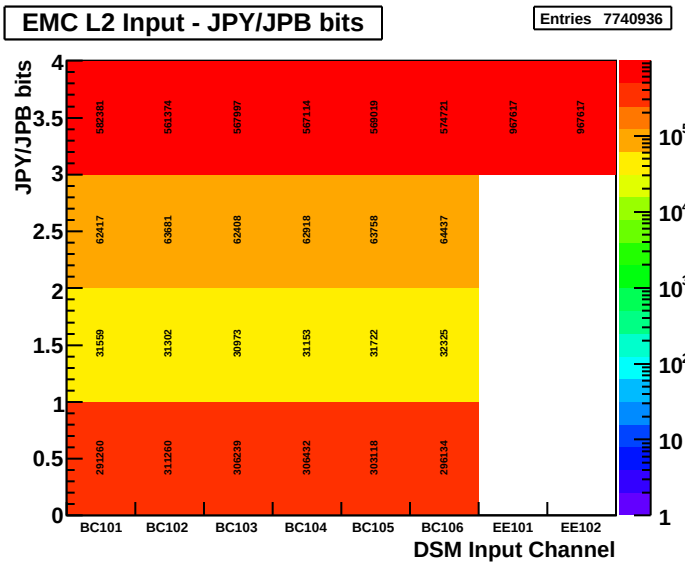

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

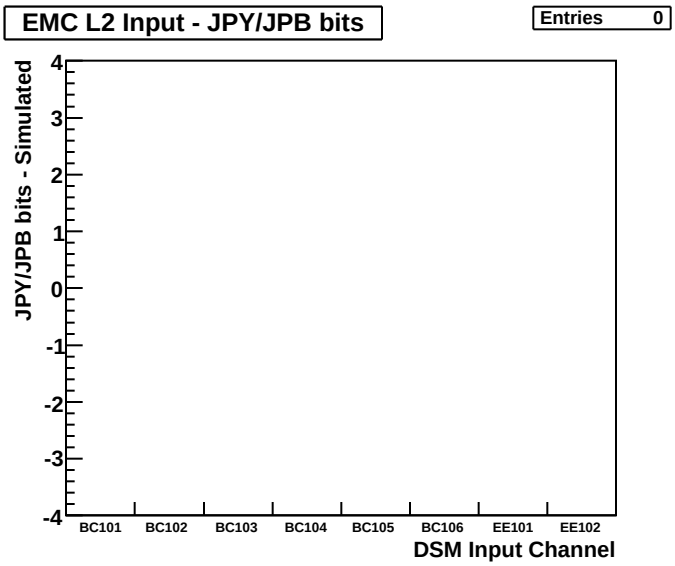

Endcap EMC L1 Input - High Tower Bits
Entries 1.16114e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

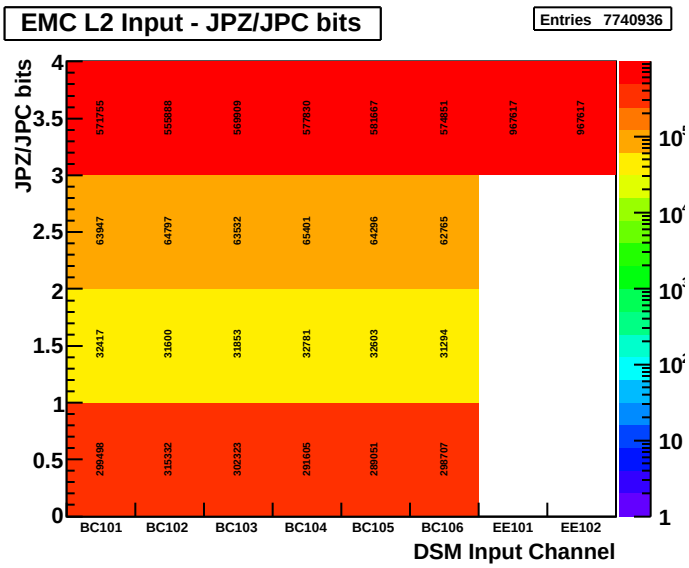

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

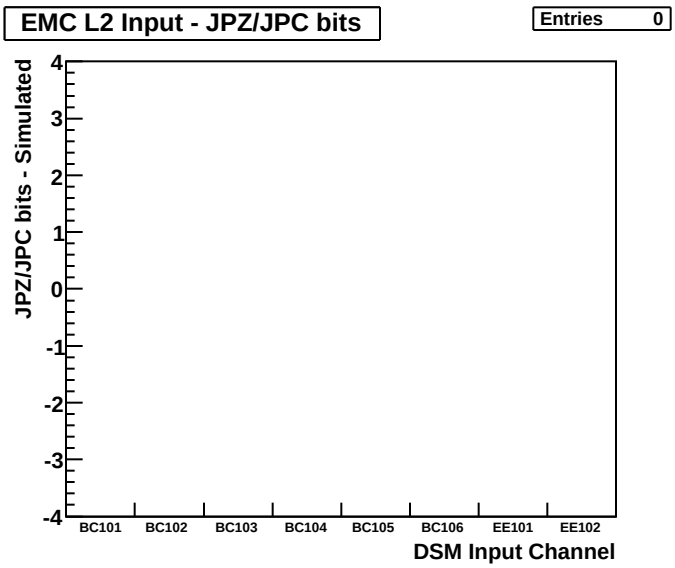

EMC L2 Input - JPX/JPA bits
Entries 7740936
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
589459 576683 576585 570739 566484 563406 967617 967617
61971 62268 62245 61399 64161 63768
31409 31772 31410 30729 32212 31363
284778 297694 298077 304750 304760 309080

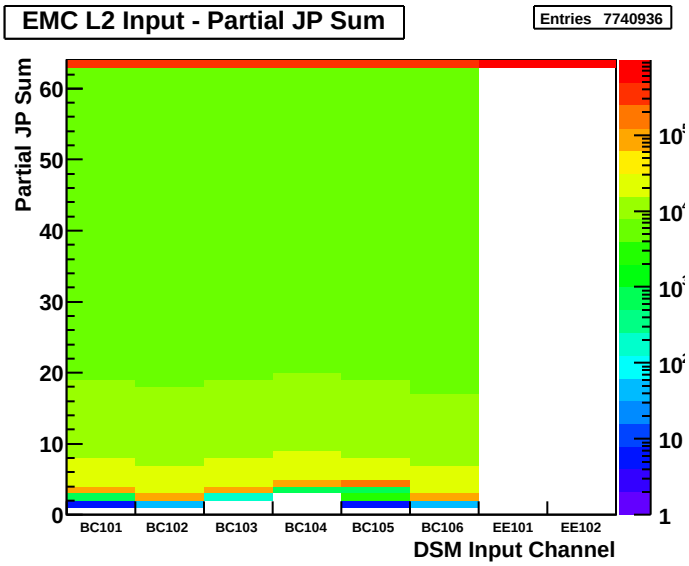

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

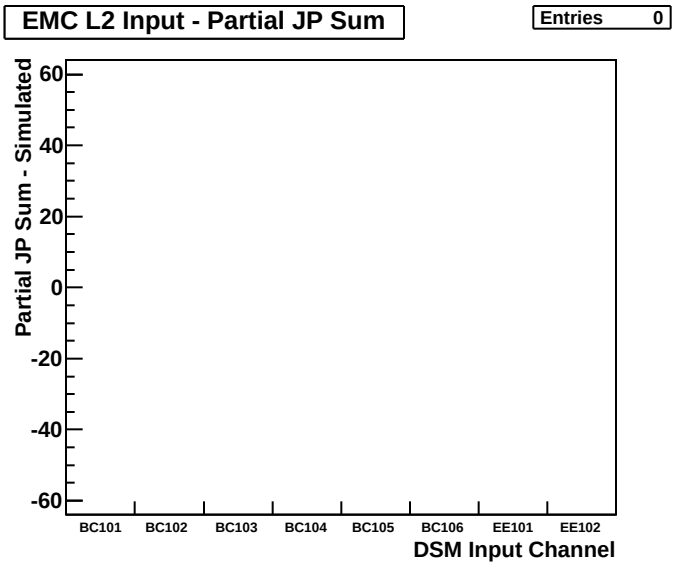

EMC L2 Input - JPY/JPB bits
Entries 7740936
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
562361 561374 567937 567114 569019 574721 967617 967617
62417 63681 62408 62918 63758 64437
31559 31302 30973 31153 31722 32325
291260 311260 306239 306432 303118 296134

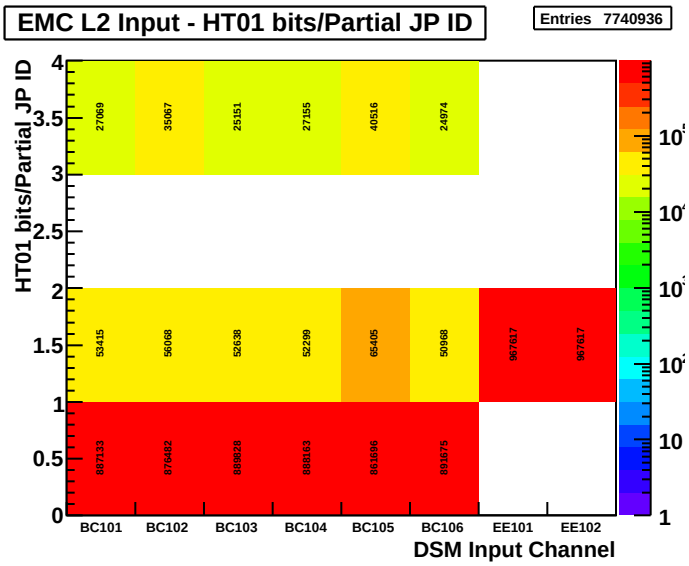

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

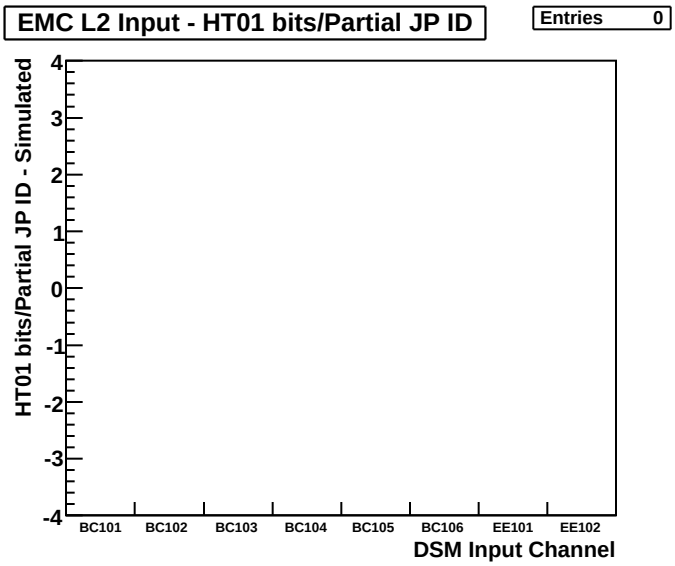

EMC L2 Input - JPZ/JPC bits
Entries 7740936
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
571755 559868 569009 577830 581667 574851 967617 967617
63947 64797 63532 65401 64296 62765
32417 31600 31853 32781 32603 31294
299498 315332 302323 291605 289051 298707

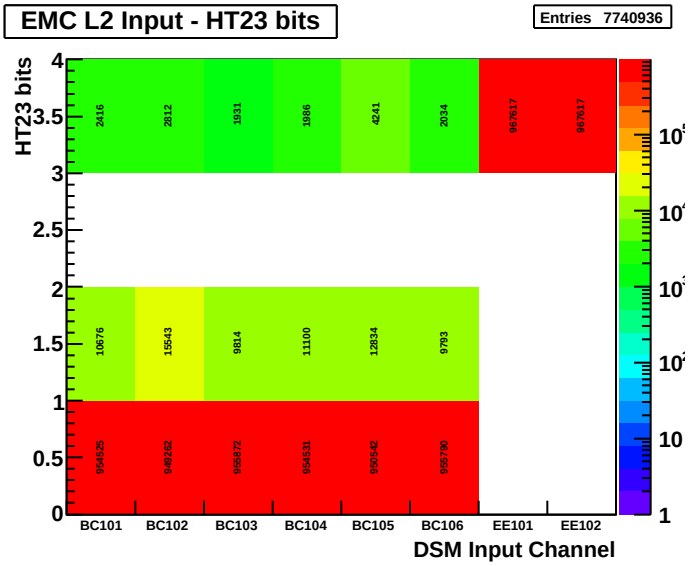

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

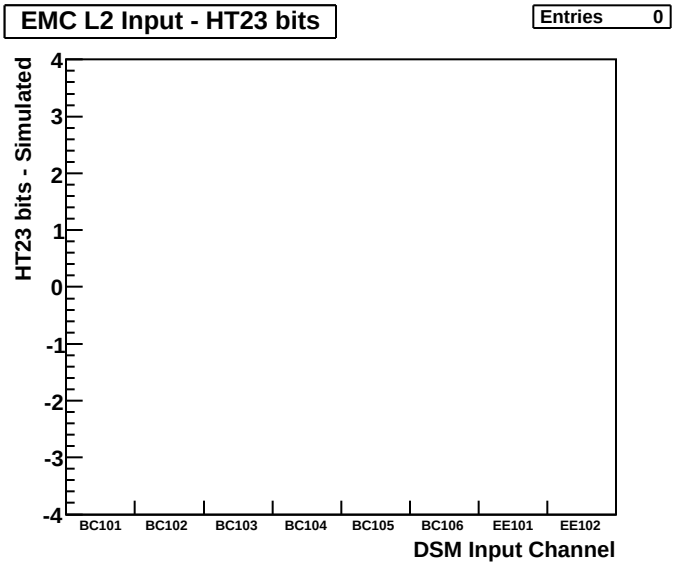

EMC L2 Input - Partial JP Sum
Entries 7740936
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

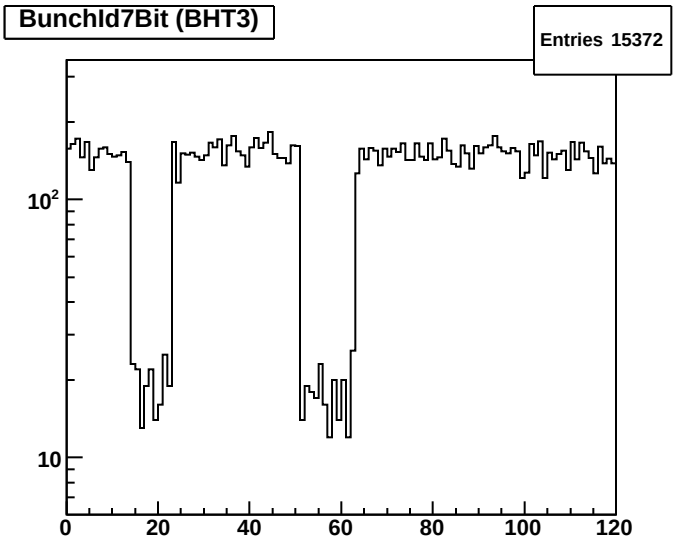

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

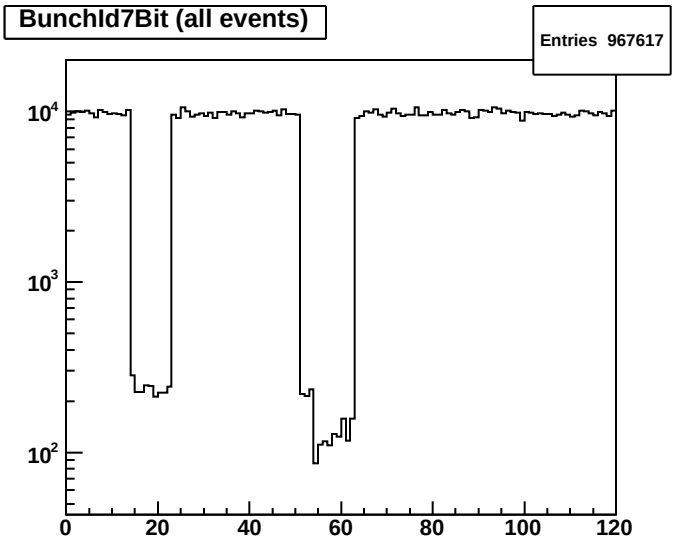

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7740936
HT01 bits/Partial JP ID
27069 35067 25151 27155 40516 24974
53415 56068 52638 52299 65405 50968 967617 967617
887133 876482 883828 888163 861696 891675
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

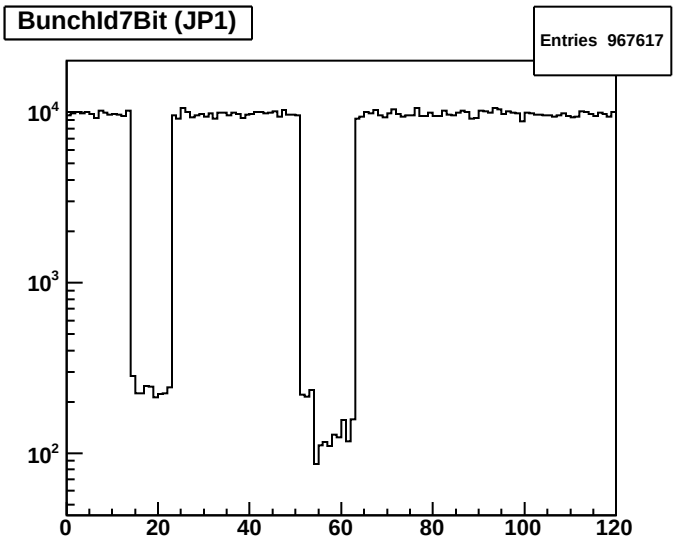

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

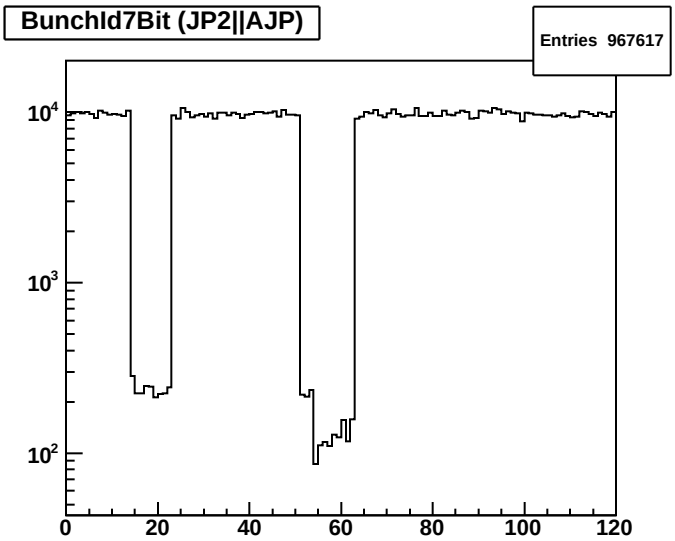

EMC L2 Input - HT23 bits
Entries 7740936
HT23 bits
2416 2612 1931 1986 4241 2034 967617 967617
10676 15543 9814 11100 12834 9793
954525 949262 955872 954531 950542 955790
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

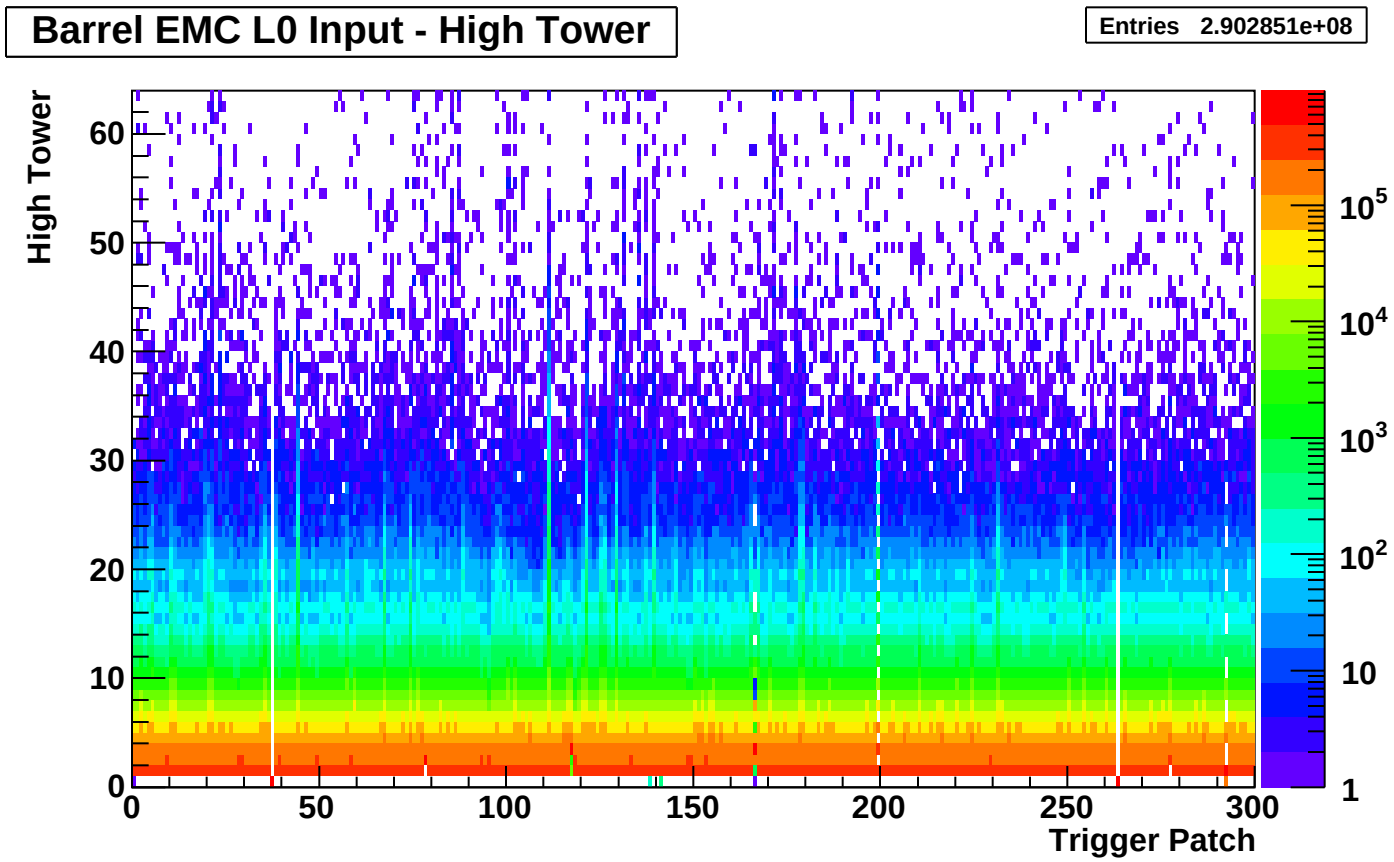

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

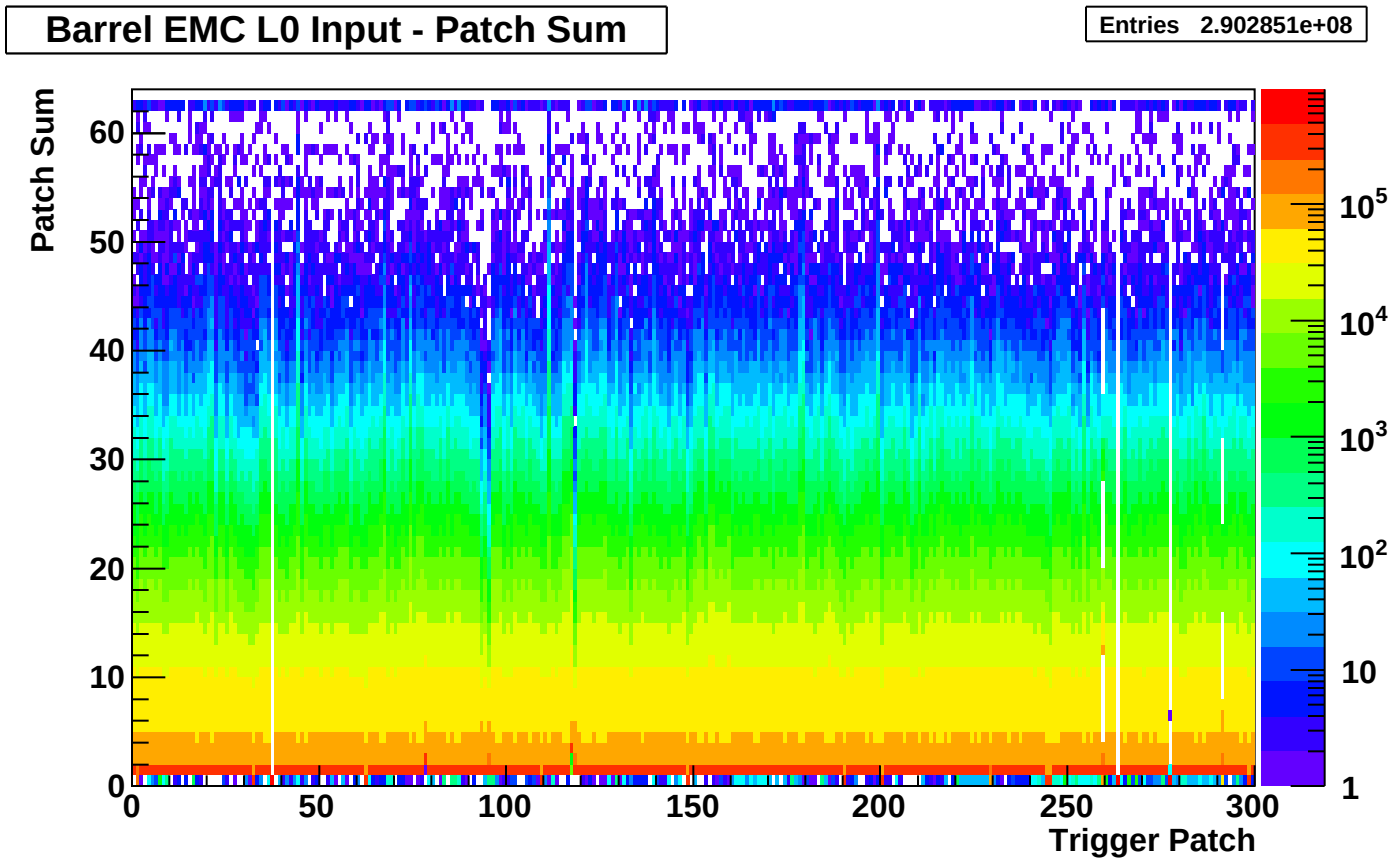

BunchId7Bit (BHT3)
Entries 15372
$10^2$
10
0
20
40
60
80
100
120

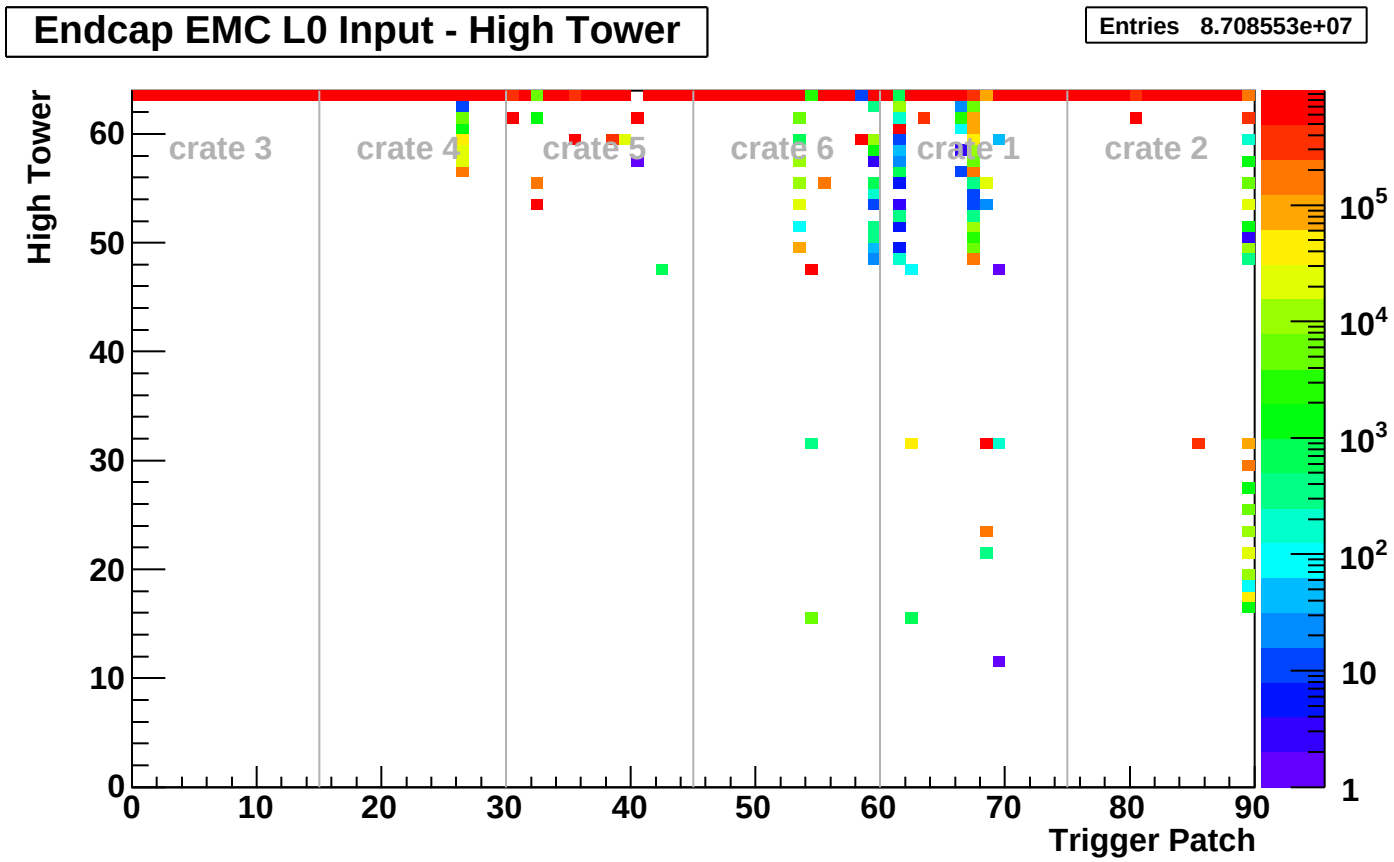

BunchId7Bit (all events)
Entries 967617
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

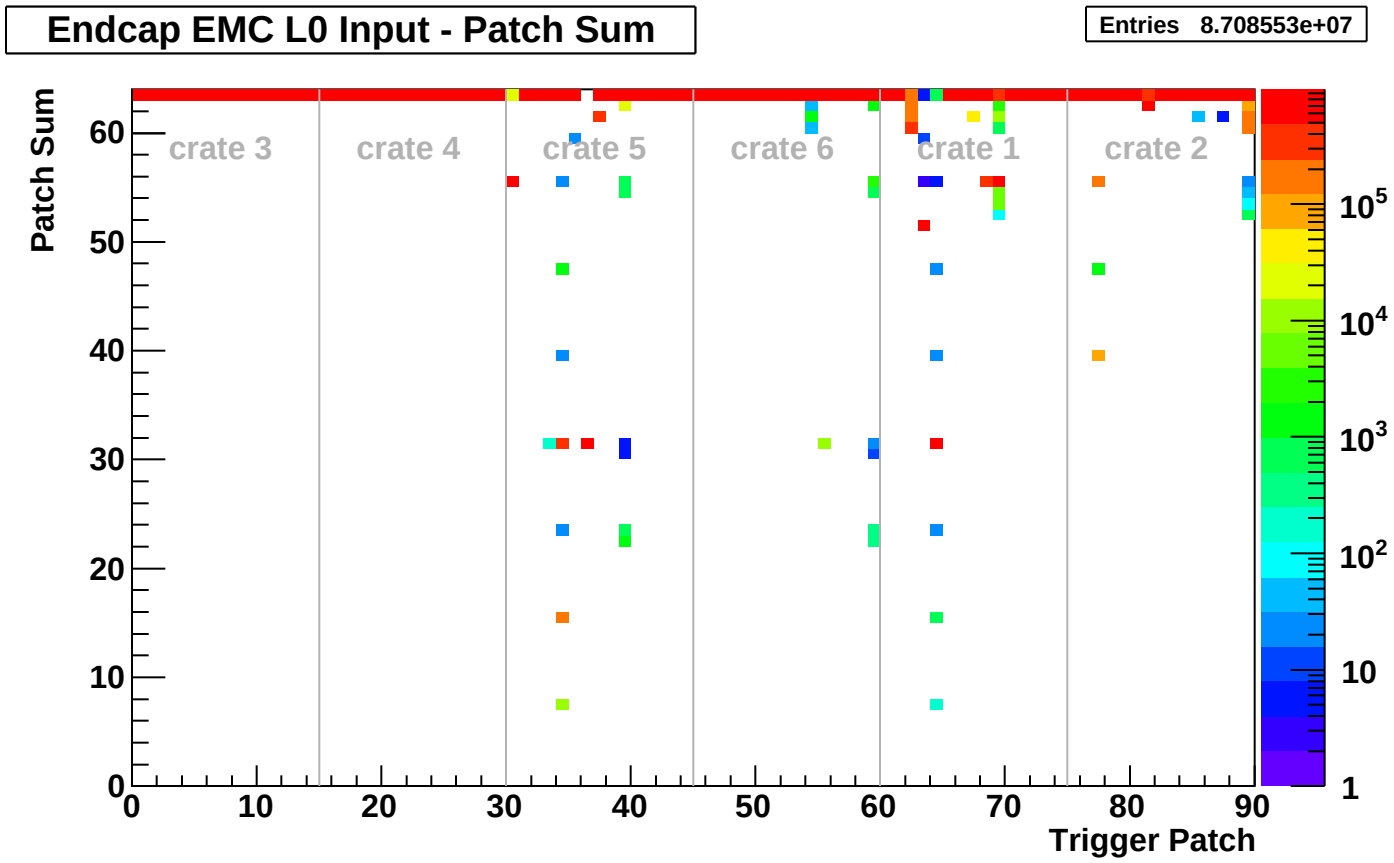

BunchId7Bit (JP1)
Entries 967617
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

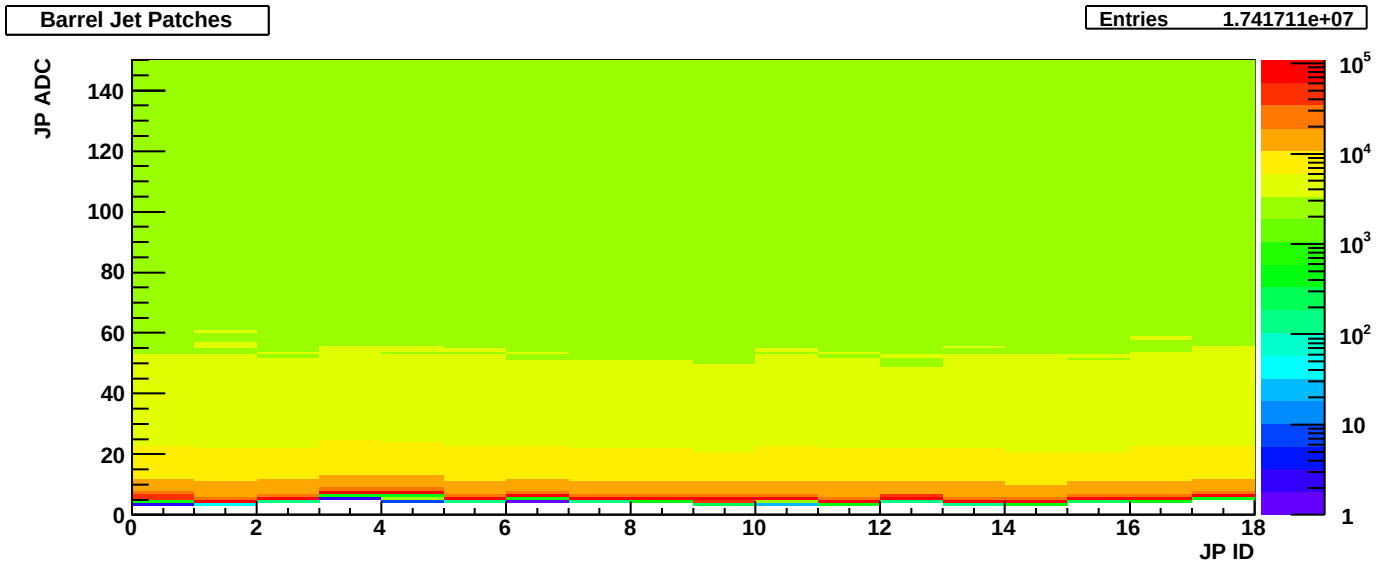

BunchId7Bit (JP2||AJP)
Entries 967617
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

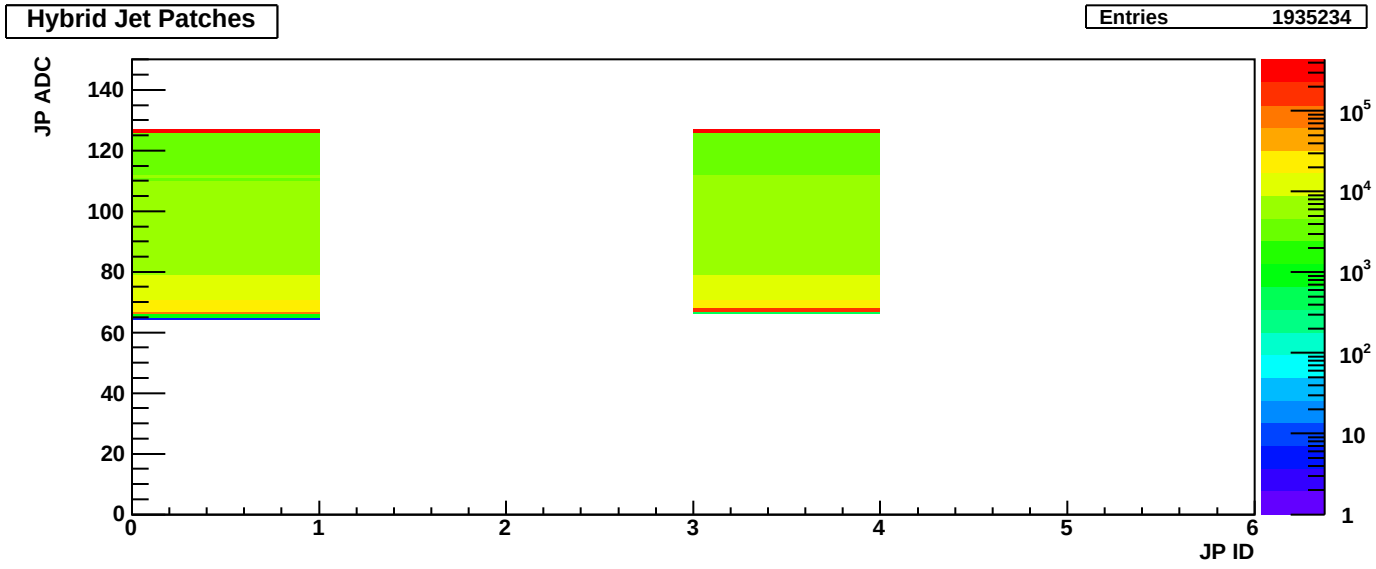

Barrel EMC L0 Input - High Tower
Entries 2.902851e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

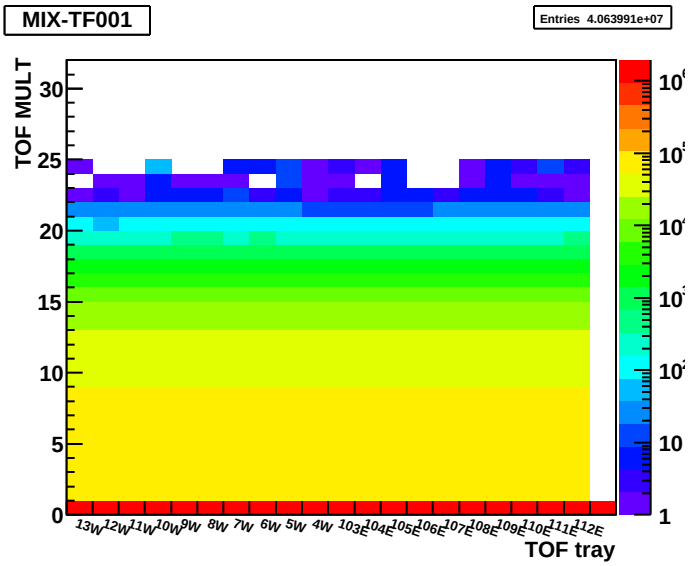

Barrel EMC L0 Input - Patch Sum
Entries 2.902851e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

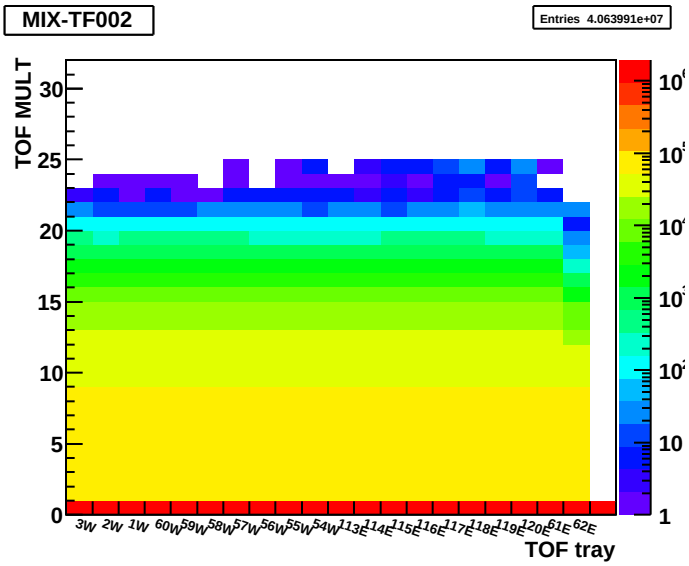

Endcap EMC L0 Input - High Tower
Entries 8.708553e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

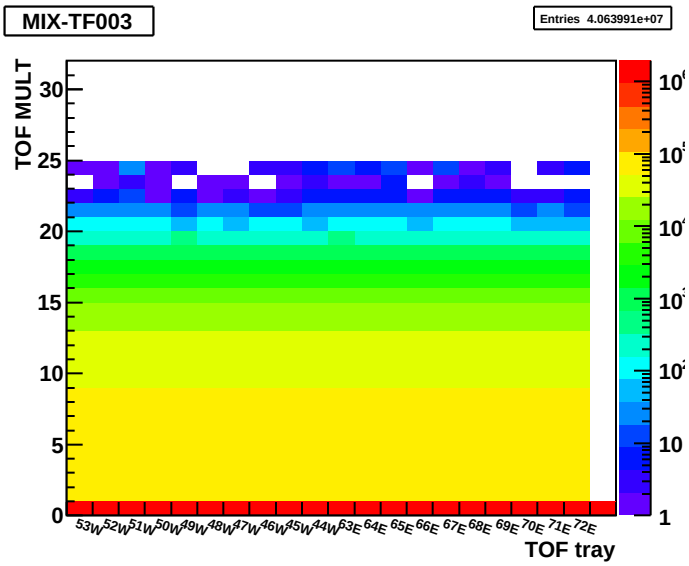

Endcap EMC L0 Input - Patch Sum
Entries 8.708553e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

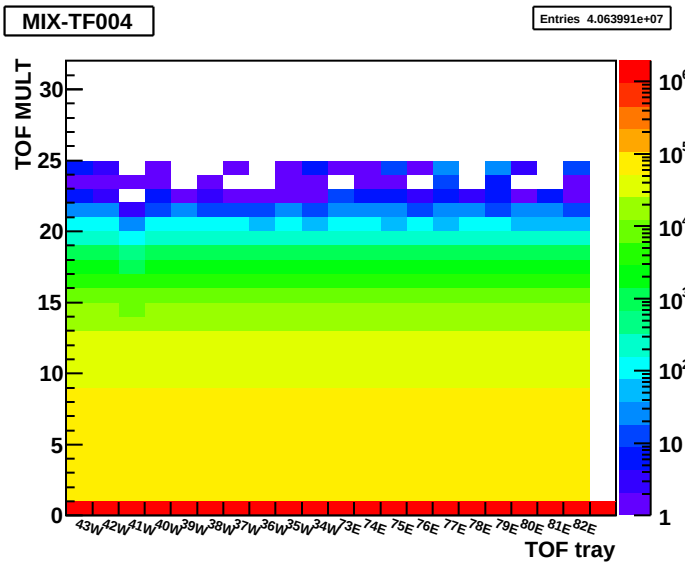

Barrel Jet Patches
Entries 1.741711e+07
JP ADC
JP ID

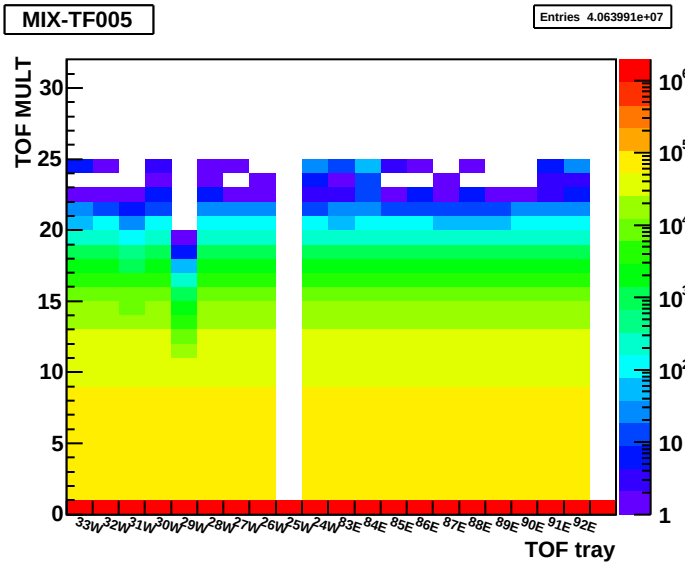

Hybrid Jet Patches
Entries 1935234
JP ADC
JP ID

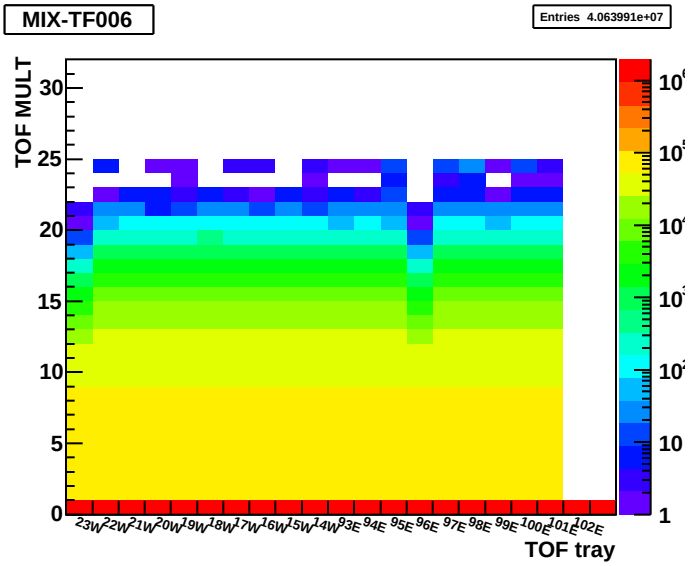

MIX-TF001
Entries 4.063991e+07
TOF MULT
TOF tray

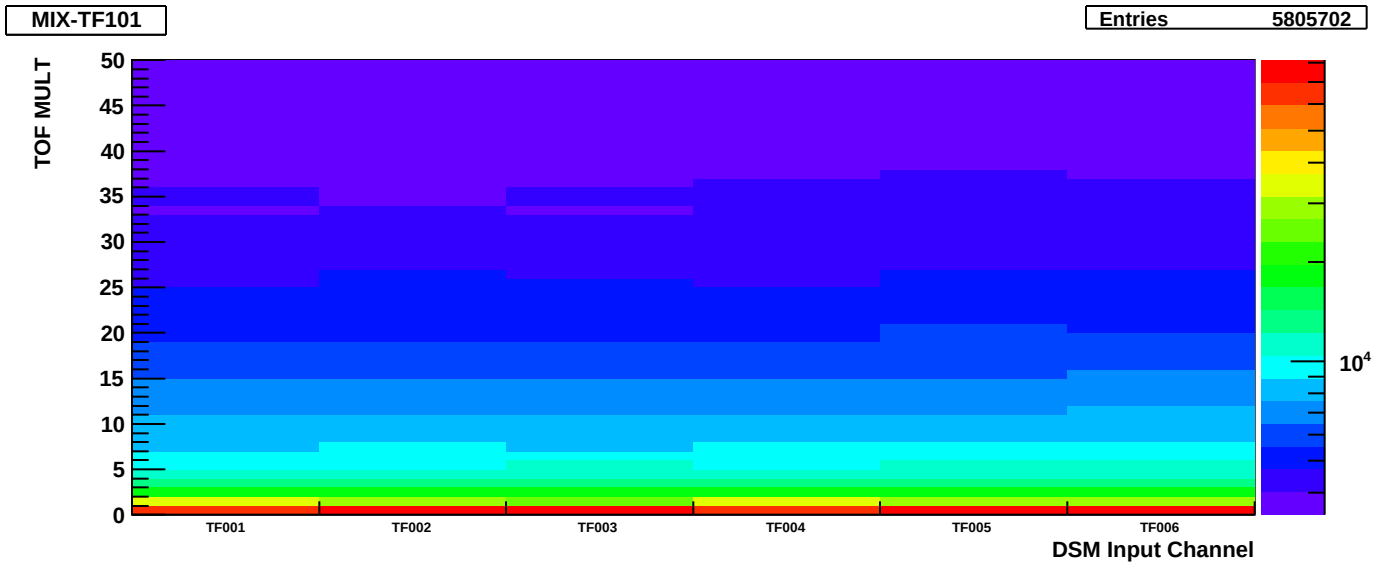

MIX-TF002
Entries 4.063991e+07
TOF MULT
TOF tray

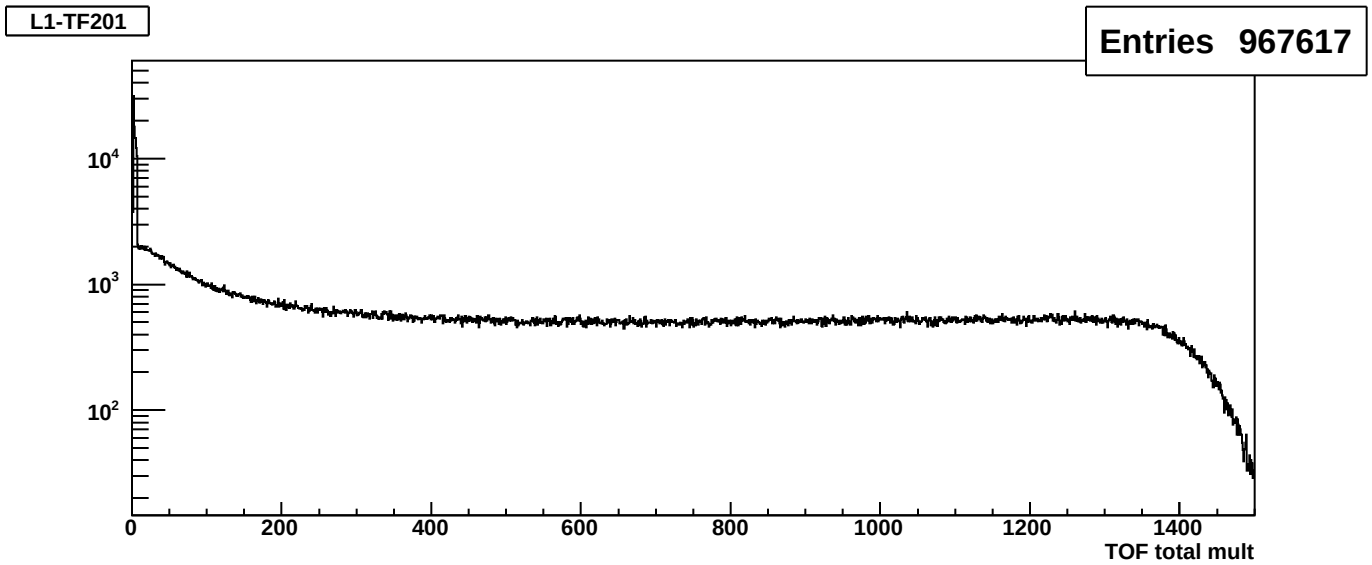

MIX-TF003
Entries 4.063991e+07
TOF MULT
TOF tray

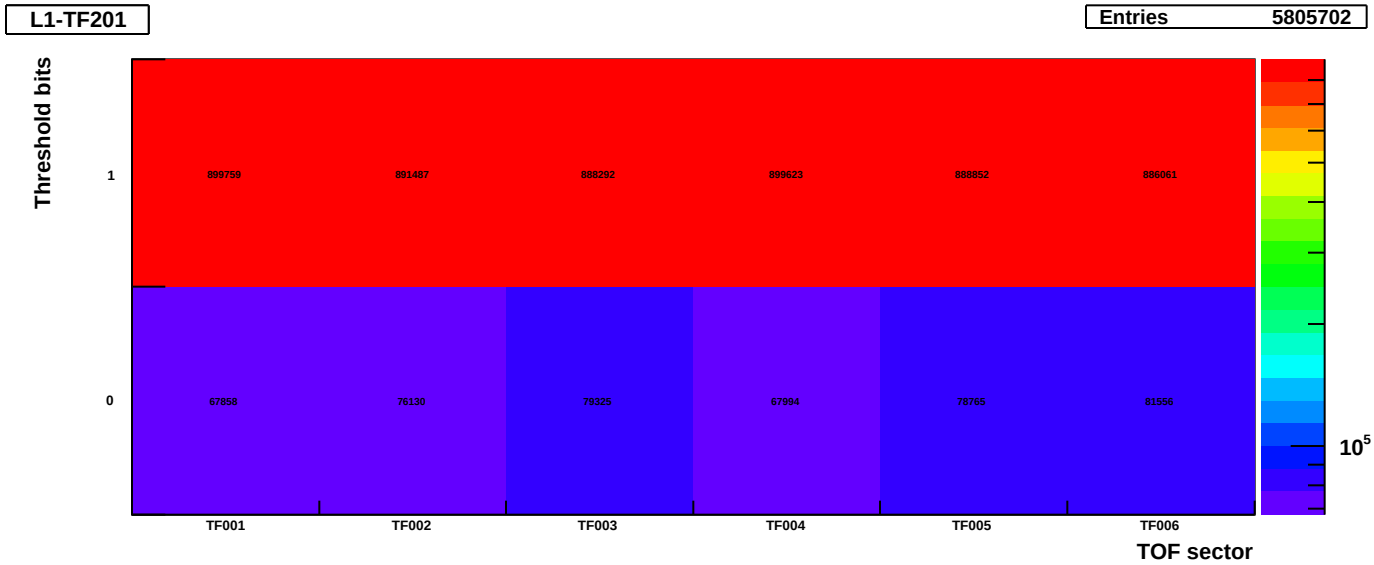

MIX-TF004
Entries 4.063991e+07
TOF MULT
TOF tray

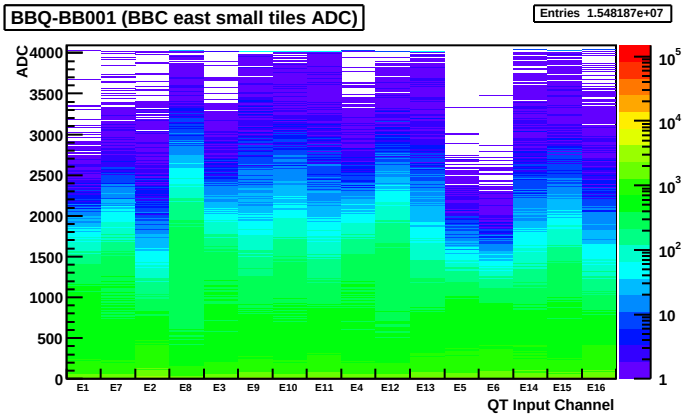

MIX-TF005
Entries 4.063991e+07
TOF MULT
TOF tray

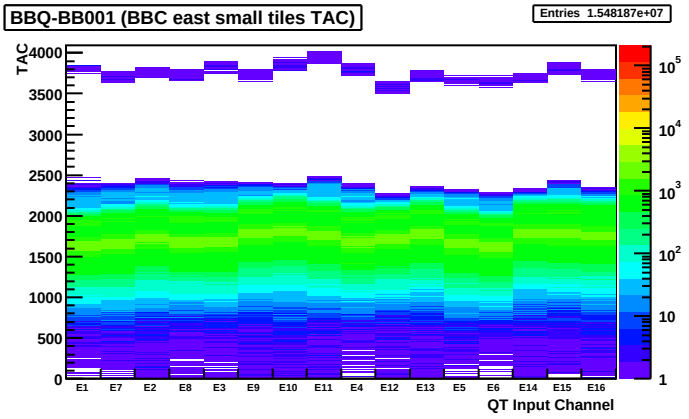

MIX-TF006
Entries 4.063991e+07
TOF MULT
TOF tray

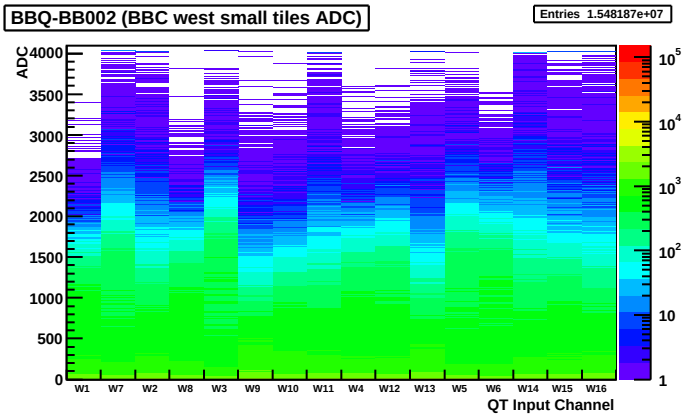

MIX-TF101
Entries 5805702
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$

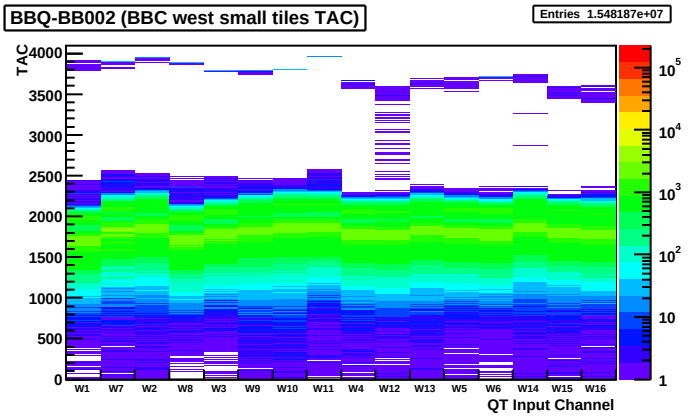

L1-TF201
Entries 967617
$10^4$
$10^3$
$10^2$
0
200
400
600
800
1000
1200
1400
TOF total mult

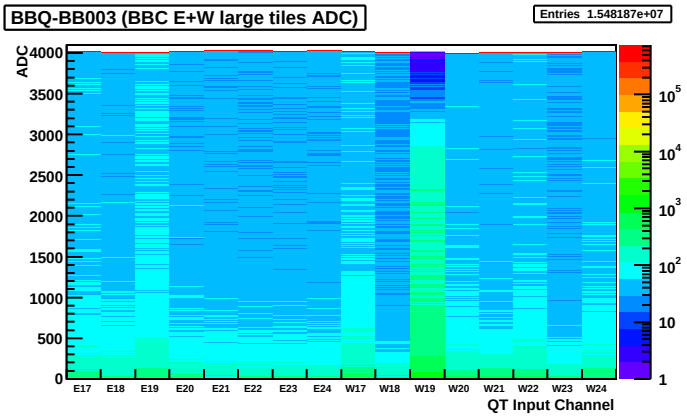

L1-TF201
Entries 5805702
Threshold bits
1
0
899759
891487
888292
899623
888852
886061
67858
76130
79325
67994
78765
81556
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^5$

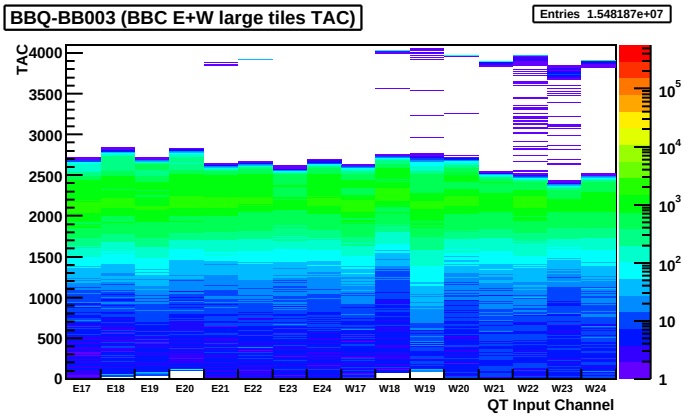

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.548187e+07
ADC
QT Input Channel

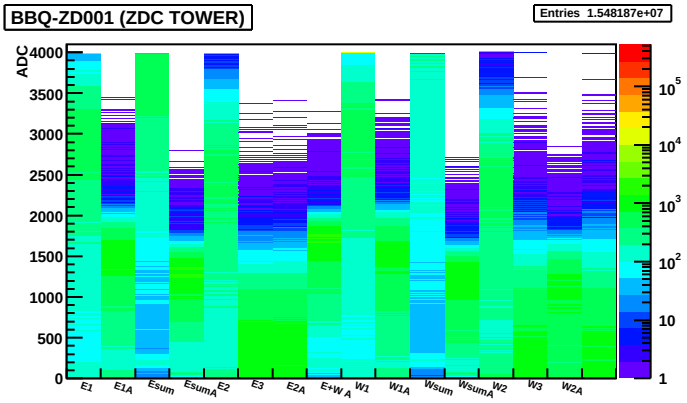

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.548187e+07
TAC
QT Input Channel

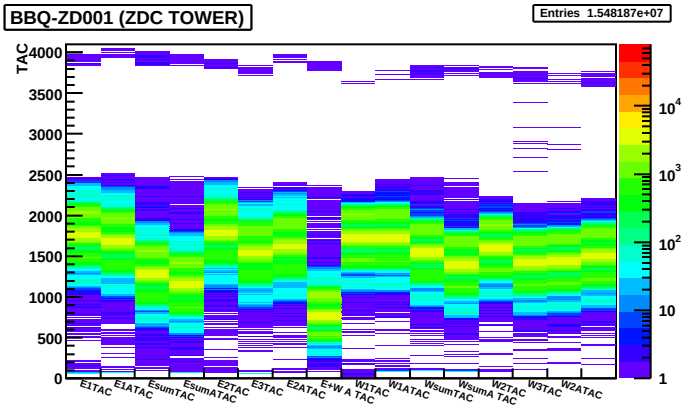

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.548187e+07
ADC
QT Input Channel

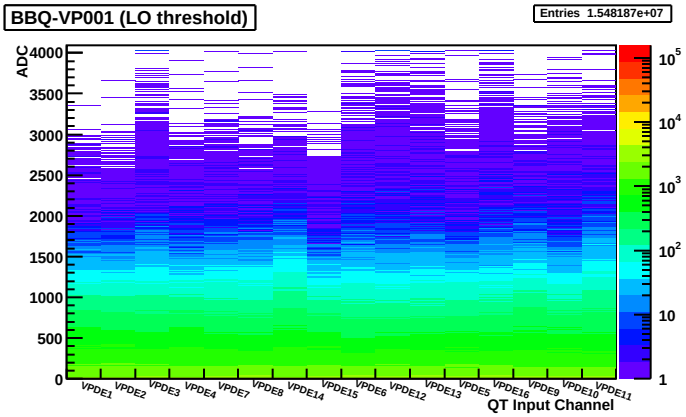

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.548187e+07
TAC
QT Input Channel

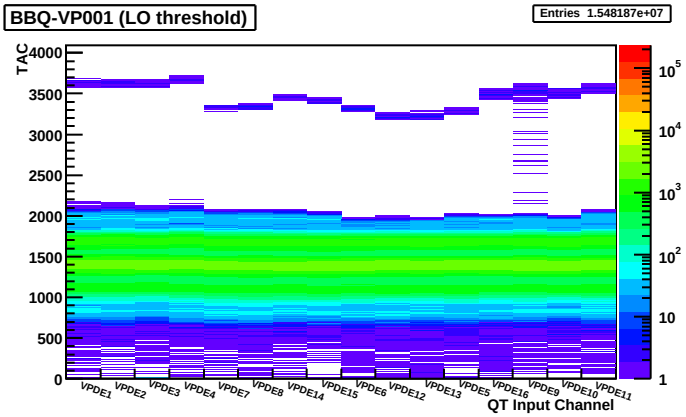

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.548187e+07
ADC
QT Input Channel

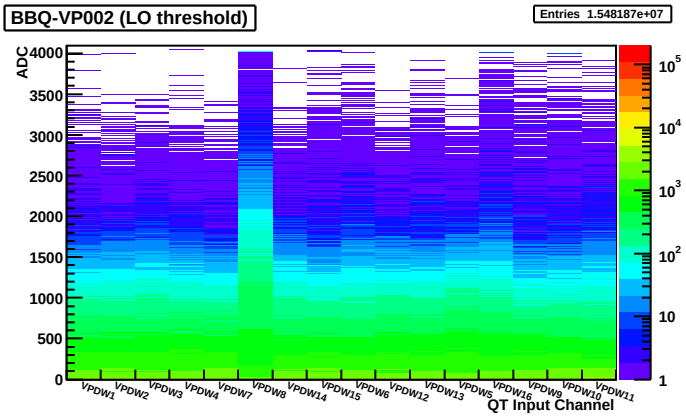

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.548187e+07
TAC
QT Input Channel

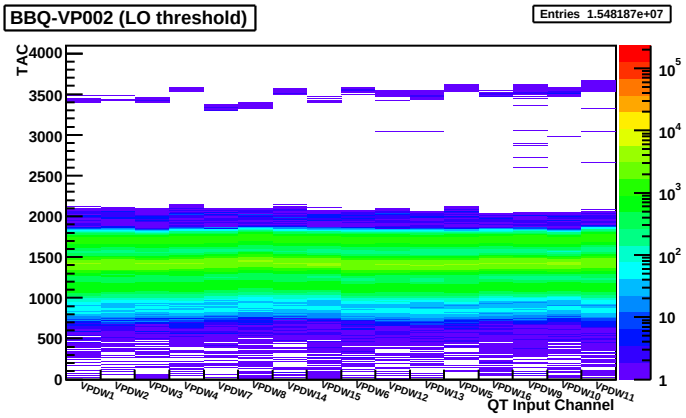

BBQ-ZD001 (ZDC TOWER)
Entries 1.548187e+07
ADC

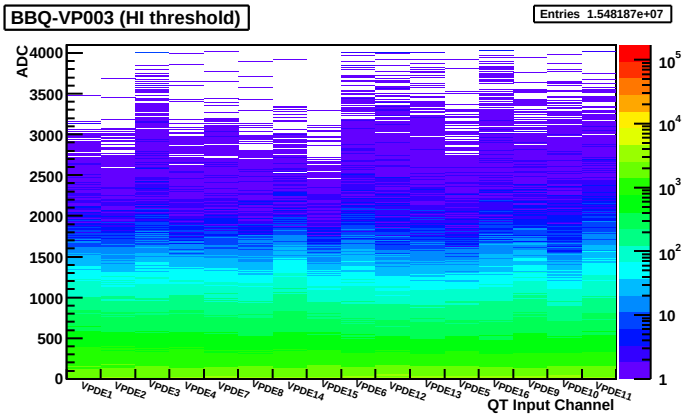

BBQ-ZD001 (ZDC TOWER)
Entries 1.548187e+07
TAC

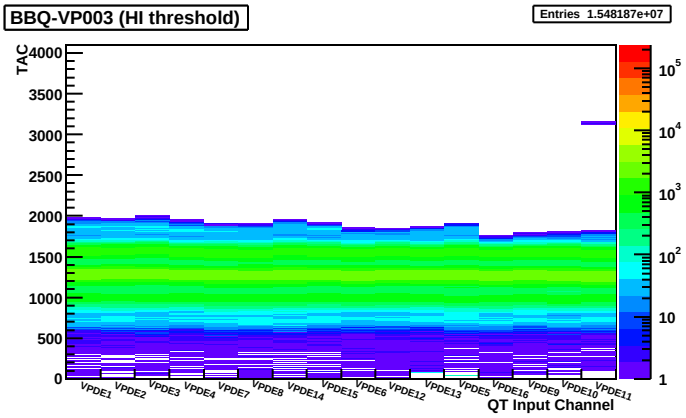

BBQ-VP001 (LO threshold)
Entries 1.548187e+07
ADC
QT Input Channel

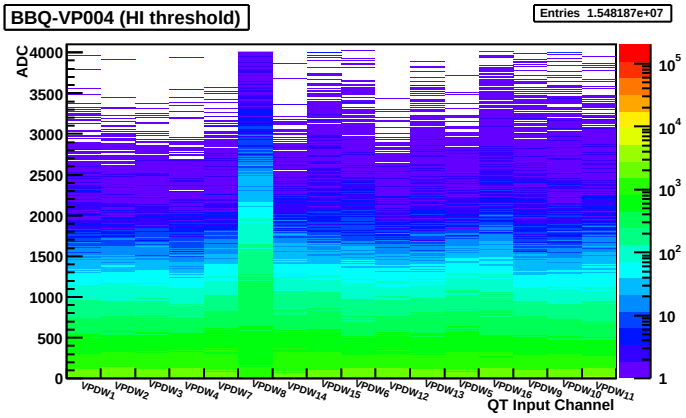

BBQ-VP001 (LO threshold)
Entries 1.548187e+07
TAC
QT Input Channel

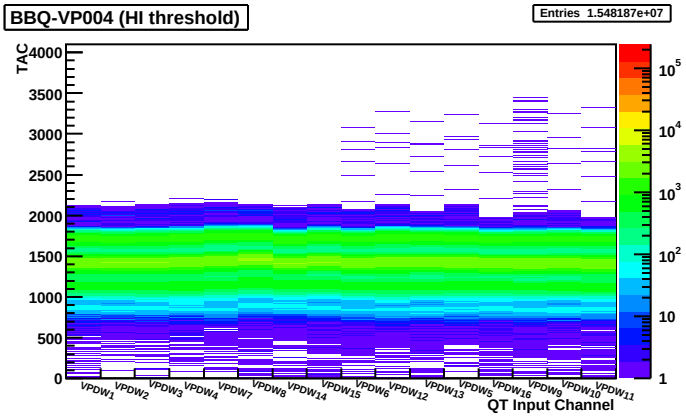

BBQ-VP002 (LO threshold)
Entries 1.548187e+07
ADC
QT Input Channel

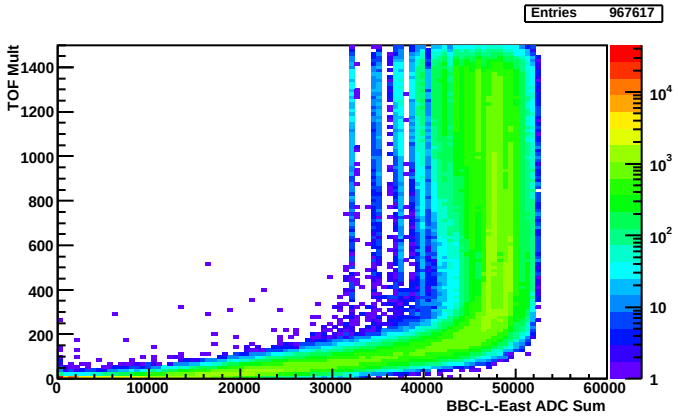

BBQ-VP002 (LO threshold)
Entries 1.548187e+07
TAC
QT Input Channel

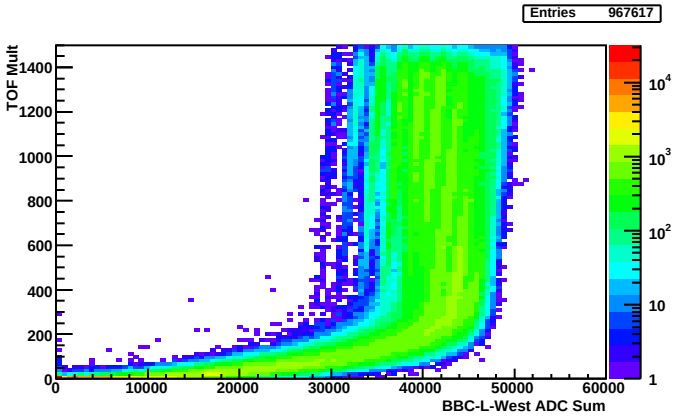

BBQ-VP003 (HI threshold)
Entries 1.548187e+07
ADC
QT Input Channel

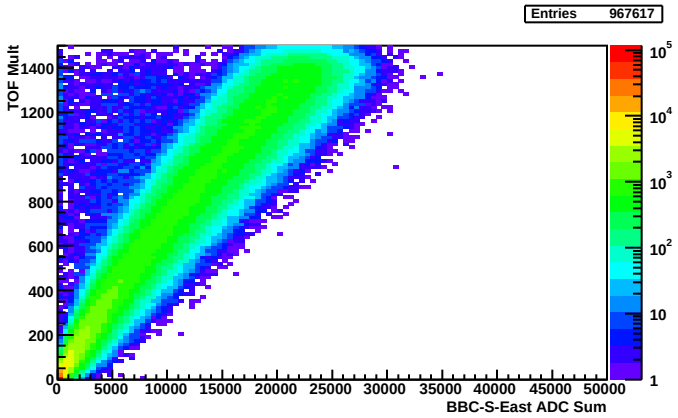

BBQ-VP003 (HI threshold)
Entries 1.548187e+07
TAC
QT Input Channel

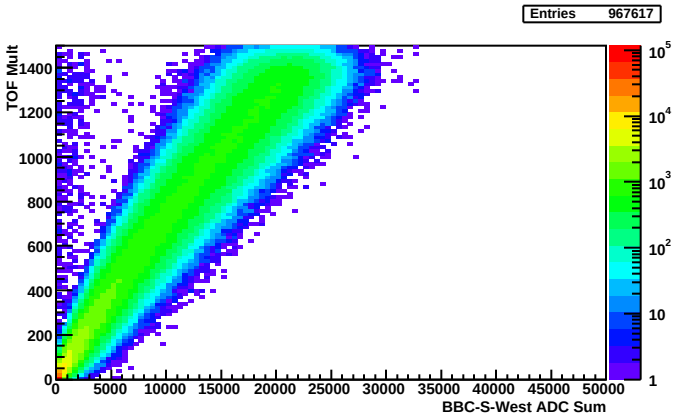

BBQ-VP004 (HI threshold)
Entries 1.548187e+07
ADC
QT Input Channel

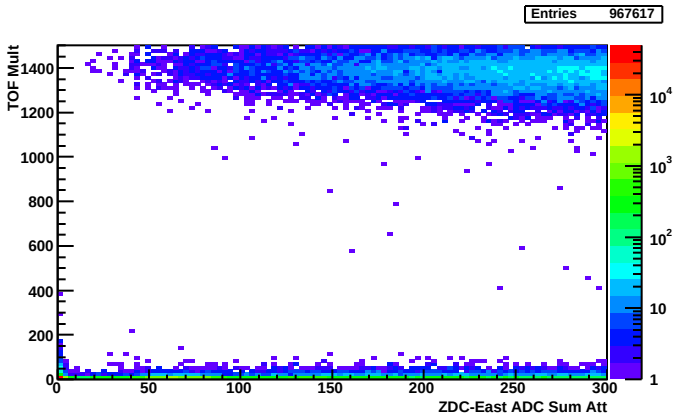

BBQ-VP004 (HI threshold)
Entries 1.548187e+07
TAC
QT Input Channel

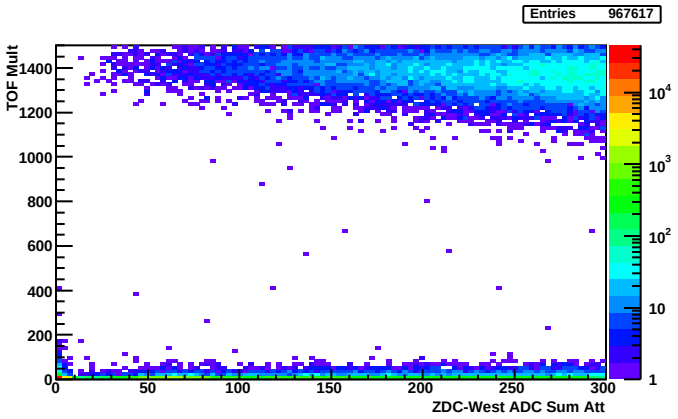

Entries 967617
TOF Mult
BBC-L-East ADC Sum

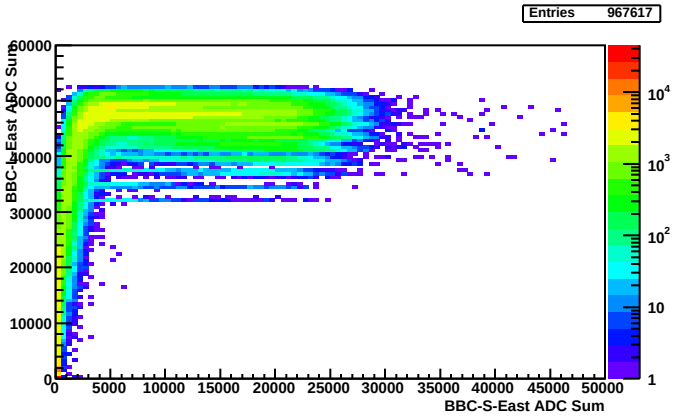

Entries 967617
TOF Mult
BBC-L-West ADC Sum

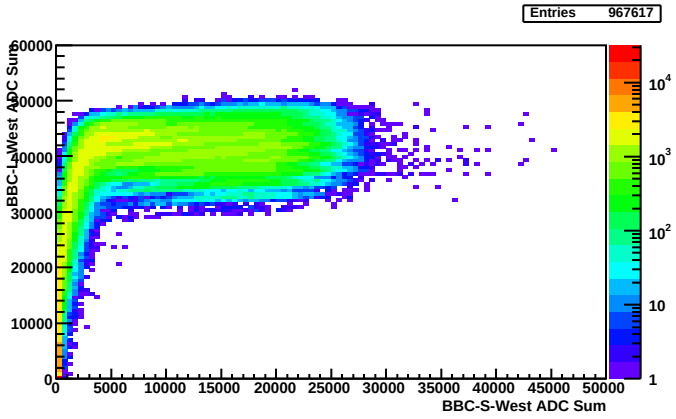

Entries 967617
TOF Mult
BBC-S-East ADC Sum

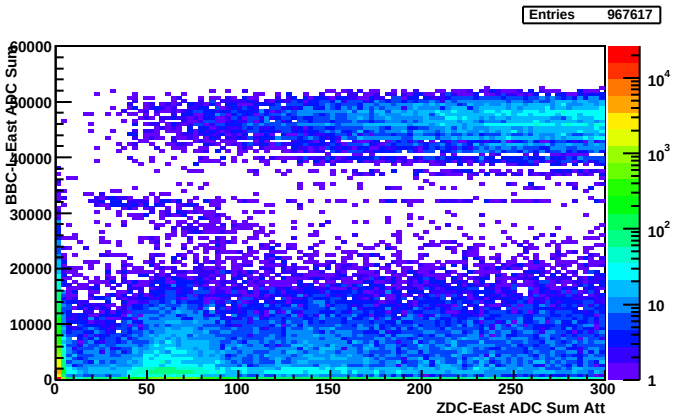

Entries 967617
TOF Mult
BBC-S-West ADC Sum

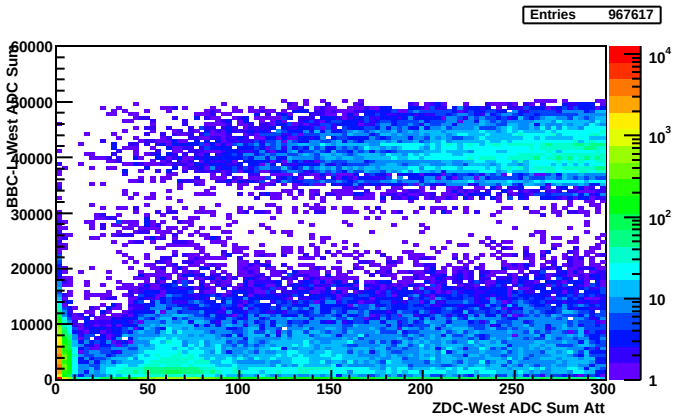

Entries 967617
TOF Mult
ZDC-East ADC Sum Att

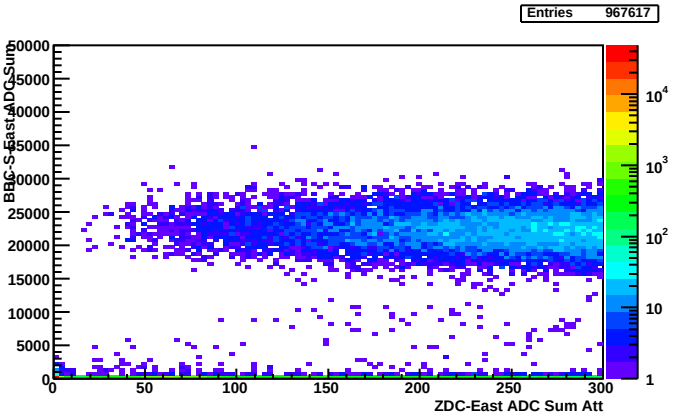

Entries 967617
TOF Mult
ZDC-West ADC Sum Att

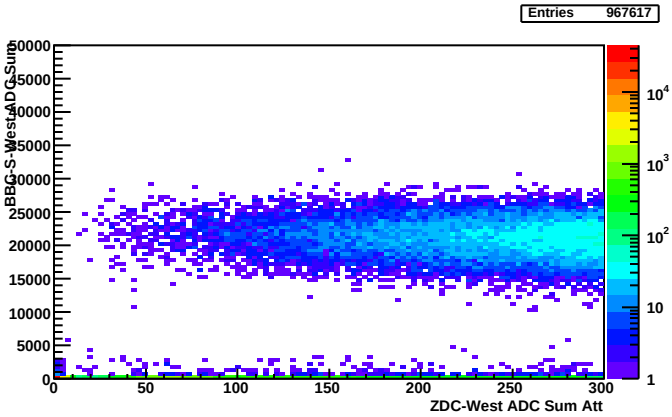

Entries 967617
BBC-L-East ADC Sum
BBC-S-East ADC Sum

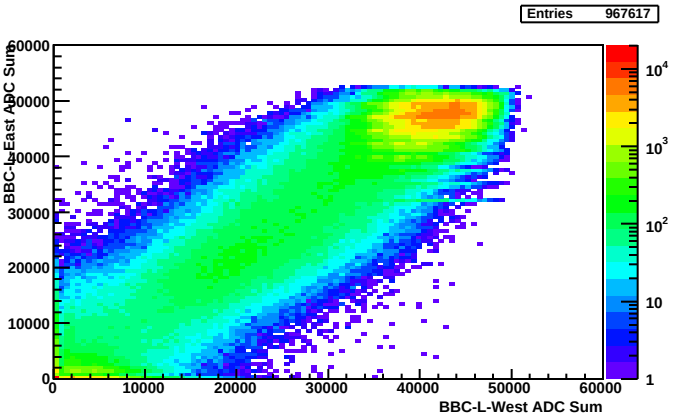

Entries 967617
BBC-L-West ADC Sum
BBC-S-West ADC Sum

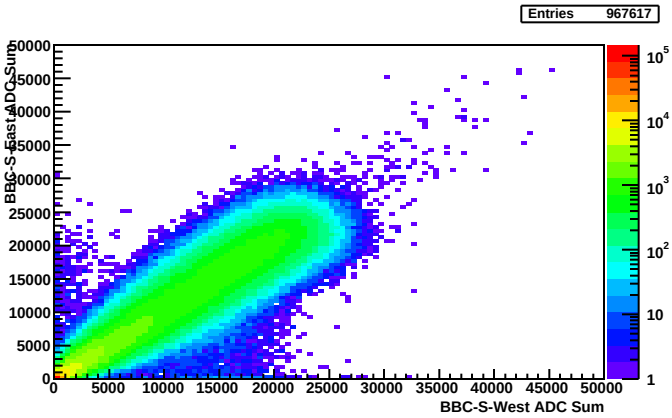

Entries 967617
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

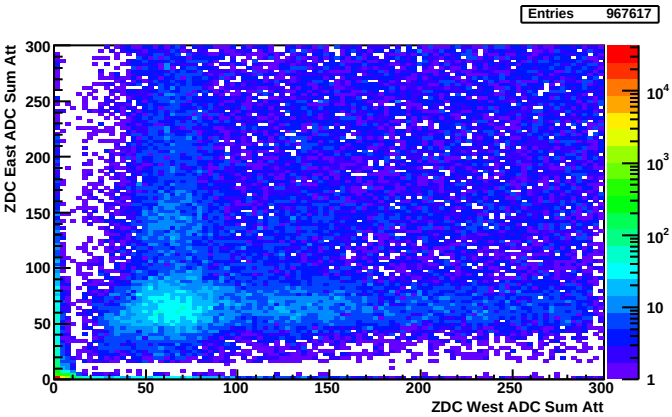

Entries 967617
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

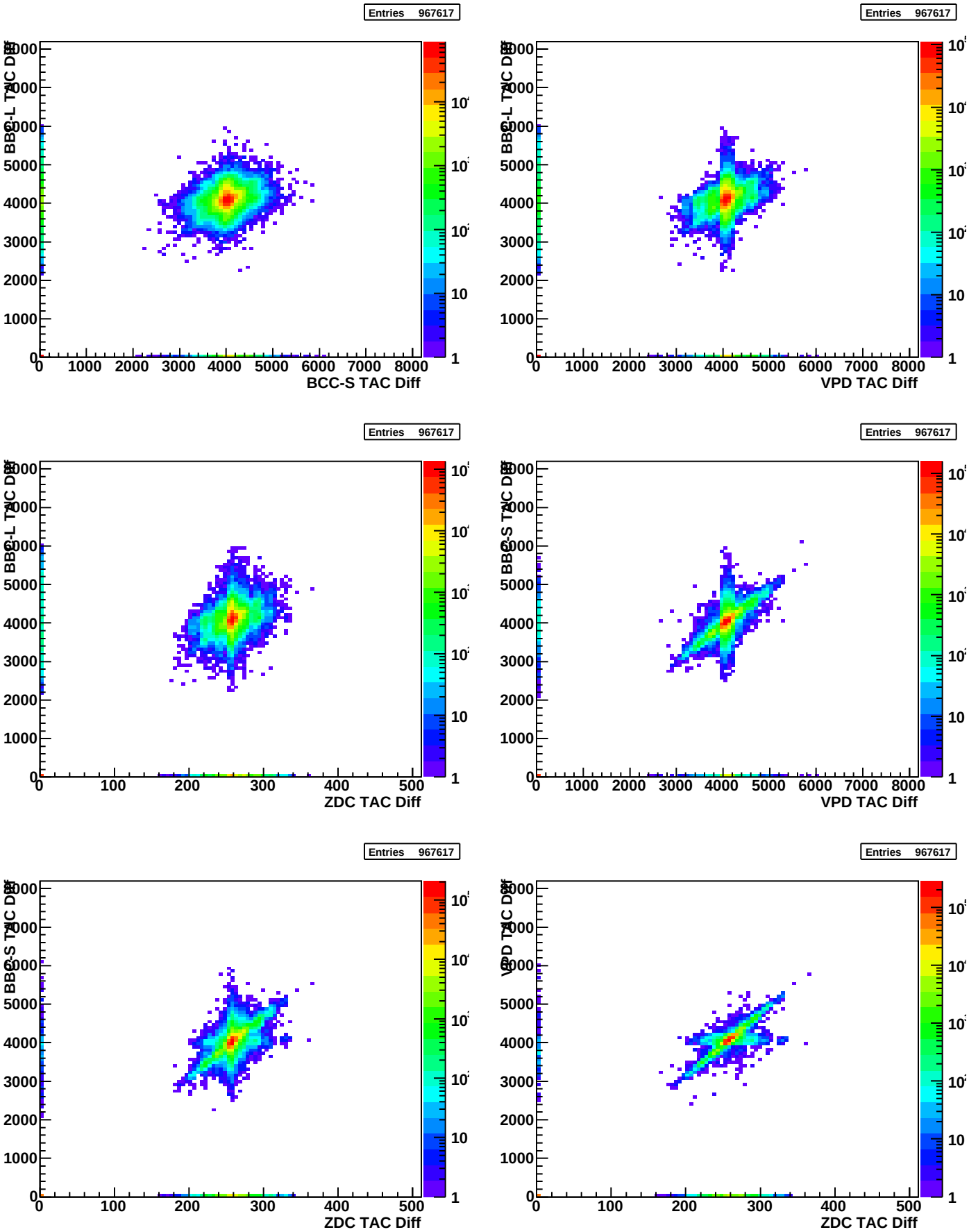

Entries 967617
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

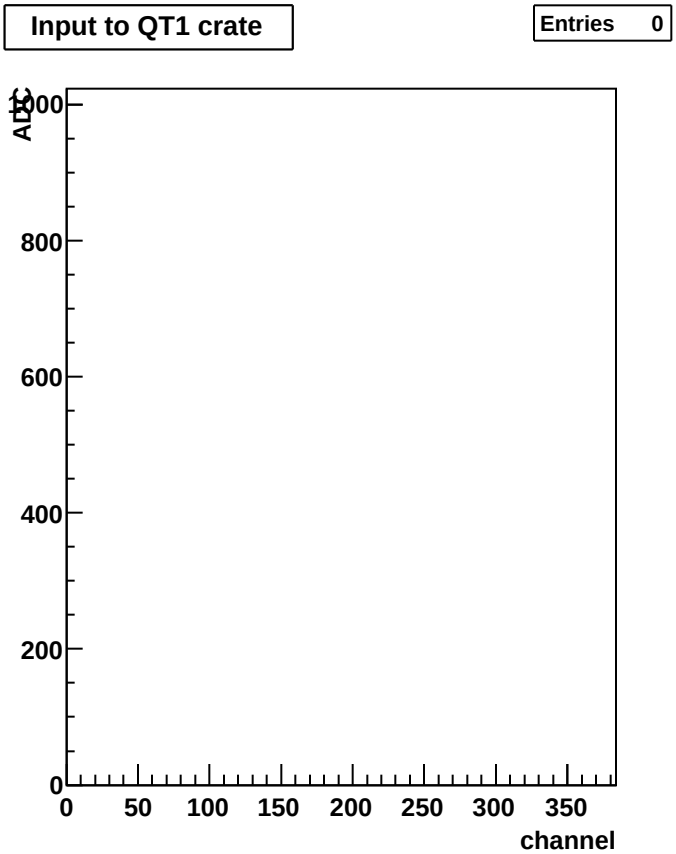

Entries 967617
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

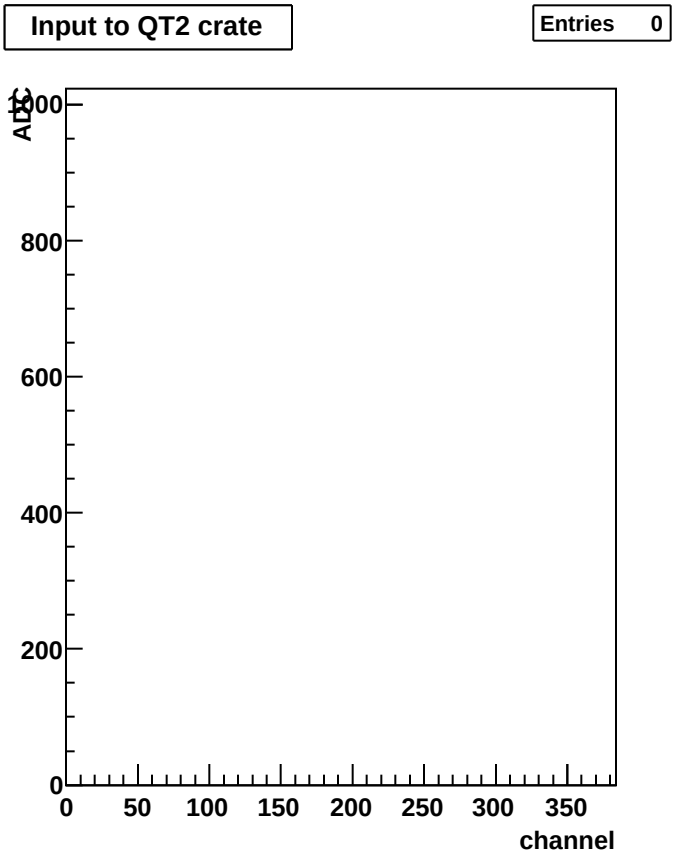

Entries 967617
BBC-L-East ADC Sum
BBC-L-West ADC Sum

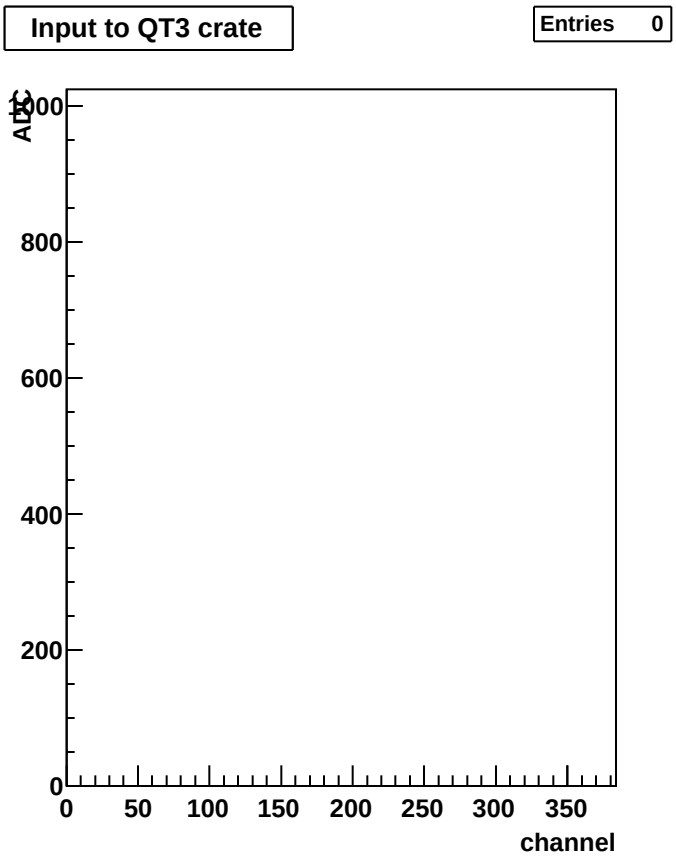

Entries 967617
BBC-S-East ADC Sum
BBC-S-West ADC Sum

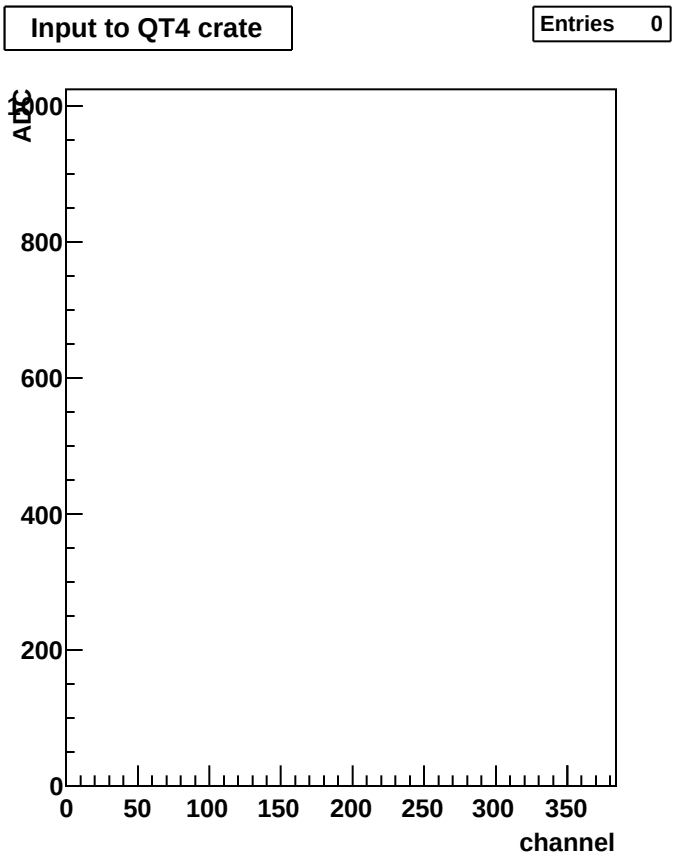

Entries 967617
ZDC East ADC Sum Att
ZDC West ADC Sum Att

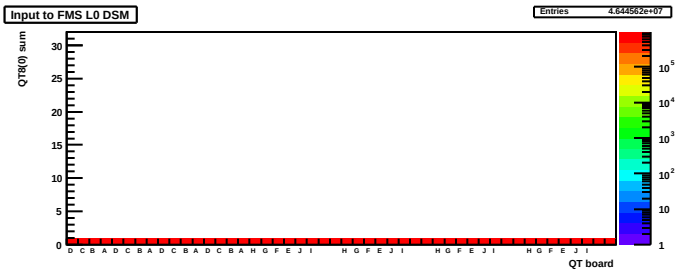

Entries 967617
BBC-L TAC Diff
BCC-S TAC Diff
Entries 967617
BBC-L TAC Diff
VPD TAC Diff
Entries 967617
BBC-L TAC Diff
ZDC TAC Diff
Entries 967617
BBC-S TAC Diff
VPD TAC Diff
Entries 967617
BBC-S TAC Diff
ZDC TAC Diff
Entries 967617
VPD TAC Diff
ZDC TAC Diff

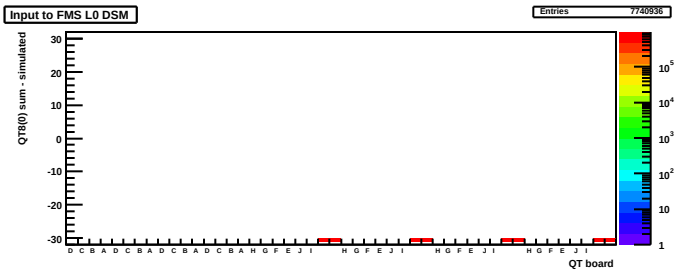

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

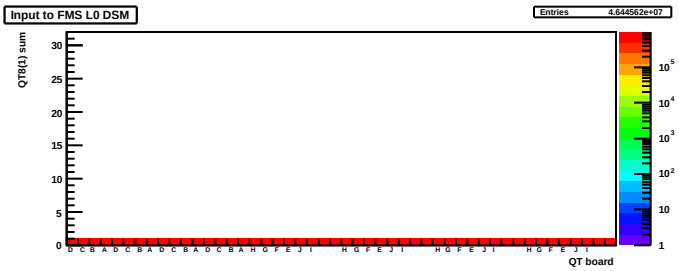

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

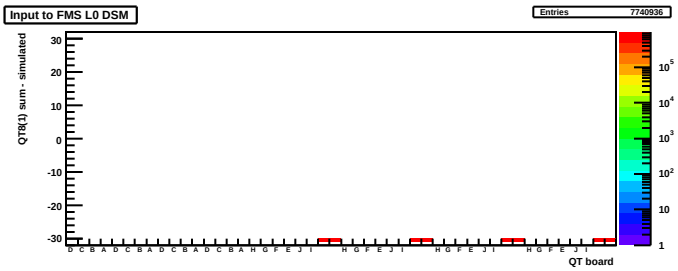

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

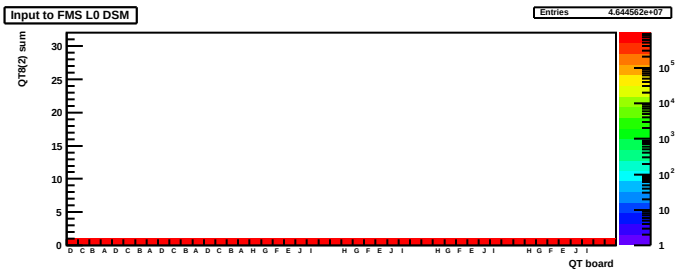

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

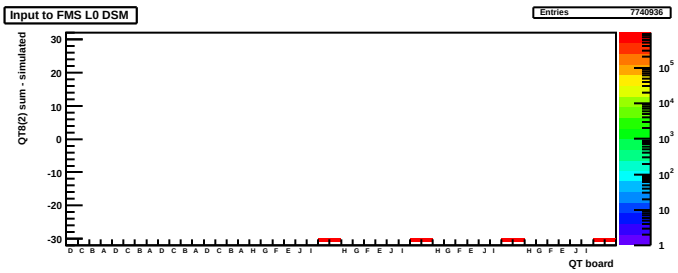

Input to FMS L0 DSM
Entries 4.644562e+07
QT8(0) sum
QT board

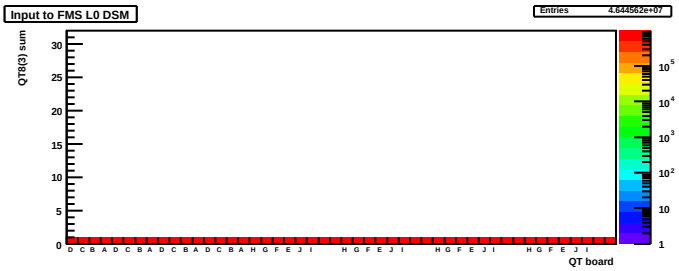

Input to FMS L0 DSM
Entries 7740936
QT8(0) sum - simulated
QT board

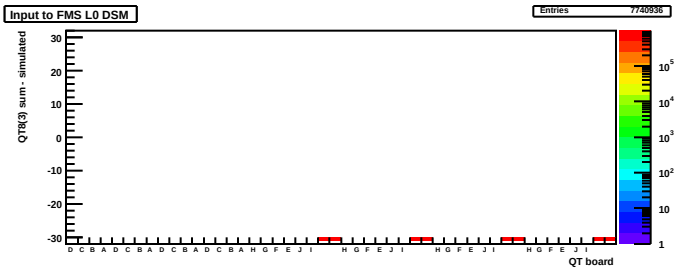

Input to FMS L0 DSM
Entries 4.644562e+07
QT8(1) sum
QT board

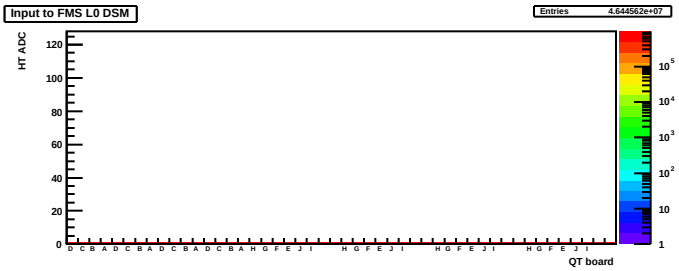

Input to FMS L0 DSM
Entries 7740936
QT8(1) sum - simulated
QT board

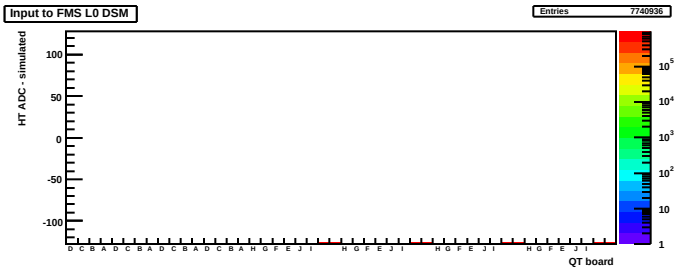

Input to FMS L0 DSM
Entries 4.644562e+07
QT8(2) sum
QT board

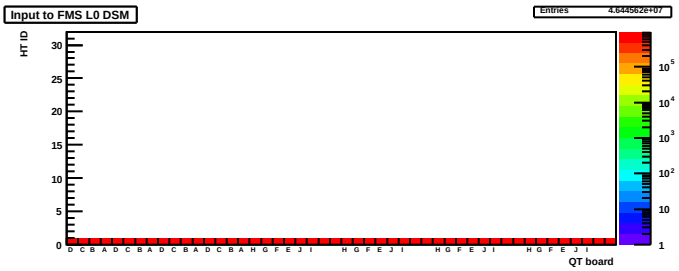

Input to FMS L0 DSM
Entries 7740936
QT8(2) sum - simulated
QT board

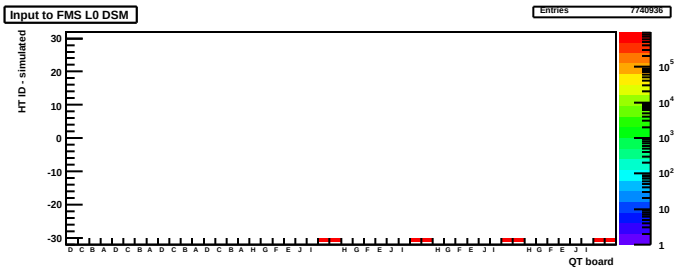

Input to FMS L0 DSM
Entries 4.644562e+07
QT8(3) sum
QT board

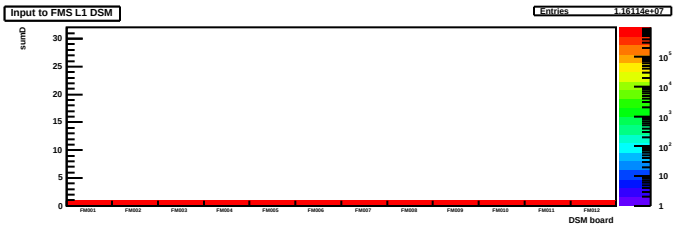

Input to FMS L0 DSM
Entries 7740936
QT8(3) sum - simulated
QT board

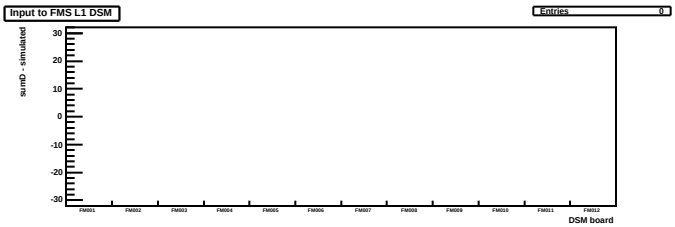

Input to FMS L0 DSM
Entries 4.644562e+07
HT ADC
QT board

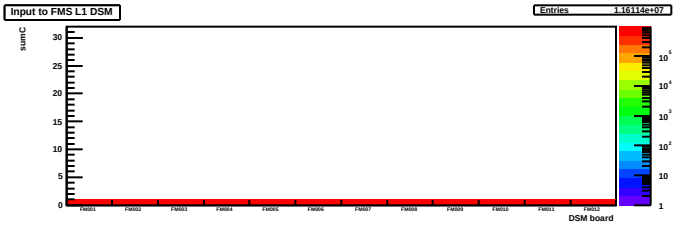

Input to FMS L0 DSM
Entries 7740936
HT ADC - simulated
QT board

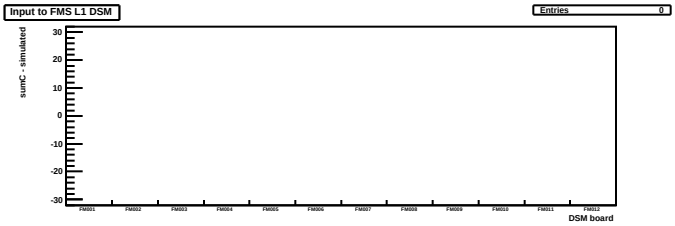

Input to FMS L0 DSM
Entries 4.644562e+07
HT ID
QT board

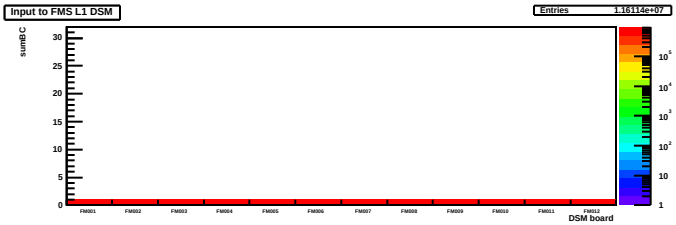

Input to FMS L0 DSM
Entries 7740936
HT ID - simulated
QT board

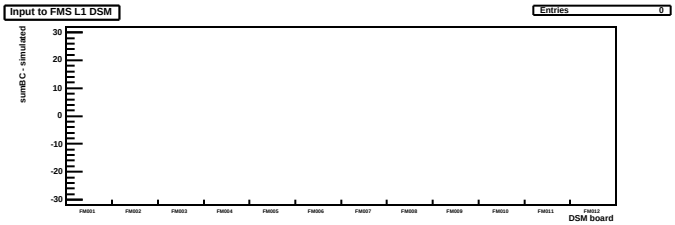

Input to FMS L1 DSM
Entries 1.16114e+07
sumD
DSM board

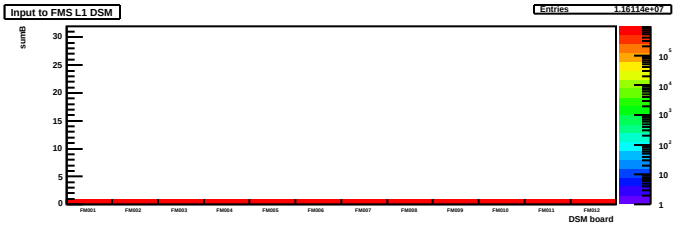

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

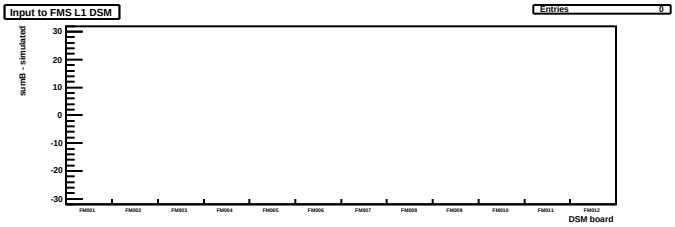

Input to FMS L1 DSM
Entries 1.16114e+07
sumC
DSM board

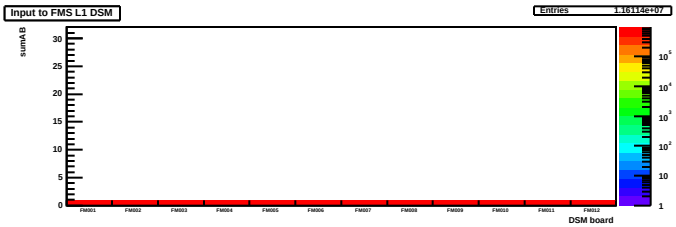

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

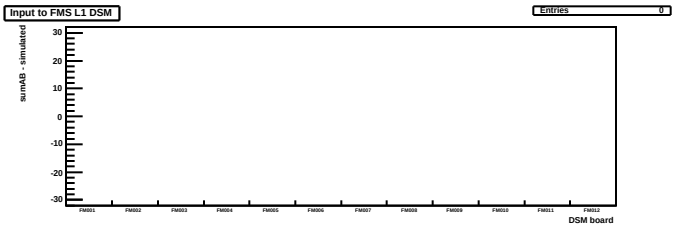

Input to FMS L1 DSM
Entries 1.16114e+07
sumBC
DSM board

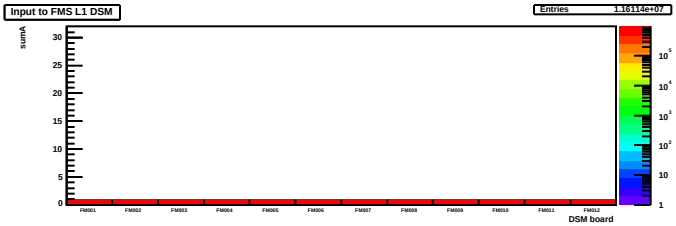

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

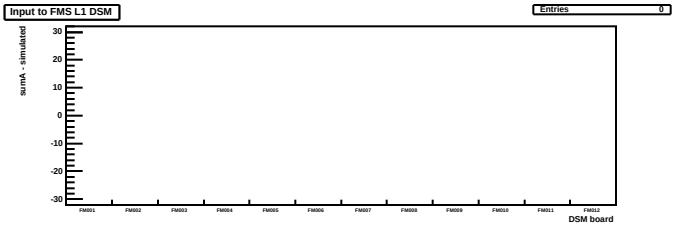

Input to FMS L1 DSM
Entries 1.16114e+07
sumB
DSM board

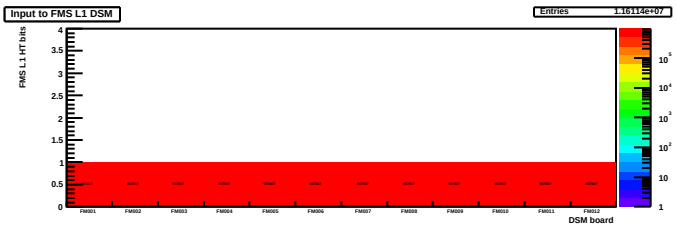

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

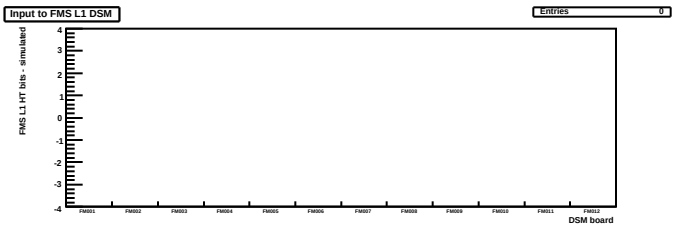

Input to FMS L1 DSM
Entries 1.16114e+07
sumAB
DSM board

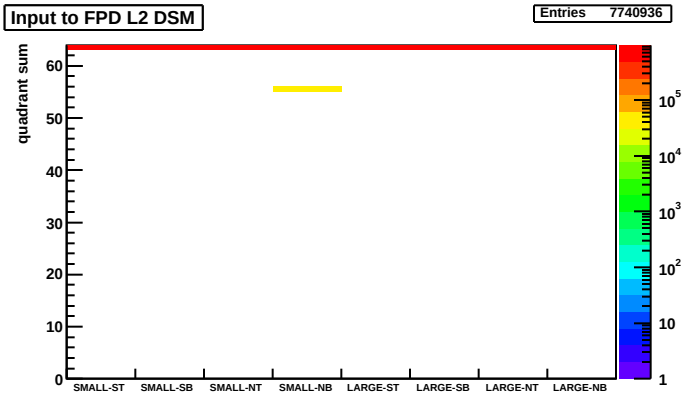

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

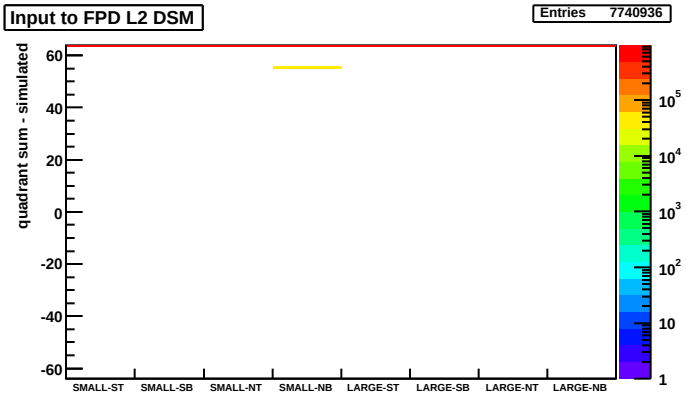

Input to FMS L1 DSM
Entries 1.16114e+07
sumA
DSM board

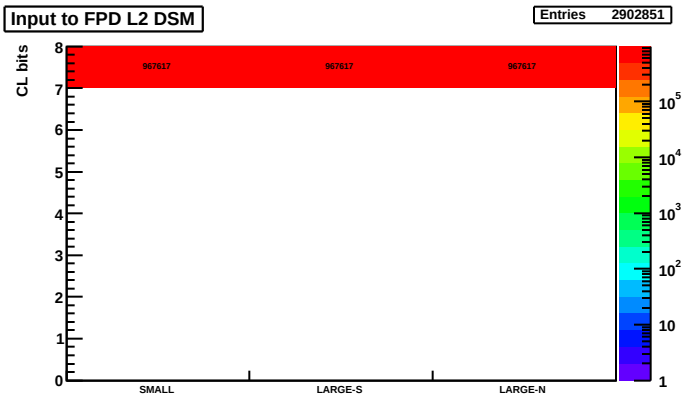

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

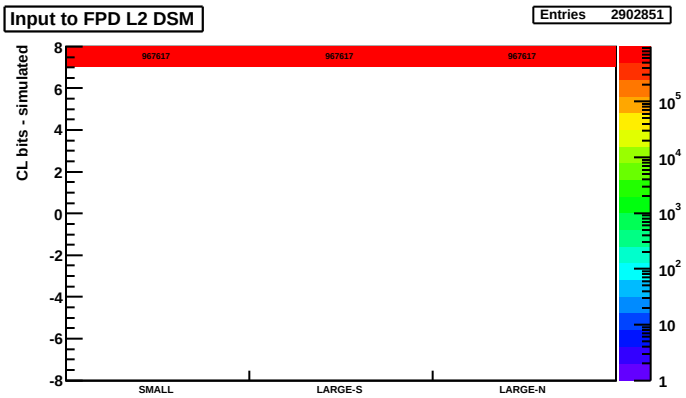

Input to FMS L1 DSM
Entries 1.16114e+07
FMS L1 HT bits
DSM board

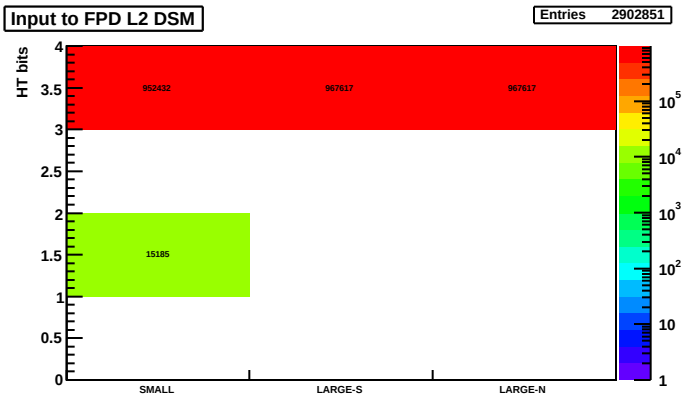

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

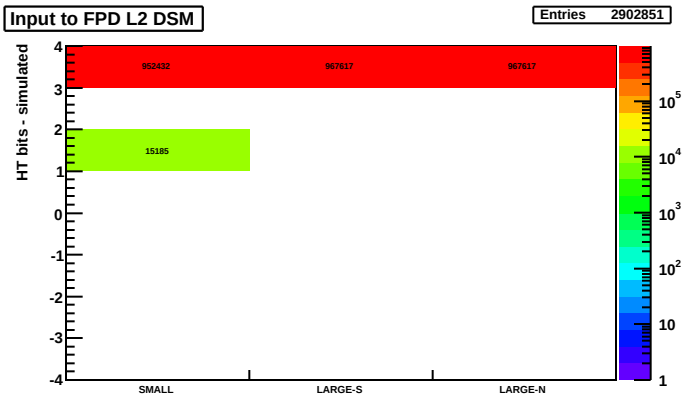

Input to FPD L2 DSM
Entries 7740936
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

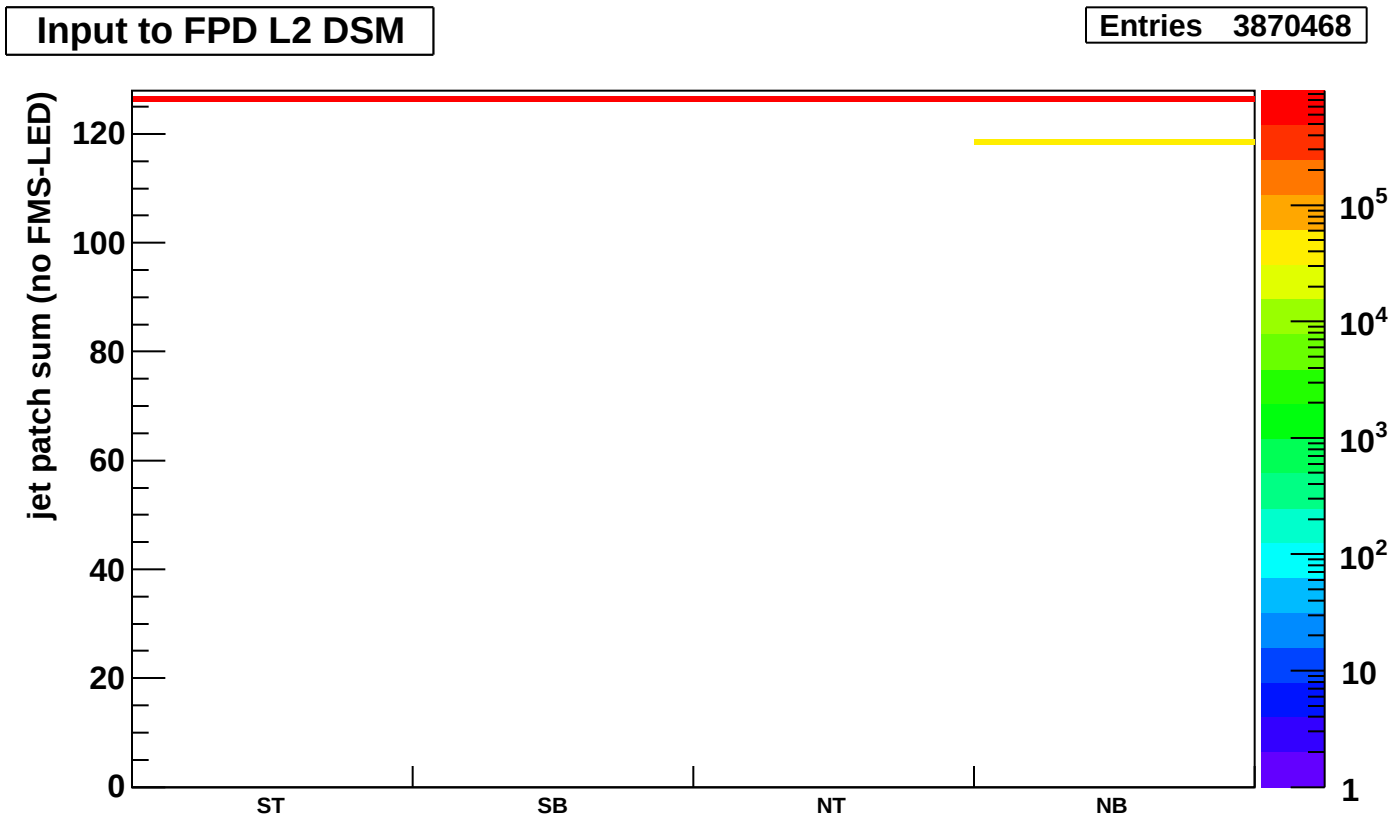

Input to FPD L2 DSM
Entries 7740936
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

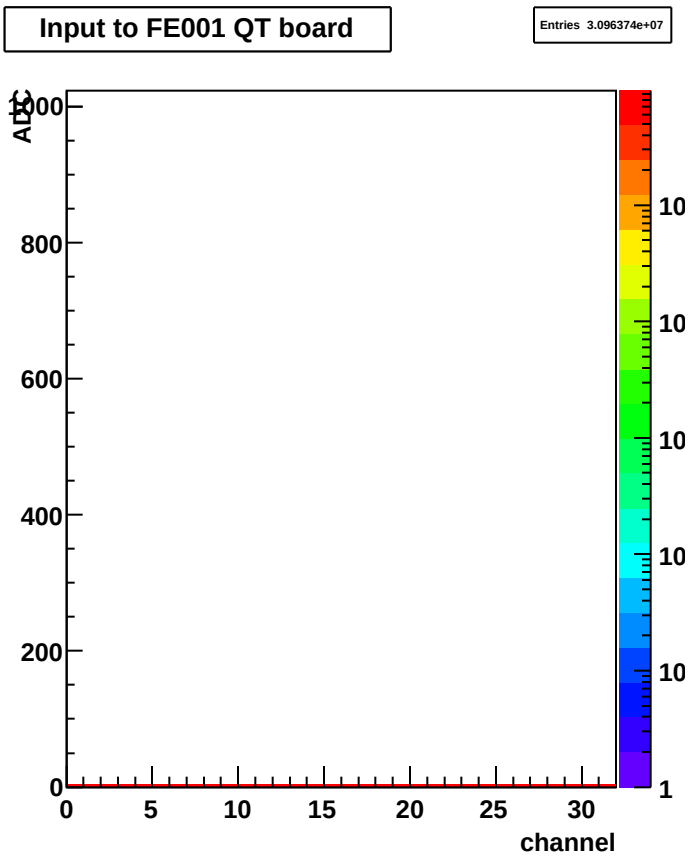

Input to FPD L2 DSM
Entries 2902851
CL bits
967617
967617
967617
SMALL
LARGE-S
LARGE-N

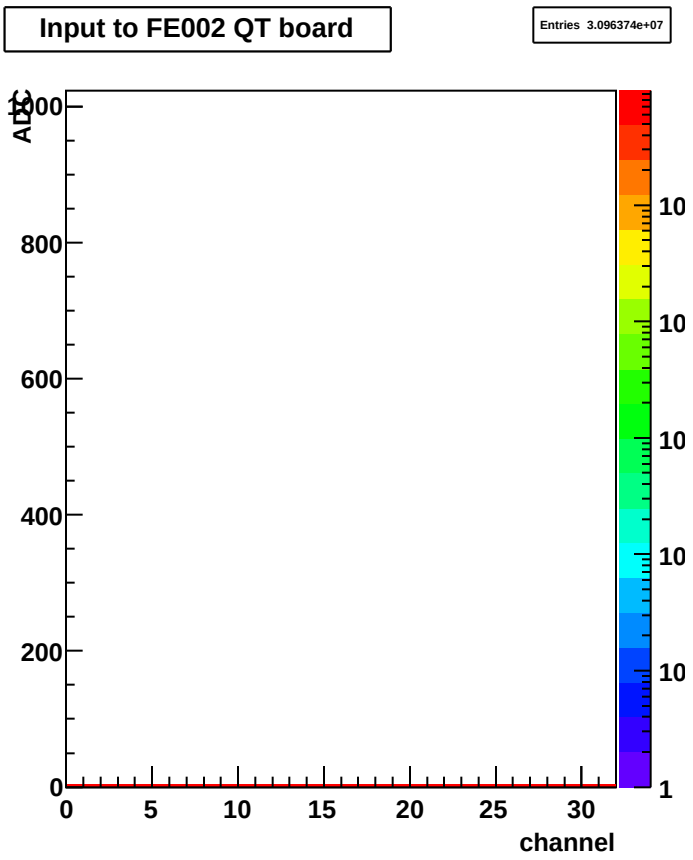

Input to FPD L2 DSM
Entries 2902851
CL bits - simulated
967617
967617
967617
SMALL
LARGE-S
LARGE-N

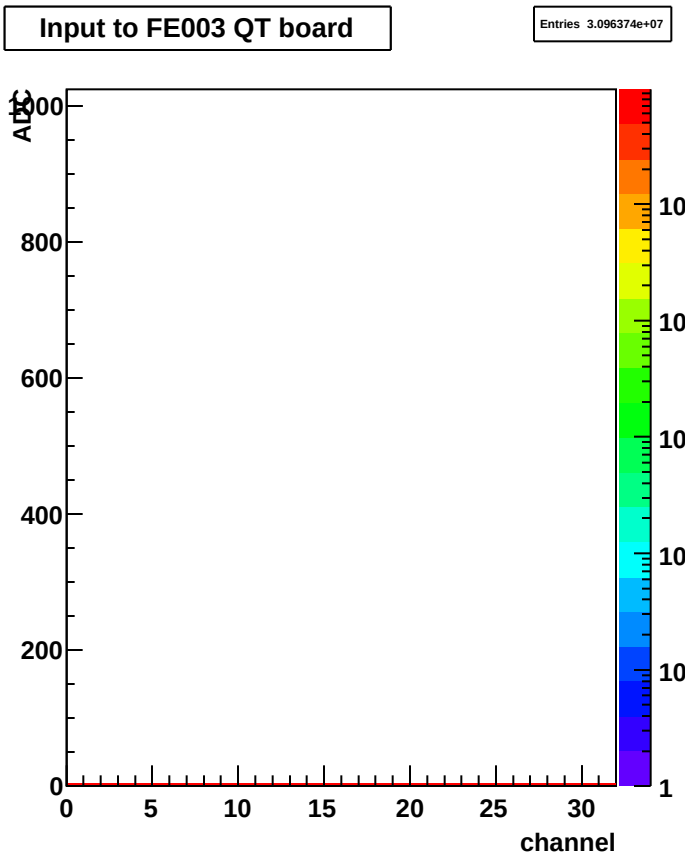

Input to FPD L2 DSM
Entries 2902851
HT bits
952432
967617
967617
15185
SMALL
LARGE-S
LARGE-N

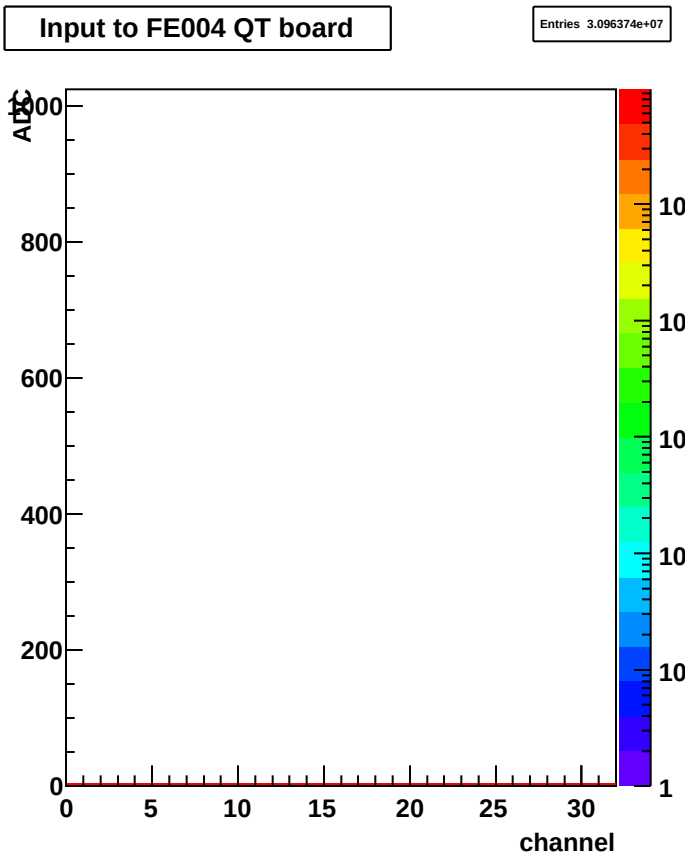

Input to FPD L2 DSM
Entries 2902851
HT bits - simulated
952432
967617
967617
15185
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3870468
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$
$10^5$

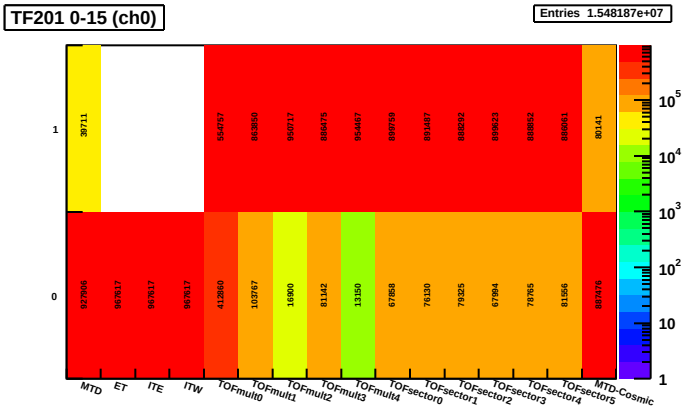

Input to FE001 QT board
Entries 3.096374e+07
ADC
channel


Input to FE002 QT board
Entries 3.096374e+07
ADC
channel

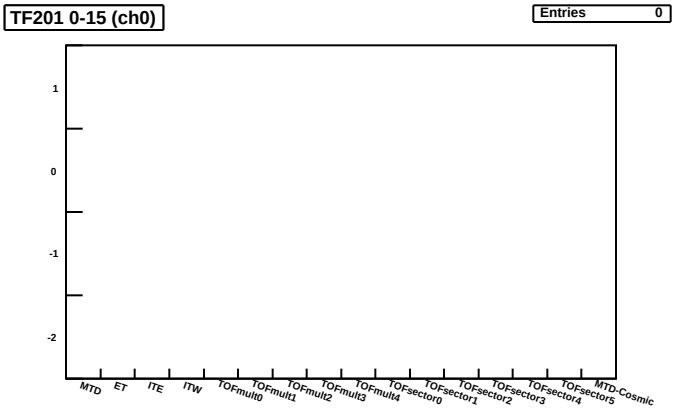

Input to FE003 QT board
Entries 3.096374e+07
ADC
channel


Input to FE004 QT board
Entries 3.096374e+07
ADC
channel

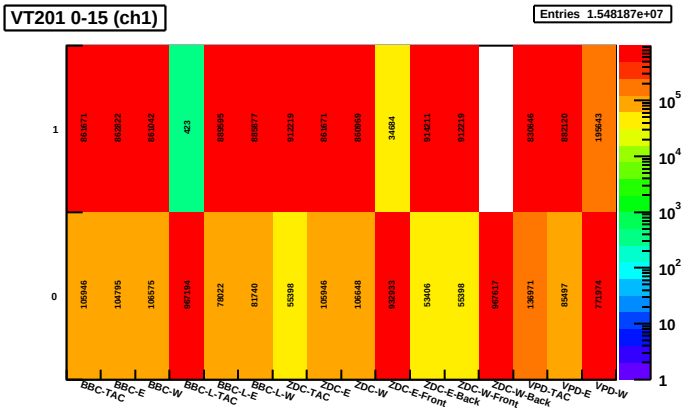
TF201 0-15 (ch0)



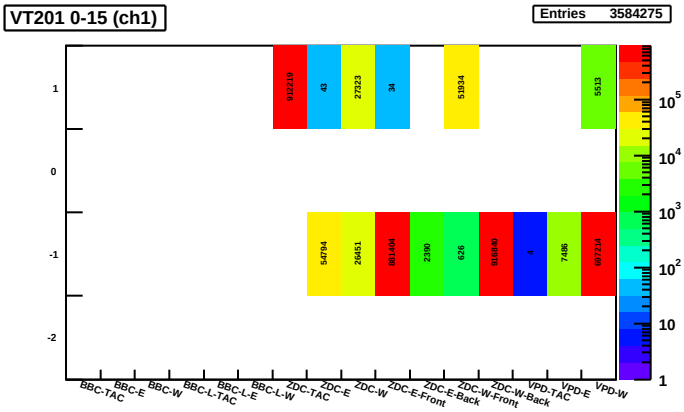

TF201 0-15 (ch0)



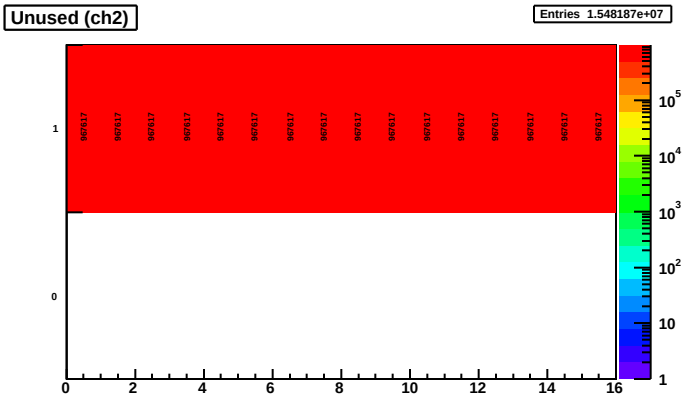

VT201 0-15 (ch1)

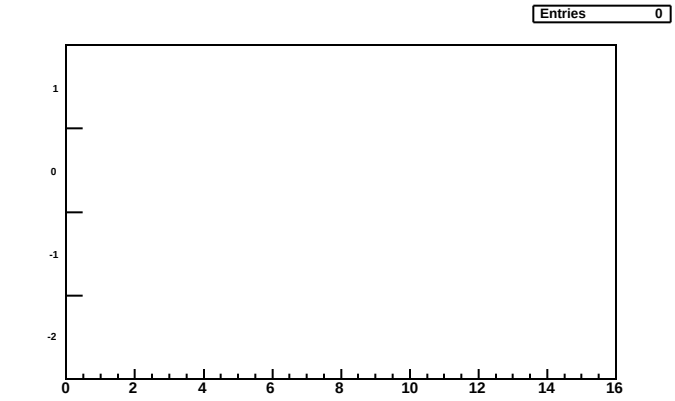



VT201 0-15 (ch1)

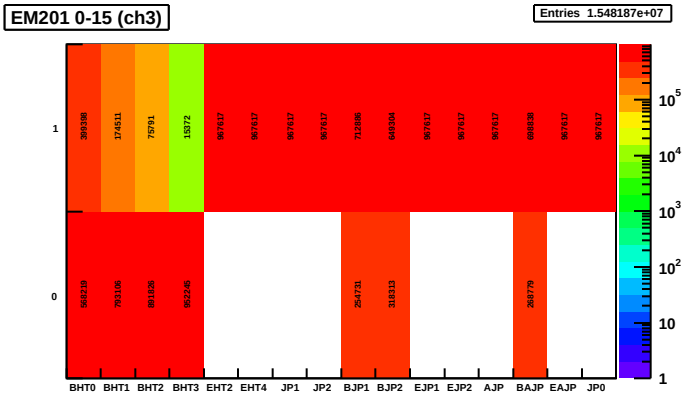



Unused (ch2)

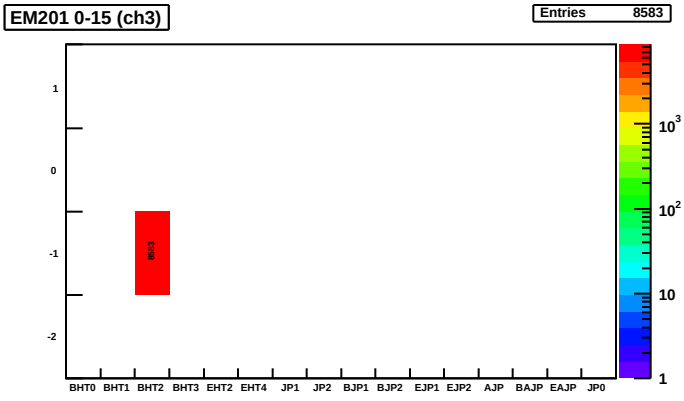


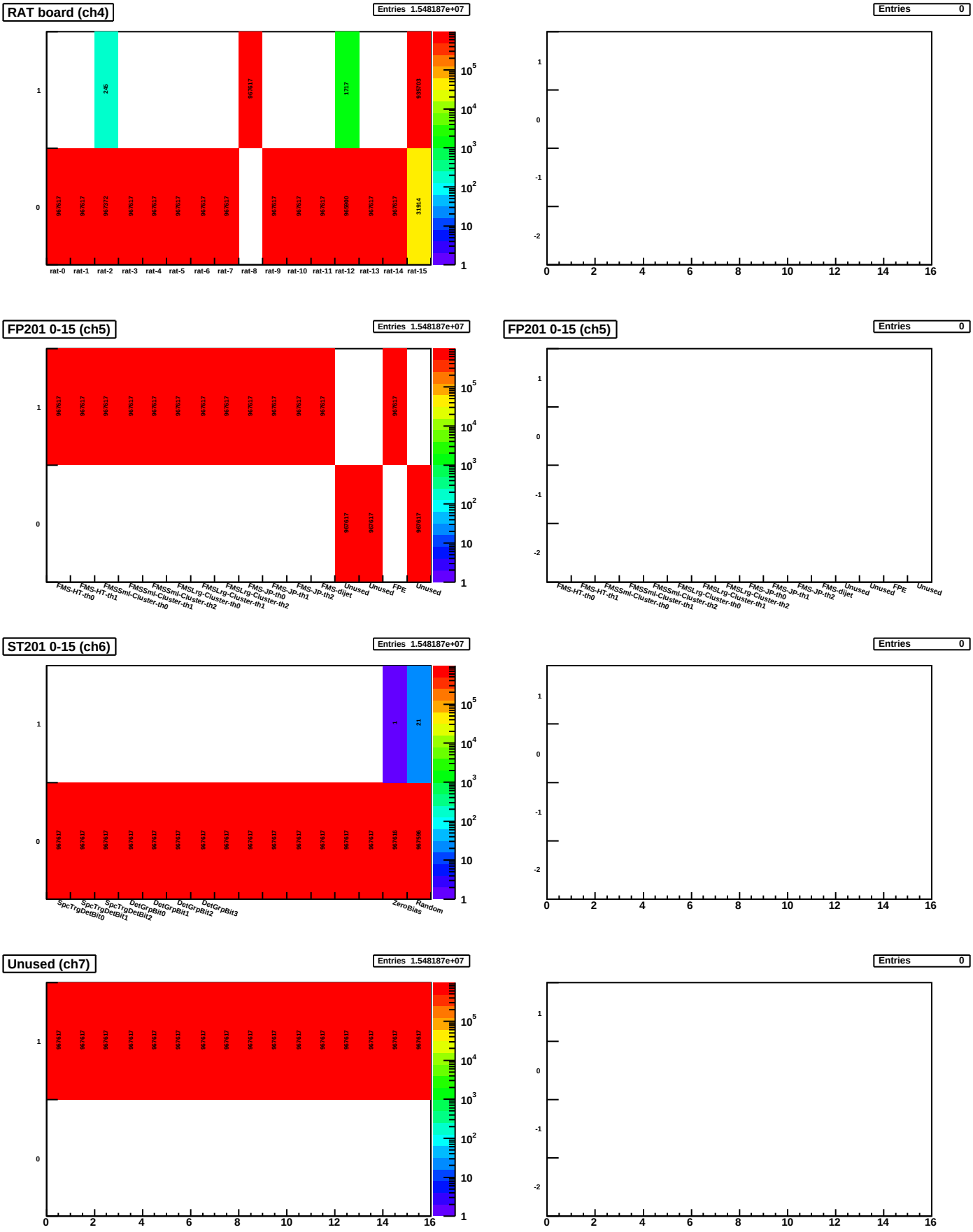




EM201 0-15 (ch3)




EM201 0-15 (ch3)




RAT board (ch4)
Entries 1.548187e+07
Entries 0
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15
FP201 0-15 (ch5)
Entries 1.548187e+07
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused
ST201 0-15 (ch6)
Entries 1.548187e+07
Entries 0
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random
Unused (ch7)
Entries 1.548187e+07
Entries 0