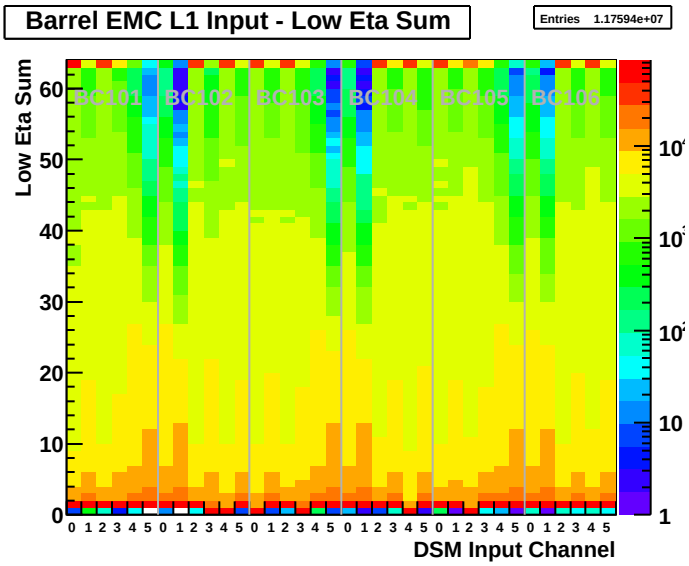
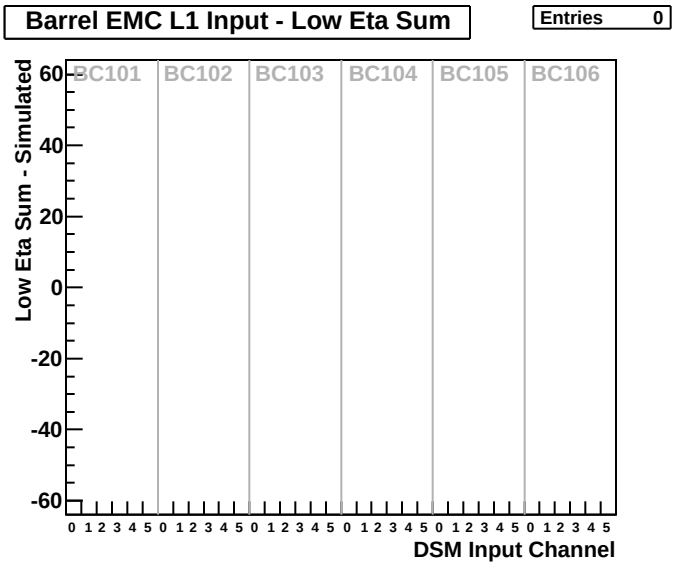
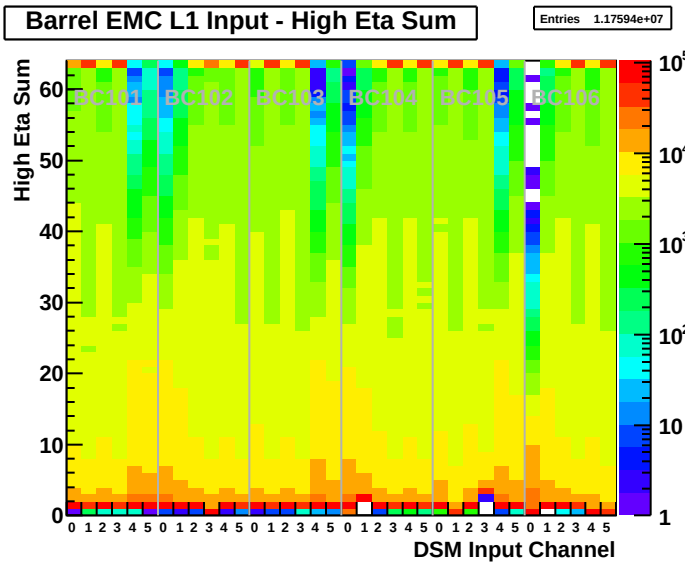
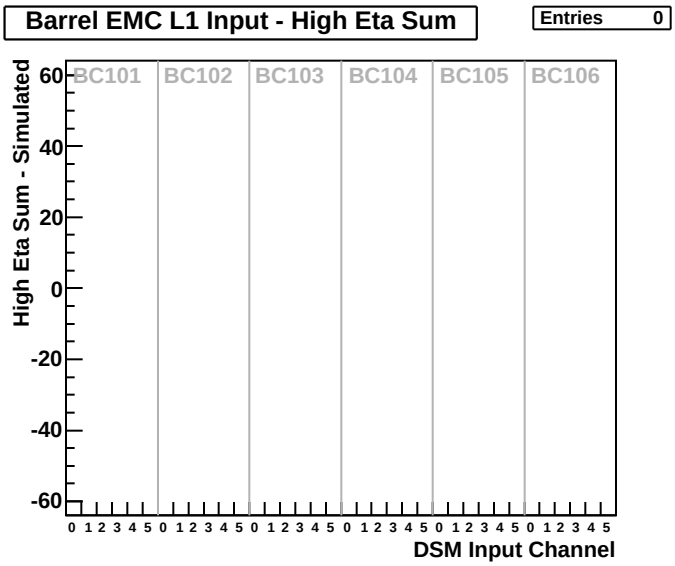
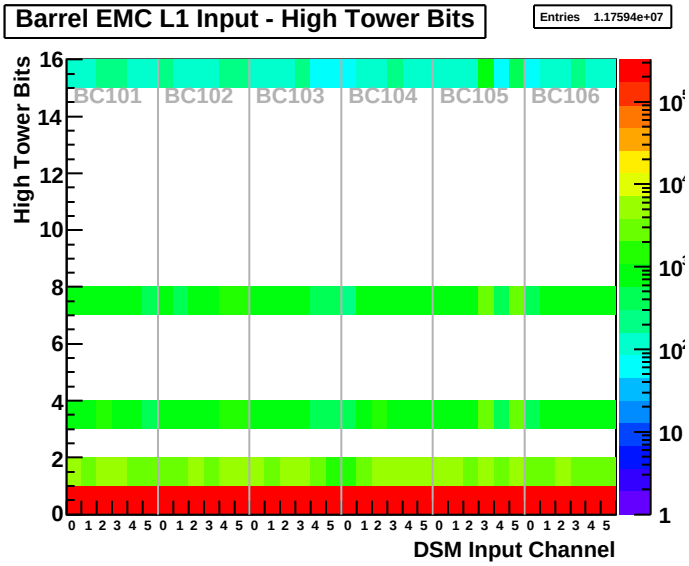
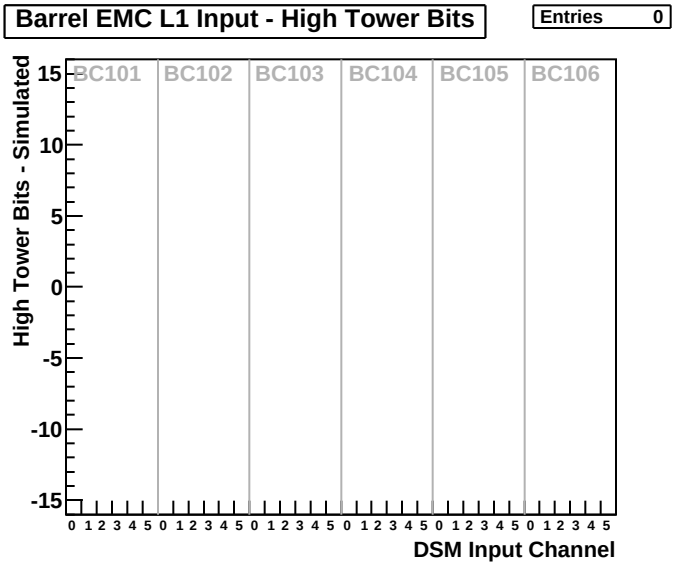

Barrel EMC L1 Input - Low Eta Sum
Entries 1.17594e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

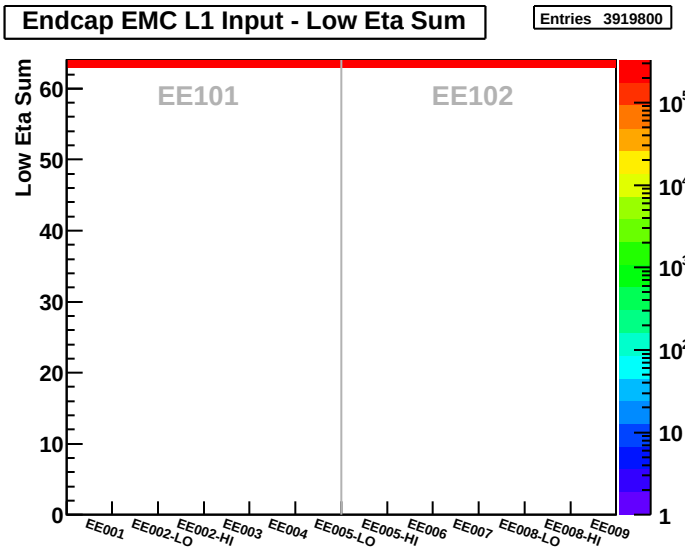

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

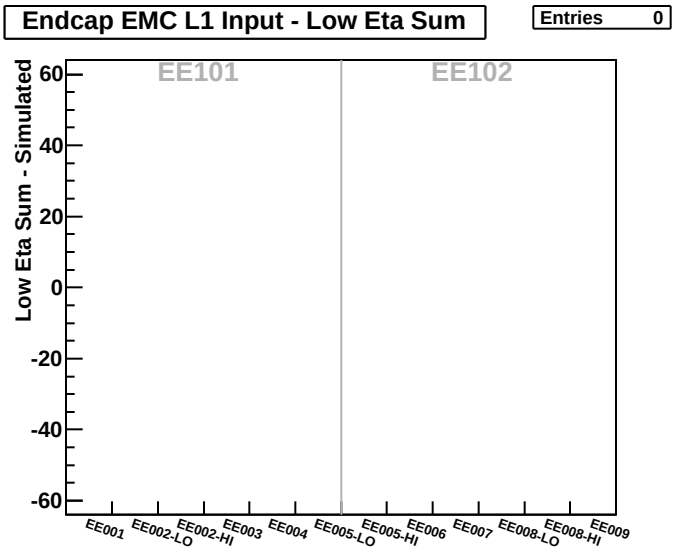

Barrel EMC L1 Input - High Eta Sum
Entries 1.17594e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

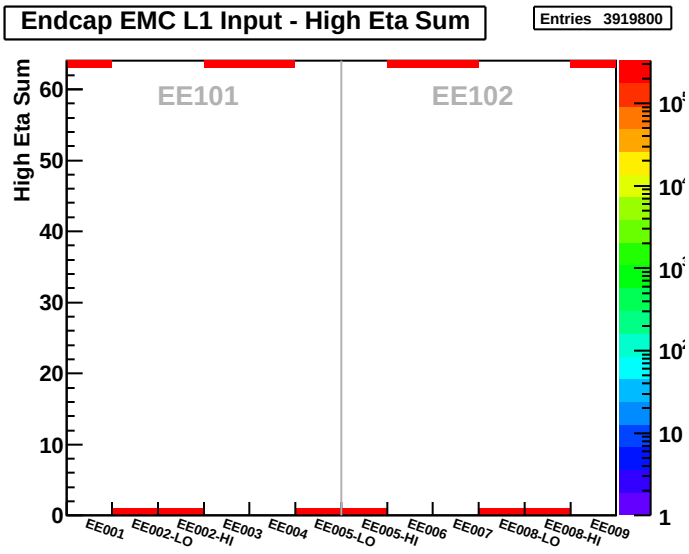

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

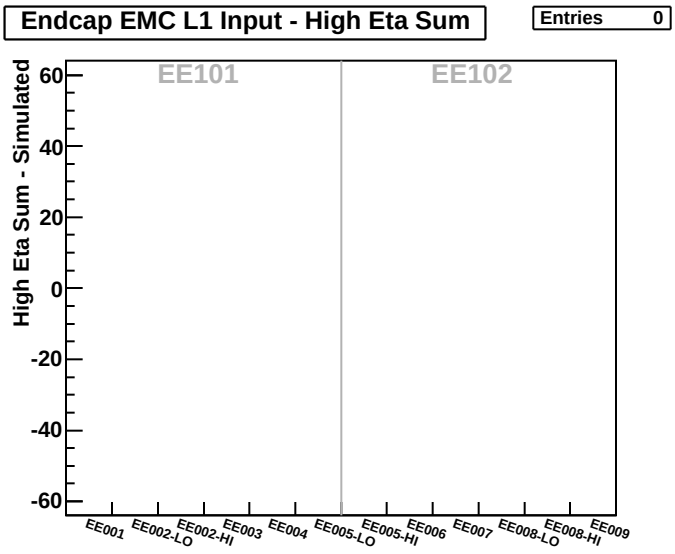

Barrel EMC L1 Input - High Tower Bits
Entries 1.17594e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

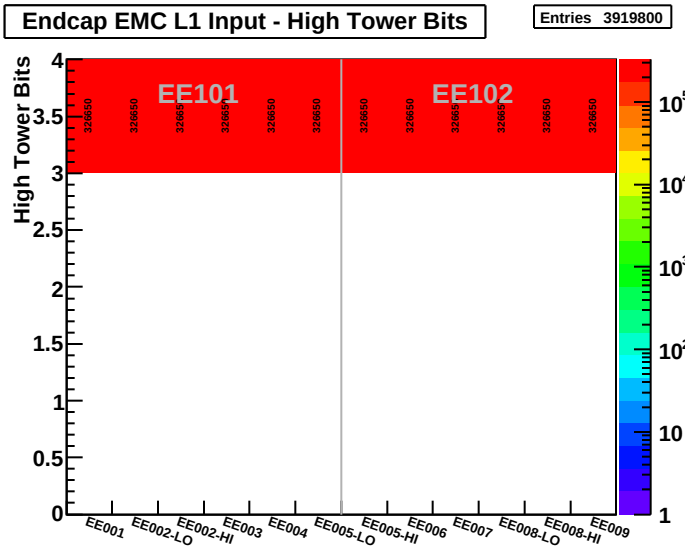

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

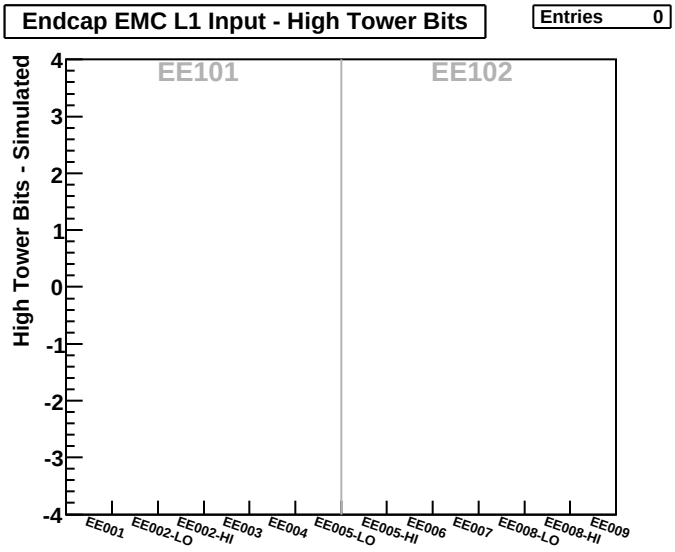

Endcap EMC L1 Input - Low Eta Sum
Entries 3919800
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

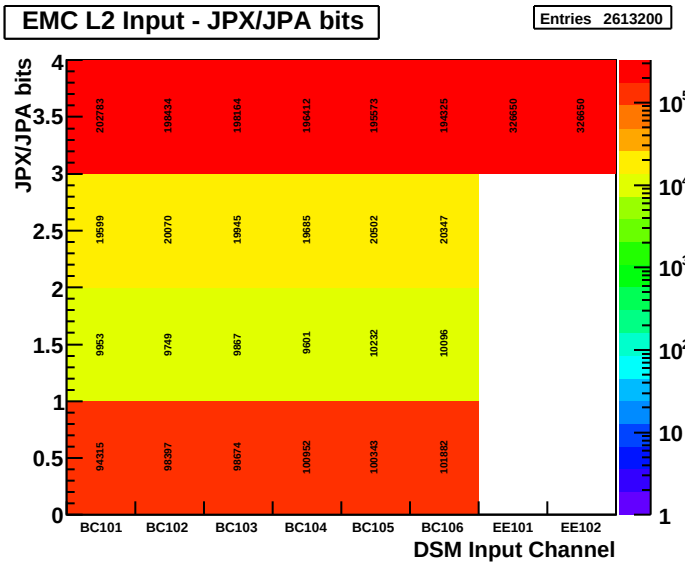

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

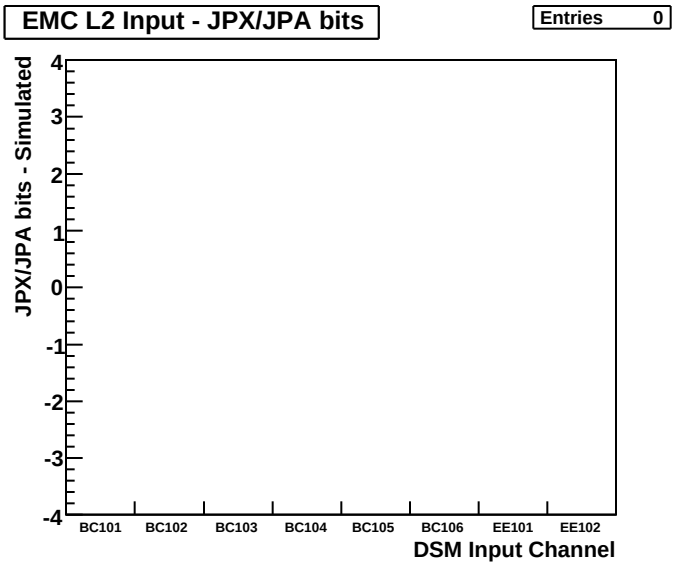

Endcap EMC L1 Input - High Eta Sum
Entries 3919800
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

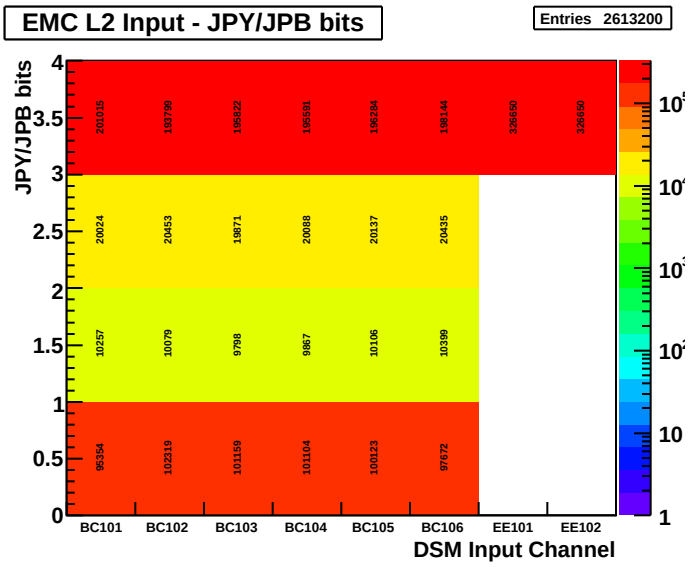

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

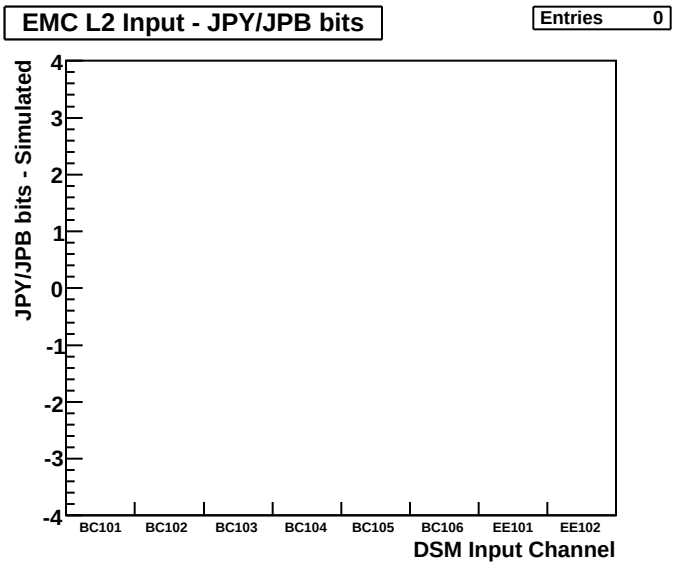

Endcap EMC L1 Input - High Tower Bits
Entries 3919800
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

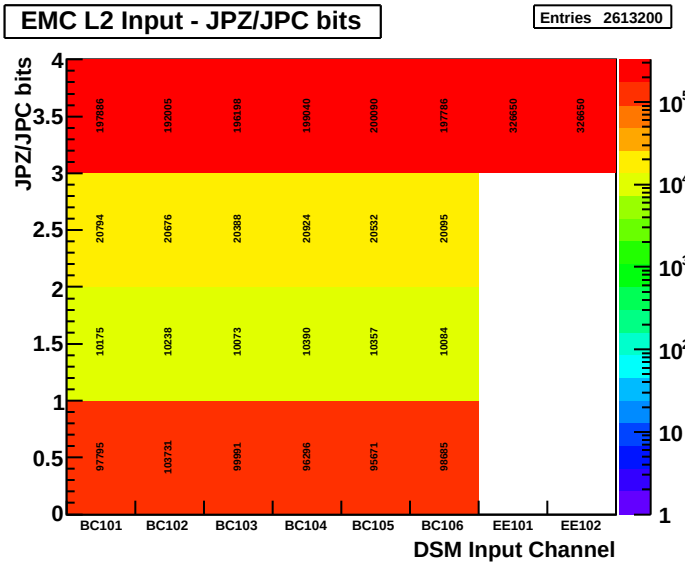

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

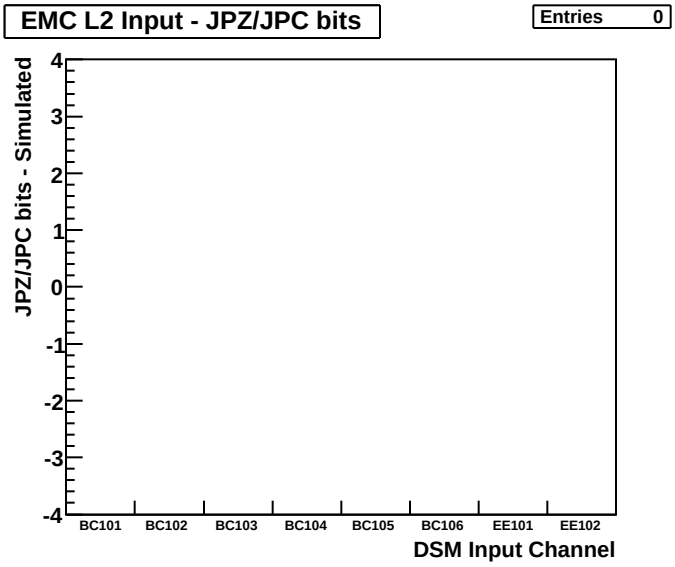

EMC L2 Input - JPX/JPA bits
Entries 2613200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
202783 198434 198164 196412 195573 194325 326650 326650
19599 20070 19945 19685 20502 20347
9953 9749 9867 9601 10232 10096
94315 98397 98674 100952 100343 101882

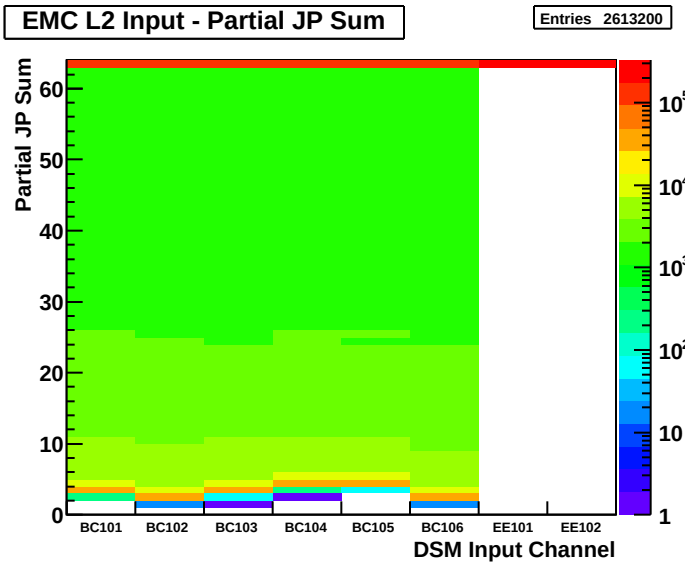

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

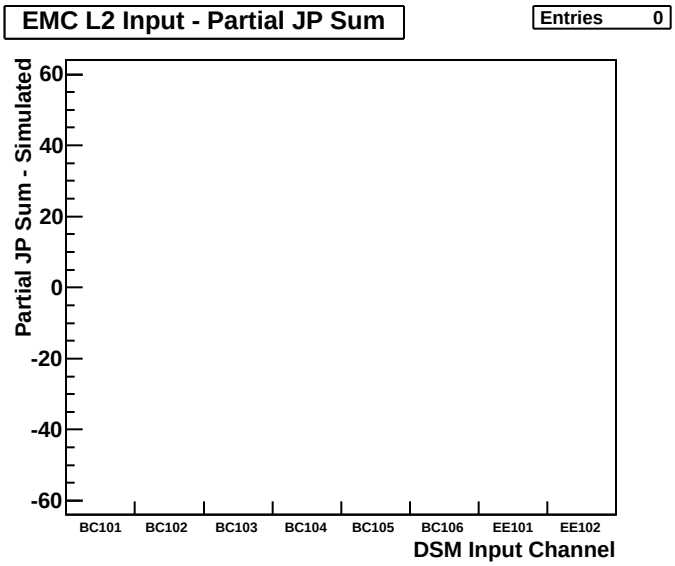

EMC L2 Input - JPY/JPB bits
Entries 2613200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
201015 193799 195822 195591 196284 198144 326650 326650
20024 20453 19871 20088 20137 20435
10257 10079 9798 9867 10106 10399
95354 102319 101159 101104 100123 97672

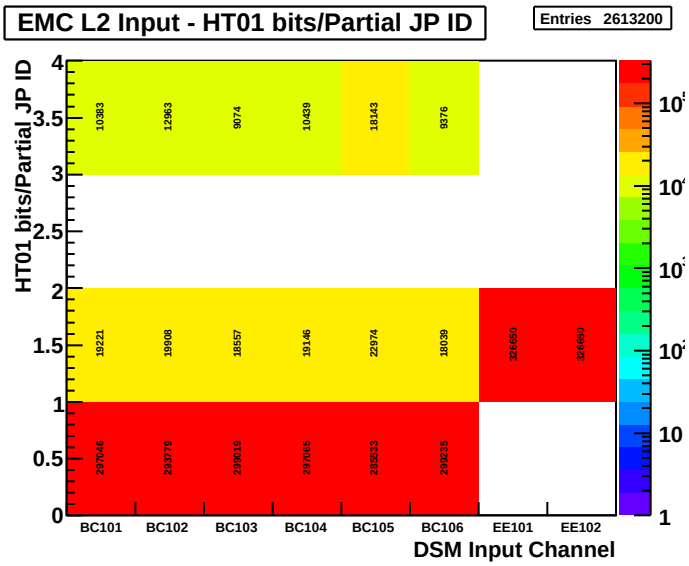

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

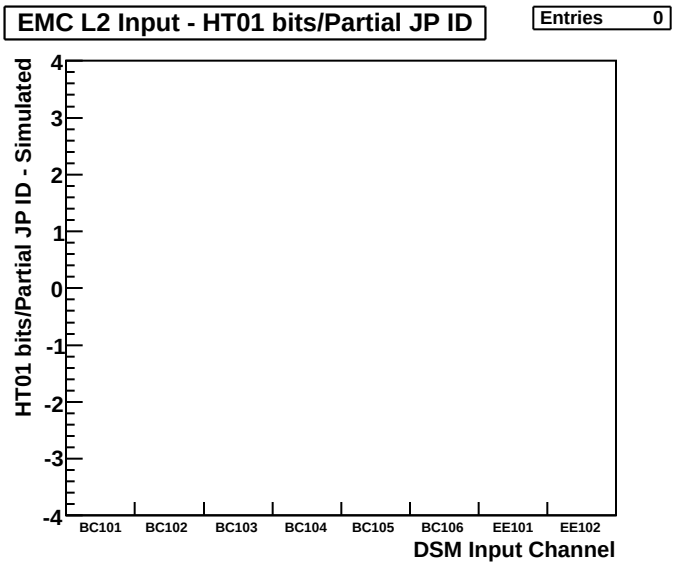

EMC L2 Input - JPZ/JPC bits
Entries 2613200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
197686 193005 196188 190040 200090 197786 326650 326650
20794 20676 20388 20924 20532 20095
10175 10238 10073 10390 10357 10084
97795 103731 99991 96296 95671 98685

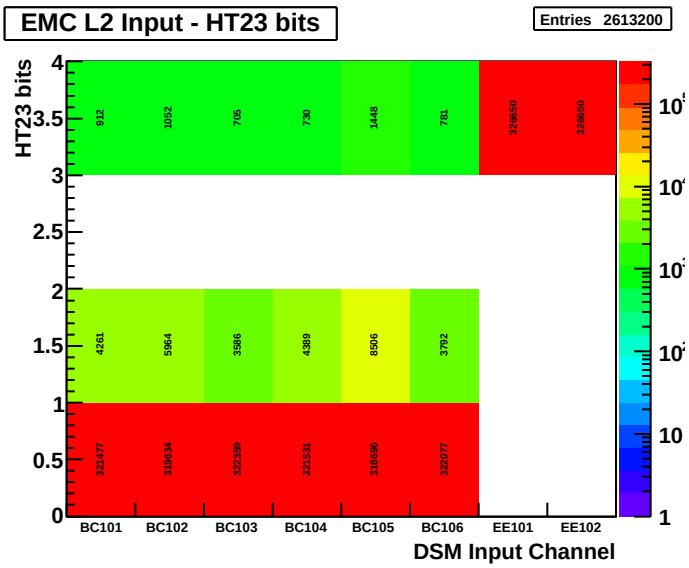

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

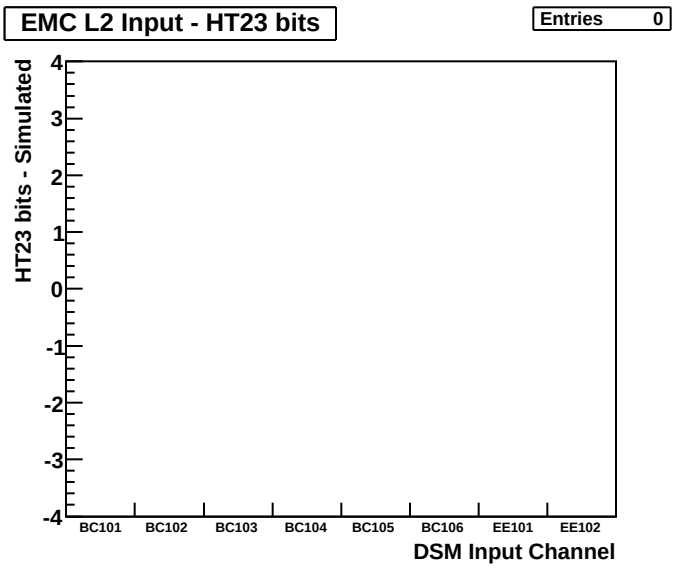

EMC L2 Input - Partial JP Sum
Entries 2613200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

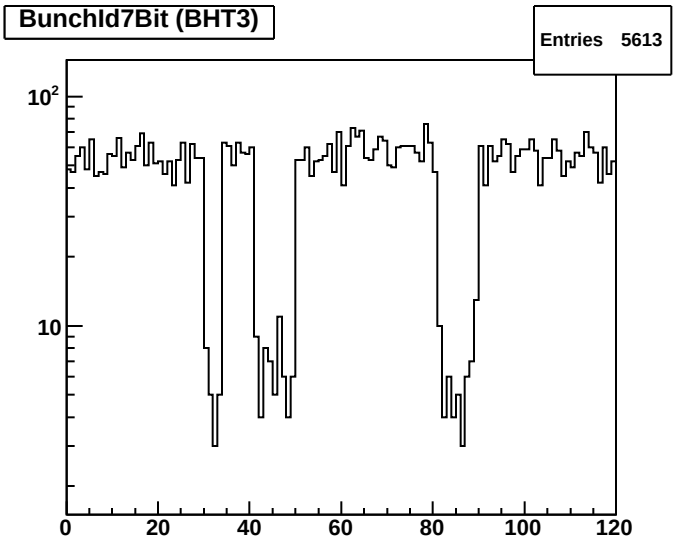

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

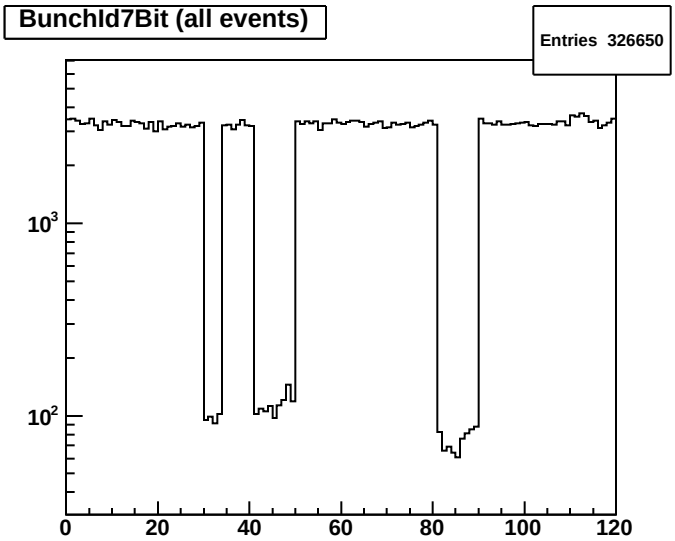

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2613200
HT01 bits/Partial JP ID
10383 12963 9074 10439 18143 9376
19221 19908 18557 19146 22974 18039 326650 326650
297046 293779 299019 297065 285533 299235
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

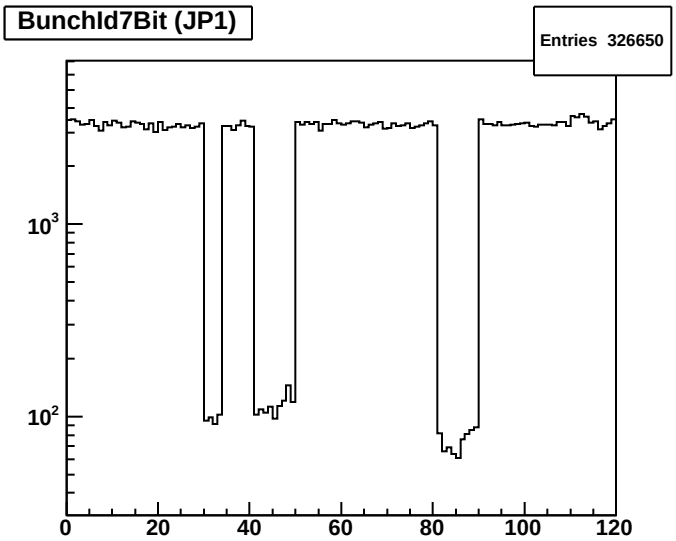

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

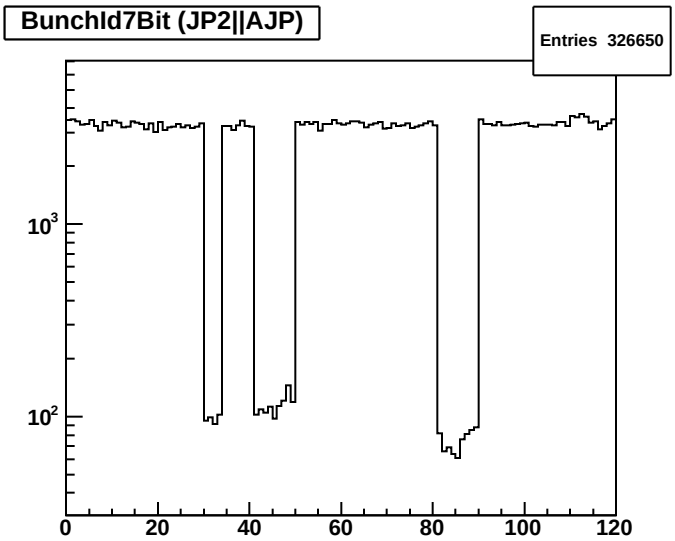

EMC L2 Input - HT23 bits
Entries 2613200
HT23 bits
912 1052 705 730 1448 781 326650 326650
4261 5964 3586 4389 8506 3792
321477 319534 322359 321531 316696 322077
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

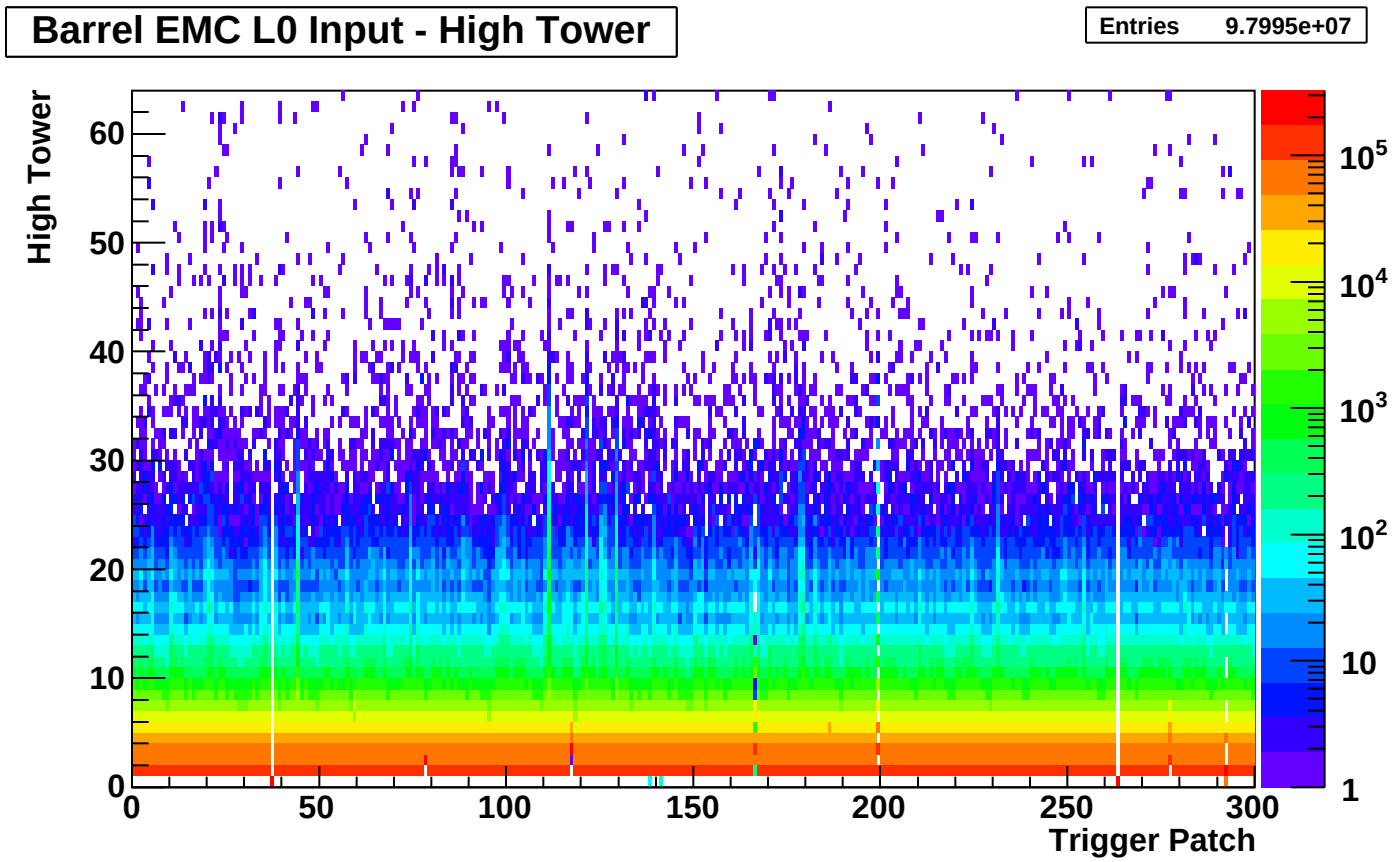

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

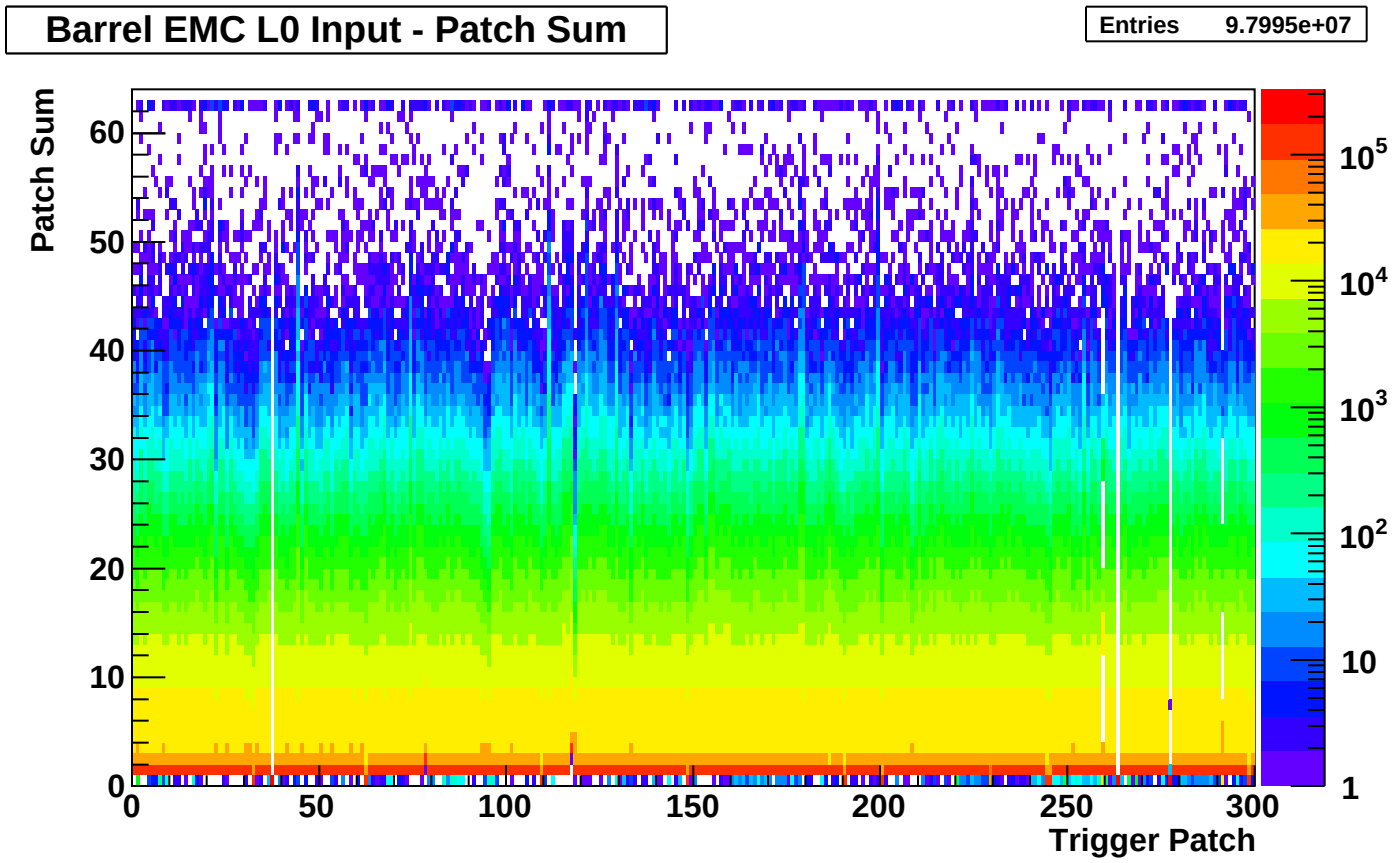

BunchId7Bit (BHT3)
Entries 5613
10^2
10
0
20
40
60
80
100
120

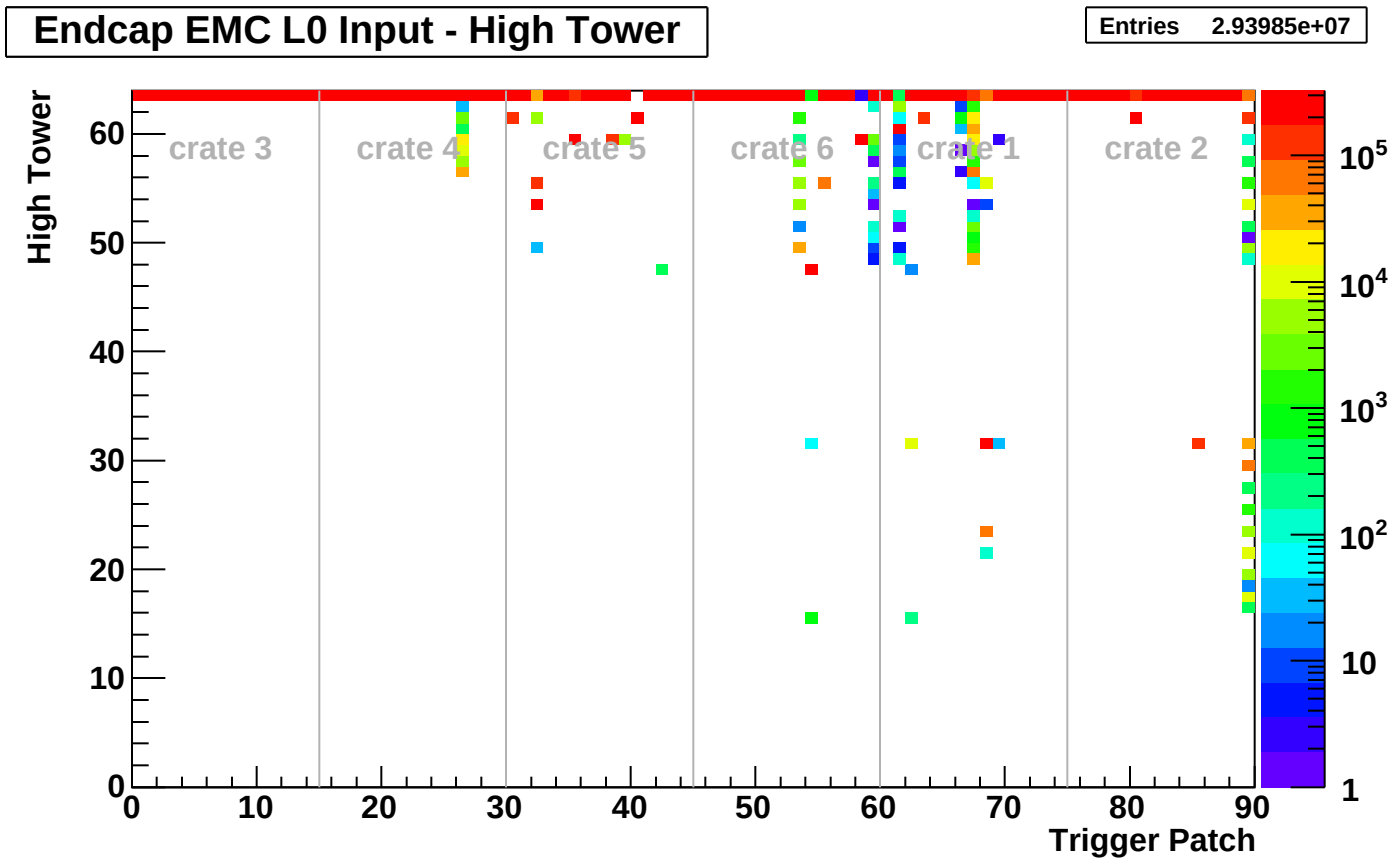

BunchId7Bit (all events)
Entries 326650
10^3
10^2
0
20
40
60
80
100
120

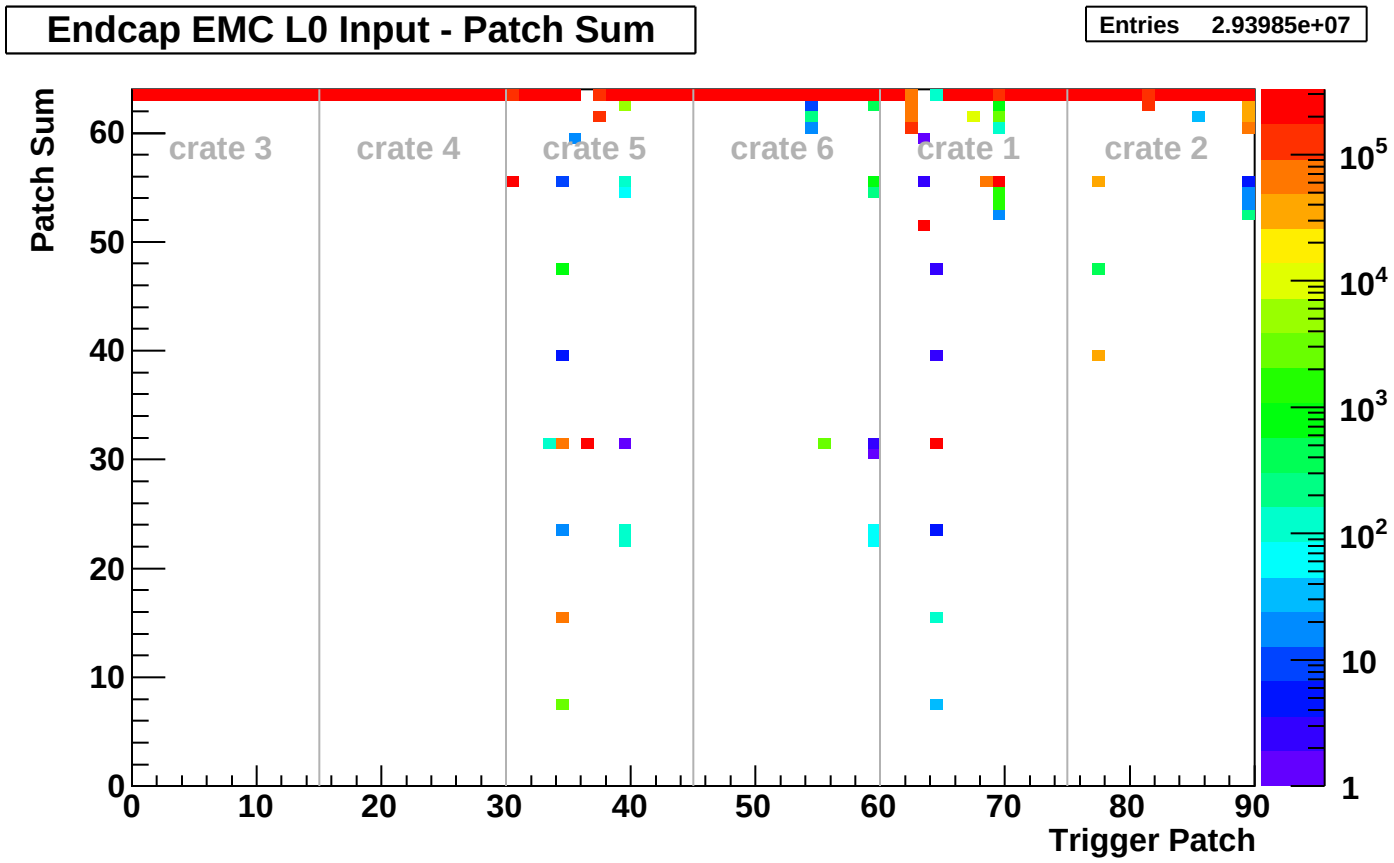

BunchId7Bit (JP1)
Entries 326650
10^3
10^2
0
20
40
60
80
100
120

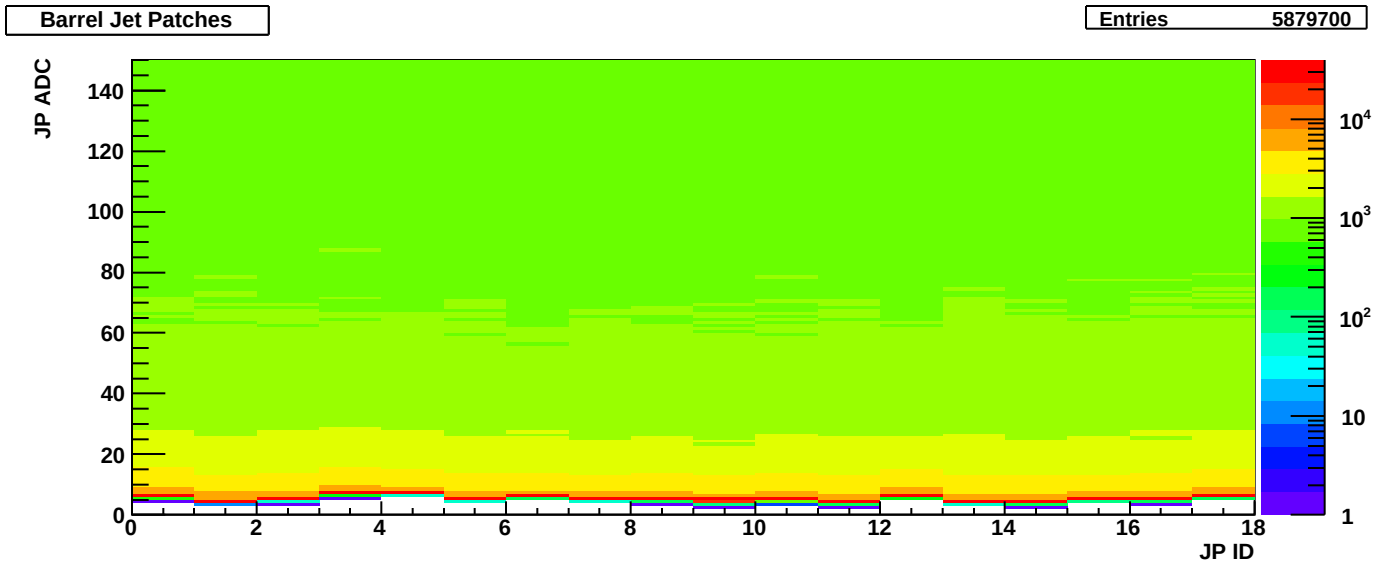

BunchId7Bit (JP2||AJP)
Entries 326650
10^3
10^2
0
20
40
60
80
100
120

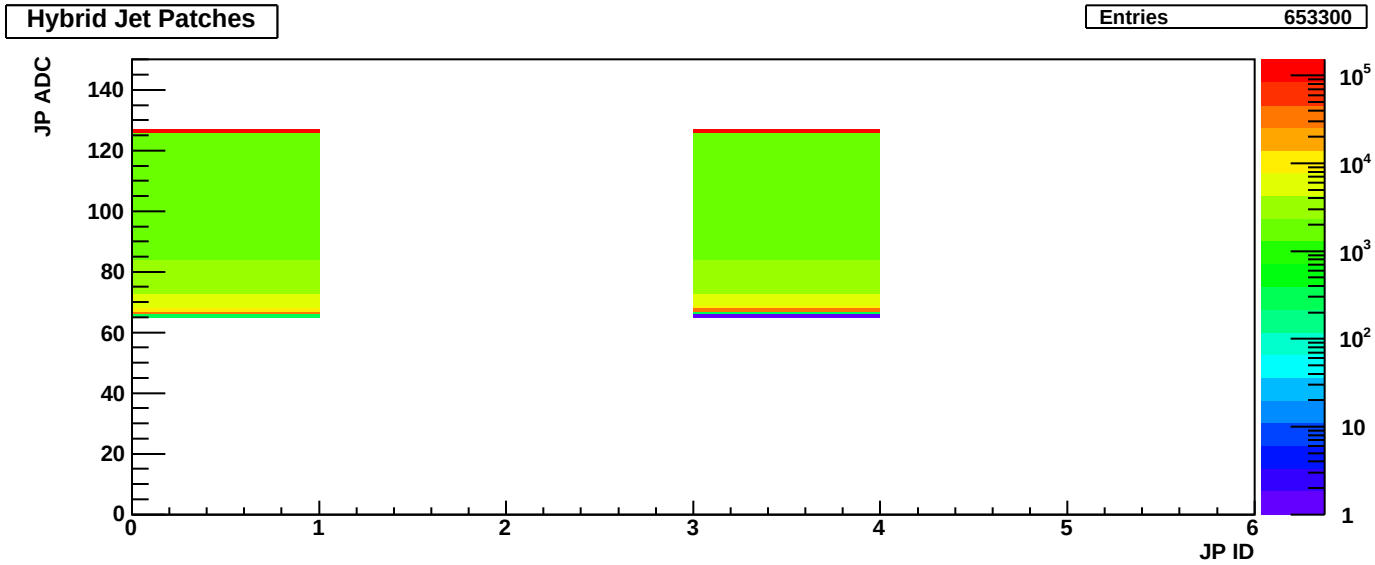

Barrel EMC L0 Input - High Tower
Entries 9.7995e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

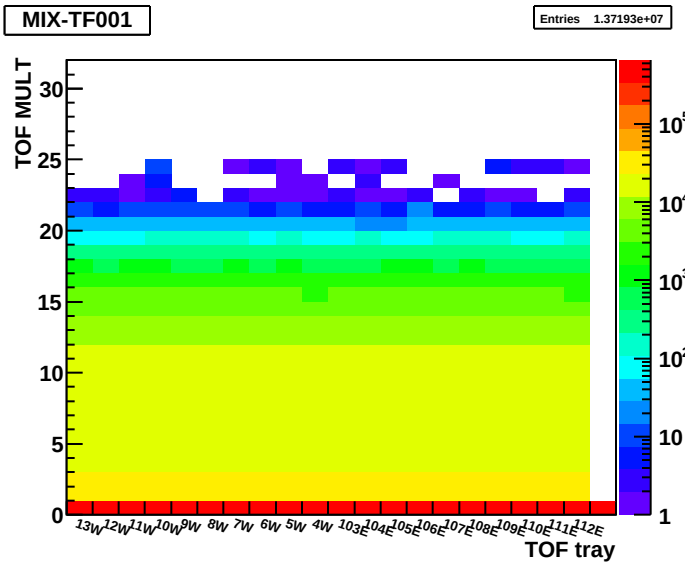

Barrel EMC L0 Input - Patch Sum
Entries 9.7995e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

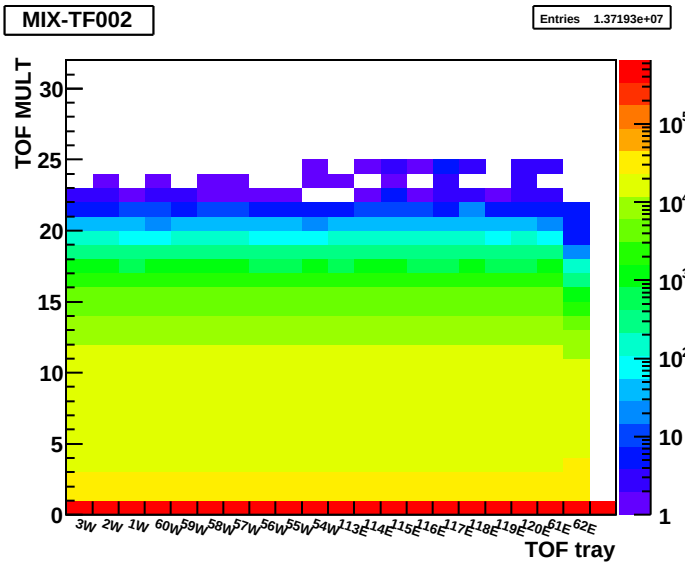

Endcap EMC L0 Input - High Tower
Entries 2.93985e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

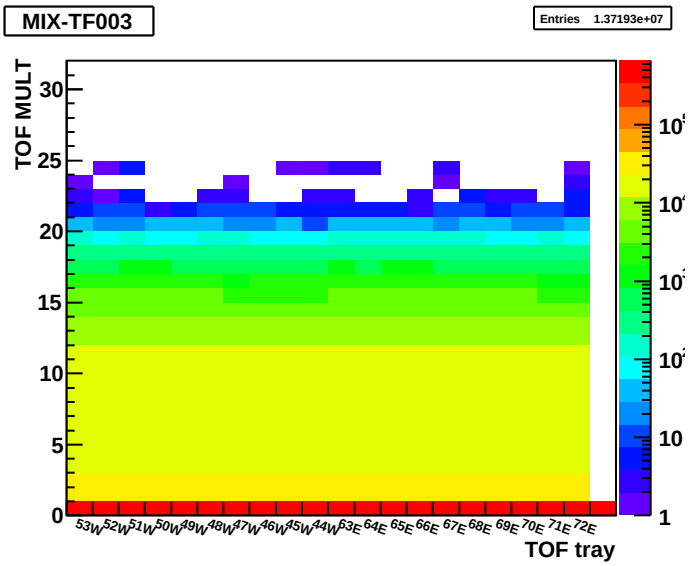

Endcap EMC L0 Input - Patch Sum
Entries 2.93985e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

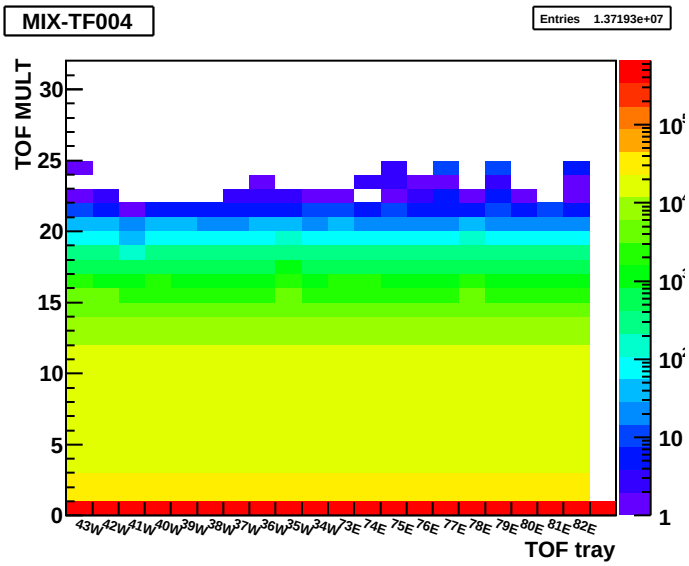

Barrel Jet Patches
Entries 5879700
JP ADC
JP ID

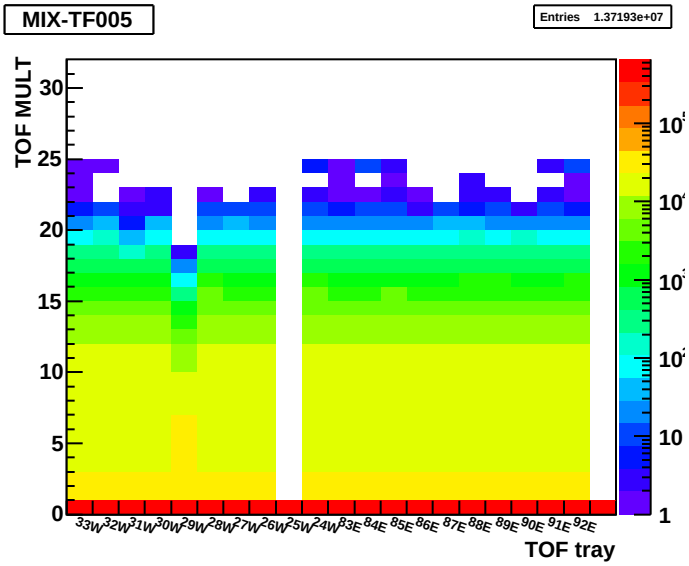

Hybrid Jet Patches
Entries 653300
JP ADC
JP ID

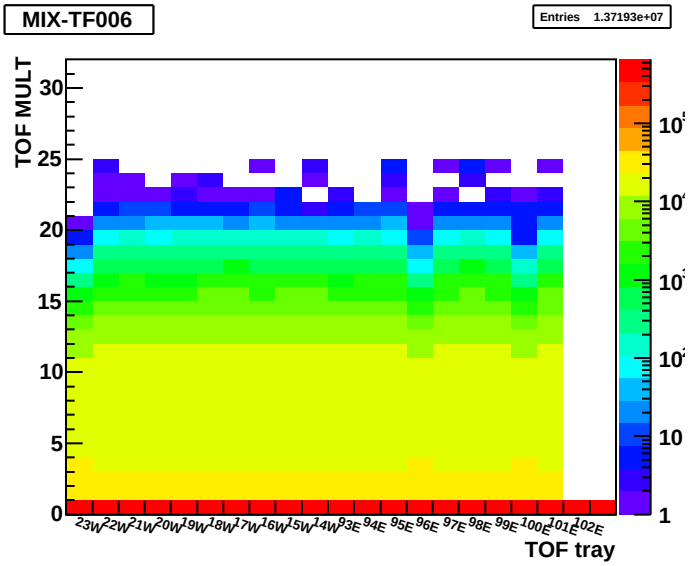

MIX-TF001
Entries 1.37193e+07
TOF MULT
TOF tray

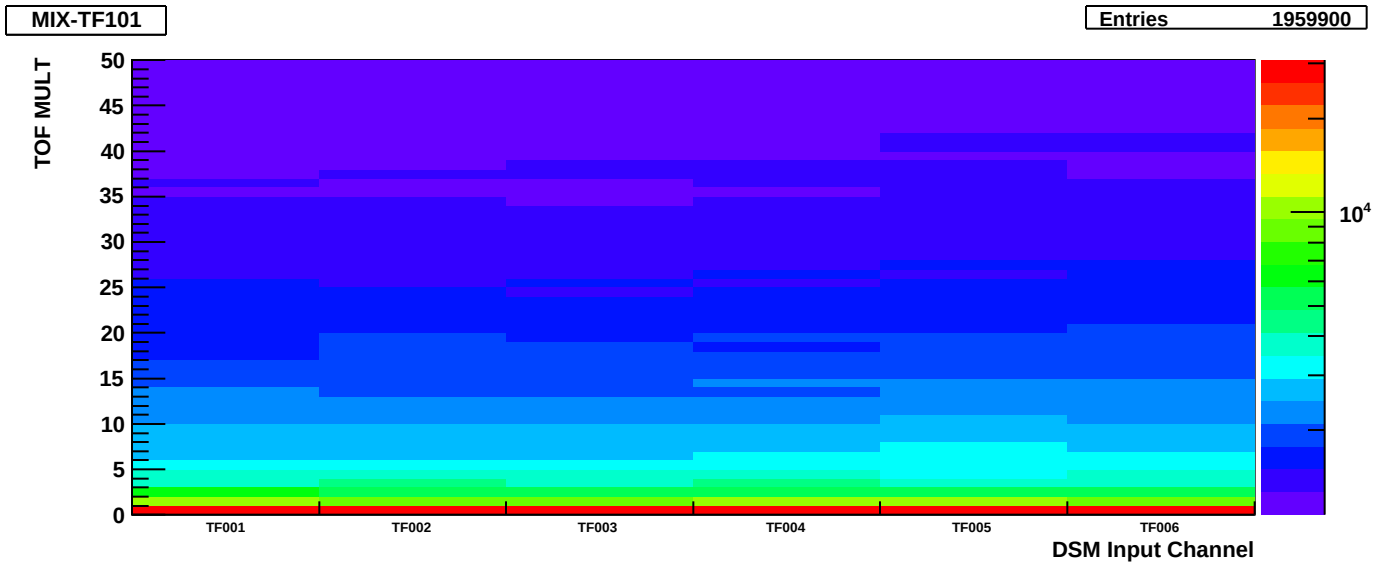

MIX-TF002
Entries 1.37193e+07
TOF MULT
TOF tray

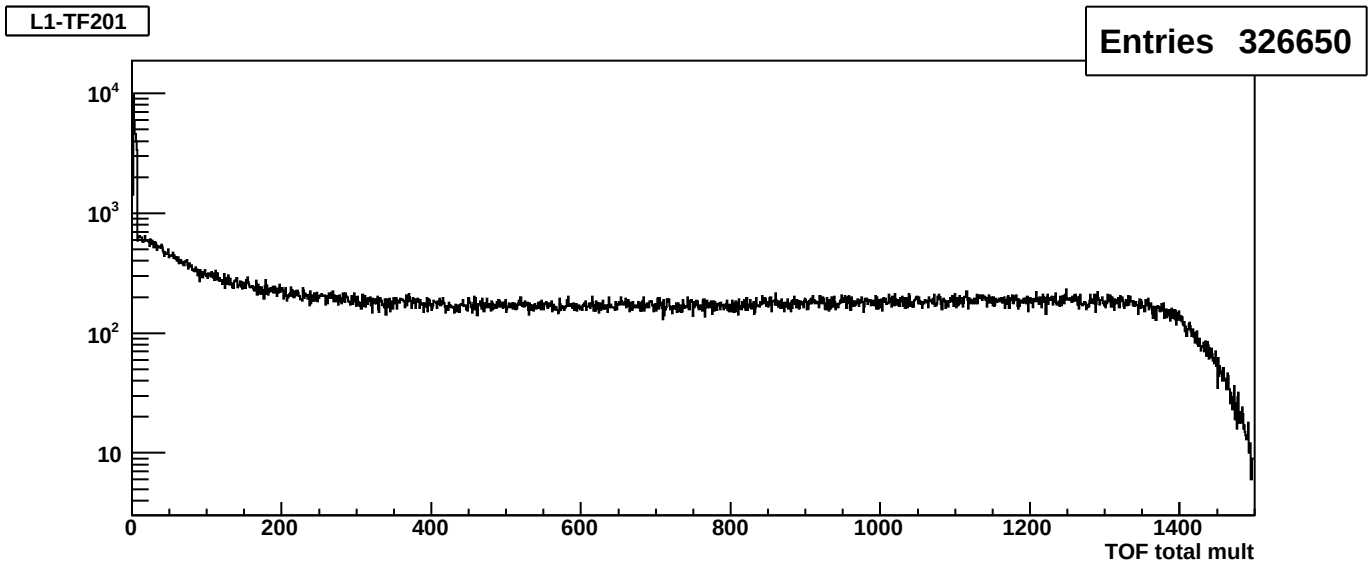

MIX-TF003
Entries 1.37193e+07
TOF MULT
TOF tray

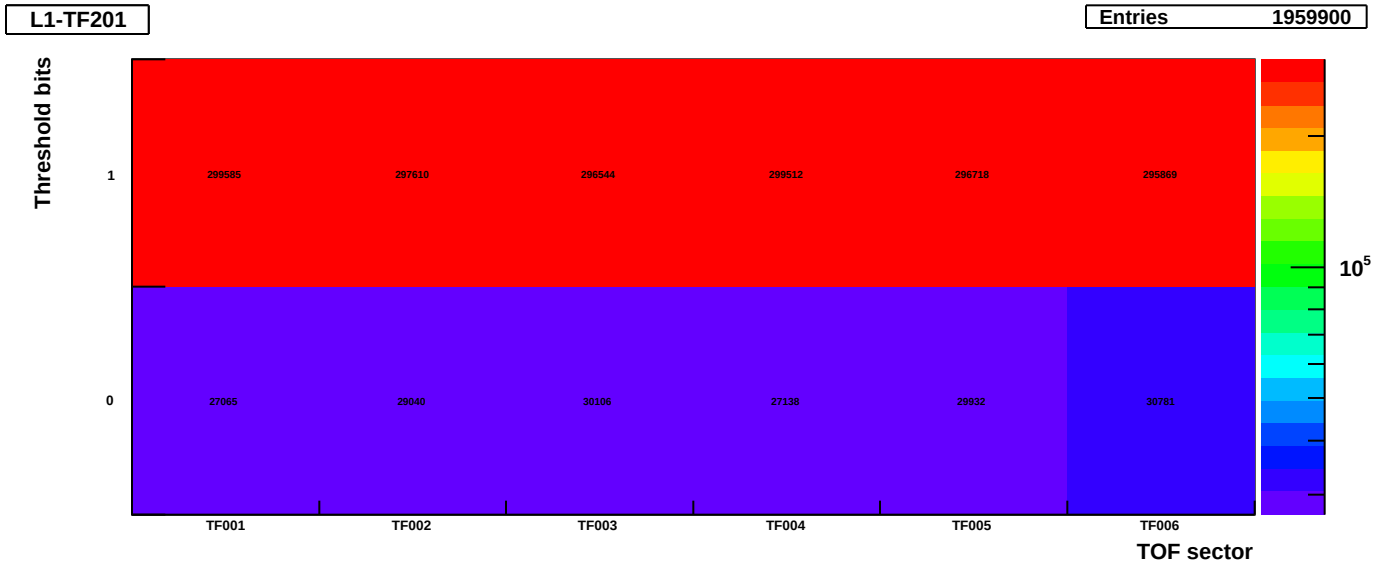

MIX-TF004
Entries 1.37193e+07
TOF MULT
TOF tray

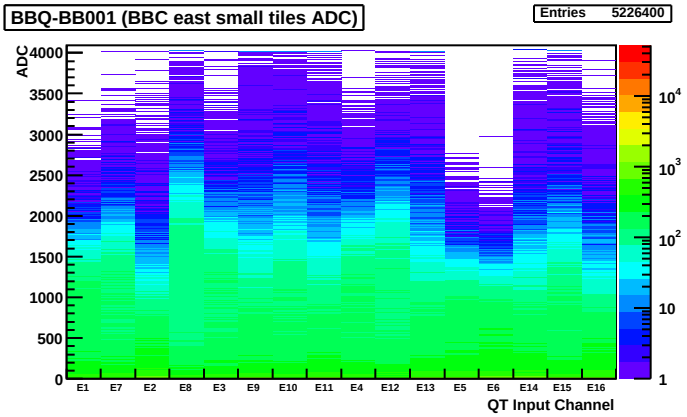

MIX-TF005
Entries 1.37193e+07
TOF MULT
TOF tray

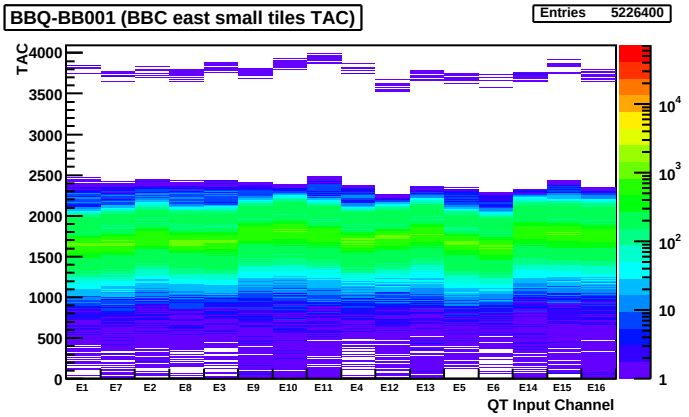

MIX-TF006
Entries 1.37193e+07
TOF MULT
TOF tray

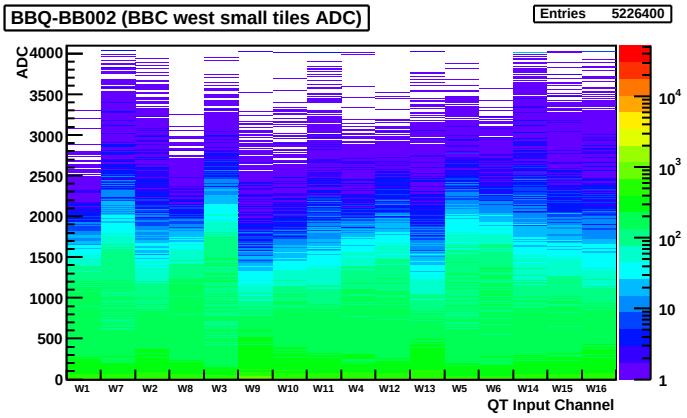

MIX-TF101
Entries 1959900
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

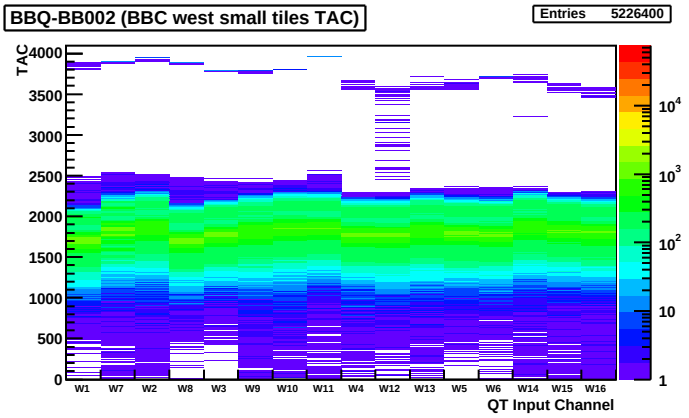

L1-TF201
Entries 326650
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

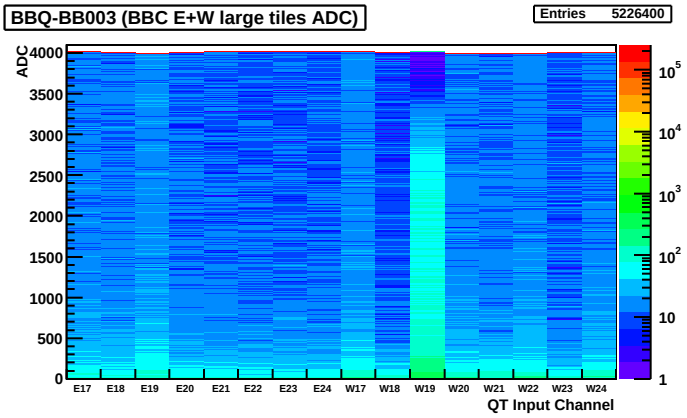

L1-TF201
Entries 1959900
Threshold bits
1
0
299585
297610
296544
299512
296718
295869
27065
29040
30106
27138
29932
30781
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

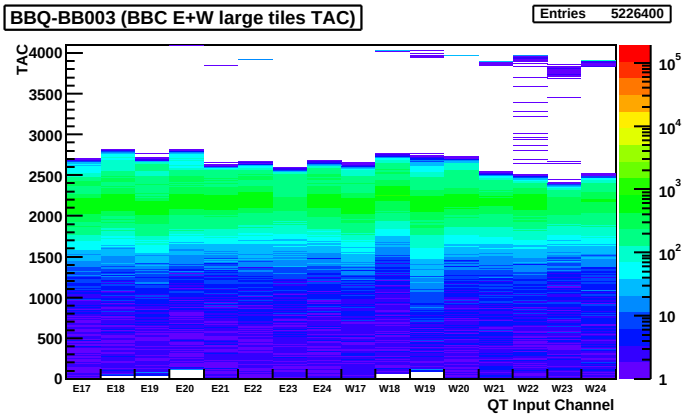

BBQ-BB001 (BBC east small tiles ADC)
Entries 5226400
ADC
QT Input Channel

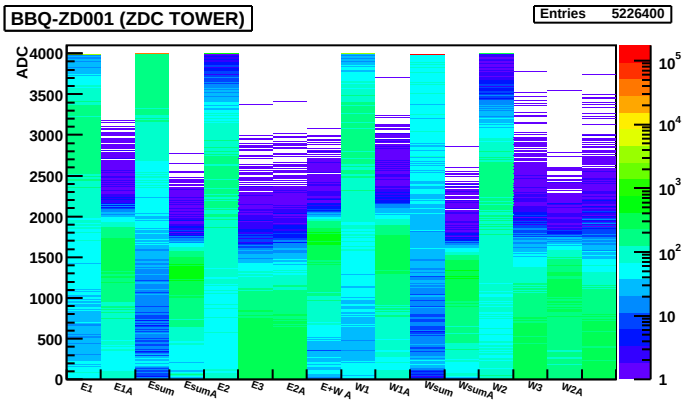

BBQ-BB001 (BBC east small tiles TAC)
Entries 5226400
TAC
QT Input Channel

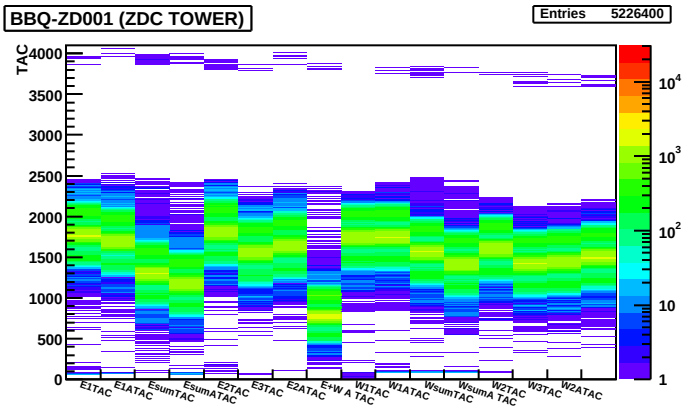

BBQ-BB002 (BBC west small tiles ADC)
Entries 5226400
ADC
QT Input Channel

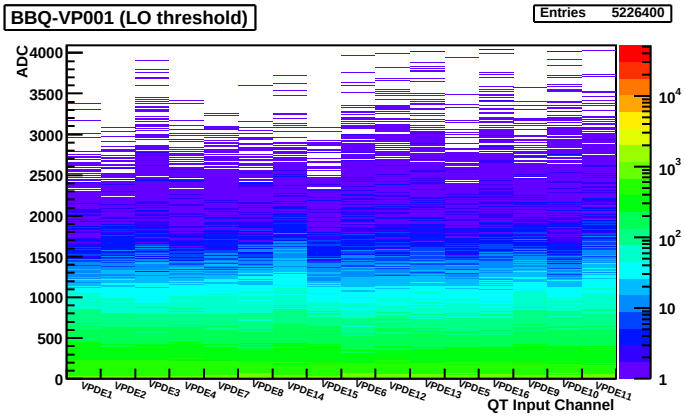

BBQ-BB002 (BBC west small tiles TAC)
Entries 5226400
TAC
QT Input Channel

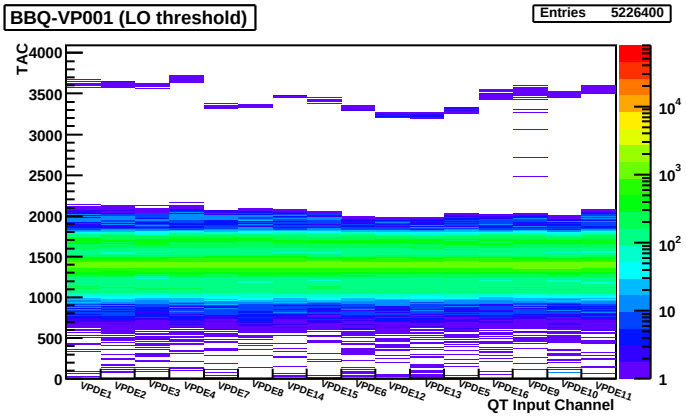

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5226400
ADC
QT Input Channel

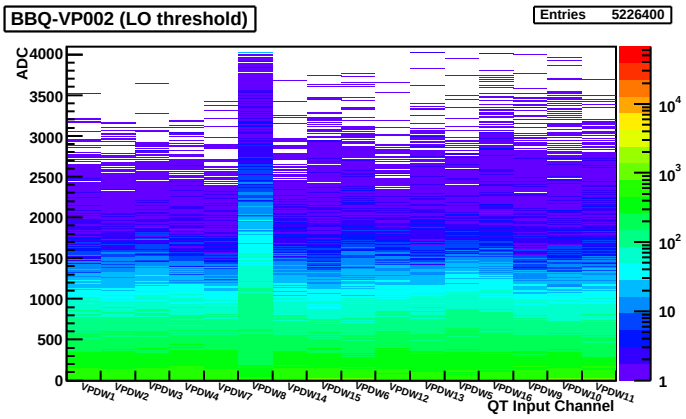

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5226400
TAC
QT Input Channel

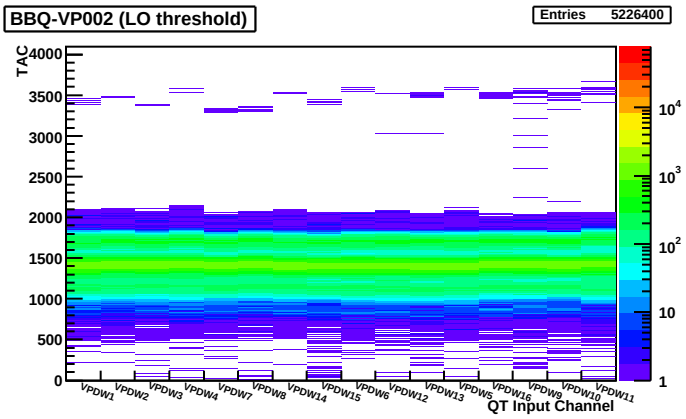

BBQ-ZD001 (ZDC TOWER)
Entries 5226400
ADC

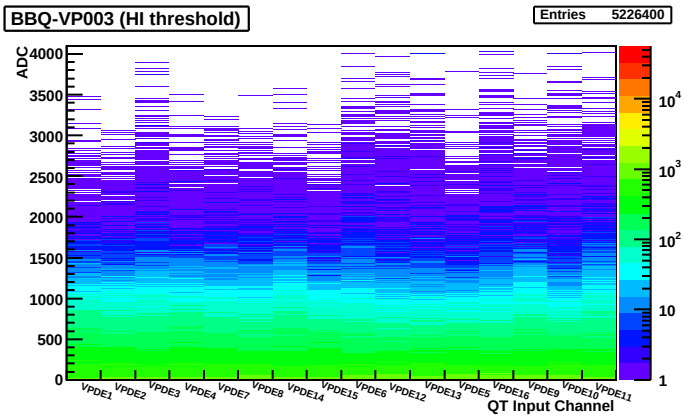

BBQ-ZD001 (ZDC TOWER)
Entries 5226400
TAC

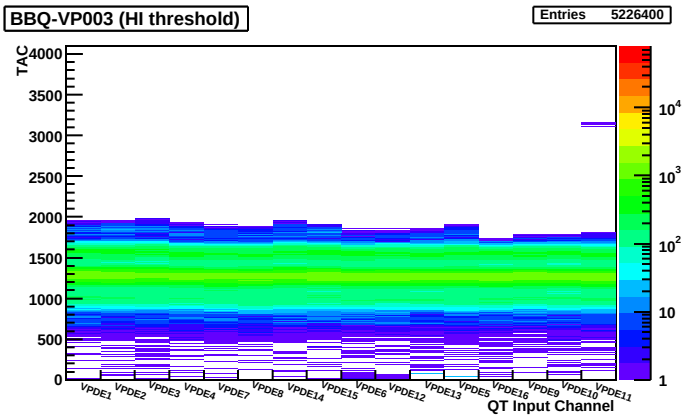

BBQ-VP001 (LO threshold)
Entries 5226400
ADC
QT Input Channel

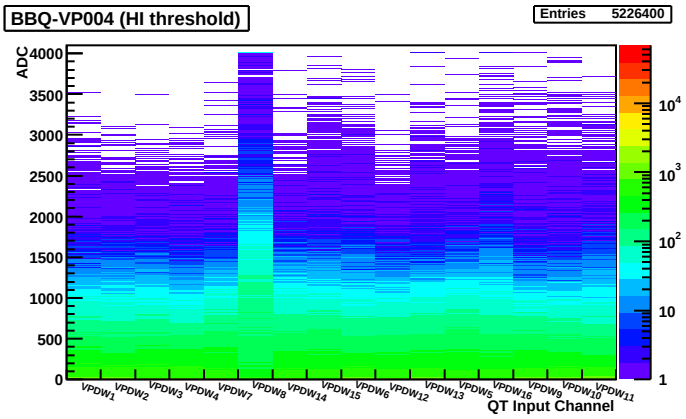

BBQ-VP001 (LO threshold)
Entries 5226400
TAC
QT Input Channel

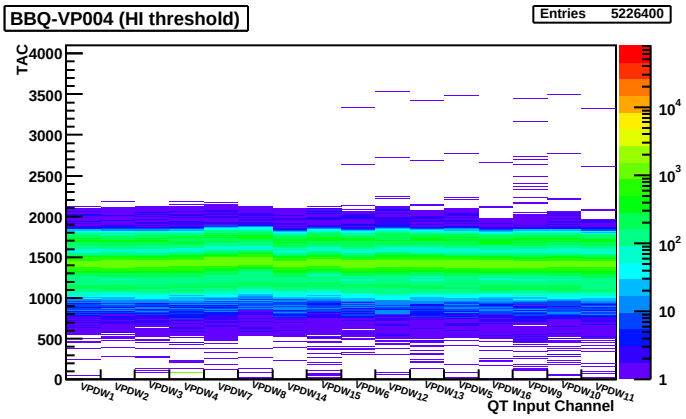

BBQ-VP002 (LO threshold)
Entries 5226400
ADC
QT Input Channel

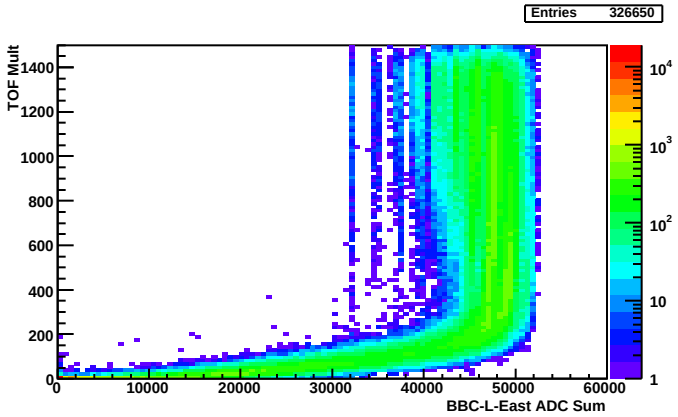

BBQ-VP002 (LO threshold)
Entries 5226400
TAC
QT Input Channel

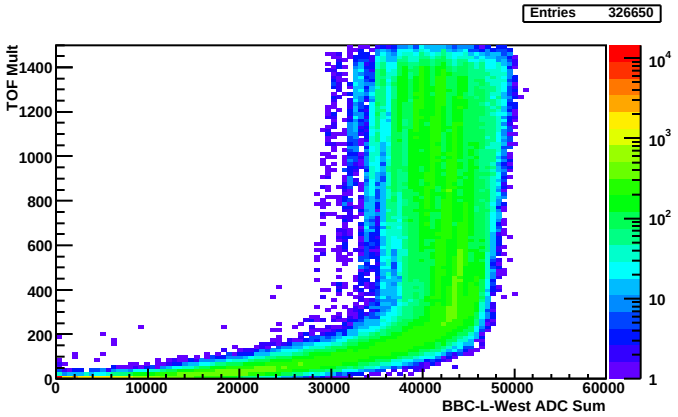

BBQ-VP003 (HI threshold)
Entries 5226400
ADC
QT Input Channel

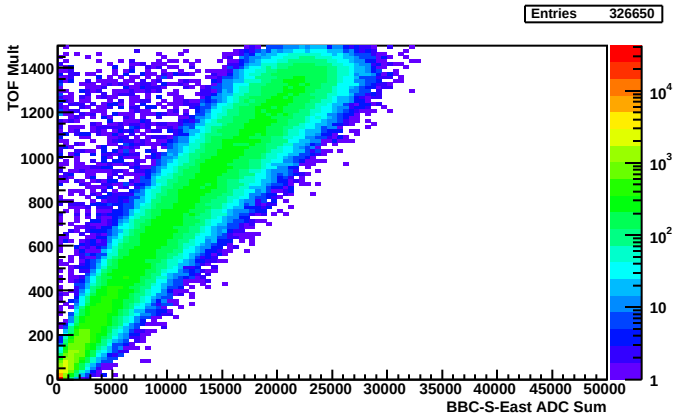

BBQ-VP003 (HI threshold)
Entries 5226400
TAC
QT Input Channel

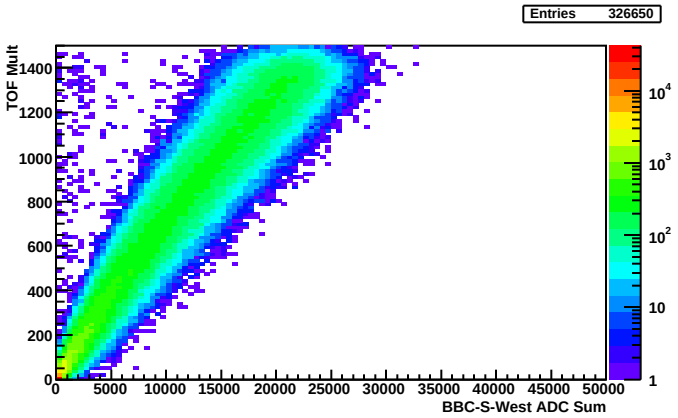

BBQ-VP004 (HI threshold)
Entries 5226400
ADC
QT Input Channel

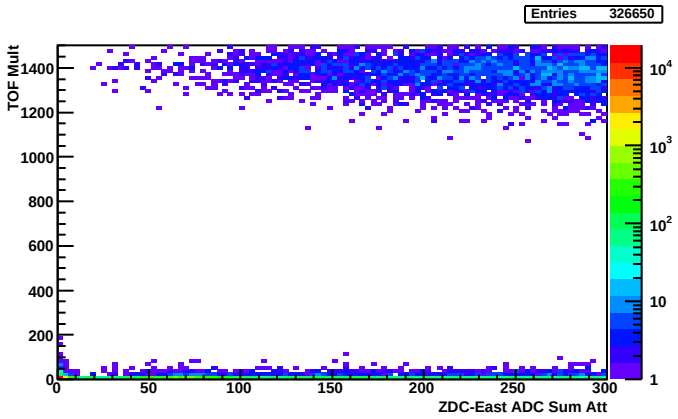

BBQ-VP004 (HI threshold)
Entries 5226400
TAC
QT Input Channel

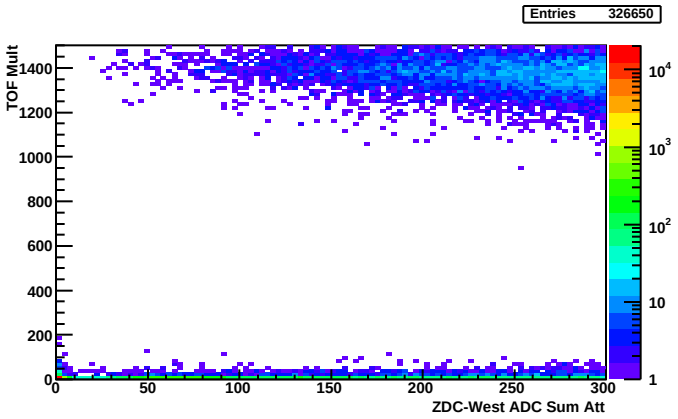

Entries 326650
TOF Mult
BBC-L-East ADC Sum

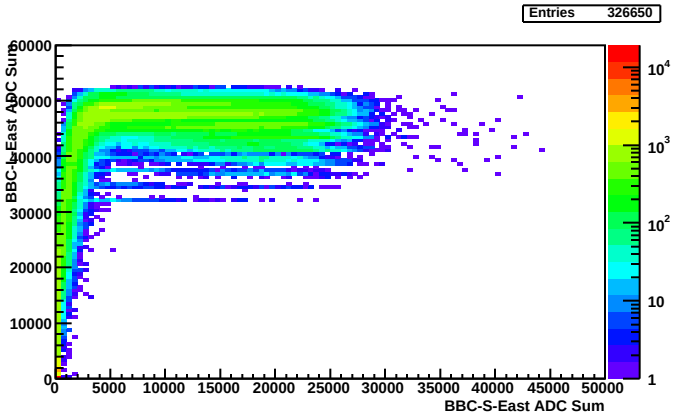

Entries 326650
TOF Mult
BBC-L-West ADC Sum

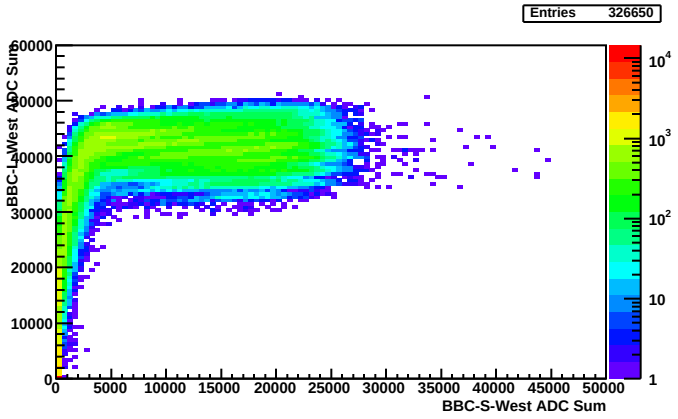

Entries 326650
TOF Mult
BBC-S-East ADC Sum

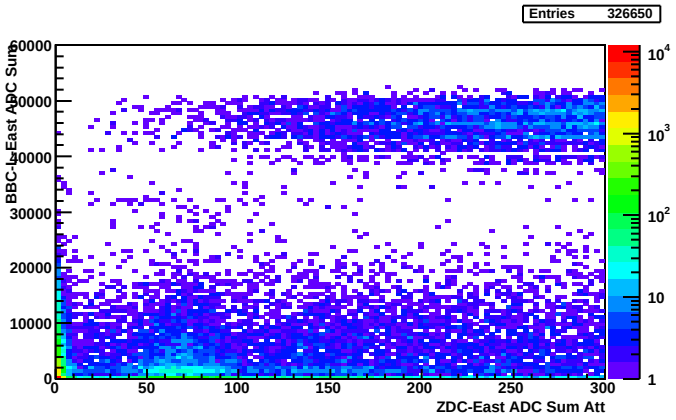

Entries 326650
TOF Mult
BBC-S-West ADC Sum

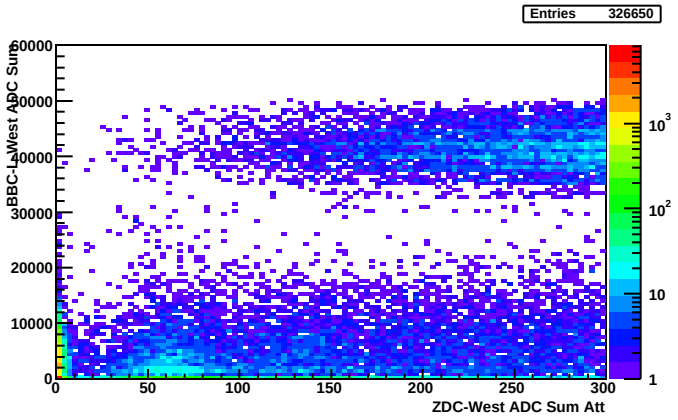

Entries 326650
TOF Mult
ZDC-East ADC Sum Att


Entries 326650
TOF Mult
ZDC-West ADC Sum Att


Entries 326650
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 326650
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 326650
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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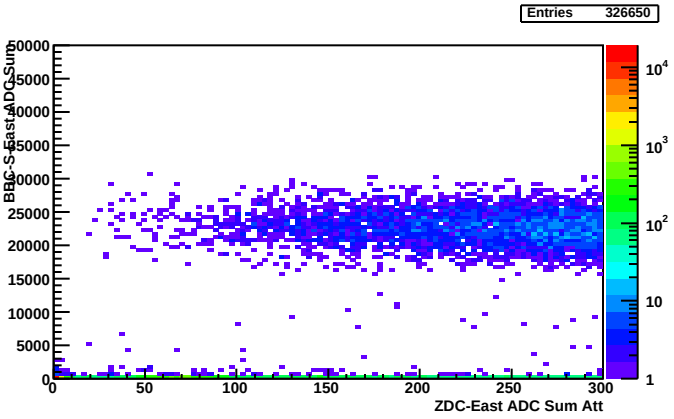
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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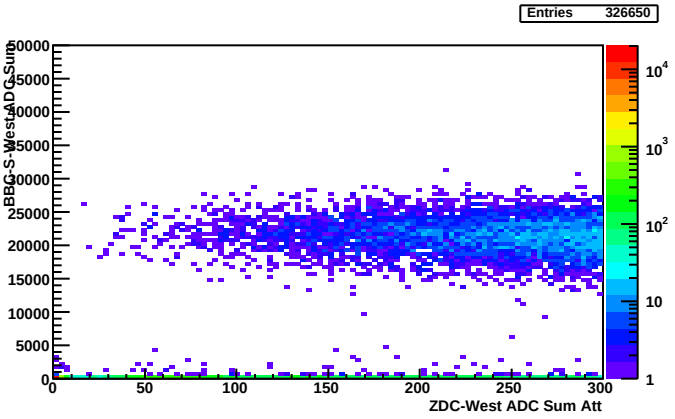
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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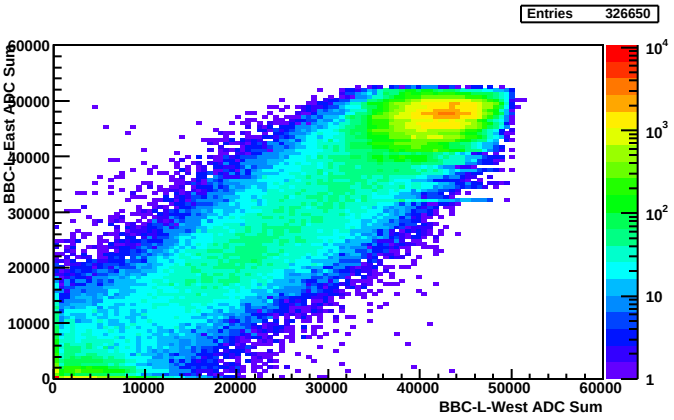
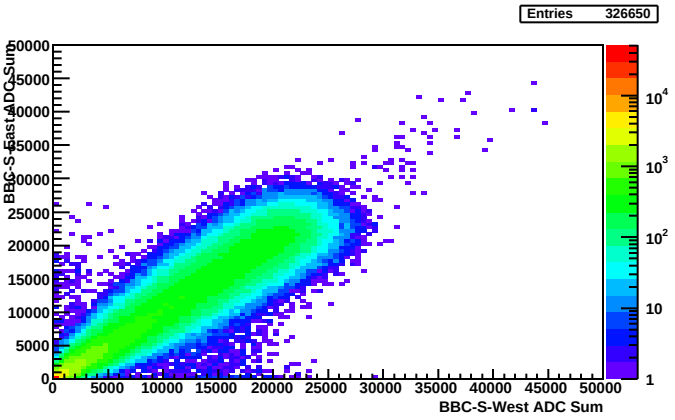
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 326650
BBC-L-East ADC Sum
BBC-L-West ADC Sum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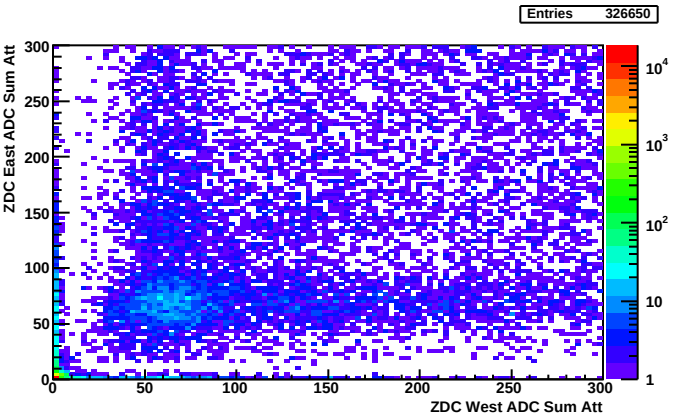
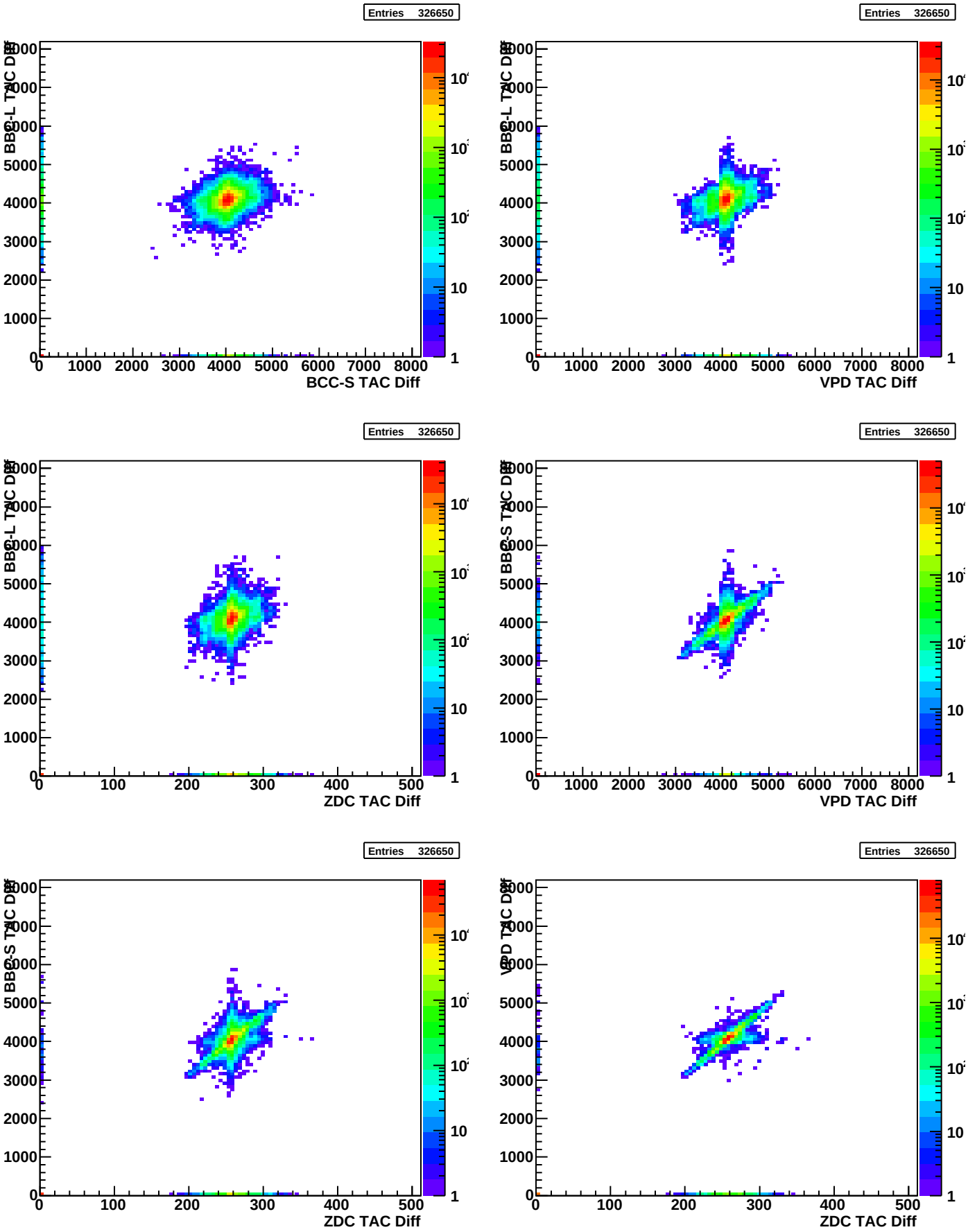
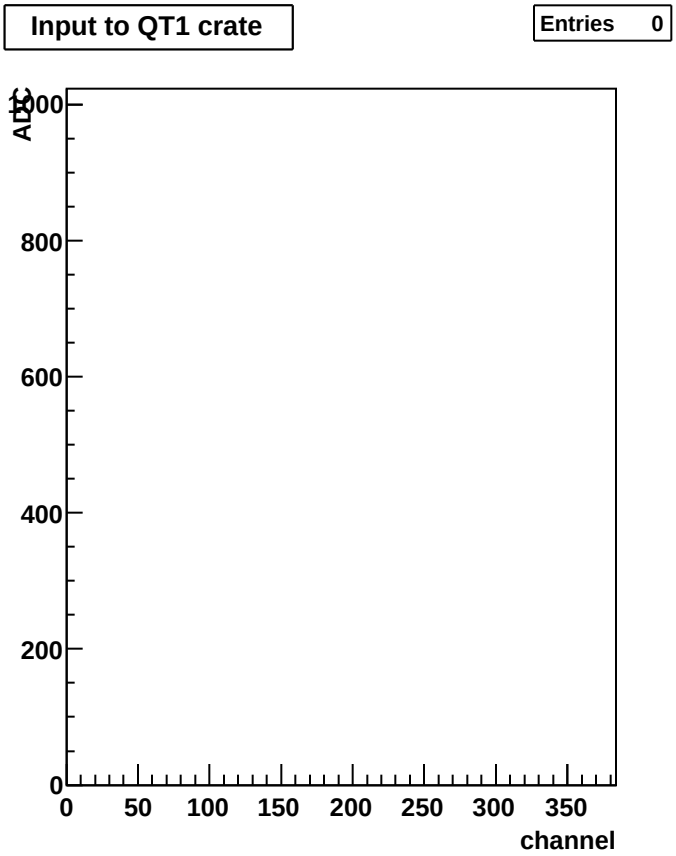
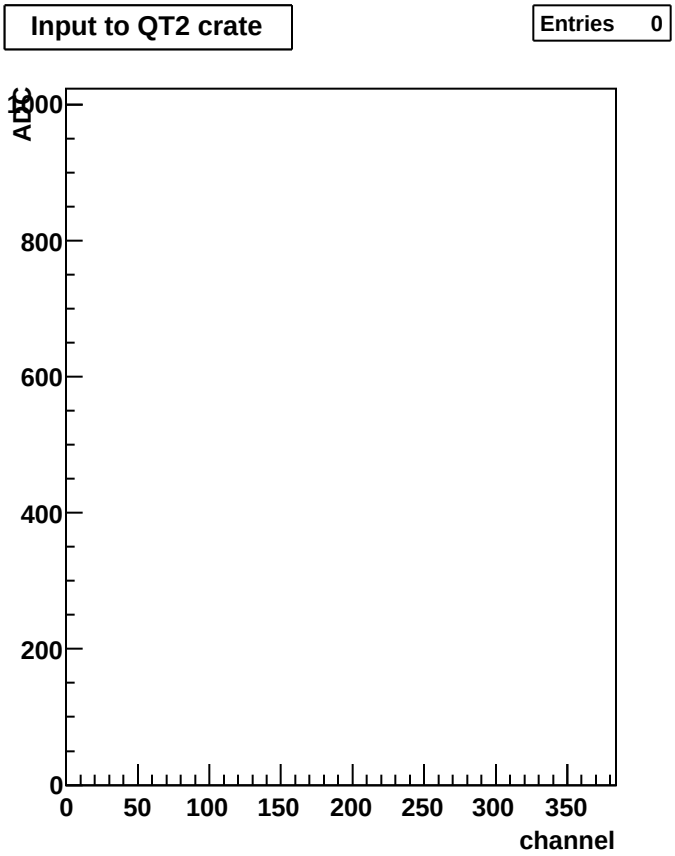
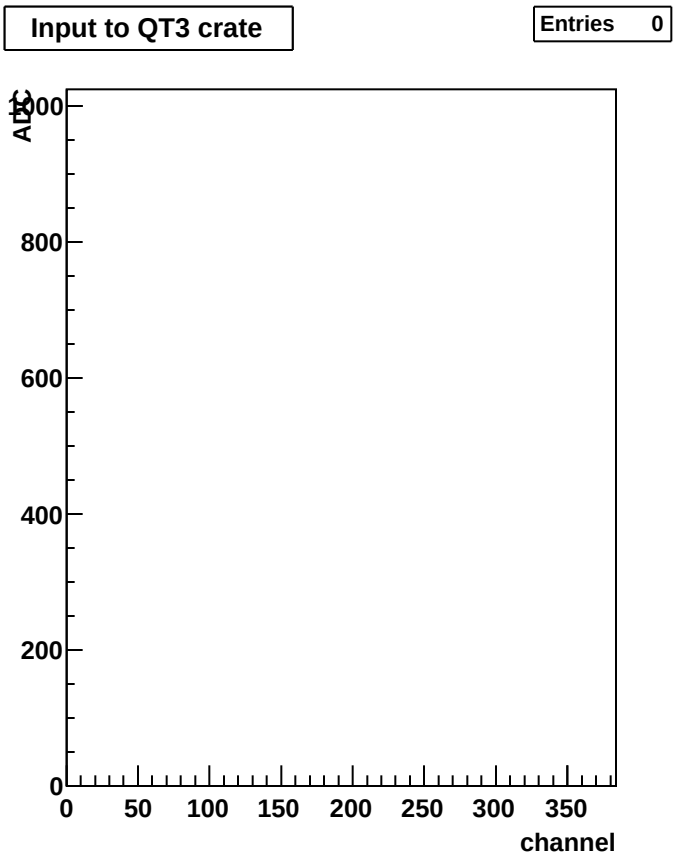
BBC-S-East ADC Sum
BBC-S-West ADC Sum

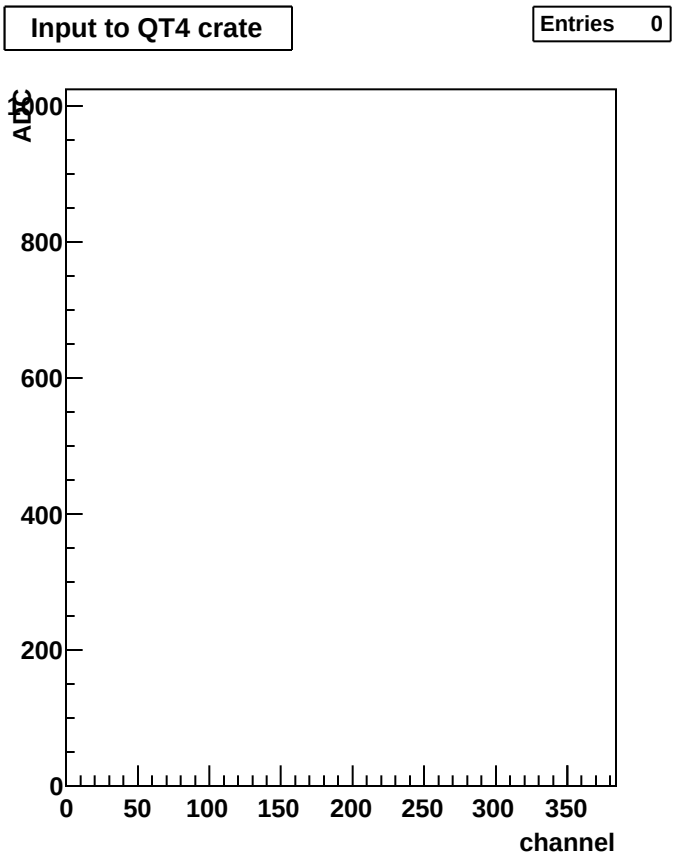

Entries 326650
ZDC East ADC Sum Att
ZDC West ADC Sum Att

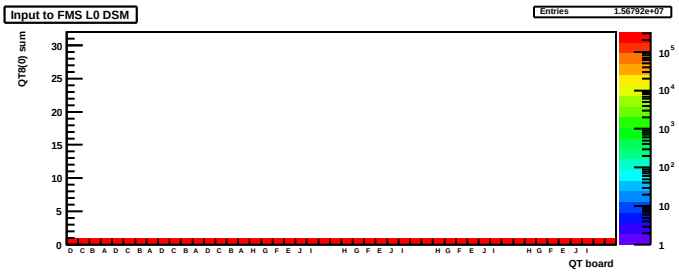

Entries 326650
BBC-L TAC Diff
BCC-S TAC Diff
Entries 326650
BBC-L TAC Diff
VPD TAC Diff
Entries 326650
BBC-L TAC Diff
ZDC TAC Diff
Entries 326650
BBC-S TAC Diff
VPD TAC Diff
Entries 326650
BBC-S TAC Diff
ZDC TAC Diff
Entries 326650
VPD TAC Diff
ZDC TAC Diff

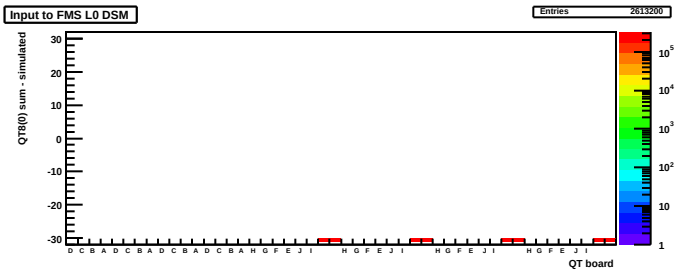

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

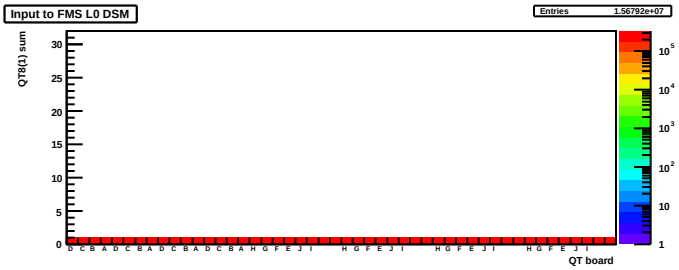

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

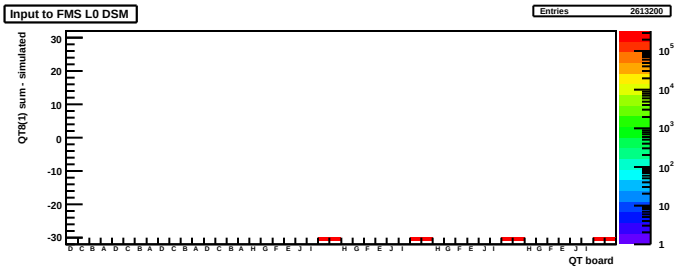

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

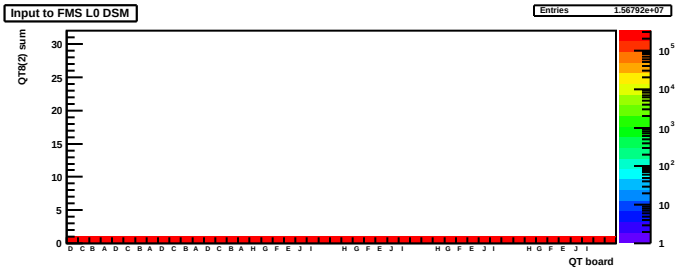

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

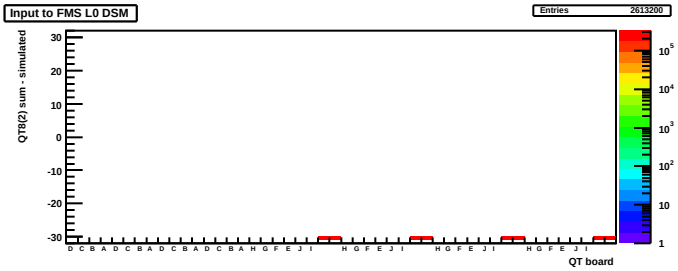

Input to FMS L0 DSM
Entries 1.56792e+07
QT8(0) sum
QT board

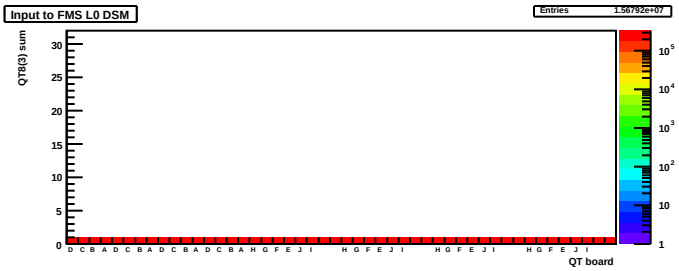

Input to FMS L0 DSM
Entries 2613200
QT8(0) sum - simulated
QT board

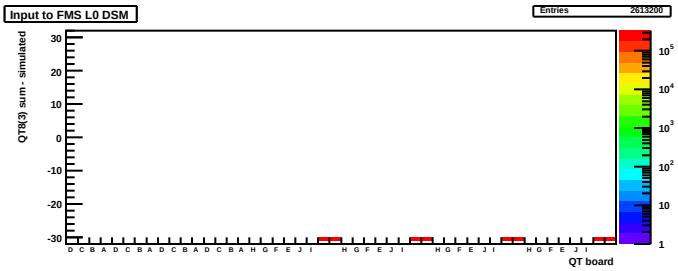

Input to FMS L0 DSM
Entries 1.56792e+07
QT8(1) sum
QT board

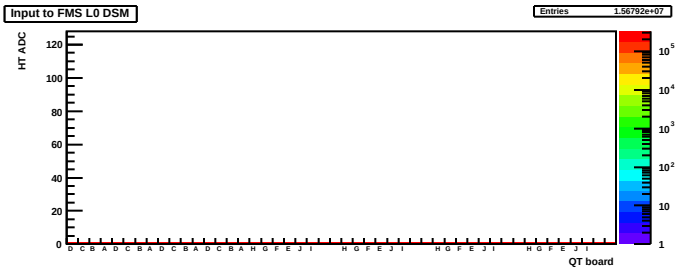

Input to FMS L0 DSM
Entries 2613200
QT8(1) sum - simulated
QT board

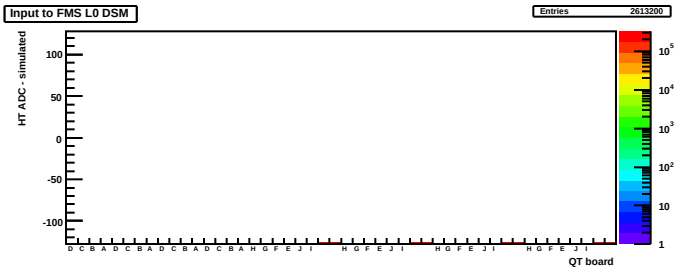

Input to FMS L0 DSM
Entries 1.56792e+07
QT8(2) sum
QT board

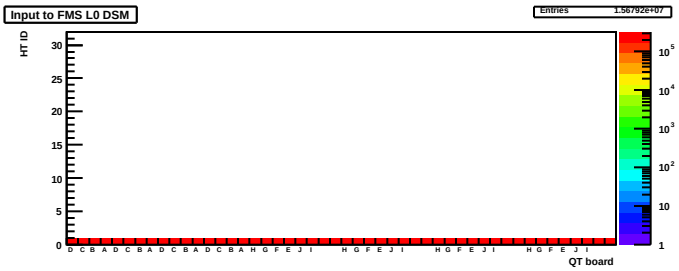

Input to FMS L0 DSM
Entries 2613200
QT8(2) sum - simulated
QT board

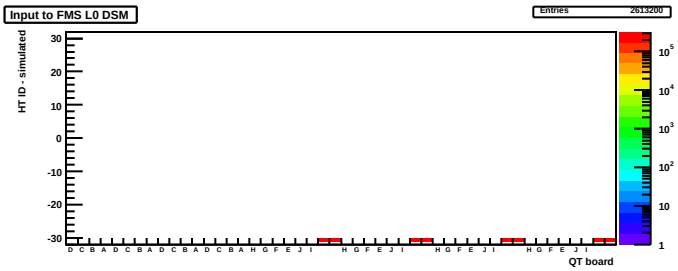

Input to FMS L0 DSM
Entries 1.56792e+07
QT8(3) sum
QT board

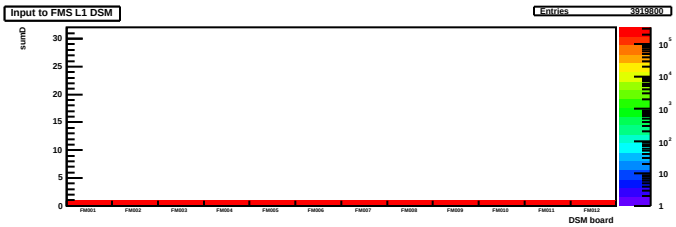

Input to FMS L0 DSM
Entries 2613200
QT8(3) sum - simulated
QT board

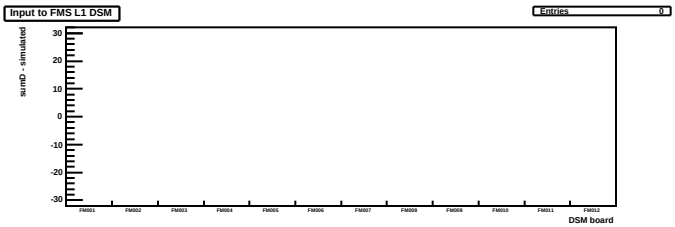

Input to FMS L0 DSM
Entries 1.56792e+07
HT ADC
QT board

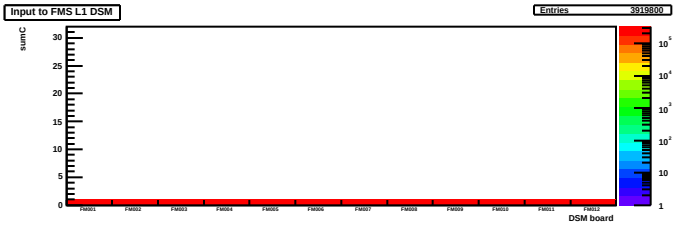

Input to FMS L0 DSM
Entries 2613200
HT ADC - simulated
QT board

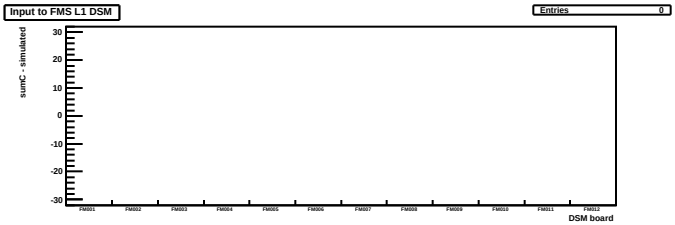

Input to FMS L0 DSM
Entries 1.56792e+07
HT ID
QT board

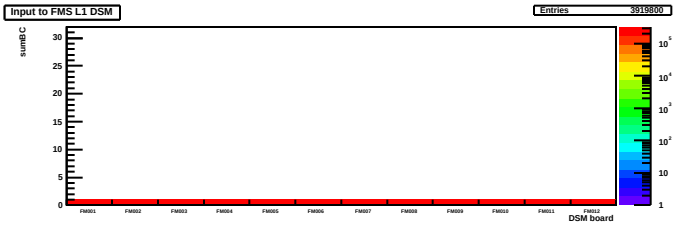

Input to FMS L0 DSM
Entries 2613200
HT ID - simulated
QT board

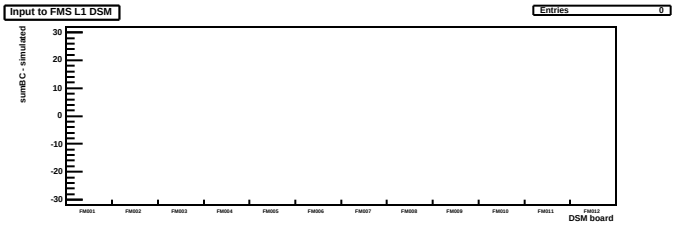

Input to FMS L1 DSM
Entries 3919800
sumD
DSM board

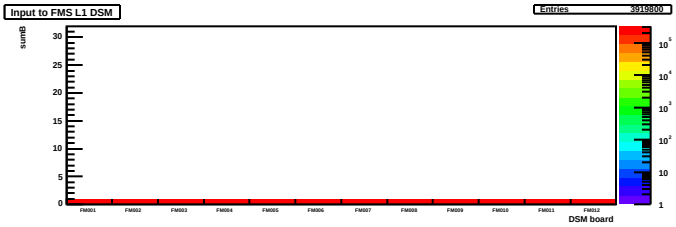

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

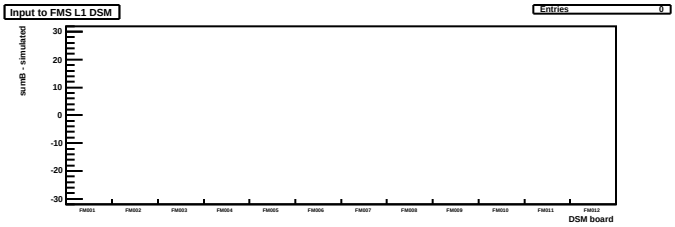

Input to FMS L1 DSM
Entries 3919800
sumC
DSM board

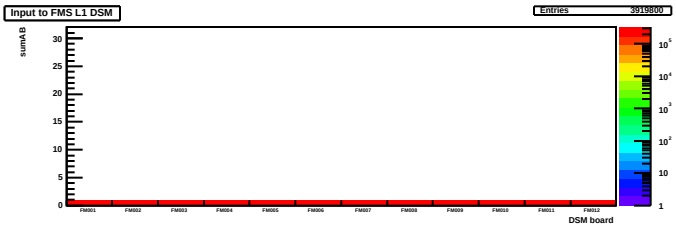

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

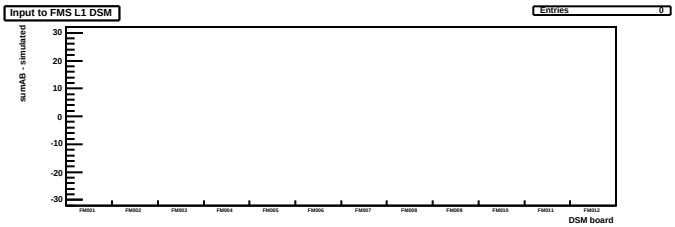

Input to FMS L1 DSM
Entries 3919800
sumBC
DSM board

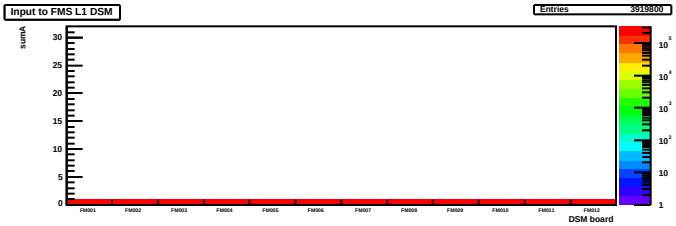

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

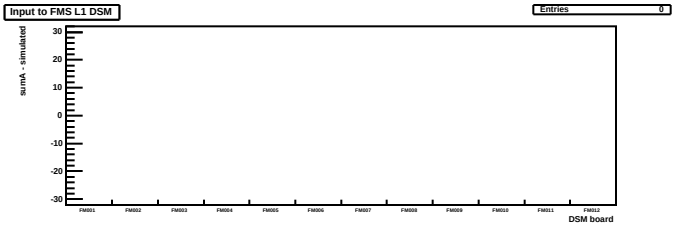

Input to FMS L1 DSM
Entries 3919800
sumB
DSM board

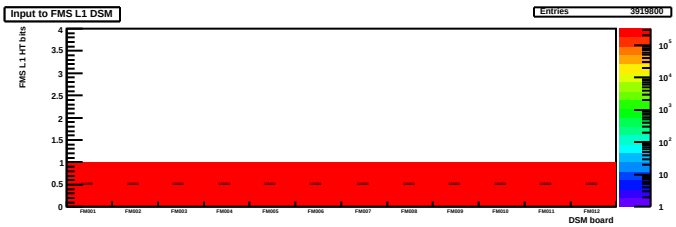

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

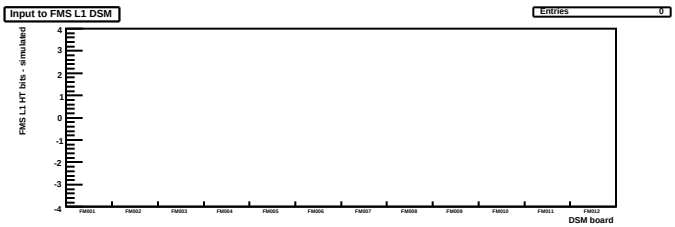

Input to FMS L1 DSM
Entries 3919800
sumAB
DSM board

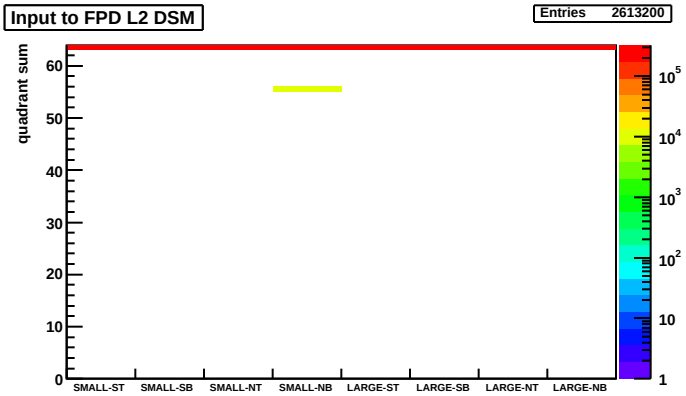

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

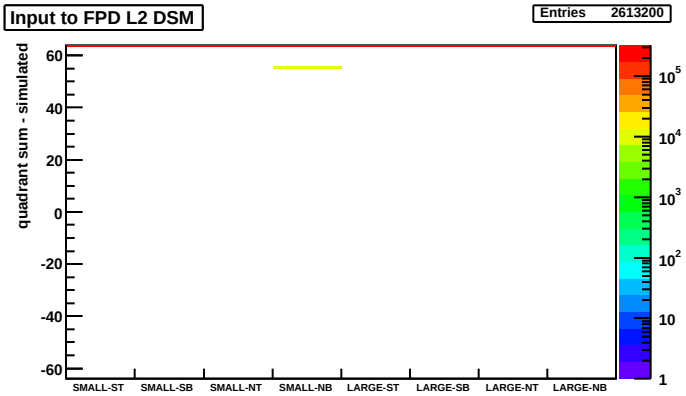

Input to FMS L1 DSM
Entries 3919800
sumA
DSM board

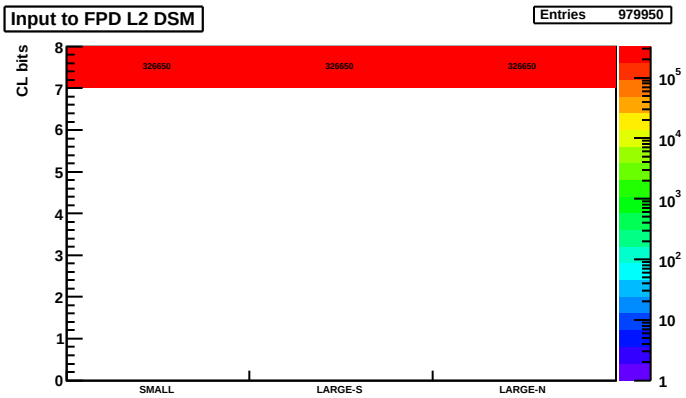

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

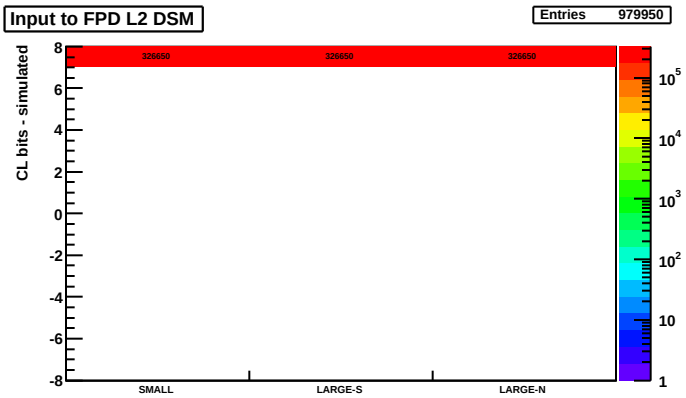

Input to FMS L1 DSM
Entries 3919800
FMS L1 HT bits
DSM board

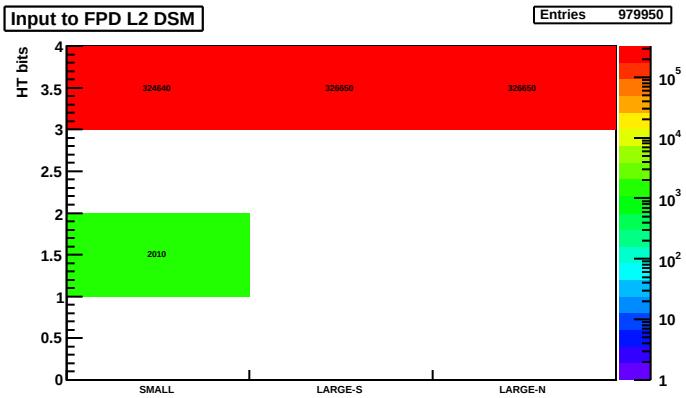

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

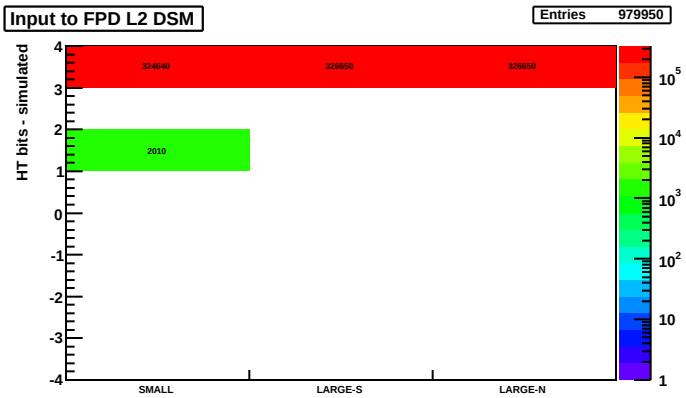

Input to FPD L2 DSM
Entries 2613200
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

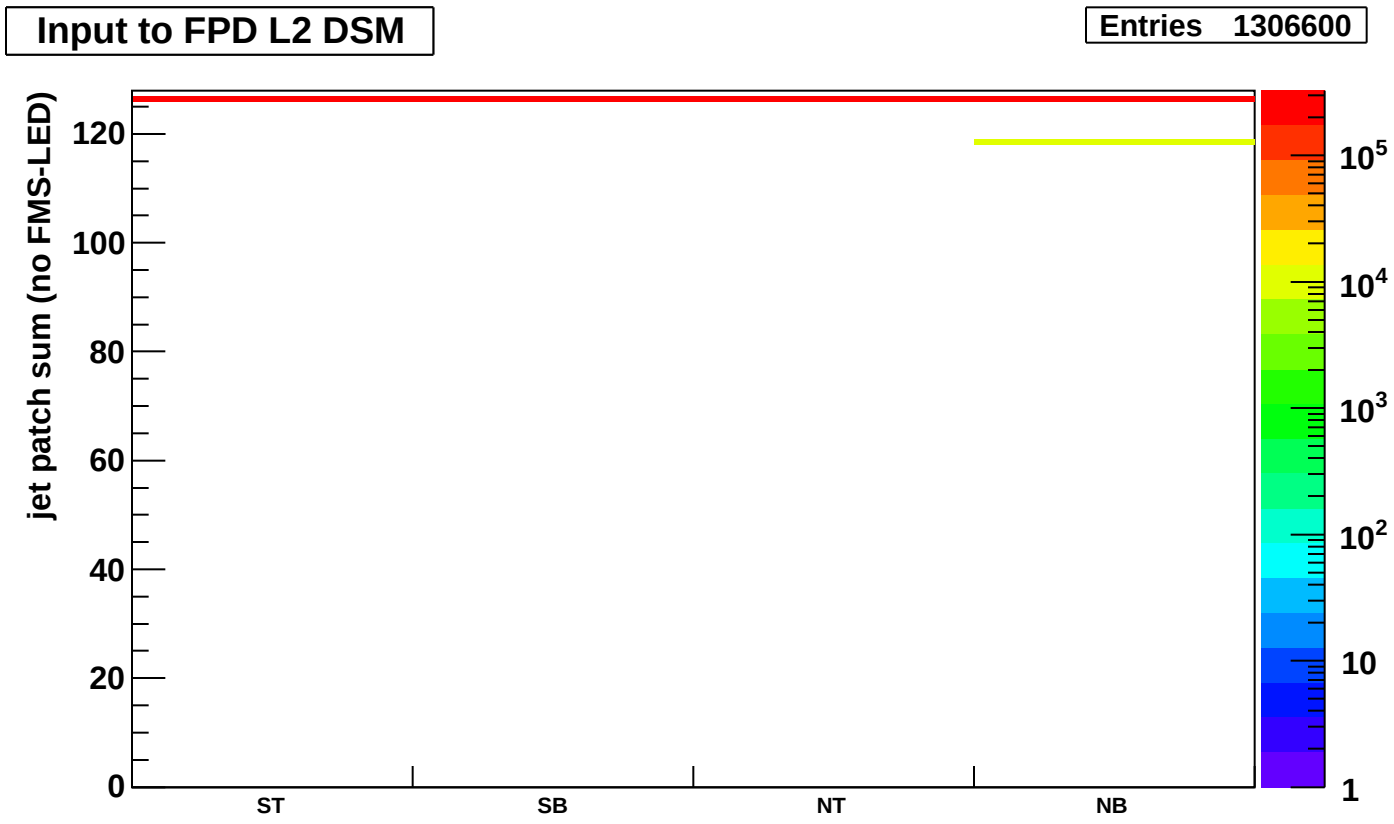

Input to FPD L2 DSM
Entries 2613200
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

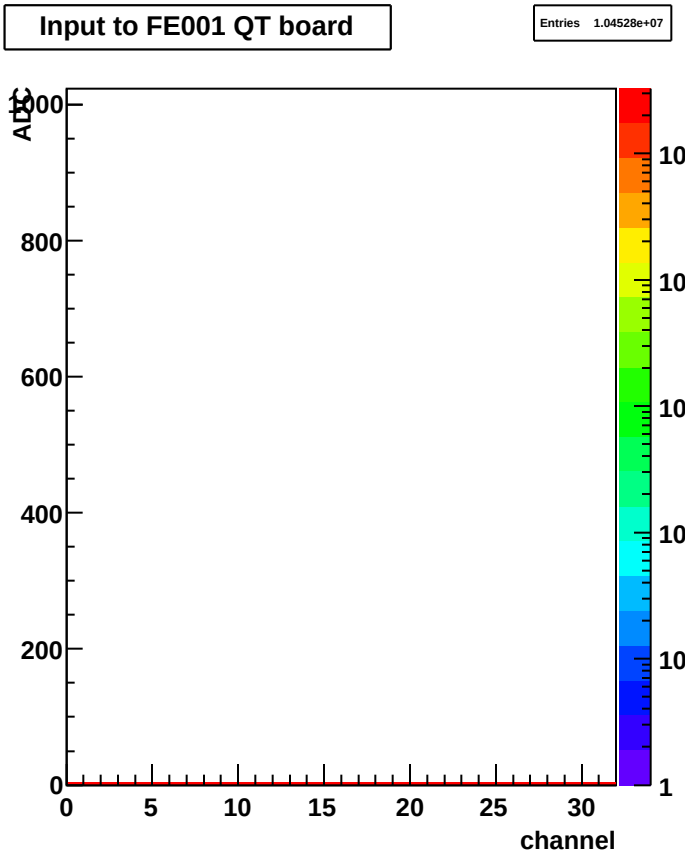

Input to FPD L2 DSM
Entries 979950
CL bits
326650
326650
326650
SMALL
LARGE-S
LARGE-N

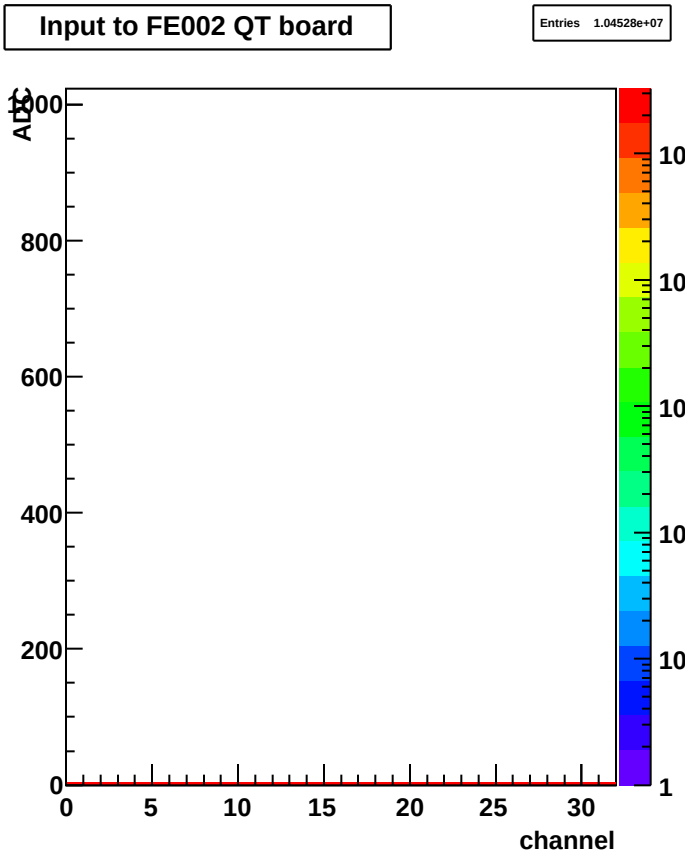

Input to FPD L2 DSM
Entries 979950
CL bits - simulated
326650
326650
326650
SMALL
LARGE-S
LARGE-N

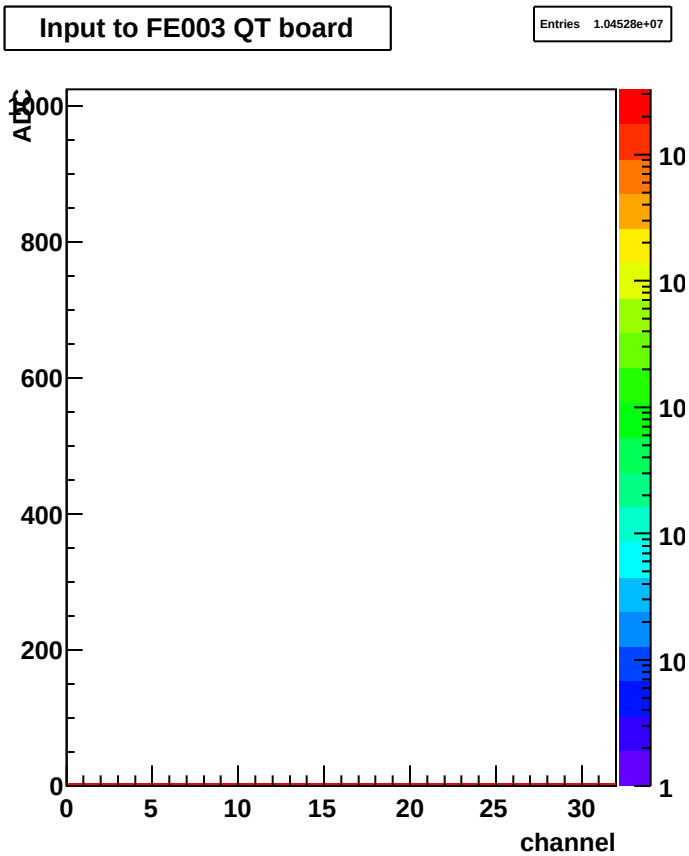

Input to FPD L2 DSM
Entries 979950
HT bits
324640
326650
326650
2010
SMALL
LARGE-S
LARGE-N

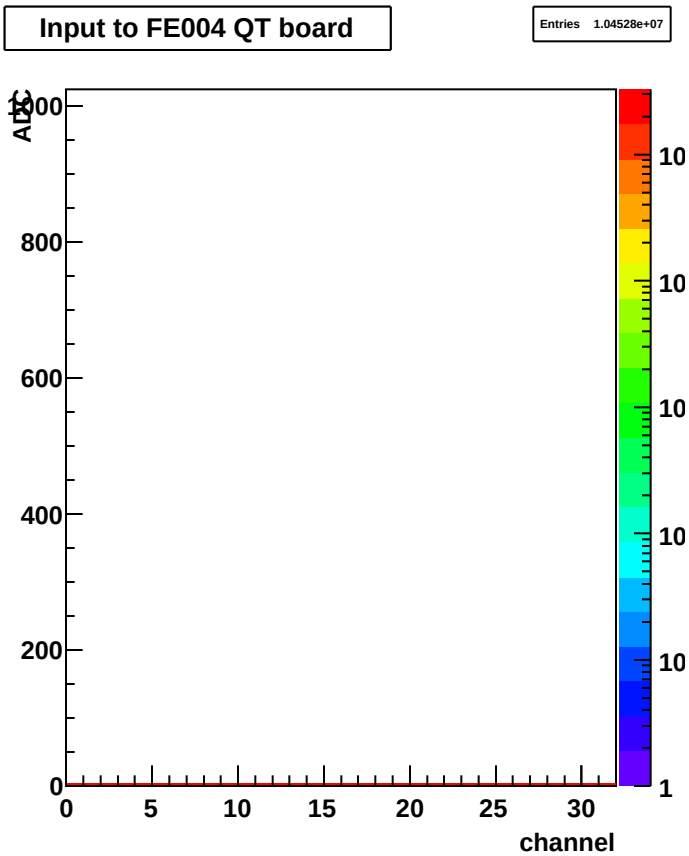

Input to FPD L2 DSM
Entries 979950
HT bits - simulated
324640
326650
326650
2010
SMALL
LARGE-S
LARGE-N

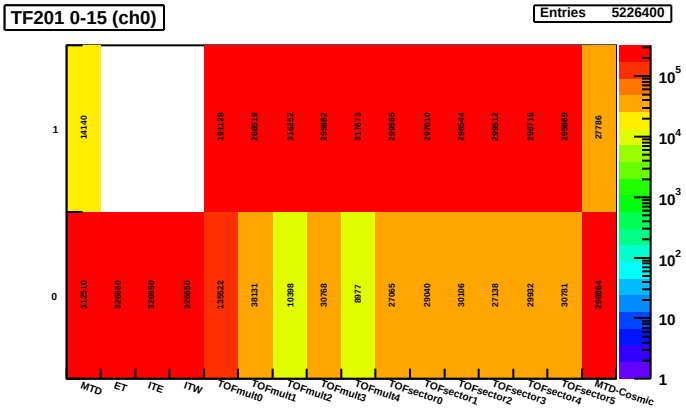

Input to FPD L2 DSM
Entries 1306600
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

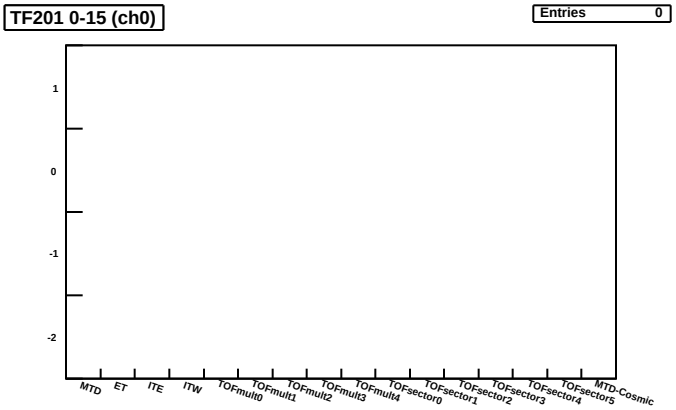

Input to FE001 QT board
Entries 1.04528e+07
ADC
channel

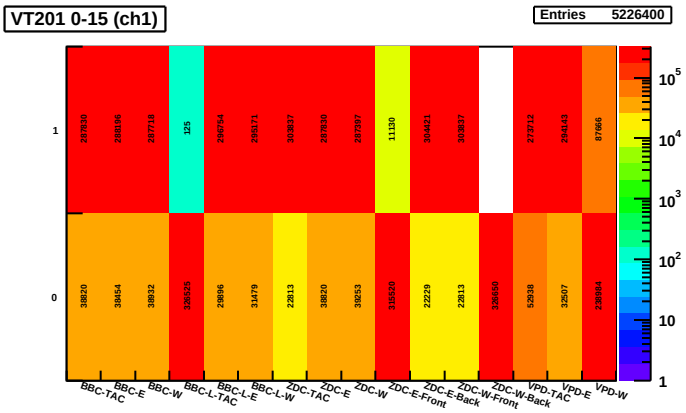

Input to FE002 QT board
Entries 1.04528e+07
ADC
channel

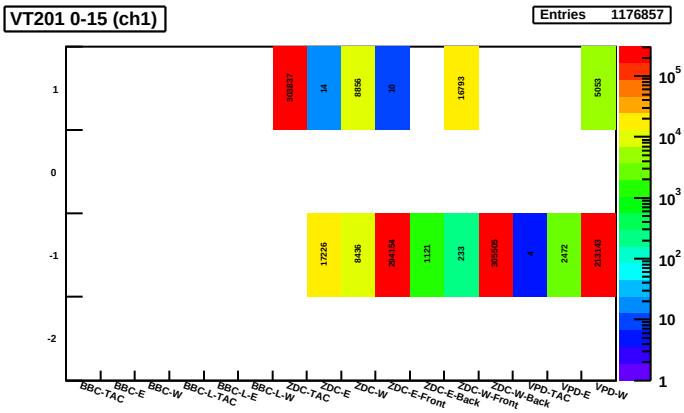

Input to FE003 QT board
Entries 1.04528e+07
ADC
channel

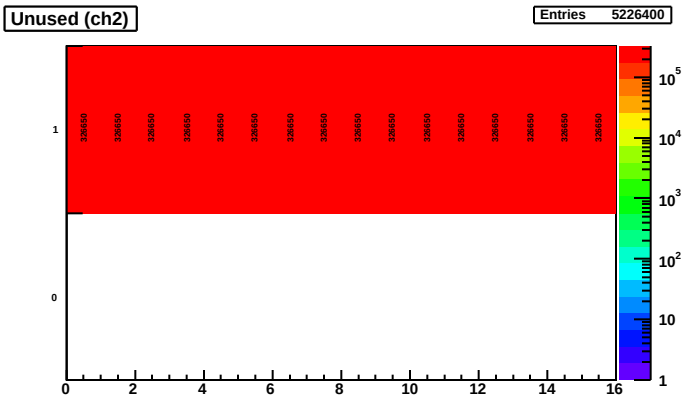

Input to FE004 QT board
Entries 1.04528e+07
ADC
channel

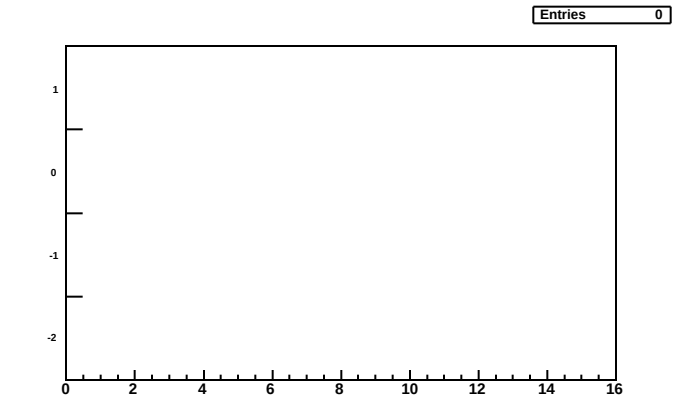

TF201 0-15 (ch0)
Entries 5226400
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

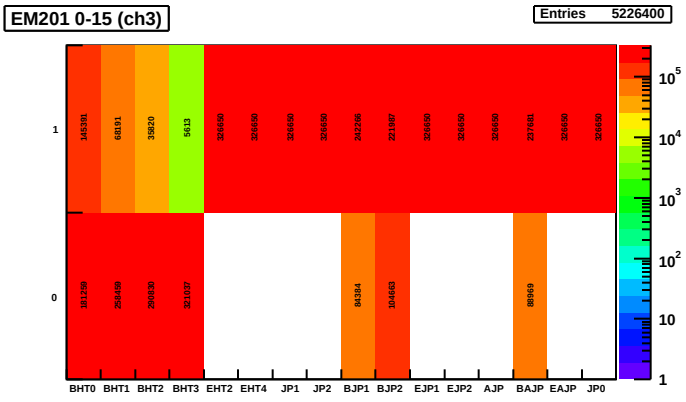

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

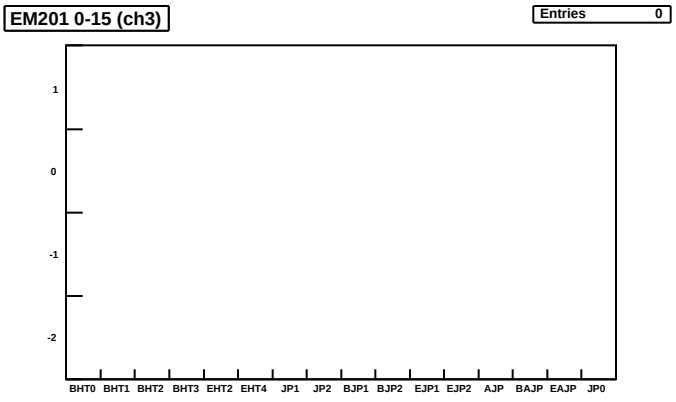

VT201 0-15 (ch1)
Entries 5226400
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1176857
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

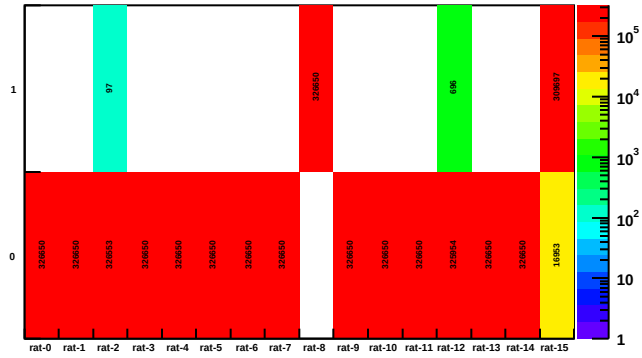

Unused (ch2)
Entries 5226400

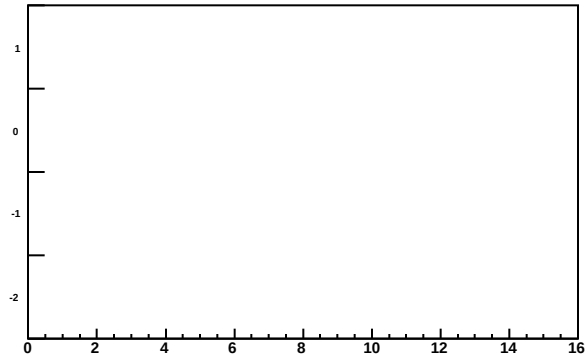

Entries 0


EM201 0-15 (ch3)
Entries 5226400
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

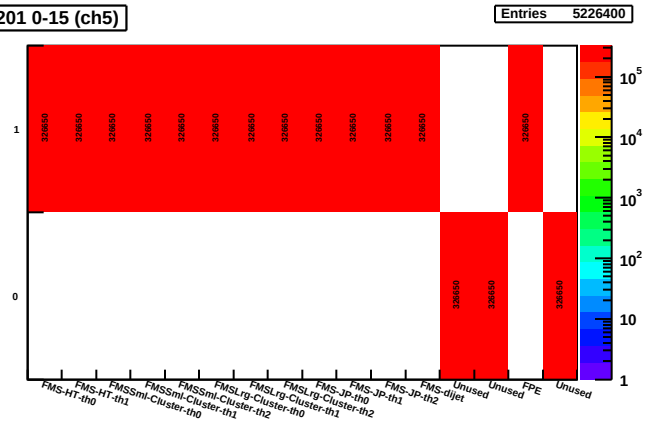

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

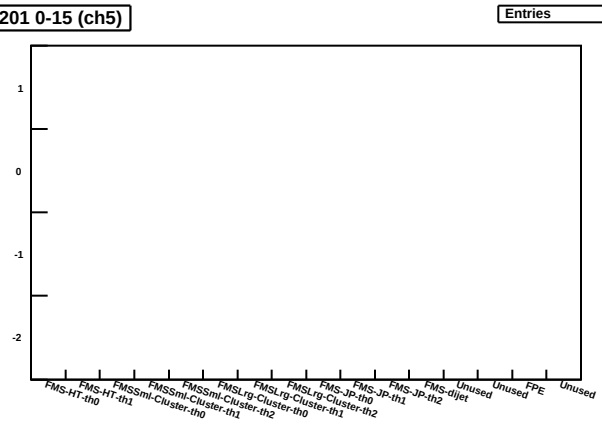




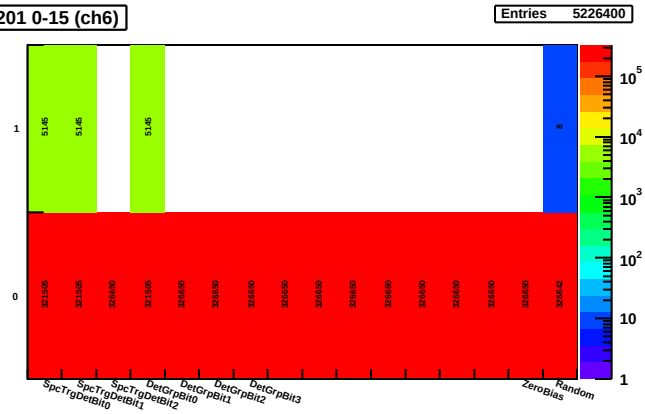
FP201 0-15 (ch5)

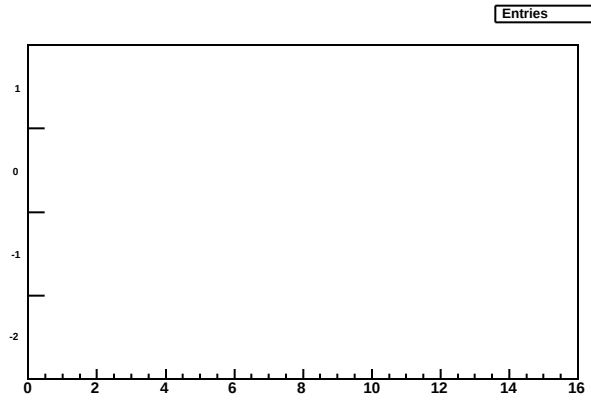
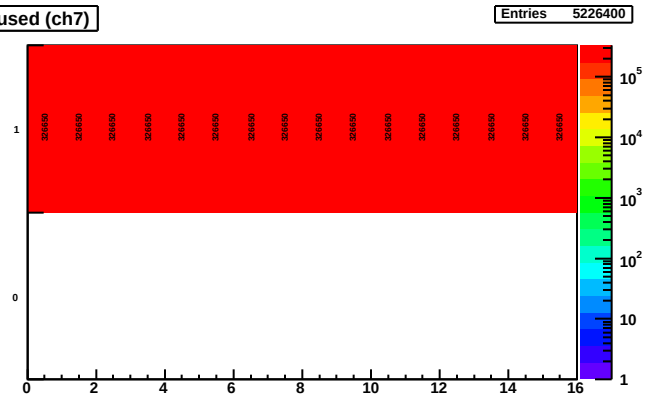
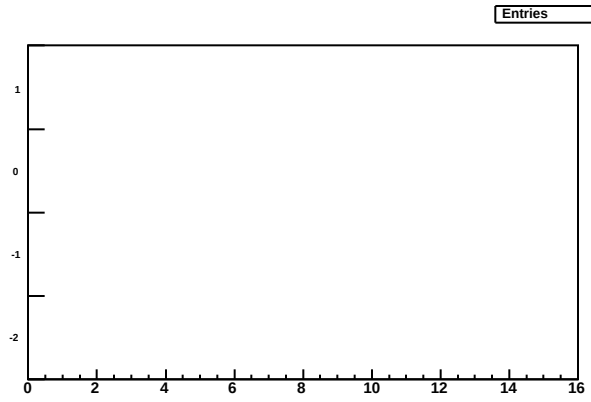


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)