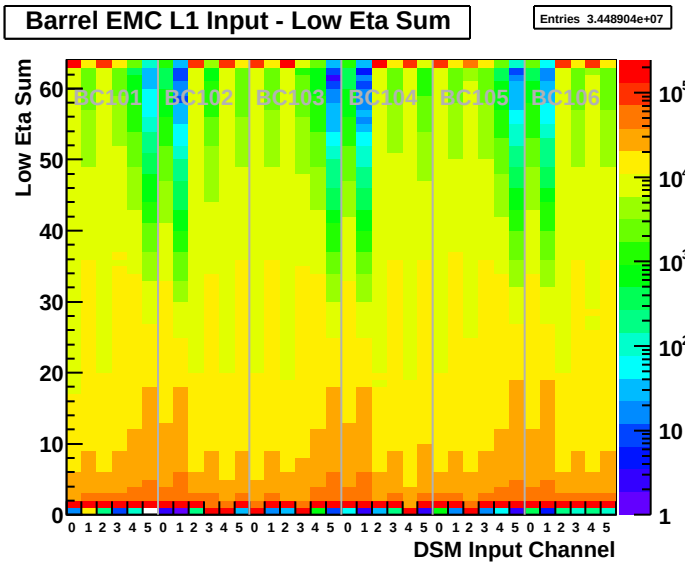
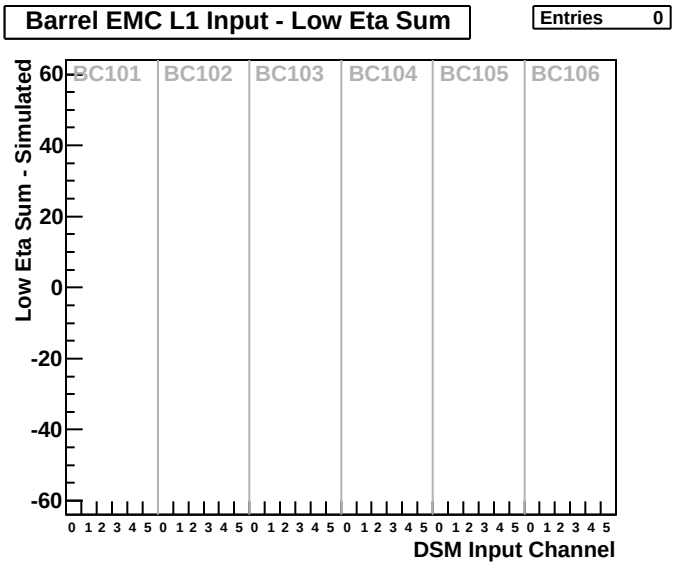
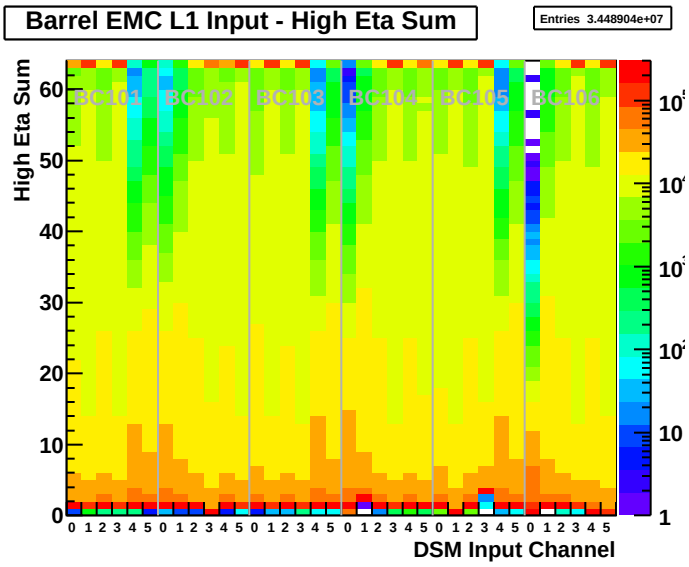
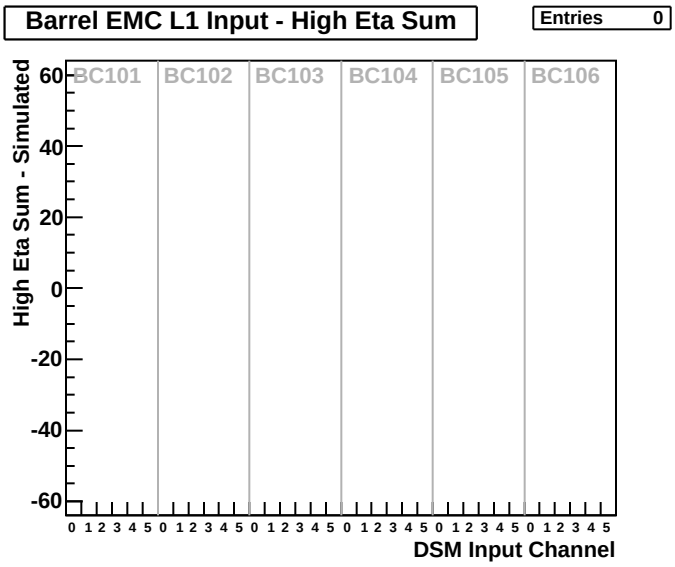
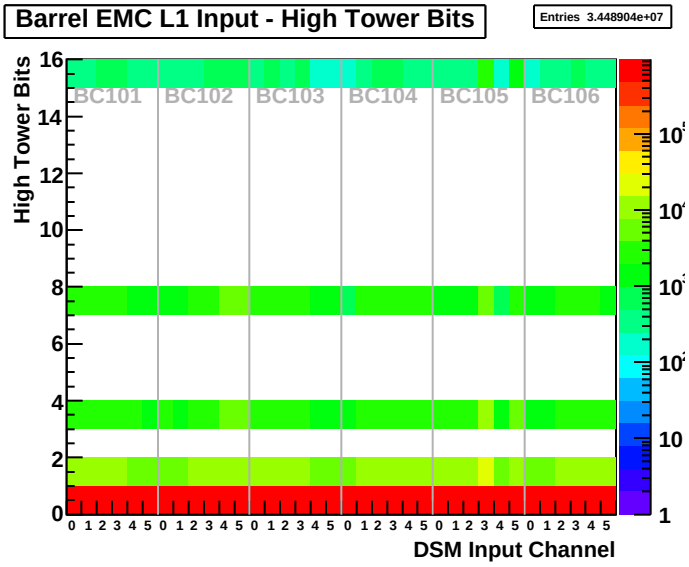
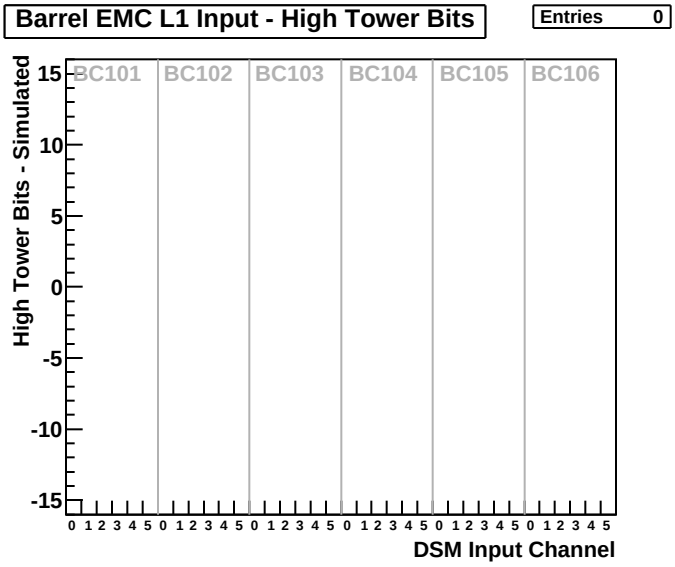

Barrel EMC L1 Input - Low Eta Sum
Entries 3.448904e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

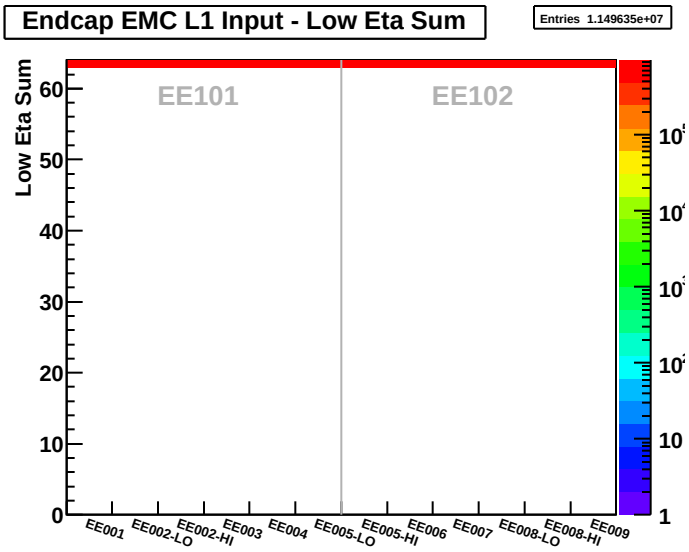

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

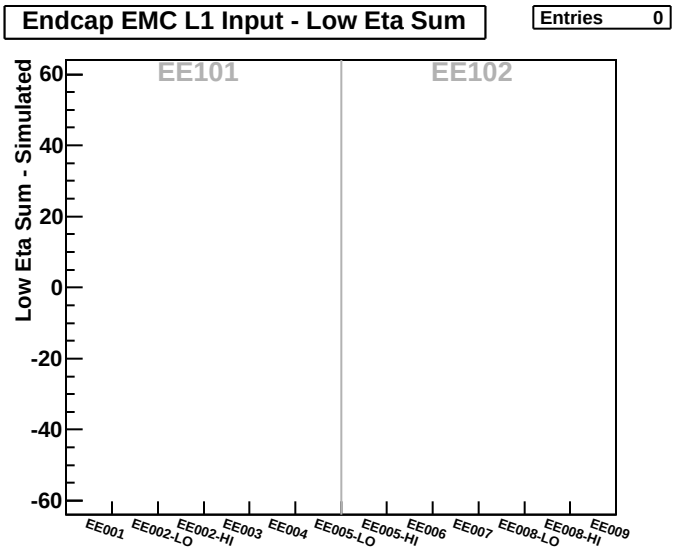

Barrel EMC L1 Input - High Eta Sum
Entries 3.448904e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

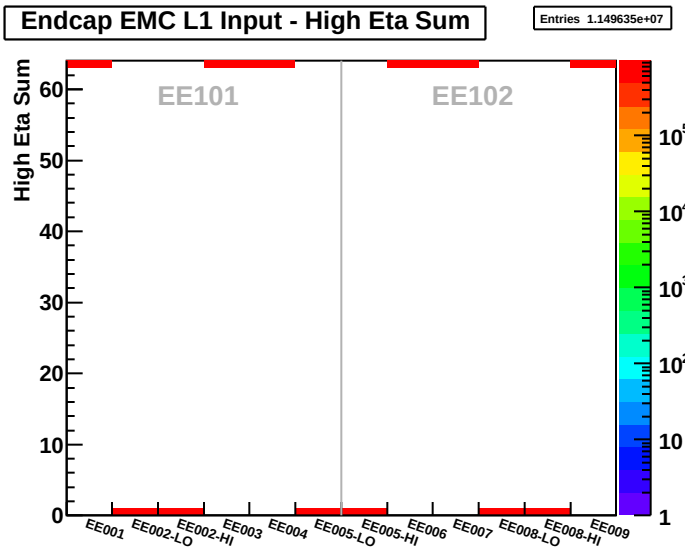

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

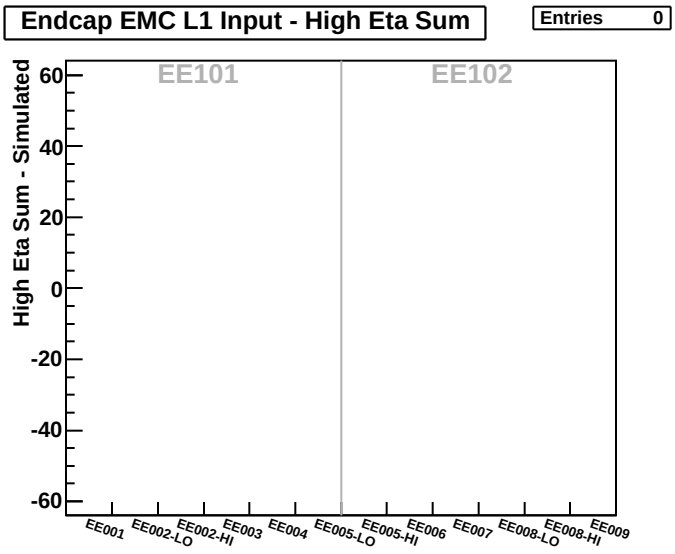

Barrel EMC L1 Input - High Tower Bits
Entries 3.448904e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

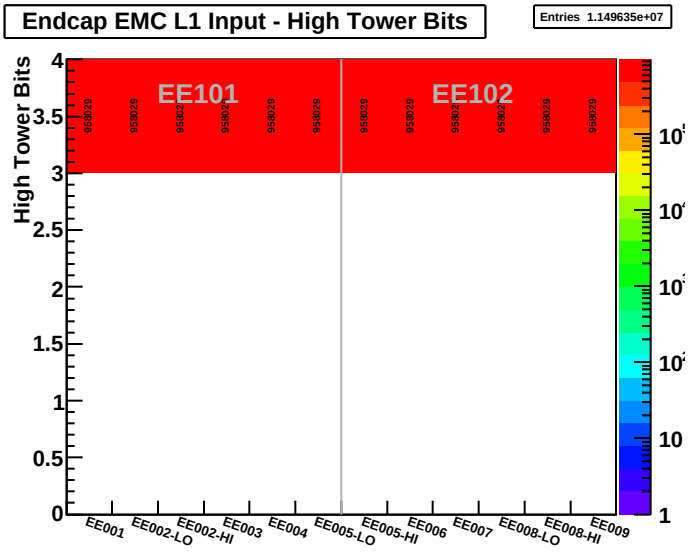

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

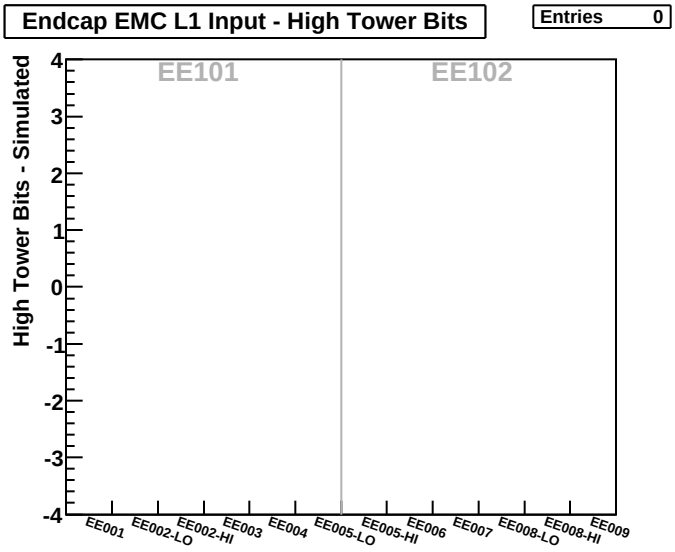

Endcap EMC L1 Input - Low Eta Sum
Entries 1.149635e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

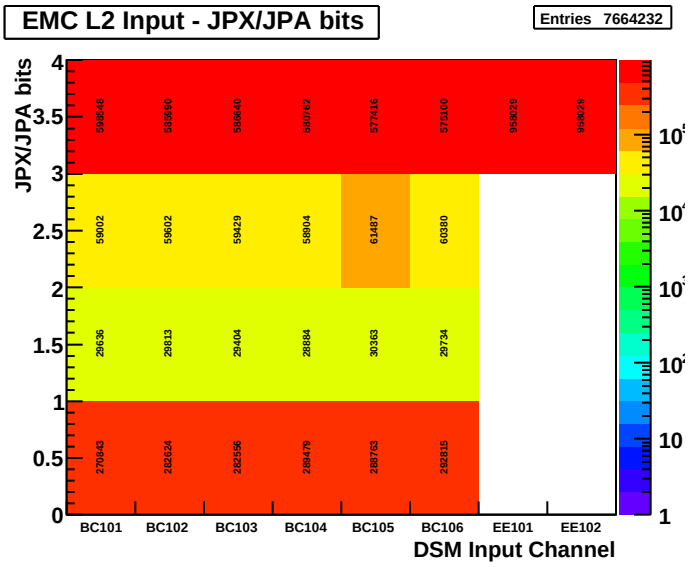

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

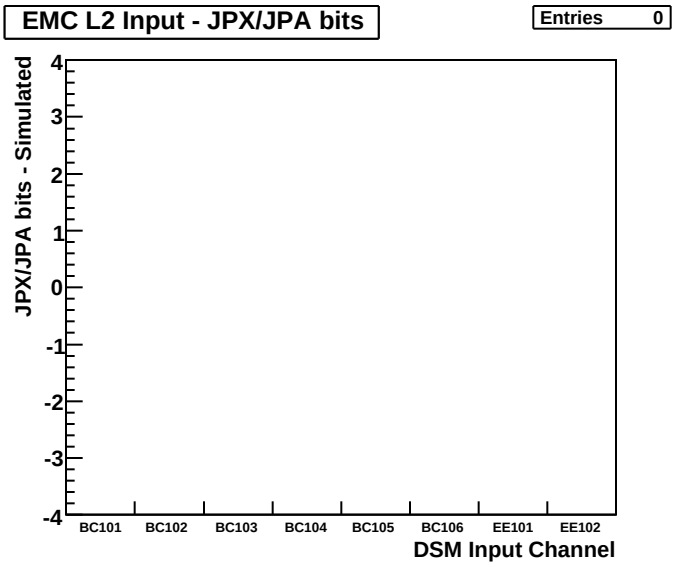

Endcap EMC L1 Input - High Eta Sum
Entries 1.149635e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

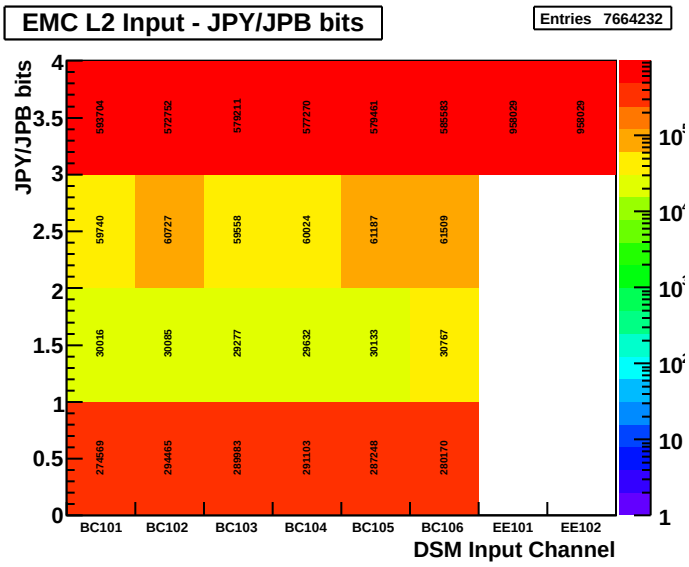

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

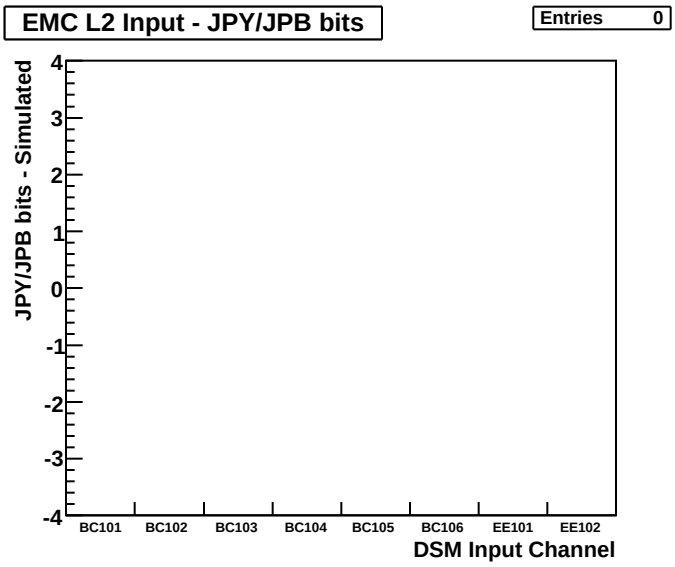

Endcap EMC L1 Input - High Tower Bits
Entries 1.149635e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

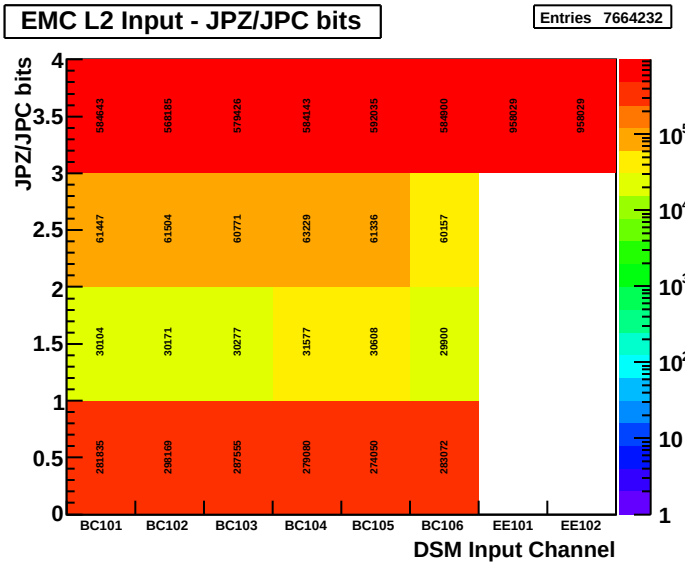

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

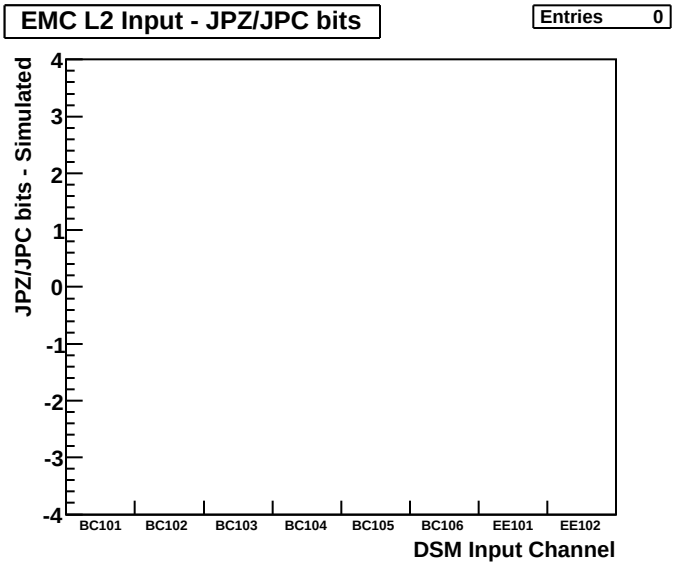

EMC L2 Input - JPX/JPA bits
Entries 7664232
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
596548 595990 596640 590762 577416 575100 958029 958029
59002 59602 59429 58904 61487 60380
29636 29813 29404 28884 30363 29734
270843 282624 282556 280479 288763 292815

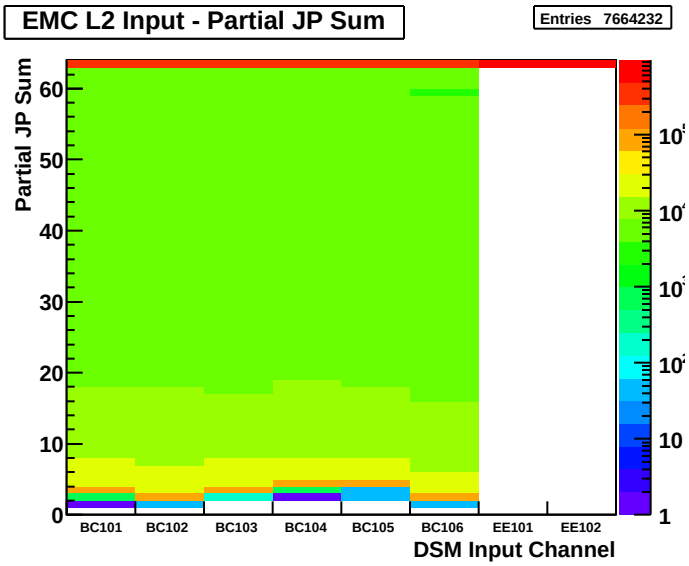

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

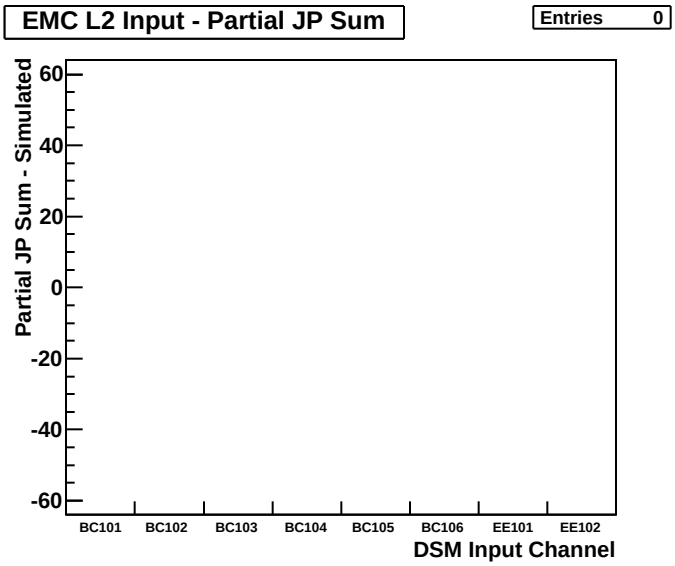

EMC L2 Input - JPY/JPB bits
Entries 7664232
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
583704 572752 579211 577270 579461 585583 958029 958029
59740 60727 58658 60024 61187 61509
30016 30085 29277 29632 30133 30767
274569 294465 288983 291103 287248 280170

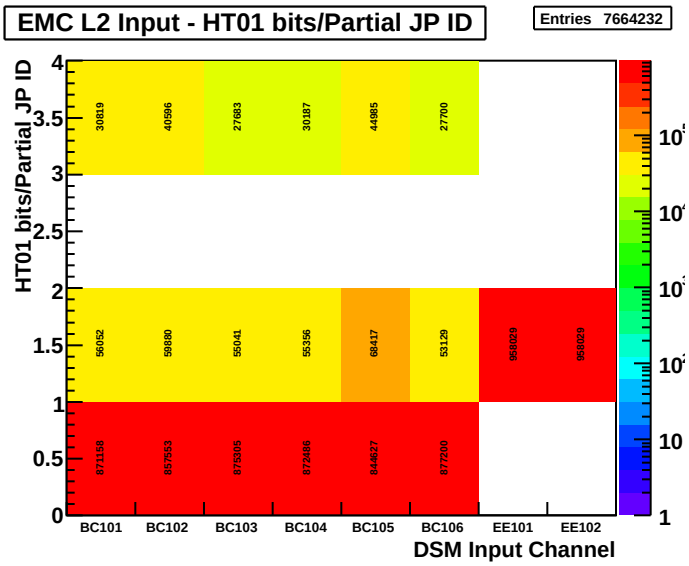

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

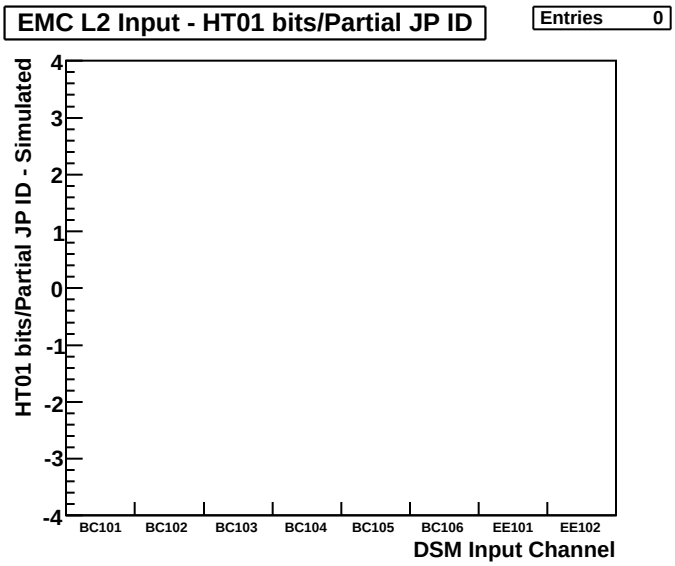

EMC L2 Input - JPZ/JPC bits
Entries 7664232
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
584643 568185 579426 584143 592035 584900 958029 958029
61447 61504 60771 63229 61336 60157
30104 30171 30277 31577 30608 29900
281835 298169 287555 279080 274050 283072

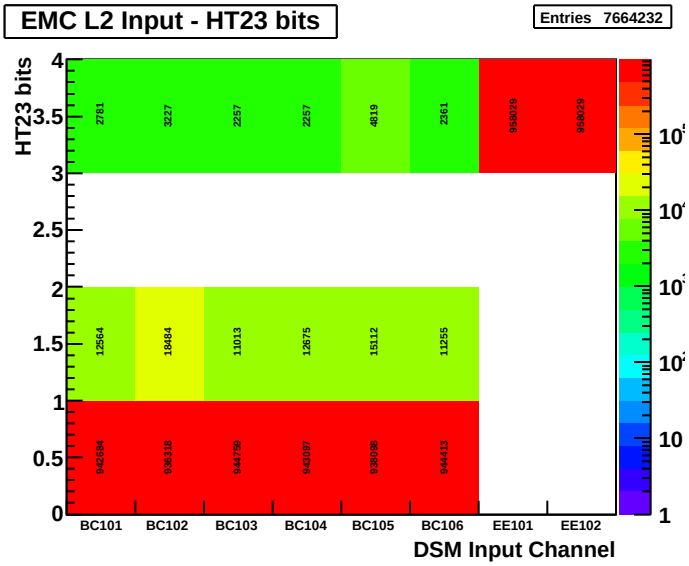

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

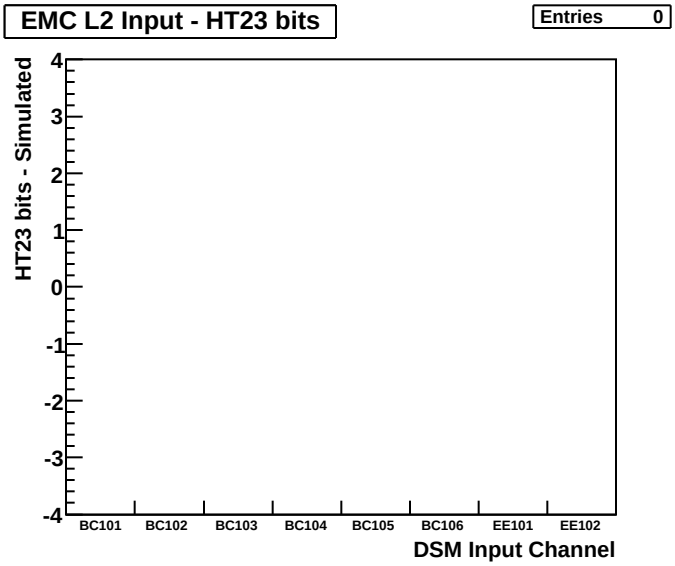

EMC L2 Input - Partial JP Sum
Entries 7664232
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

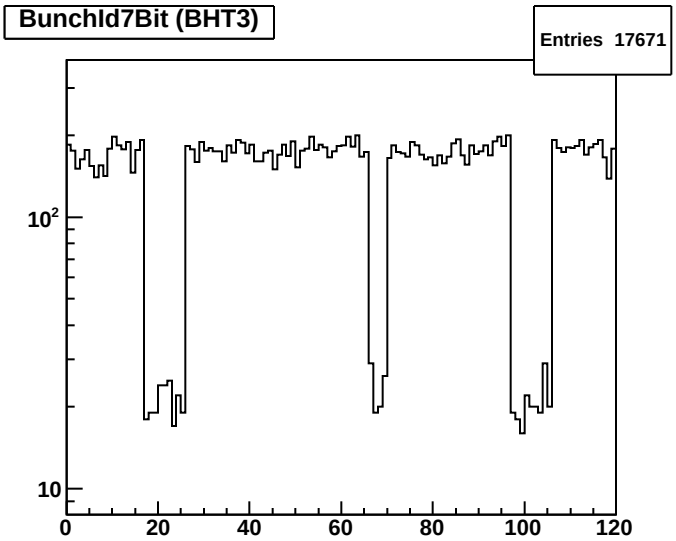

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

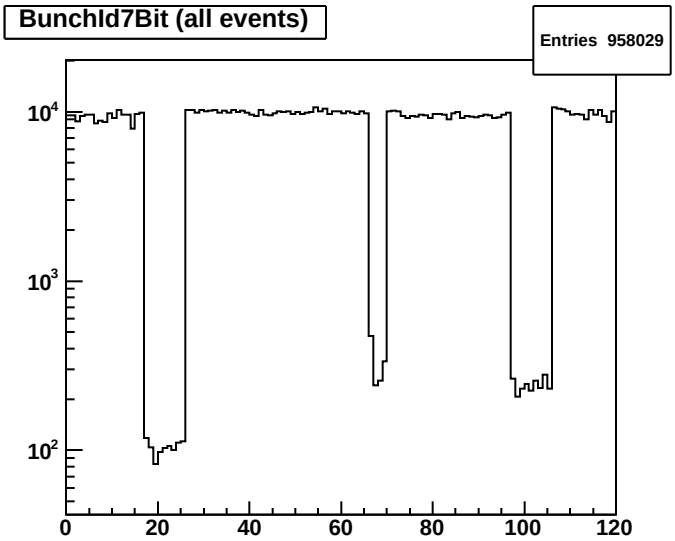

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7664232
HT01 bits/Partial JP ID
30819 40596 27683 30187 44985 27700
56052 59880 55041 55356 68417 53129 958029 958029
871158 857553 875305 872486 844627 877200
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

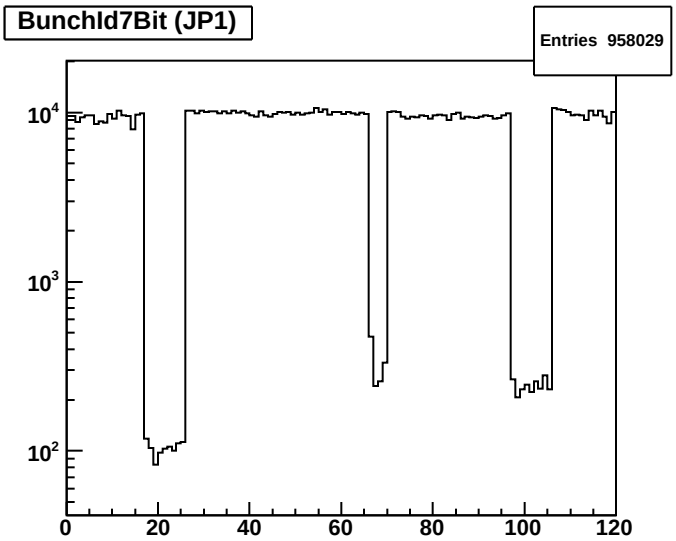

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

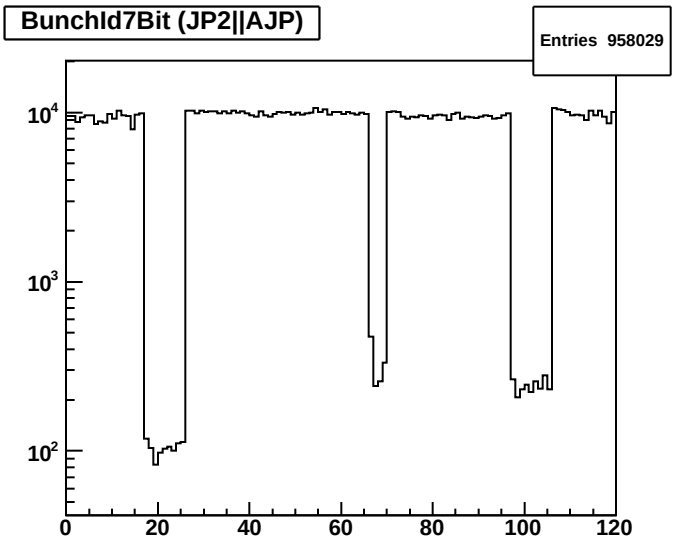

EMC L2 Input - HT23 bits
Entries 7664232
HT23 bits
2781 3227 2257 2257 4919 2361 958029 958029
12564 18484 11013 12675 15112 11255
942584 936318 944759 943097 938098 944413
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

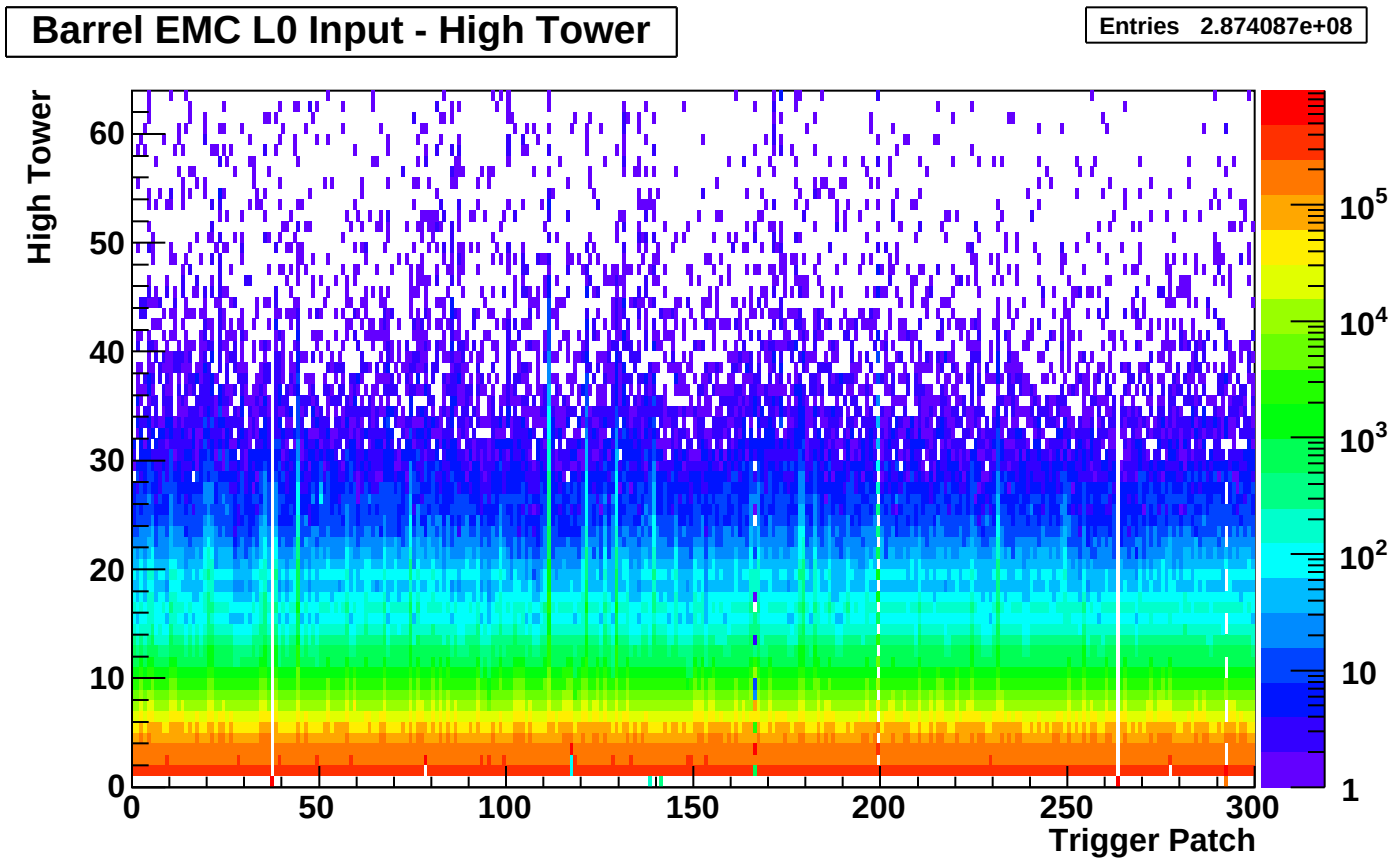

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

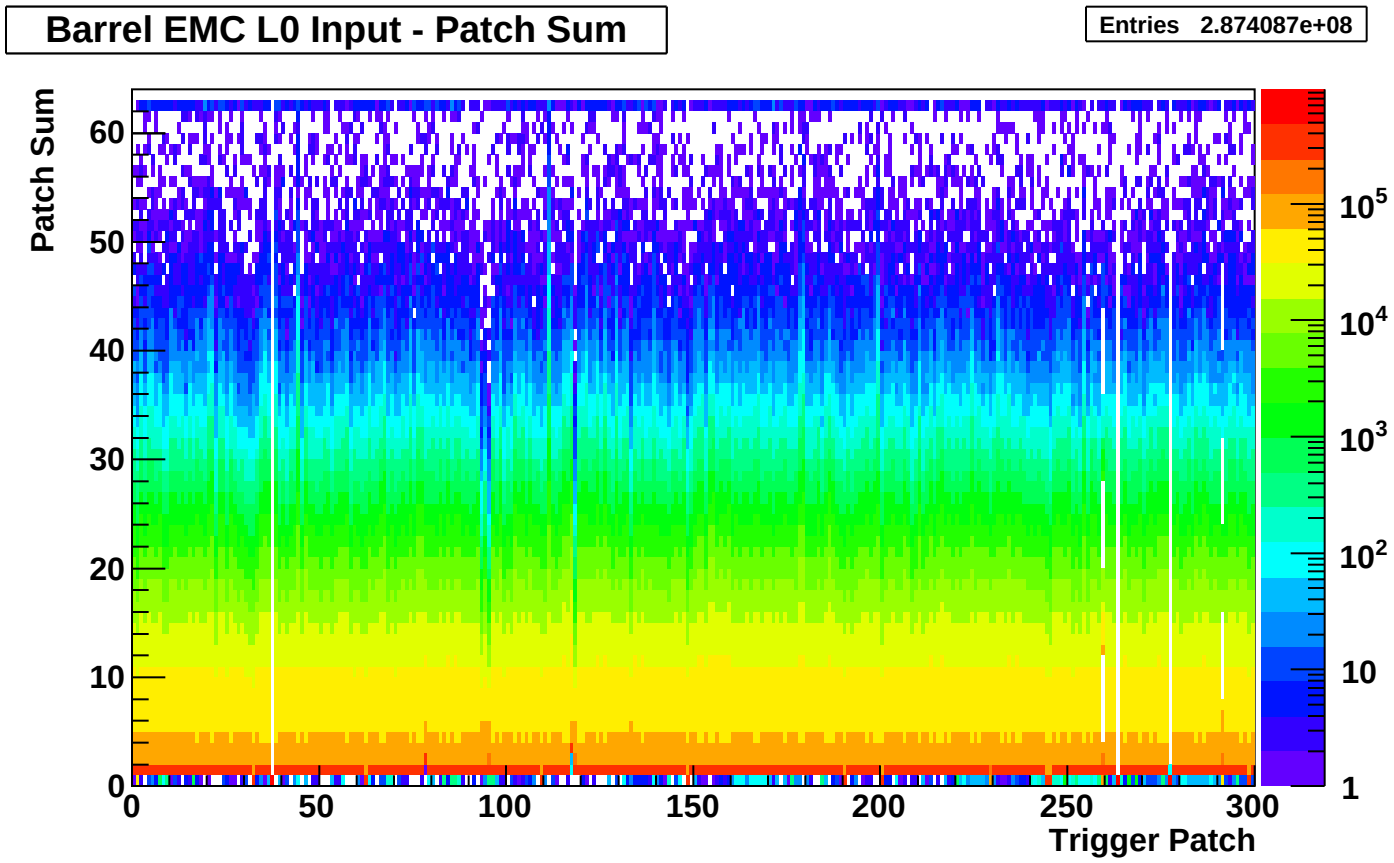

BunchId7Bit (BHT3)
Entries 17671
10^2
10
0
20
40
60
80
100
120

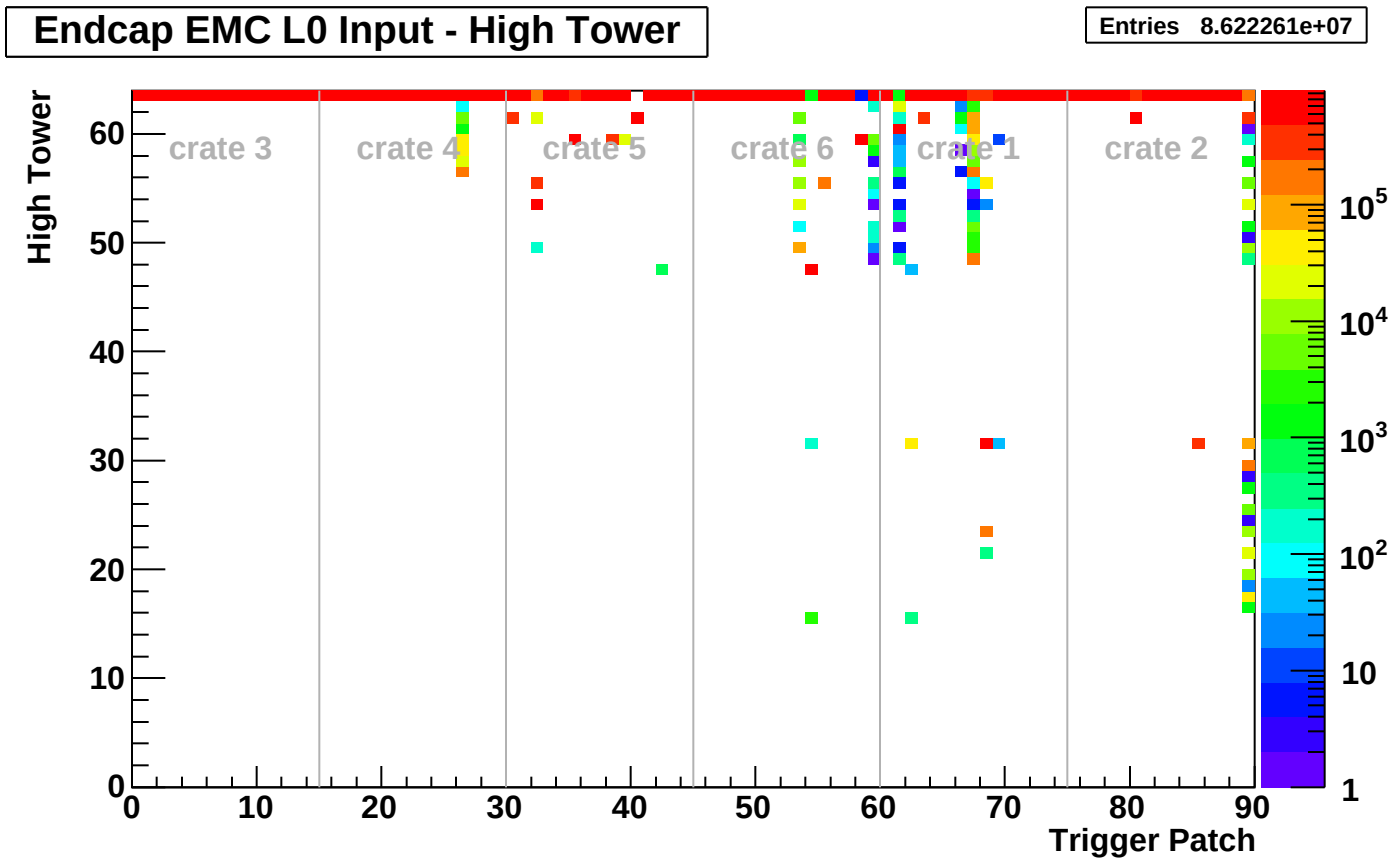

BunchId7Bit (all events)
Entries 958029
10^4
10^3
10^2
0
20
40
60
80
100
120

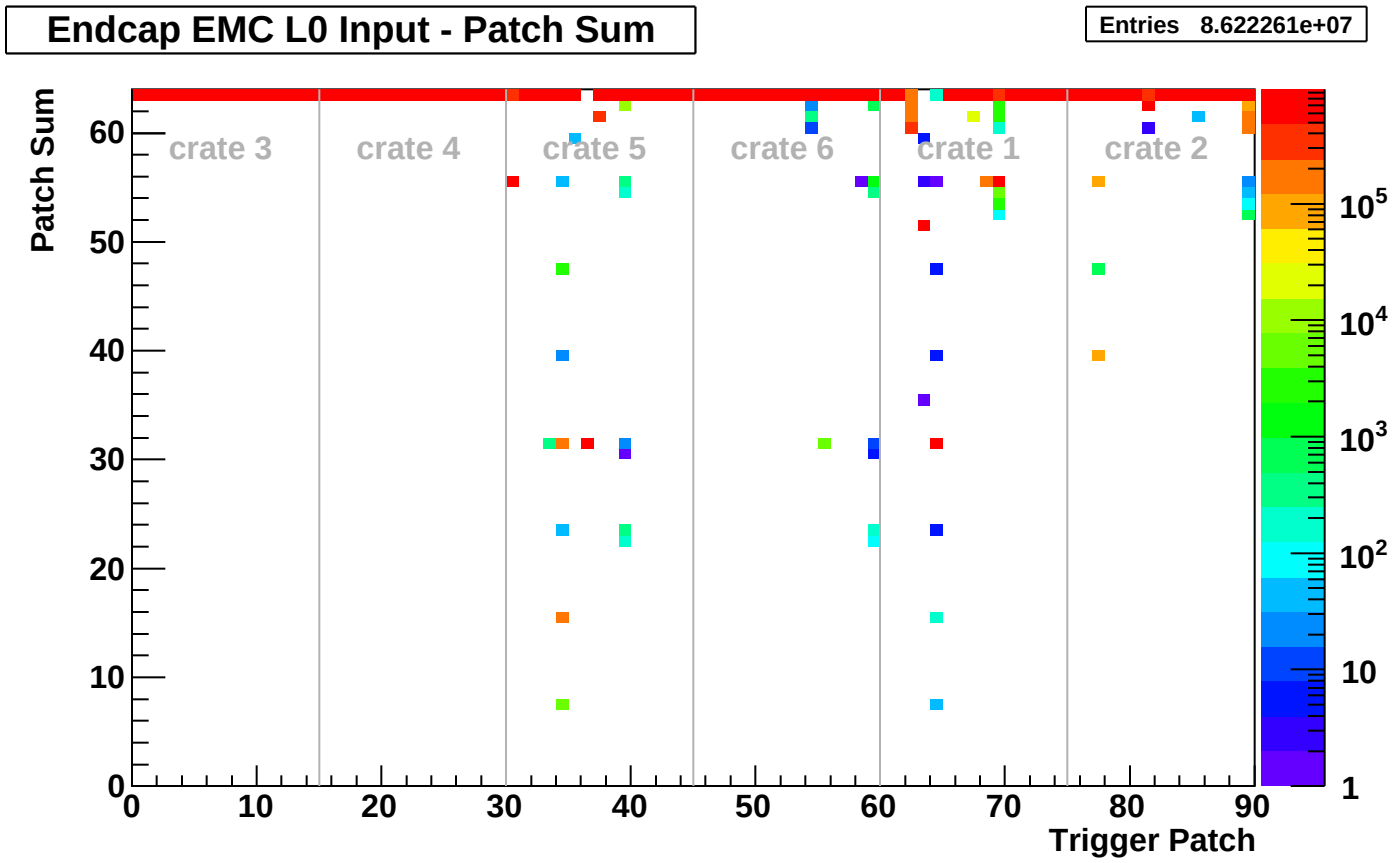

BunchId7Bit (JP1)
Entries 958029
10^4
10^3
10^2
0
20
40
60
80
100
120

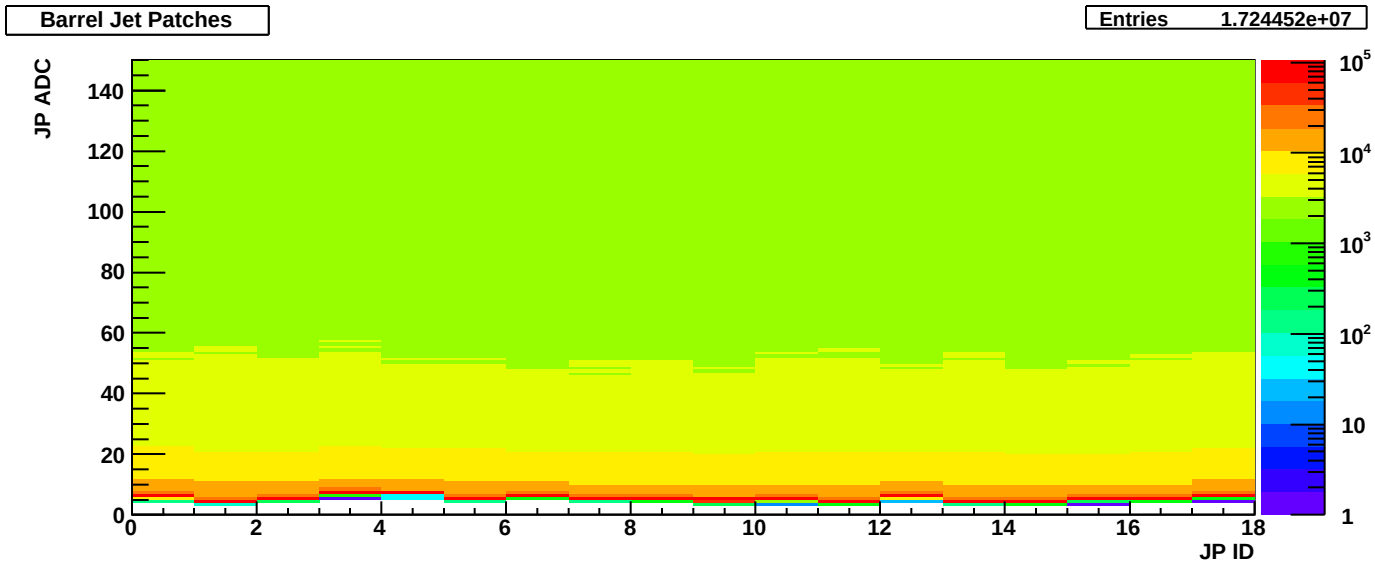

BunchId7Bit (JP2||AJP)
Entries 958029
10^4
10^3
10^2
0
20
40
60
80
100
120

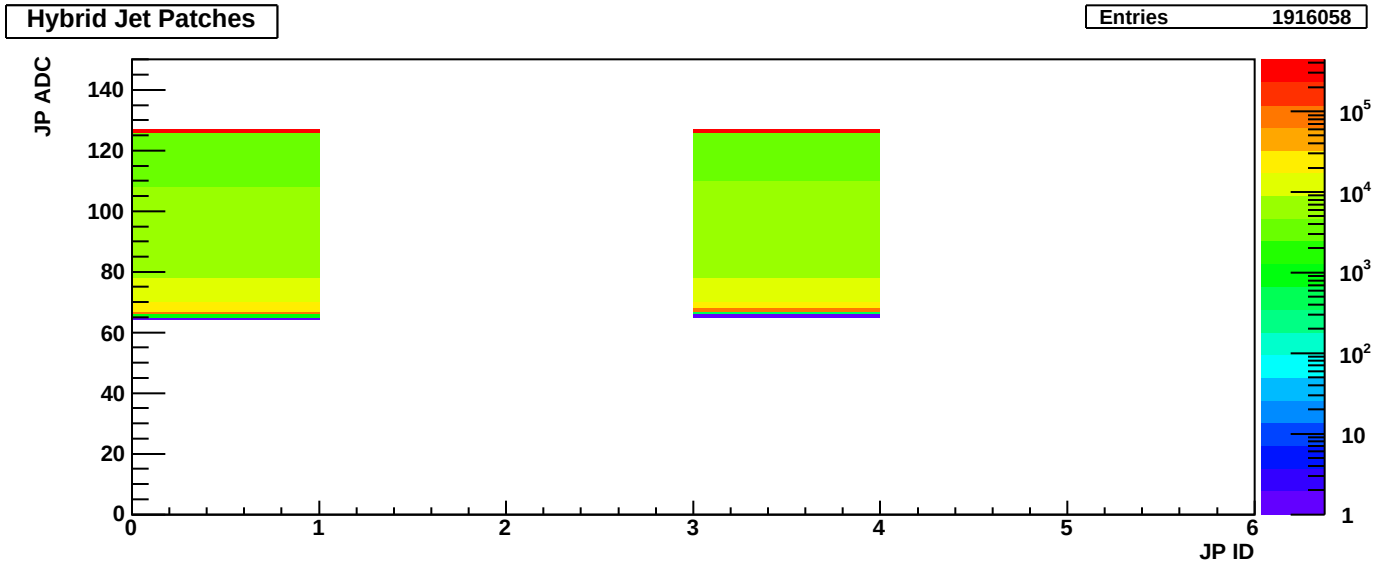

Barrel EMC L0 Input - High Tower
Entries 2.874087e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

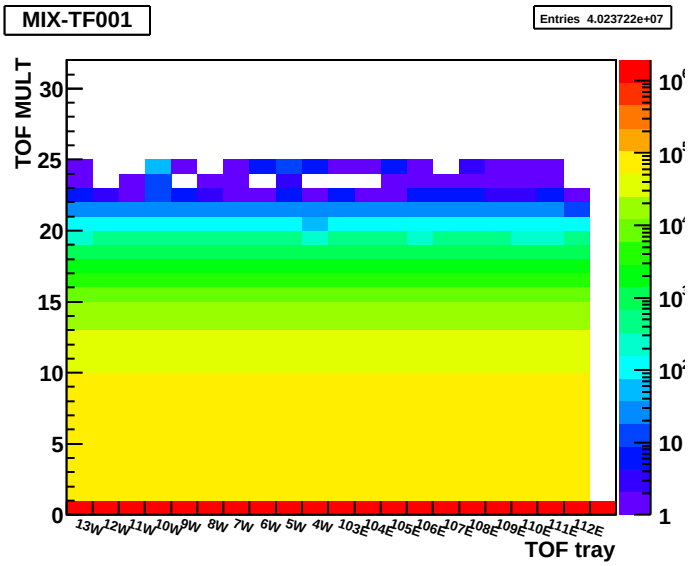

Barrel EMC L0 Input - Patch Sum
Entries 2.874087e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

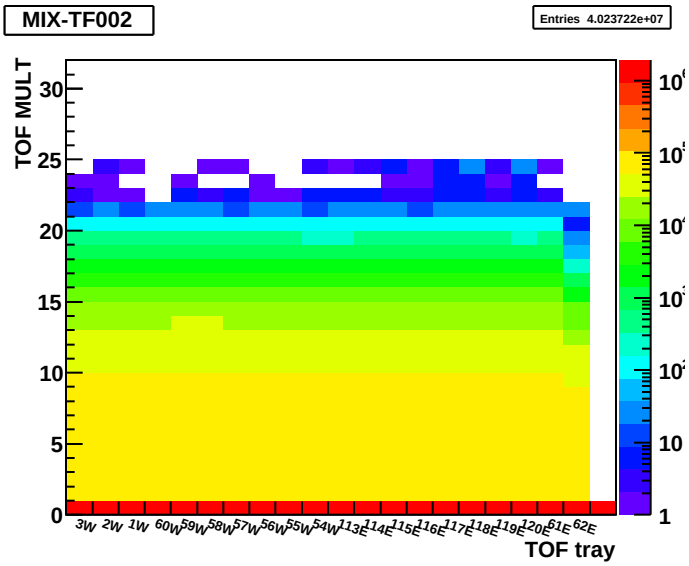

Endcap EMC L0 Input - High Tower
Entries 8.622261e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

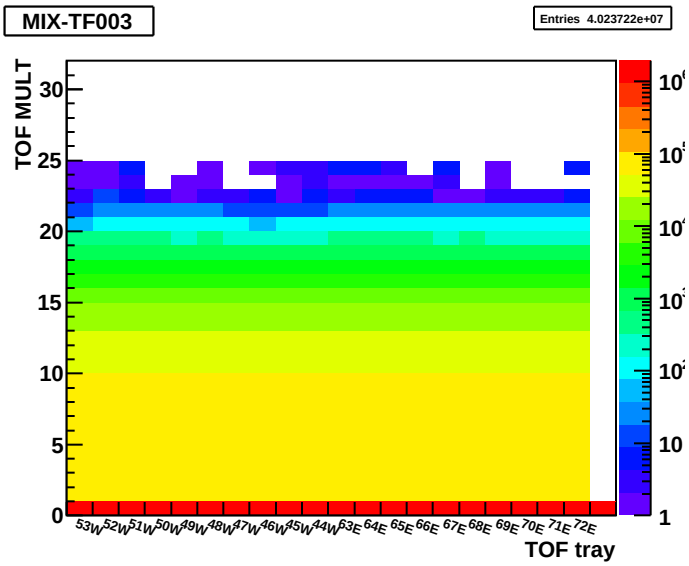

Endcap EMC L0 Input - Patch Sum
Entries 8.622261e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

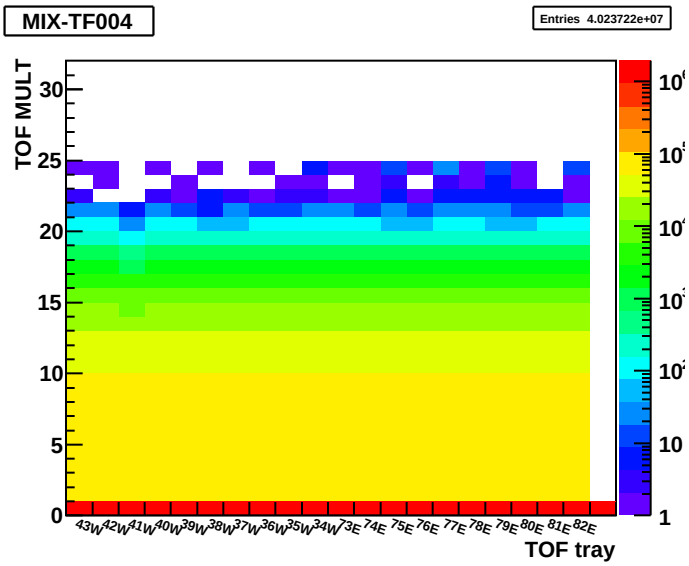

Barrel Jet Patches
Entries 1.724452e+07
JP ADC
JP ID

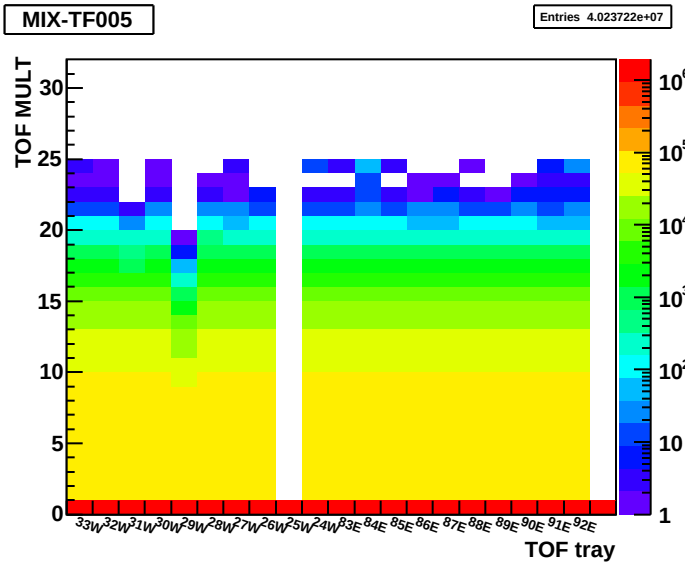

Hybrid Jet Patches
Entries 1916058
JP ADC
JP ID

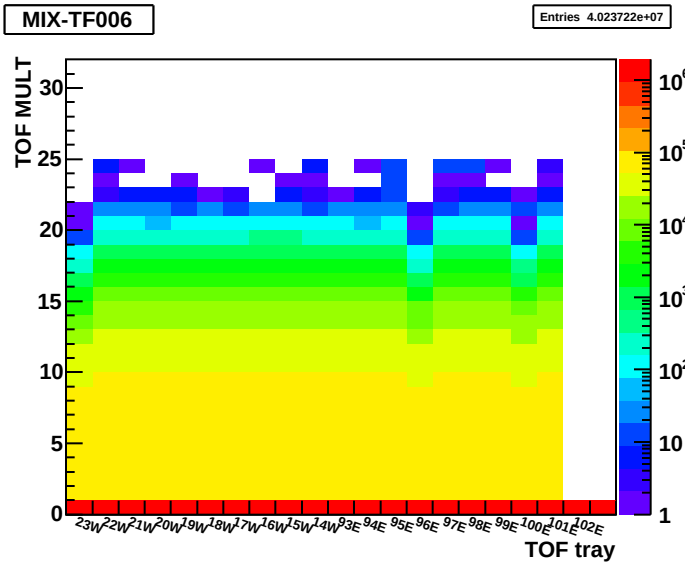

MIX-TF001
Entries 4.023722e+07
TOF MULT
TOF tray

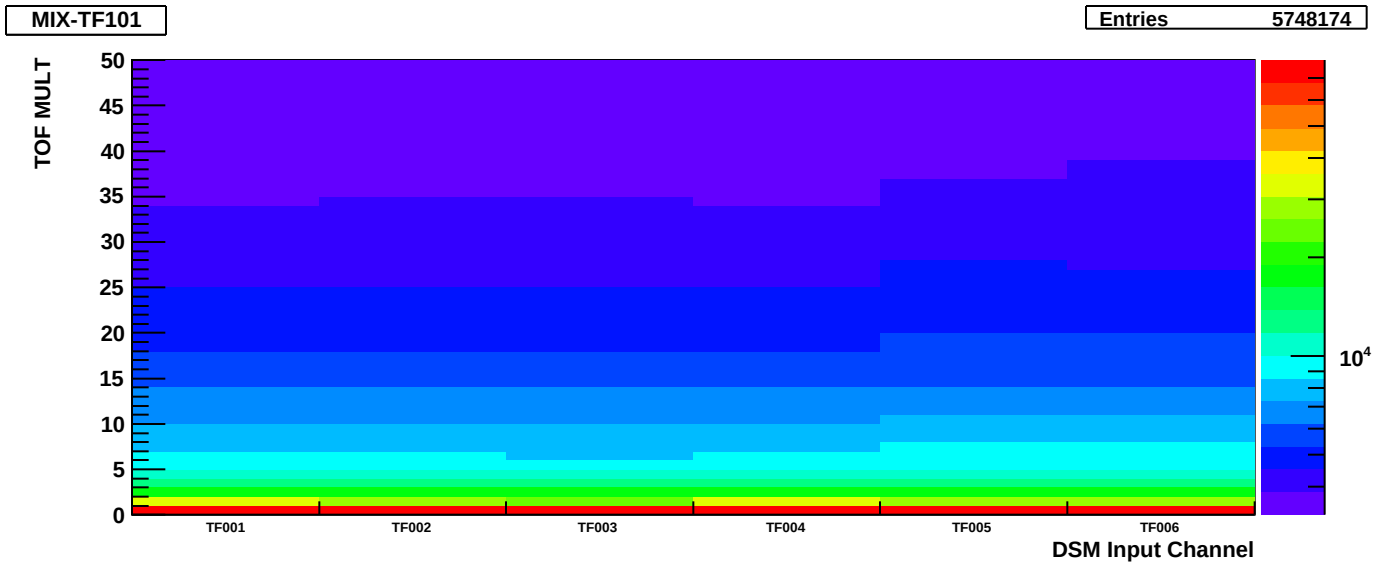

MIX-TF002
Entries 4.023722e+07
TOF MULT
TOF tray

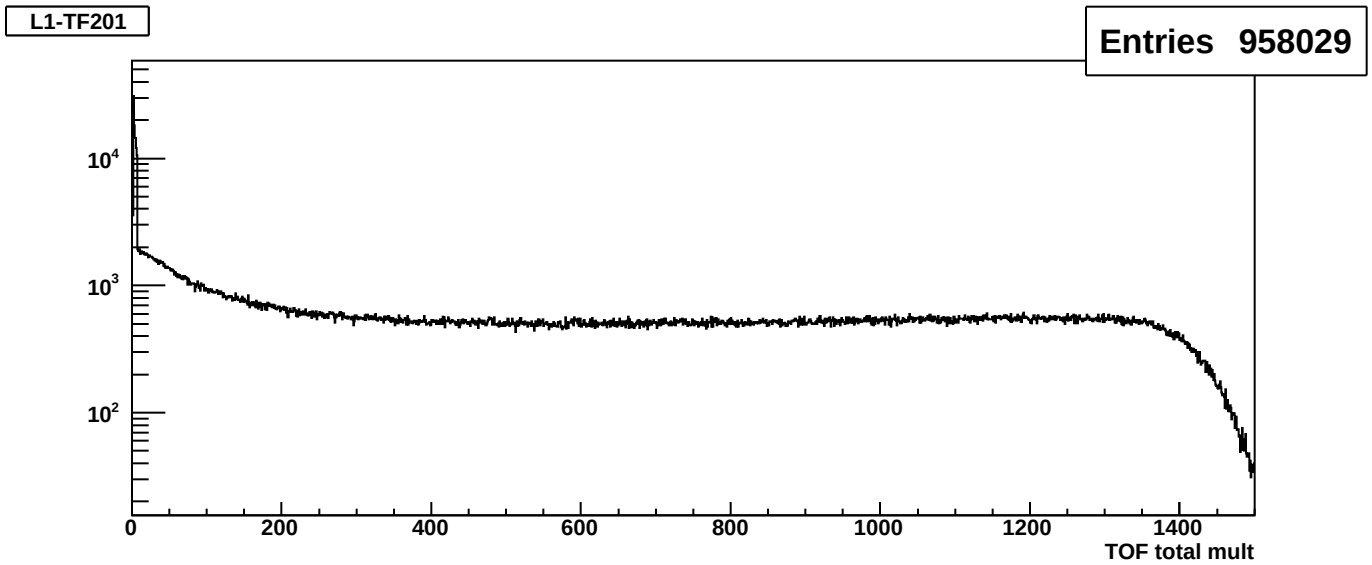

MIX-TF003
Entries 4.023722e+07
TOF MULT
TOF tray

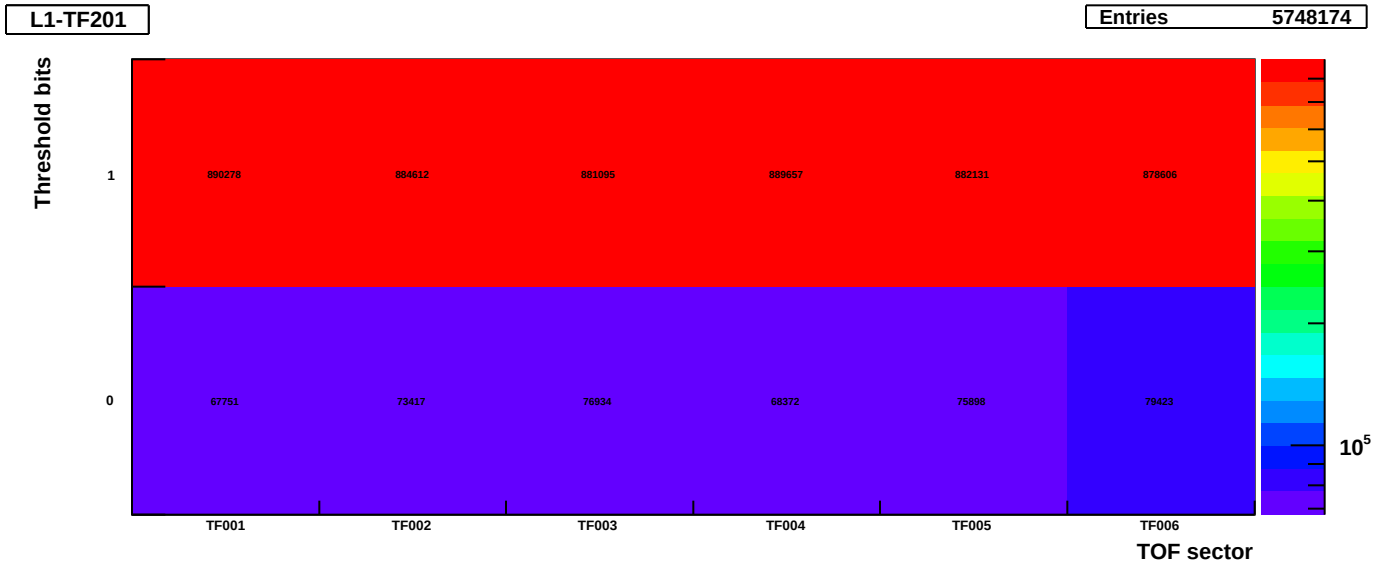

MIX-TF004
Entries 4.023722e+07
TOF MULT
TOF tray

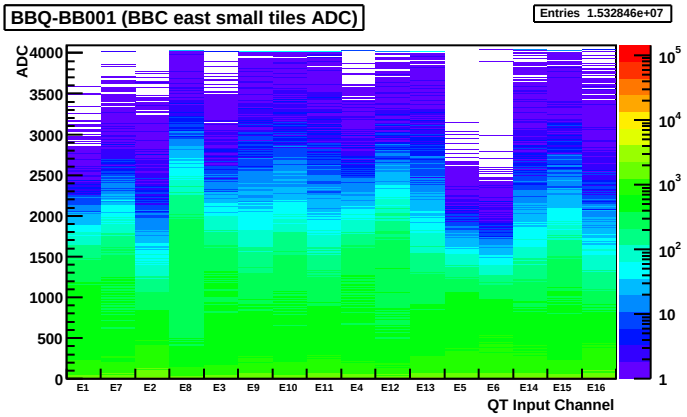

MIX-TF005
Entries 4.023722e+07
TOF MULT
TOF tray

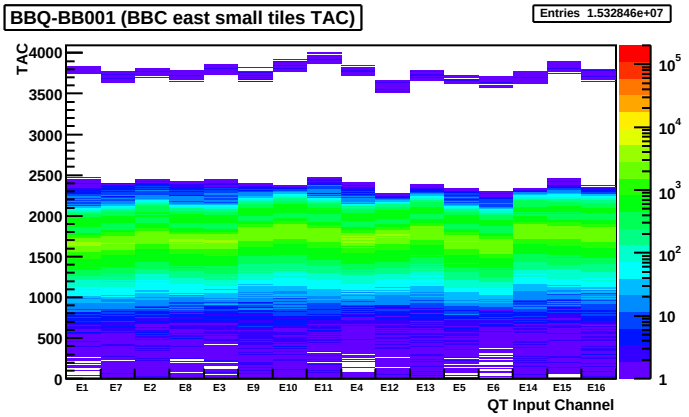

MIX-TF006
Entries 4.023722e+07
TOF MULT
TOF tray

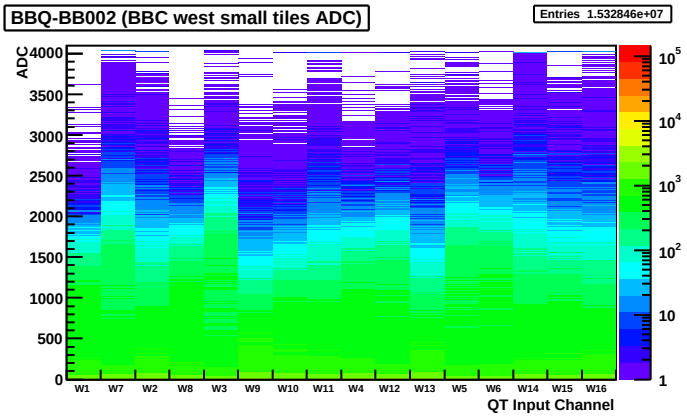

MIX-TF101
Entries 5748174
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

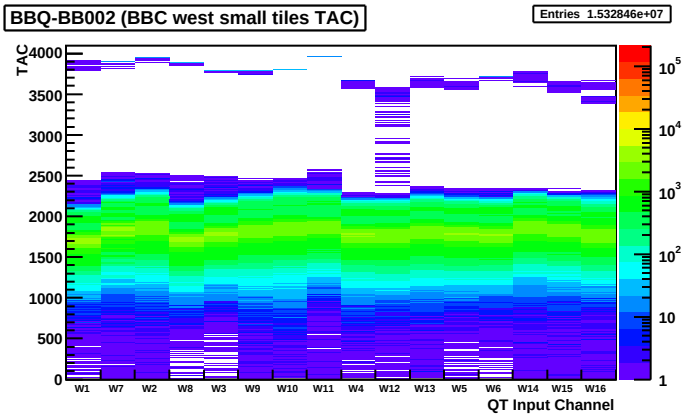

L1-TF201
Entries 958029
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

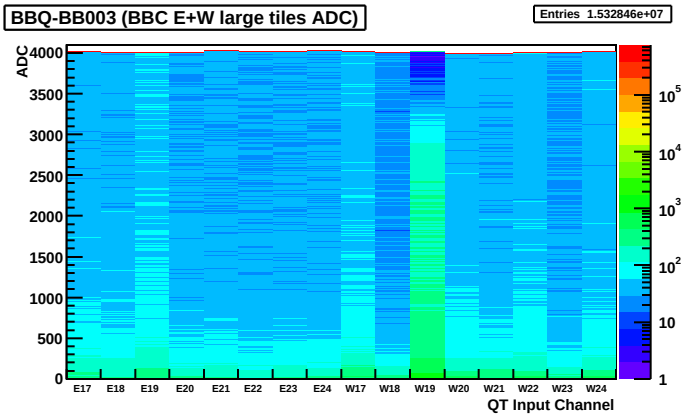

L1-TF201
Entries 5748174
Threshold bits
1
0
890278
884612
881095
889657
882131
878606
67751
73417
76934
68372
75898
79423
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

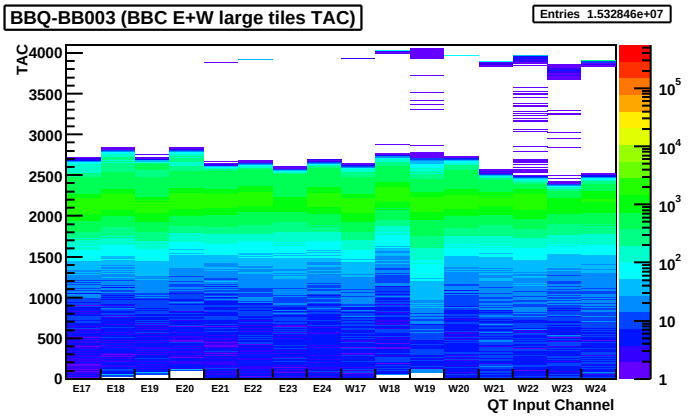

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.532846e+07
ADC
QT Input Channel

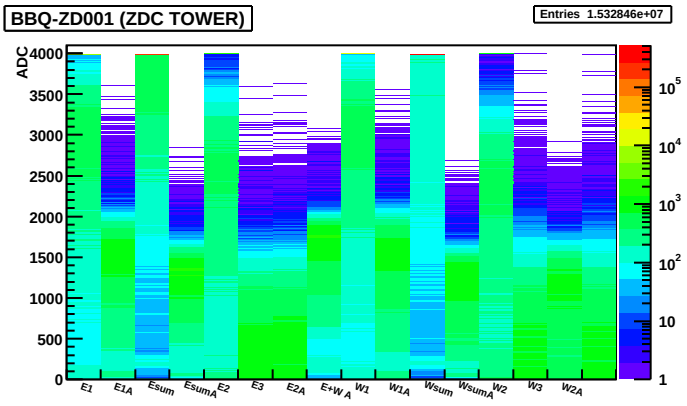

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.532846e+07
TAC
QT Input Channel

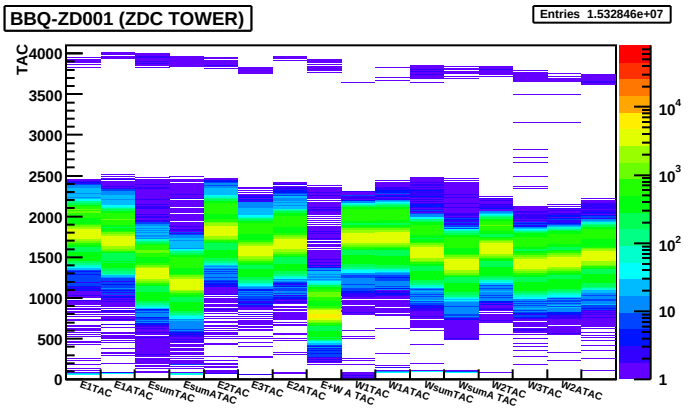

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.532846e+07
ADC
QT Input Channel

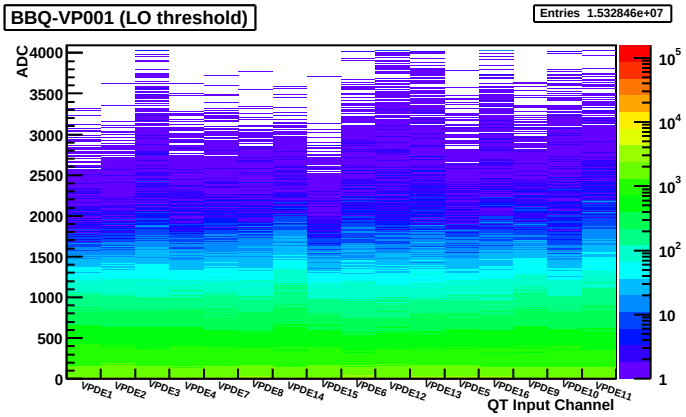

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.532846e+07
TAC
QT Input Channel

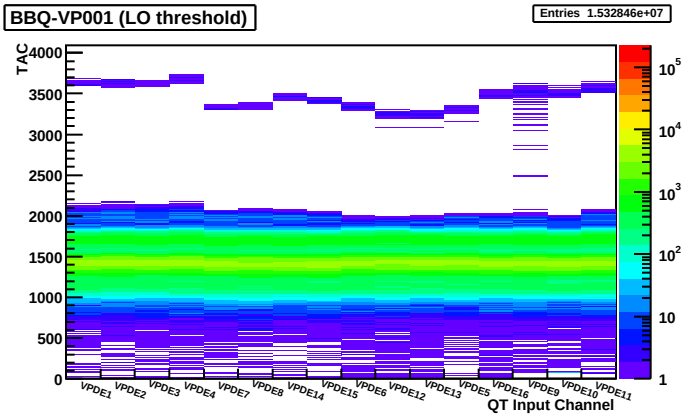

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.532846e+07
ADC
QT Input Channel

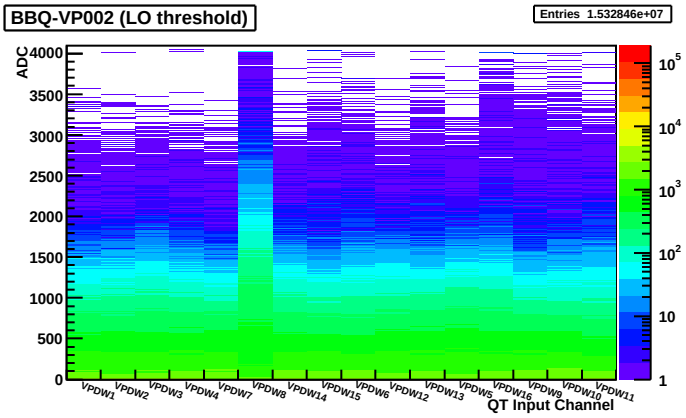

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.532846e+07
TAC
QT Input Channel

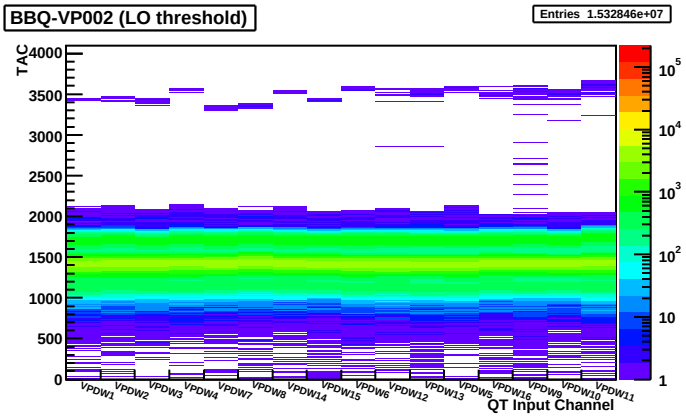

BBQ-ZD001 (ZDC TOWER)
Entries 1.532846e+07
ADC

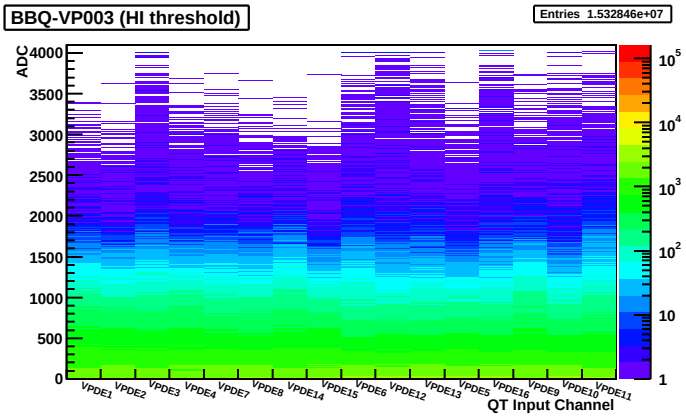

BBQ-ZD001 (ZDC TOWER)
Entries 1.532846e+07
TAC

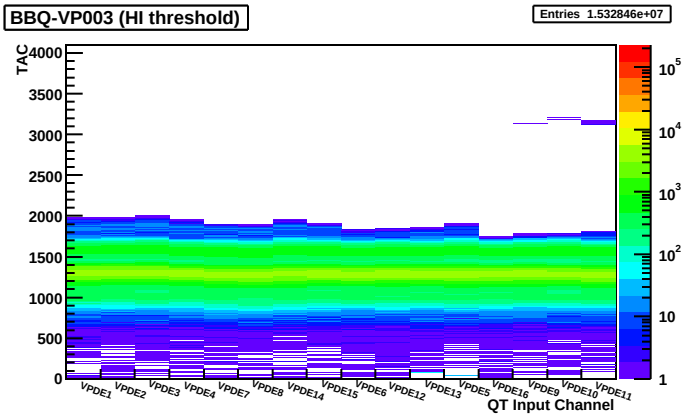

BBQ-VP001 (LO threshold)
Entries 1.532846e+07
ADC
QT Input Channel

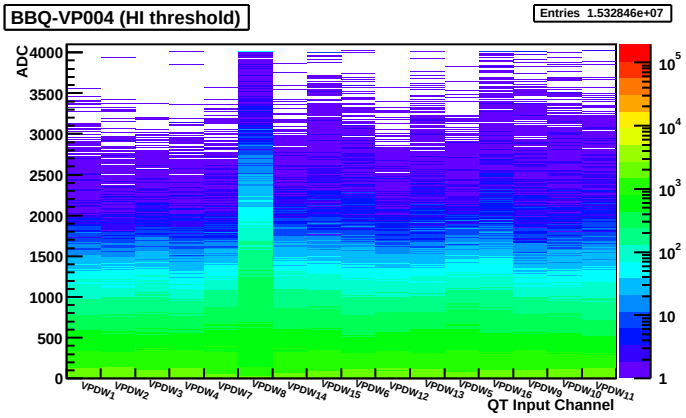

BBQ-VP001 (LO threshold)
Entries 1.532846e+07
TAC
QT Input Channel

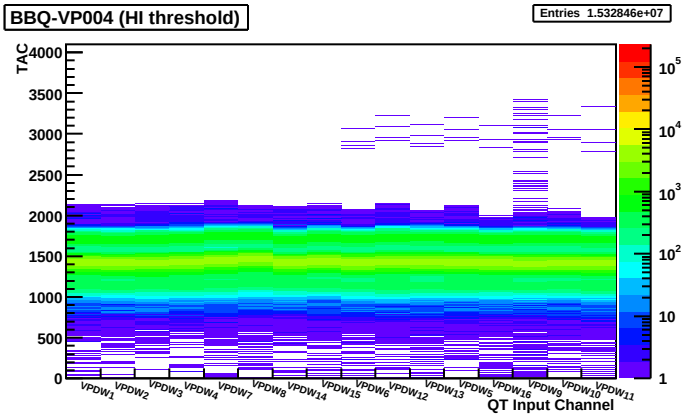

BBQ-VP002 (LO threshold)
Entries 1.532846e+07
ADC
QT Input Channel

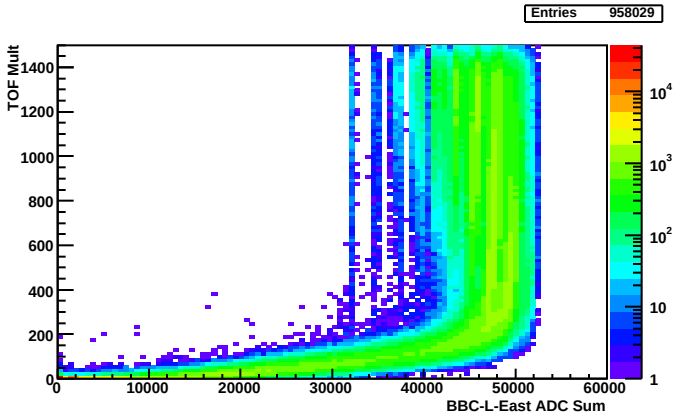

BBQ-VP002 (LO threshold)
Entries 1.532846e+07
TAC
QT Input Channel

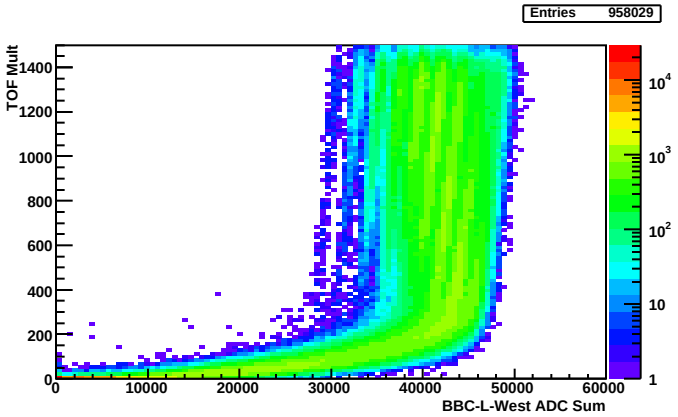

BBQ-VP003 (HI threshold)
Entries 1.532846e+07
ADC
QT Input Channel

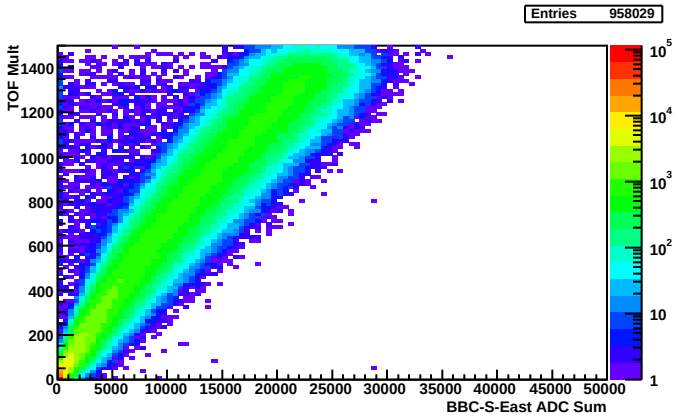

BBQ-VP003 (HI threshold)
Entries 1.532846e+07
TAC
QT Input Channel

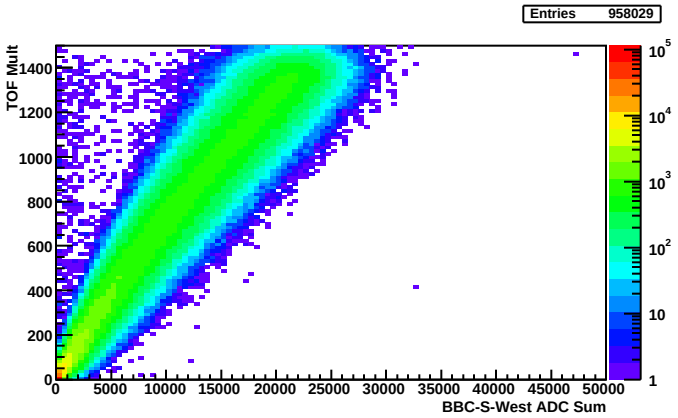

BBQ-VP004 (HI threshold)
Entries 1.532846e+07
ADC
QT Input Channel

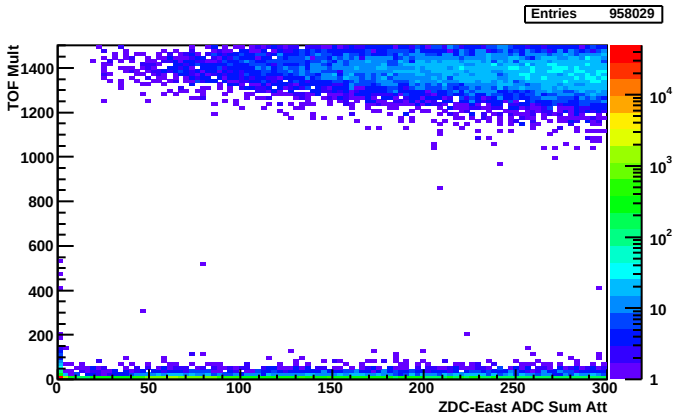

BBQ-VP004 (HI threshold)
Entries 1.532846e+07
TAC
QT Input Channel

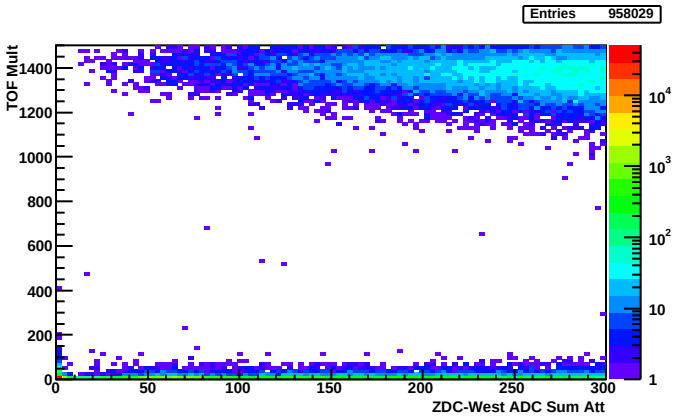

Entries 958029
TOF Mult
BBC-L-East ADC Sum

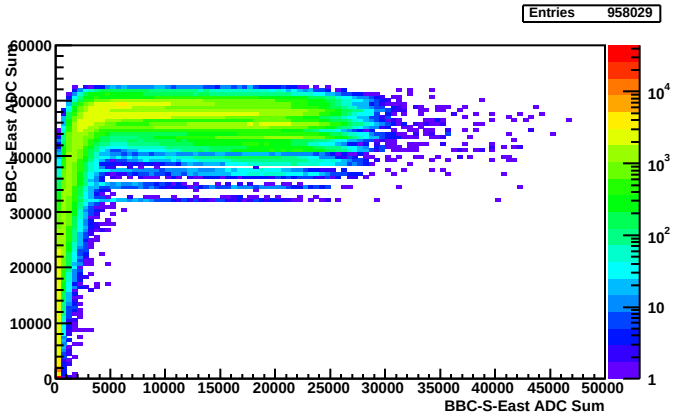

Entries 958029
TOF Mult
BBC-L-West ADC Sum

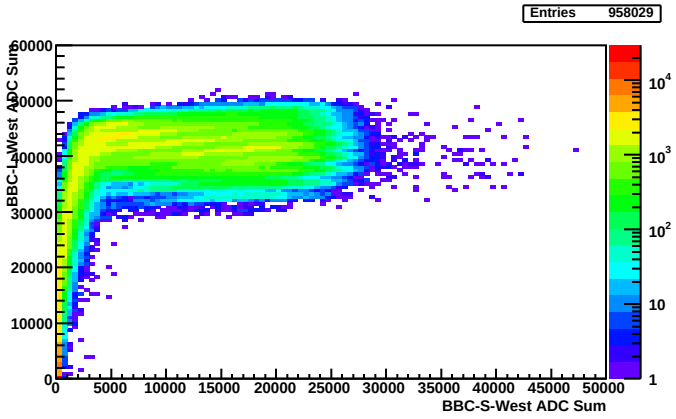

Entries 958029
TOF Mult
BBC-S-East ADC Sum

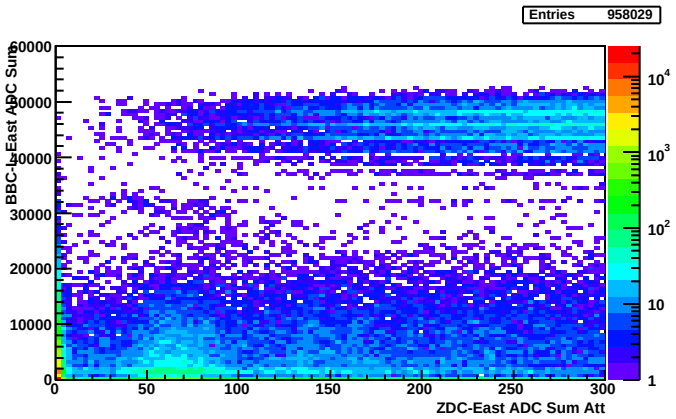

Entries 958029
TOF Mult
BBC-S-West ADC Sum

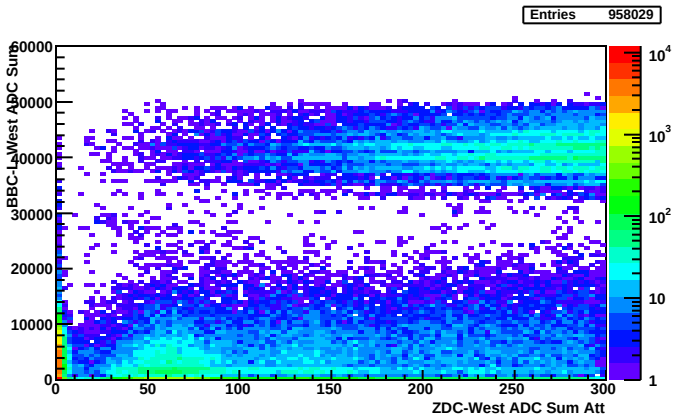

Entries 958029
TOF Mult
ZDC-East ADC Sum Att

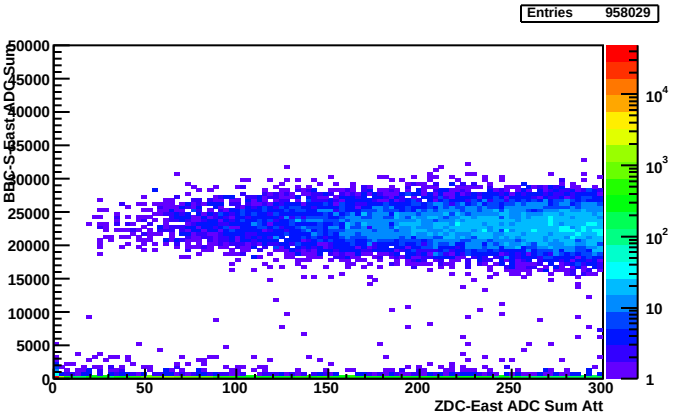

Entries 958029
TOF Mult
ZDC-West ADC Sum Att

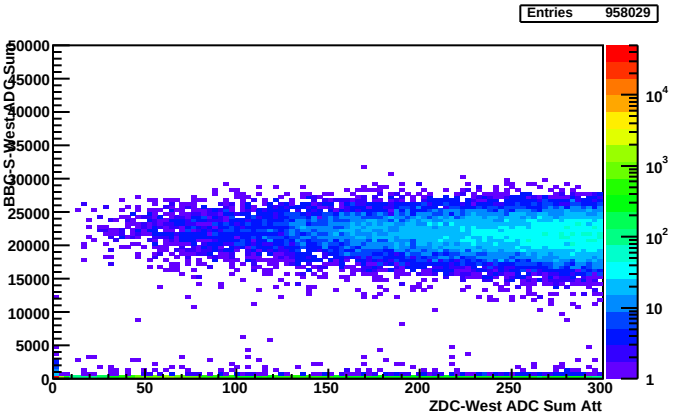

Entries 958029
BBC-L-East ADC Sum
BBC-S-East ADC Sum

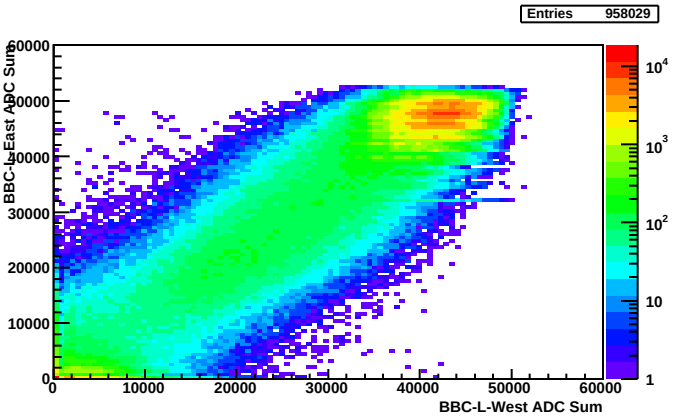

Entries 958029
BBC-L-West ADC Sum
BBC-S-West ADC Sum

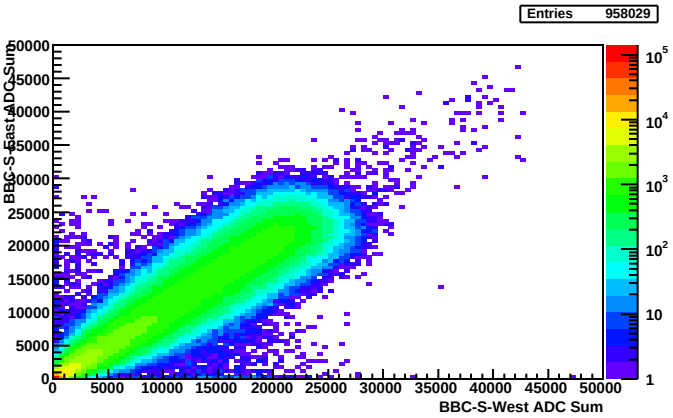

Entries 958029
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

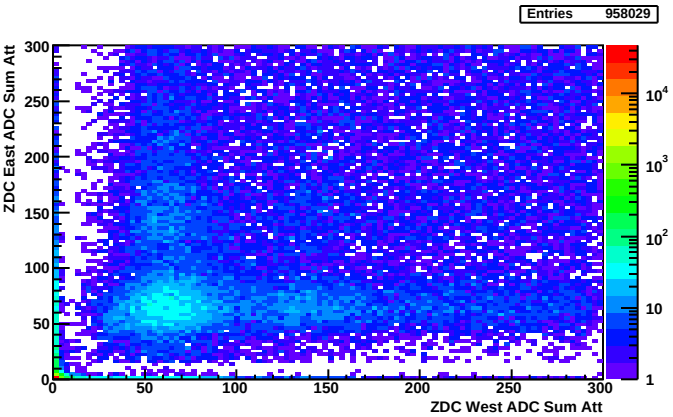

Entries 958029
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

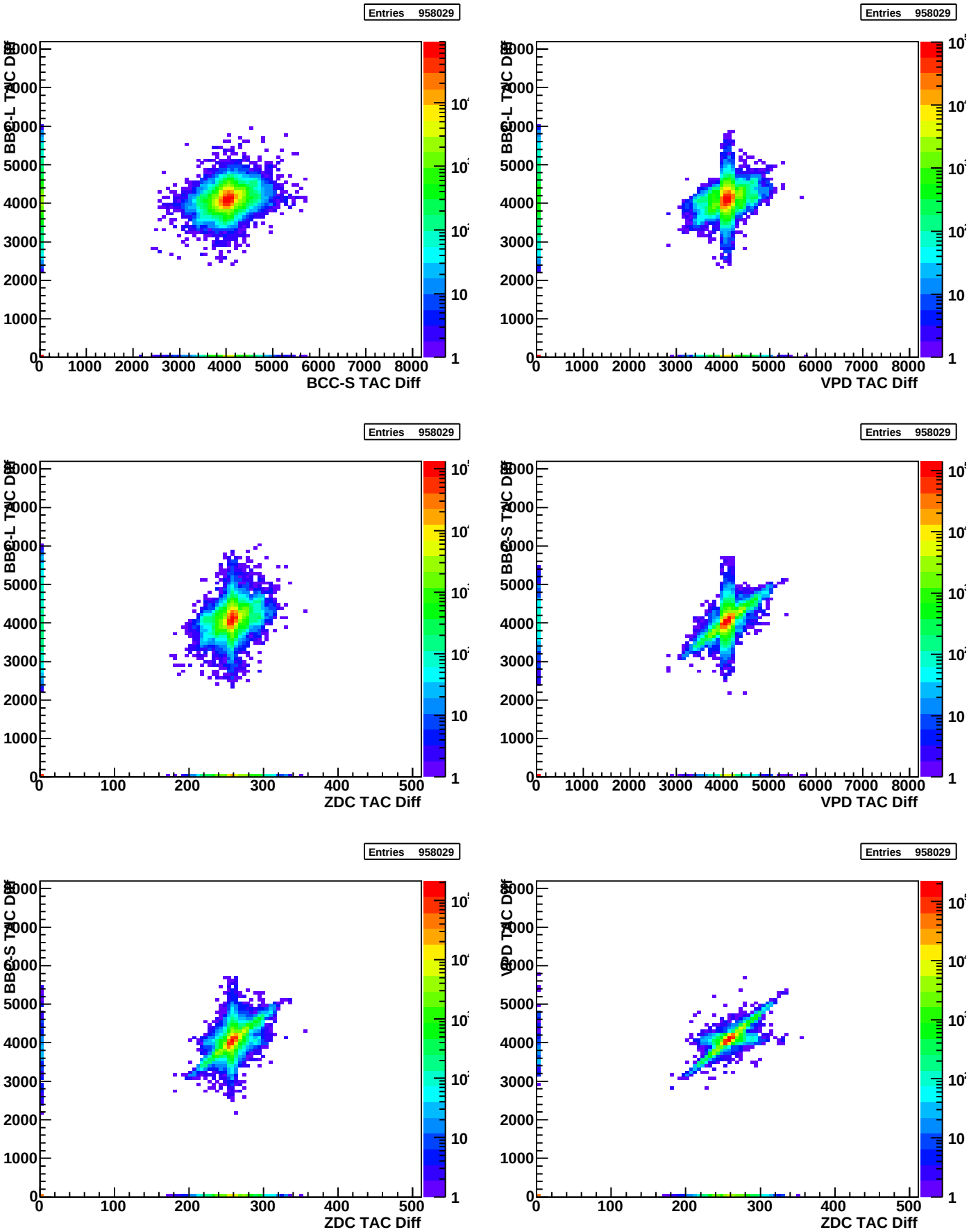

Entries 958029
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

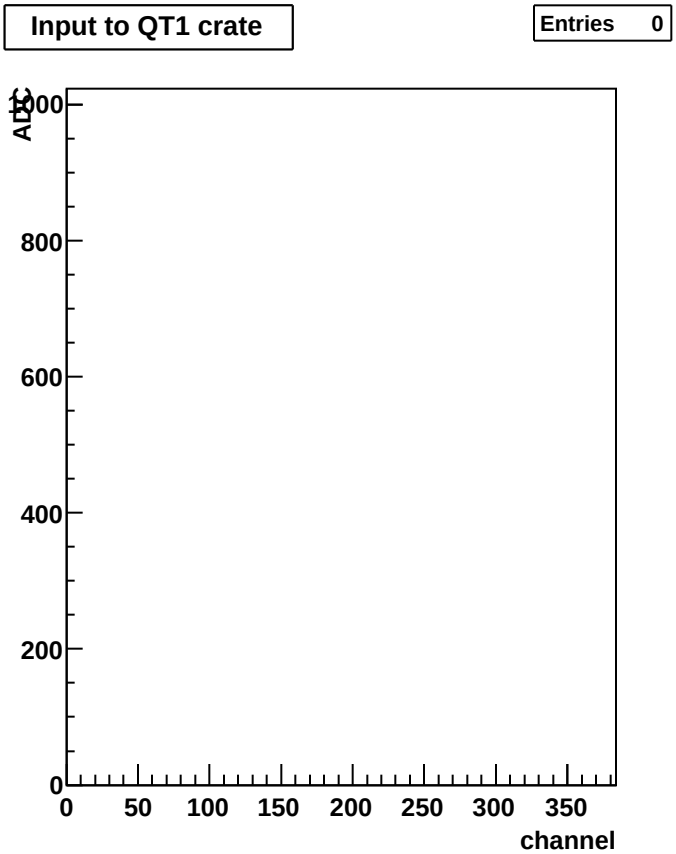

Entries 958029
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

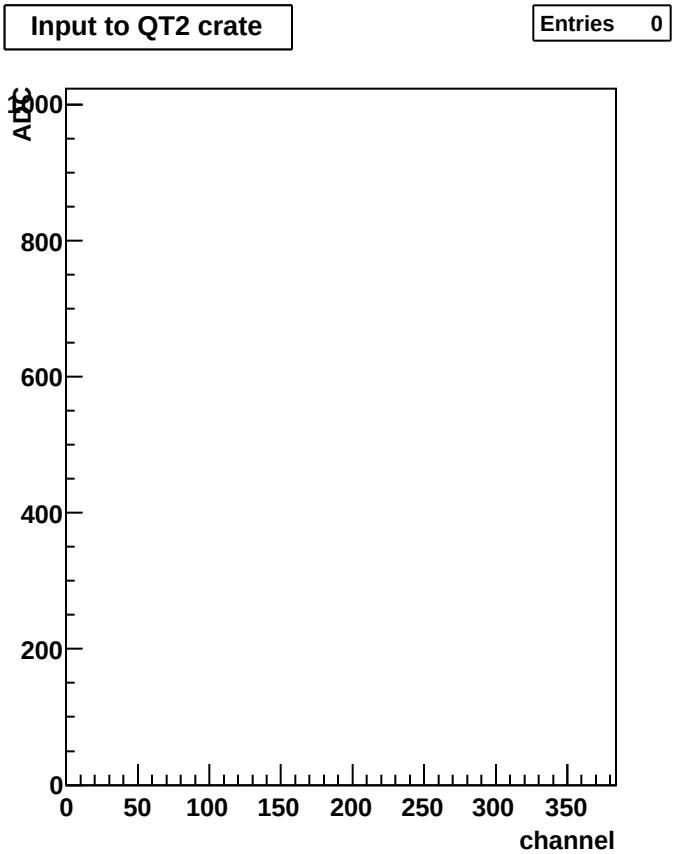

Entries 958029
BBC-L-East ADC Sum
BBC-L-West ADC Sum

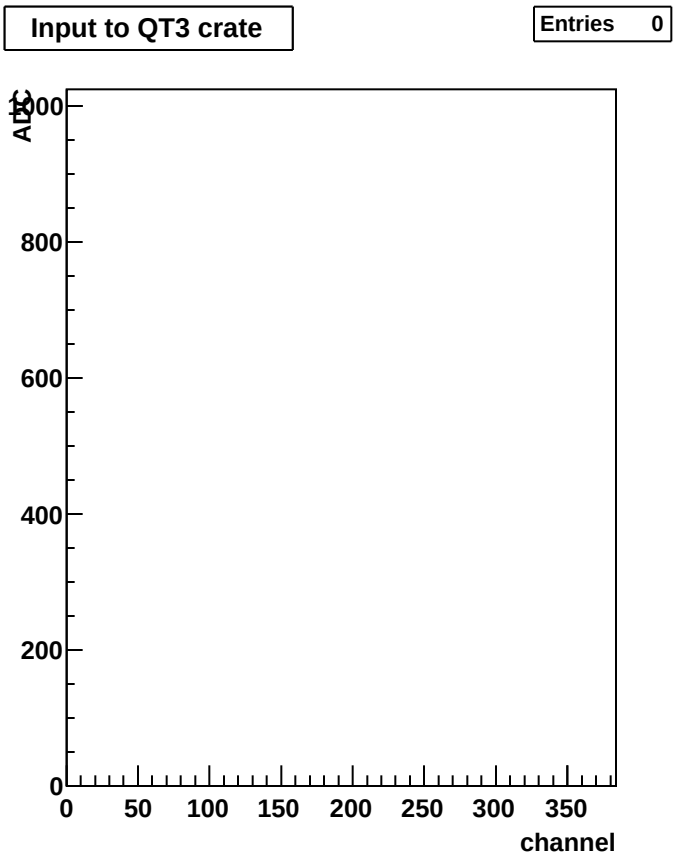

Entries 958029
BBC-S-East ADC Sum
BBC-S-West ADC Sum

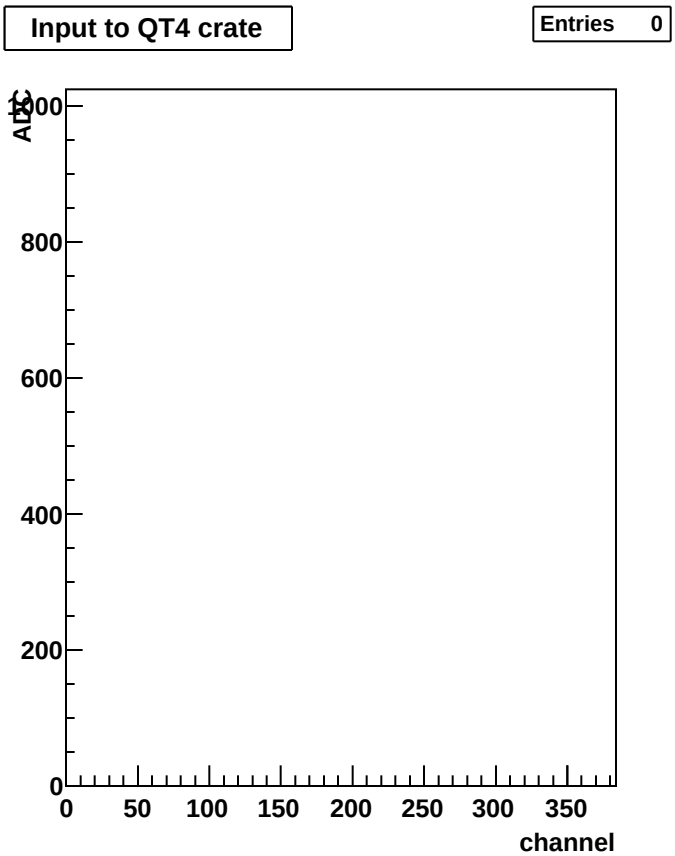

Entries 958029
ZDC East ADC Sum Att
ZDC West ADC Sum Att

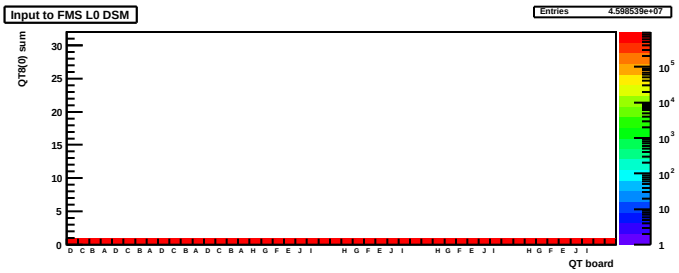

Entries 958029
BBC-L TAC Diff
BCC-S TAC Diff
Entries 958029
BBC-L TAC Diff
VPD TAC Diff
Entries 958029
BBC-L TAC Diff
ZDC TAC Diff
Entries 958029
BBC-S TAC Diff
VPD TAC Diff
Entries 958029
BBC-S TAC Diff
ZDC TAC Diff
Entries 958029
VPD TAC Diff
ZDC TAC Diff

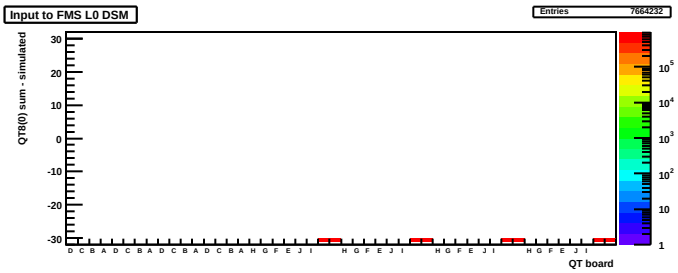

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

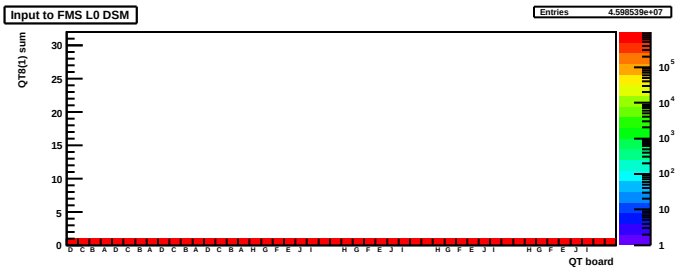

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

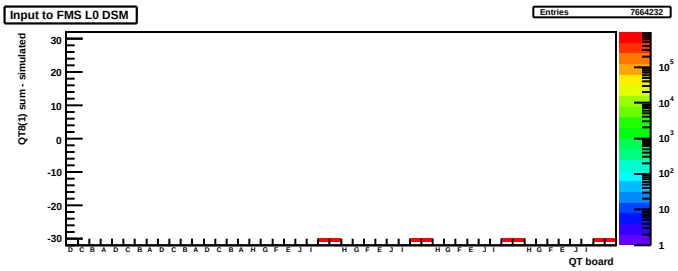

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

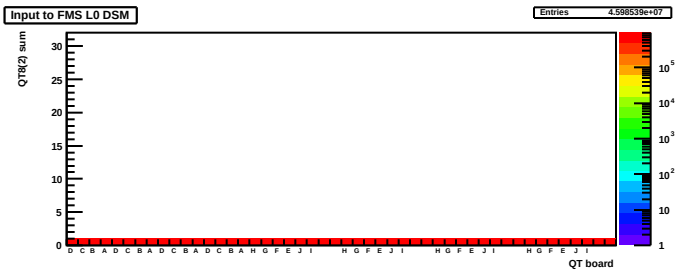

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

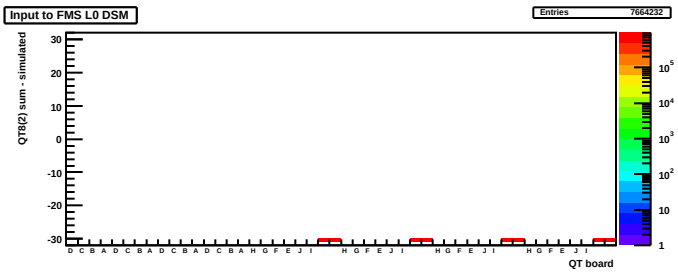

Input to FMS L0 DSM
Entries 4.598539e+07
QT8(0) sum
QT board

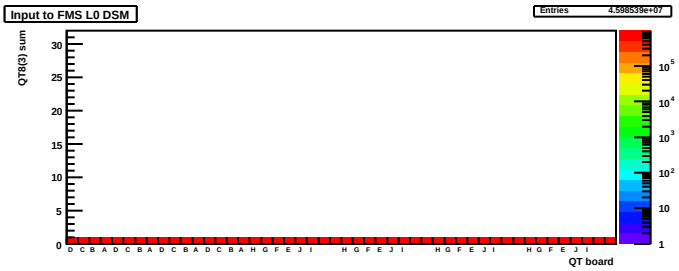

Input to FMS L0 DSM
Entries 7664232
QT8(0) sum - simulated
QT board

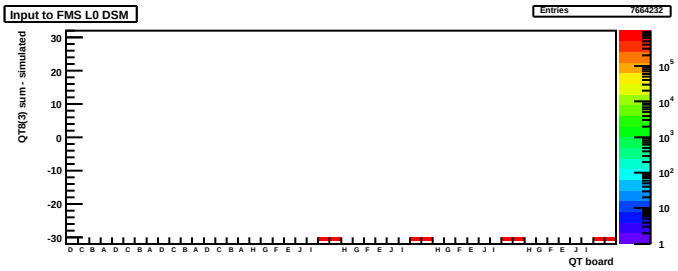

Input to FMS L0 DSM
Entries 4.598539e+07
QT8(1) sum
QT board

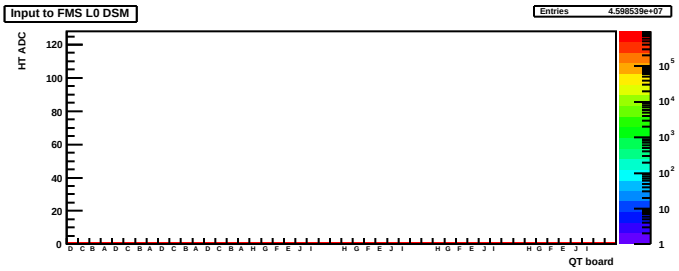

Input to FMS L0 DSM
Entries 7664232
QT8(1) sum - simulated
QT board

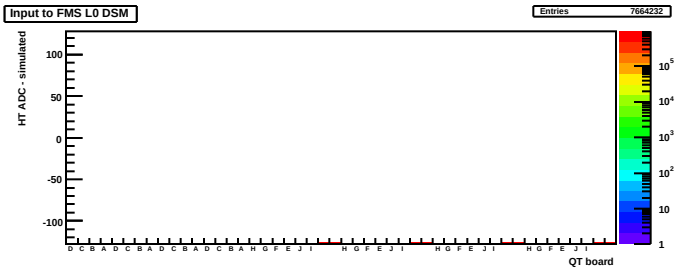

Input to FMS L0 DSM
Entries 4.598539e+07
QT8(2) sum
QT board

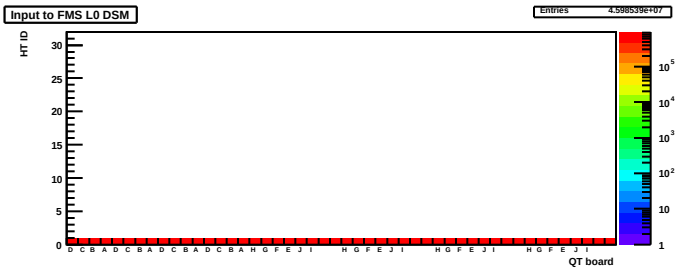

Input to FMS L0 DSM
Entries 7664232
QT8(2) sum - simulated
QT board

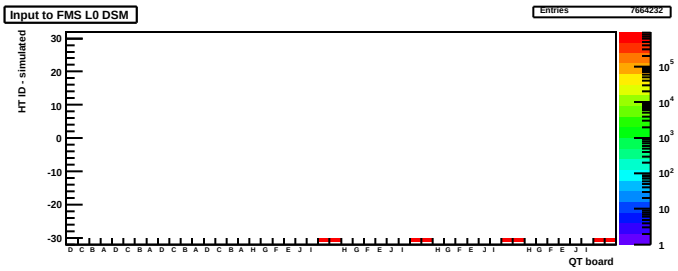

Input to FMS L0 DSM
Entries 4.598539e+07
QT8(3) sum
QT board

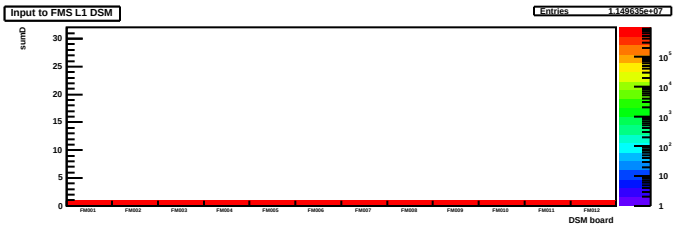

Input to FMS L0 DSM
Entries 7664232
QT8(3) sum - simulated
QT board

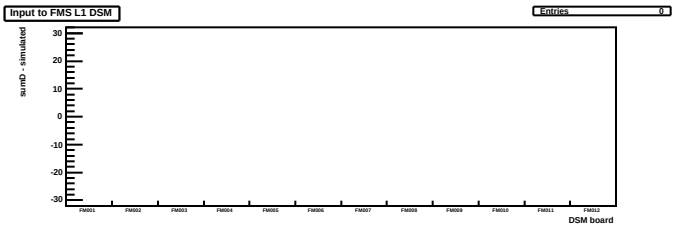

Input to FMS L0 DSM
Entries 4.598539e+07
HT ADC
QT board

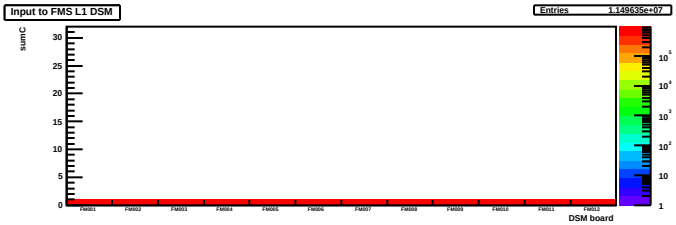

Input to FMS L0 DSM
Entries 7664232
HT ADC - simulated
QT board

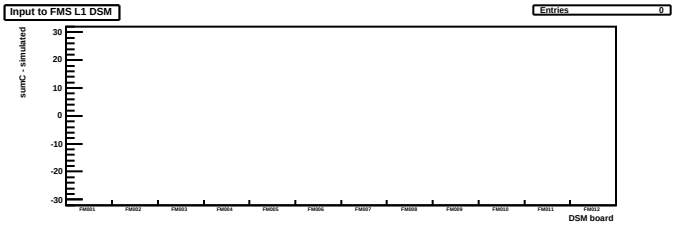

Input to FMS L0 DSM
Entries 4.598539e+07
HT ID
QT board

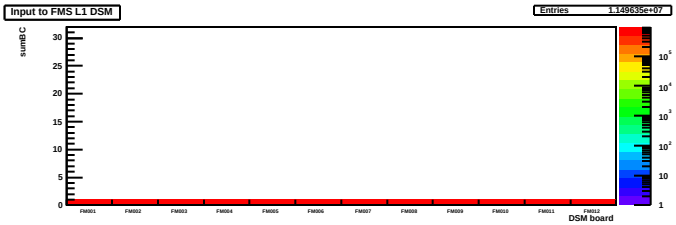

Input to FMS L0 DSM
Entries 7664232
HT ID - simulated
QT board

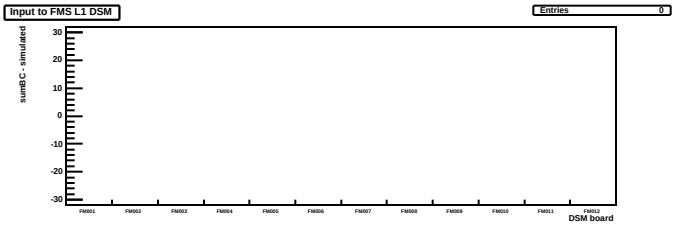

Input to FMS L1 DSM
Entries 1.149635e+07
sumD
DSM board

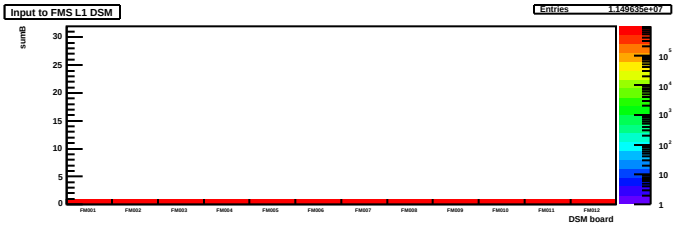

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

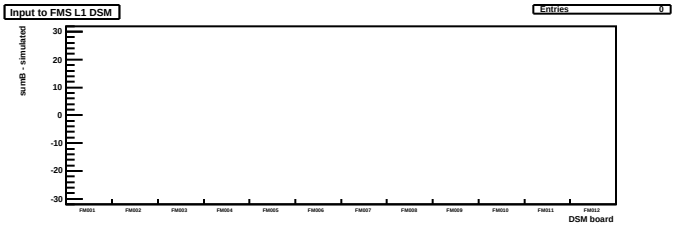

Input to FMS L1 DSM
Entries 1.149635e+07
sumC
DSM board

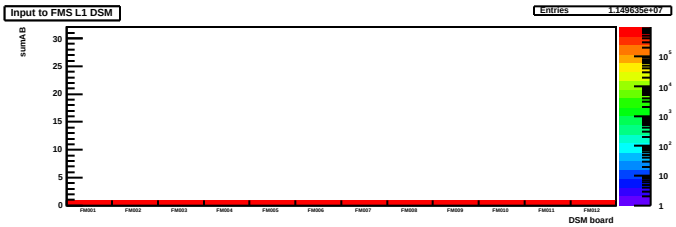

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

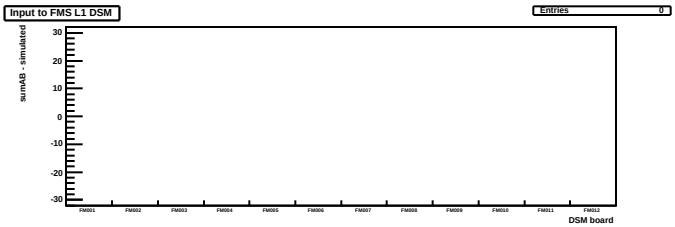

Input to FMS L1 DSM
Entries 1.149635e+07
sumBC
DSM board

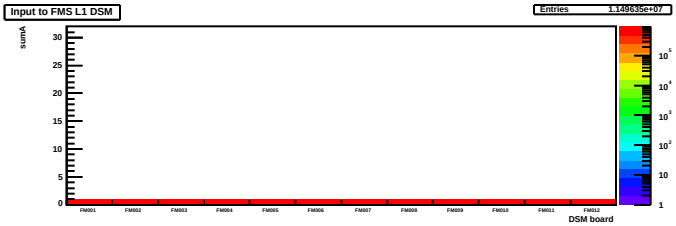

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

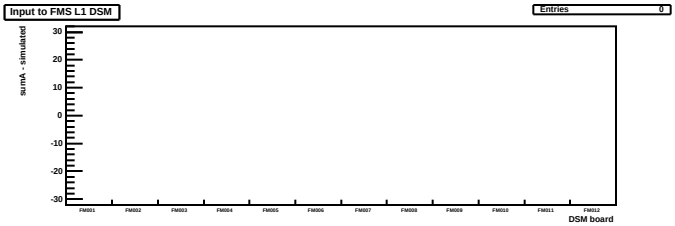

Input to FMS L1 DSM
Entries 1.149635e+07
sumB
DSM board

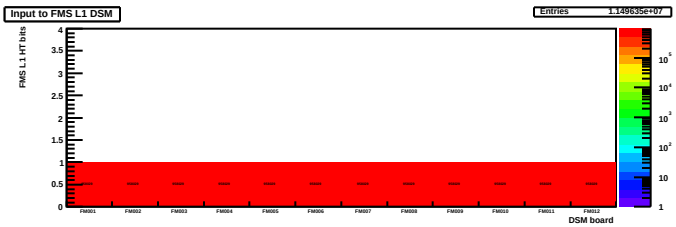

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

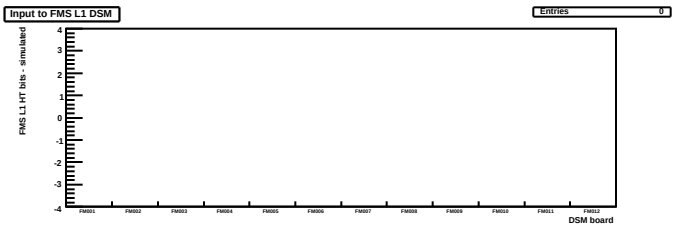

Input to FMS L1 DSM
Entries 1.149635e+07
sumAB
DSM board

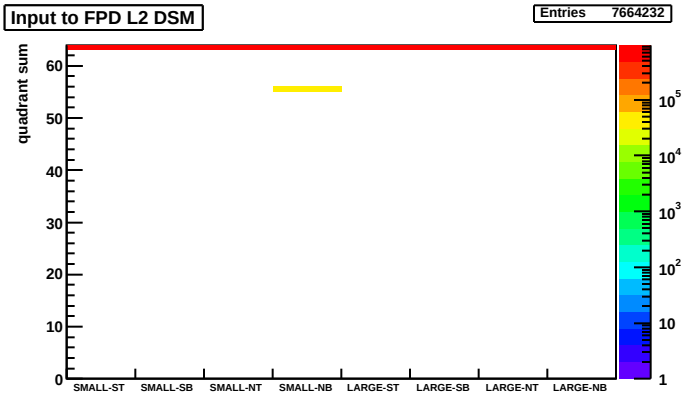

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

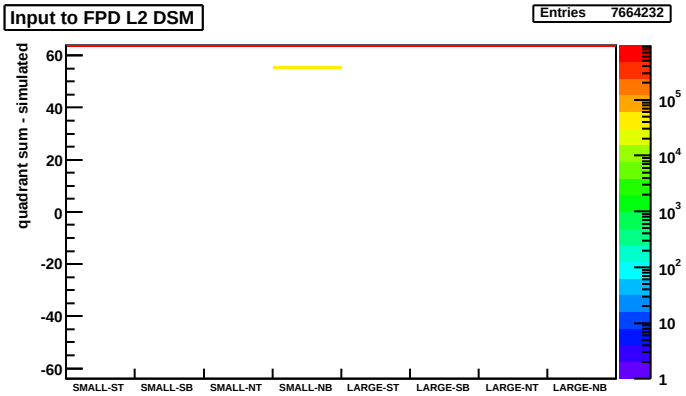

Input to FMS L1 DSM
Entries 1.149635e+07
sumA
DSM board

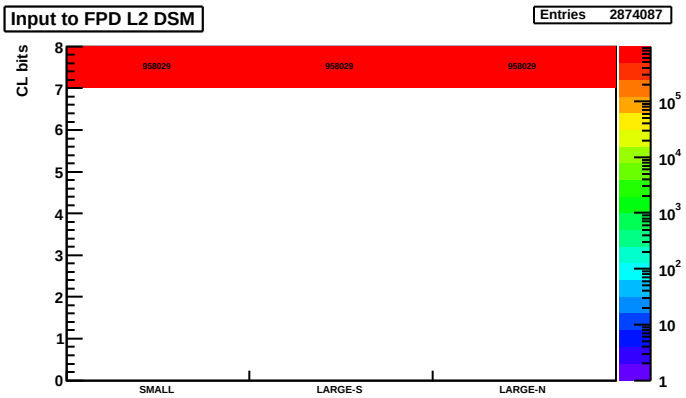

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

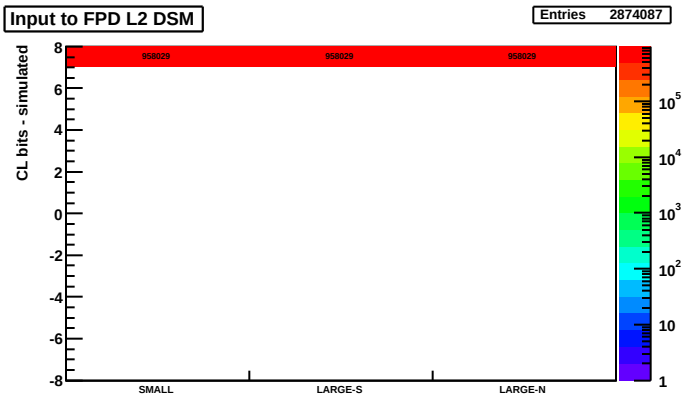

Input to FMS L1 DSM
Entries 1.149635e+07
FMS L1 HT bits
DSM board

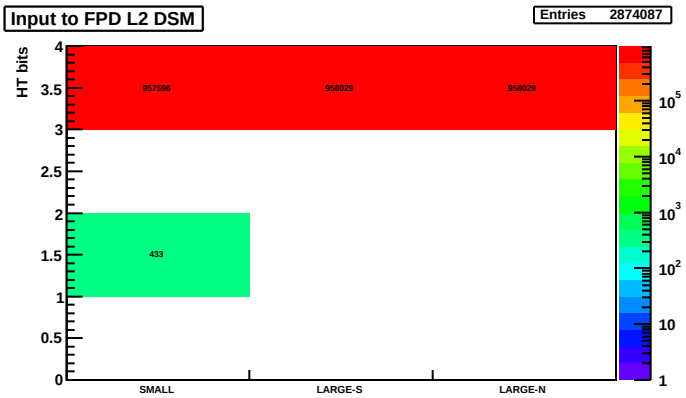

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

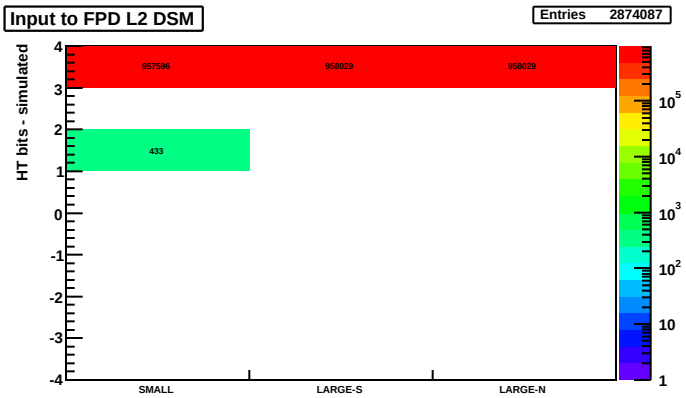

Input to FPD L2 DSM
Entries 7664232
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

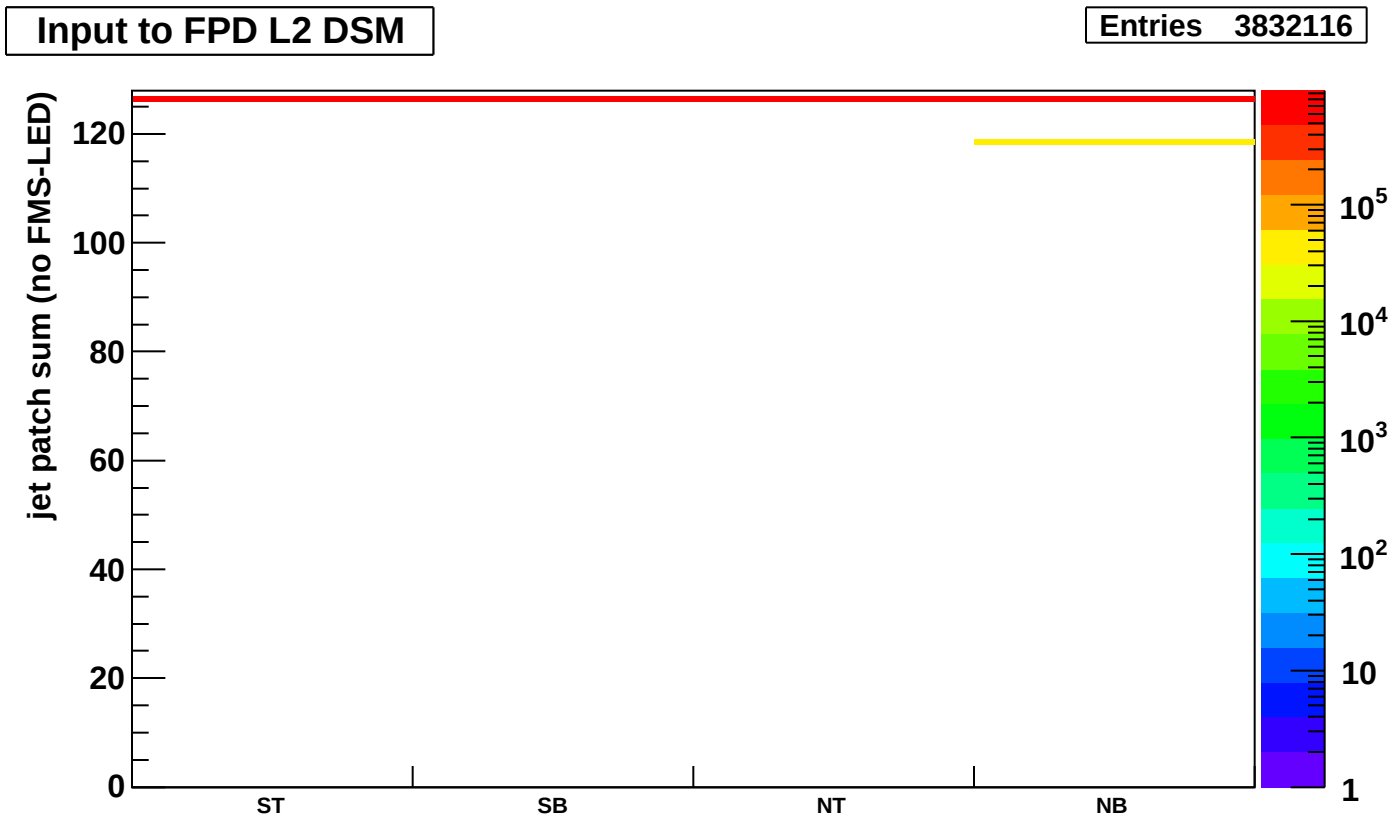

Input to FPD L2 DSM
Entries 7664232
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

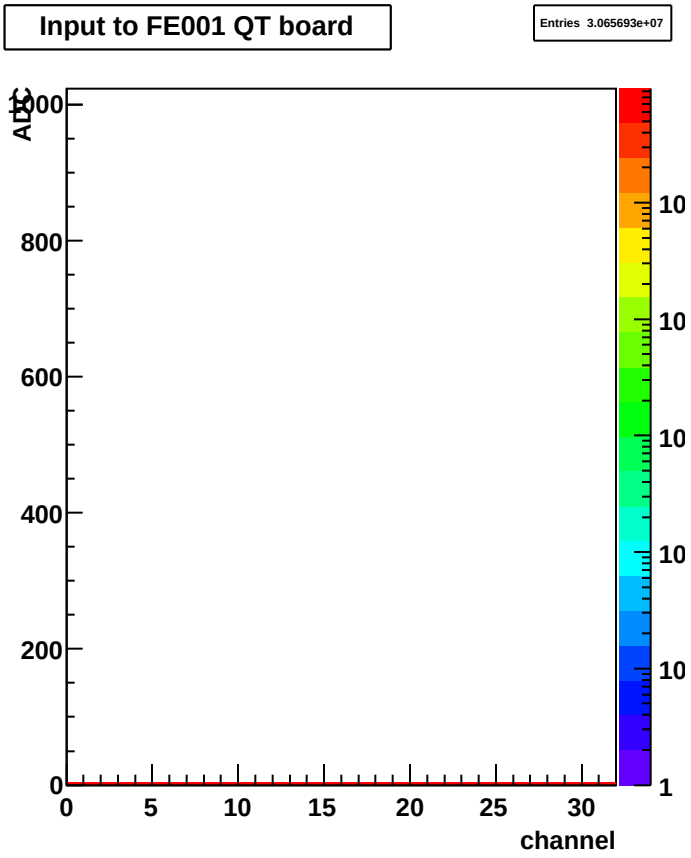

Input to FPD L2 DSM
Entries 2874087
CL bits
958029
958029
958029
SMALL
LARGE-S
LARGE-N

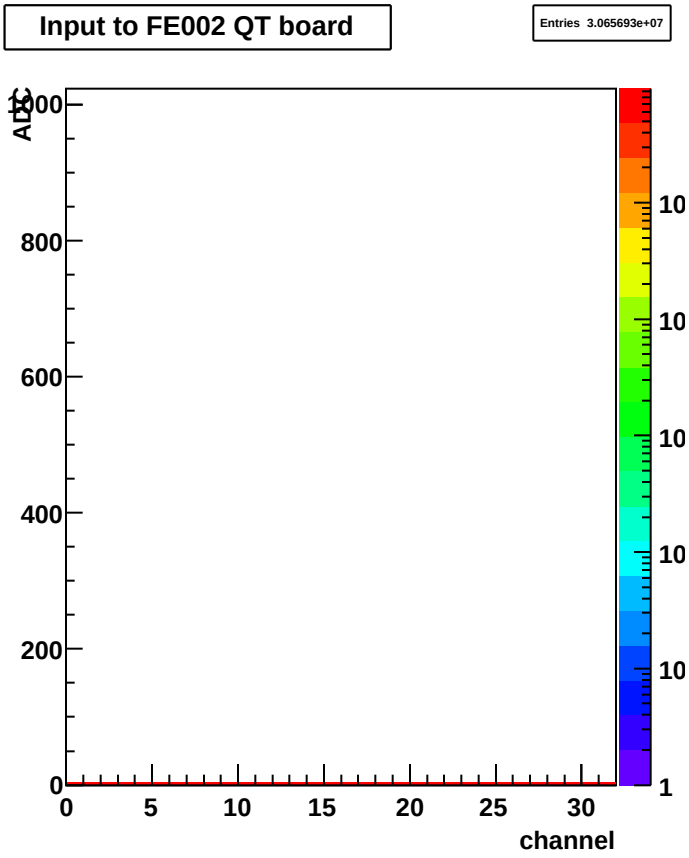

Input to FPD L2 DSM
Entries 2874087
CL bits - simulated
958029
958029
958029
SMALL
LARGE-S
LARGE-N

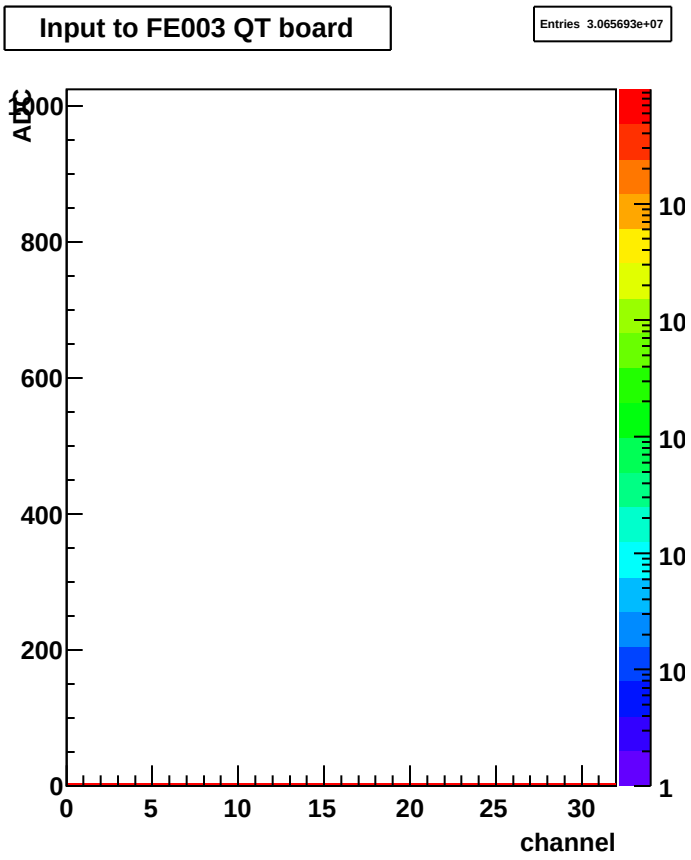

Input to FPD L2 DSM
Entries 2874087
HT bits
957596
958029
958029
433
SMALL
LARGE-S
LARGE-N

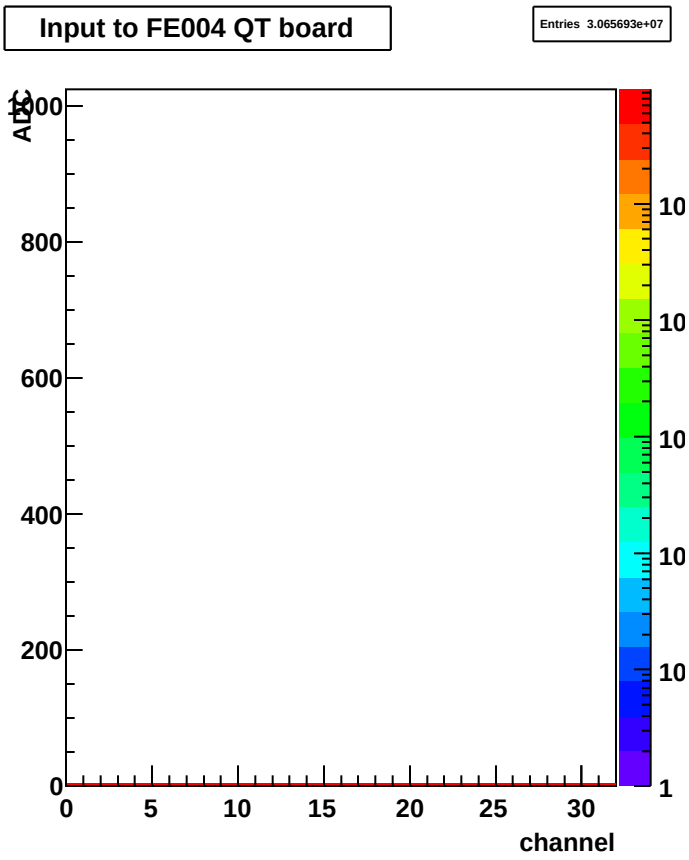

Input to FPD L2 DSM
Entries 2874087
HT bits - simulated
957596
958029
958029
433
SMALL
LARGE-S
LARGE-N

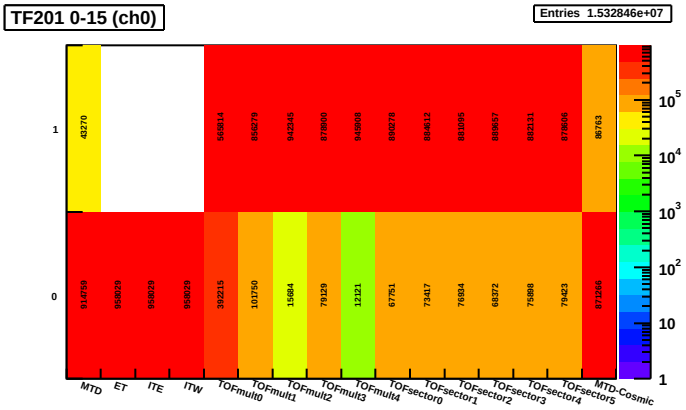

Input to FPD L2 DSM
Entries 3832116
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

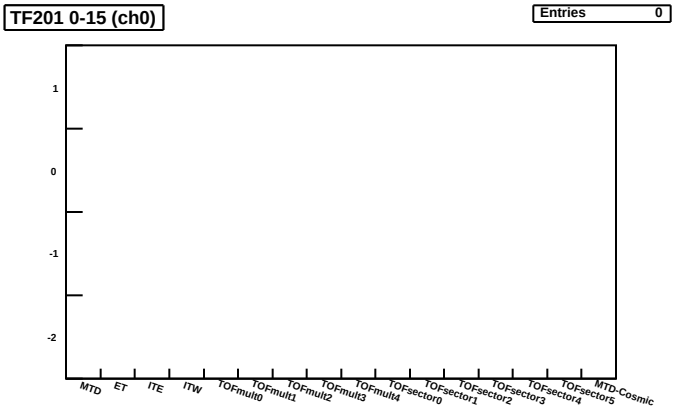

Input to FE001 QT board
Entries 3.065693e+07
ADC
channel

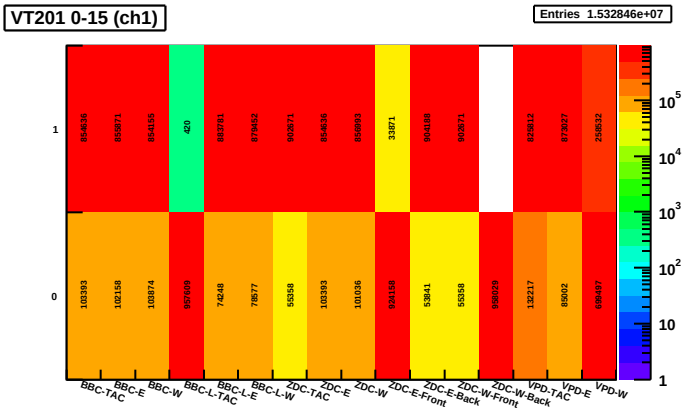

Input to FE002 QT board
Entries 3.065693e+07
ADC
channel

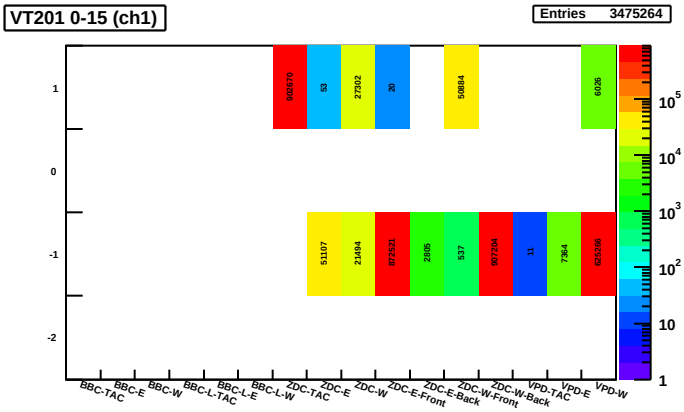

Input to FE003 QT board
Entries 3.065693e+07
ADC
channel

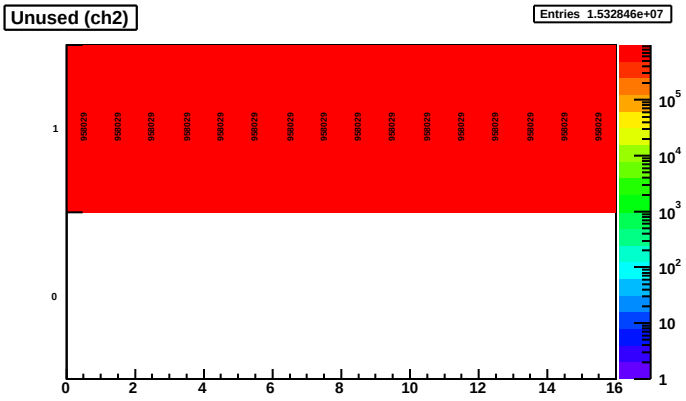

Input to FE004 QT board
Entries 3.065693e+07
ADC
channel

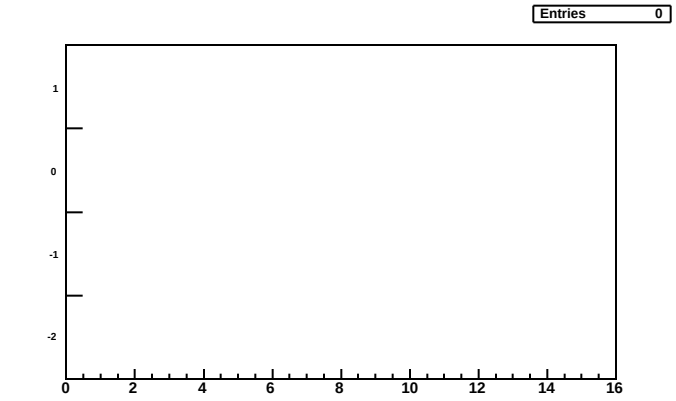

TF201 0-15 (ch0)
Entries 1.532846e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

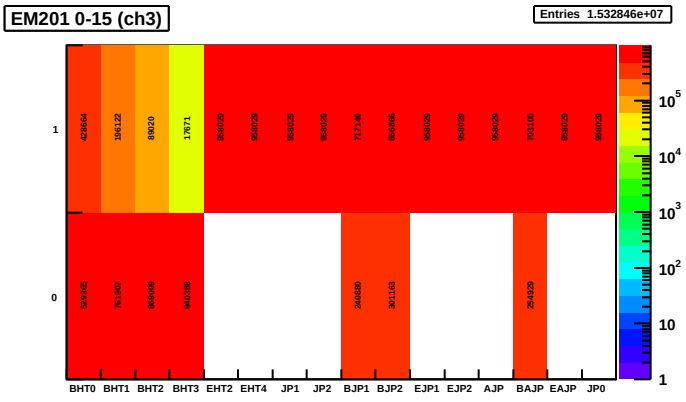

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

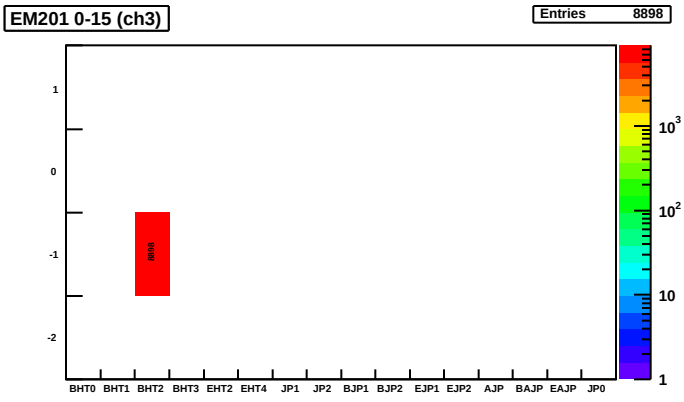

VT201 0-15 (ch1)
Entries 1.532846e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

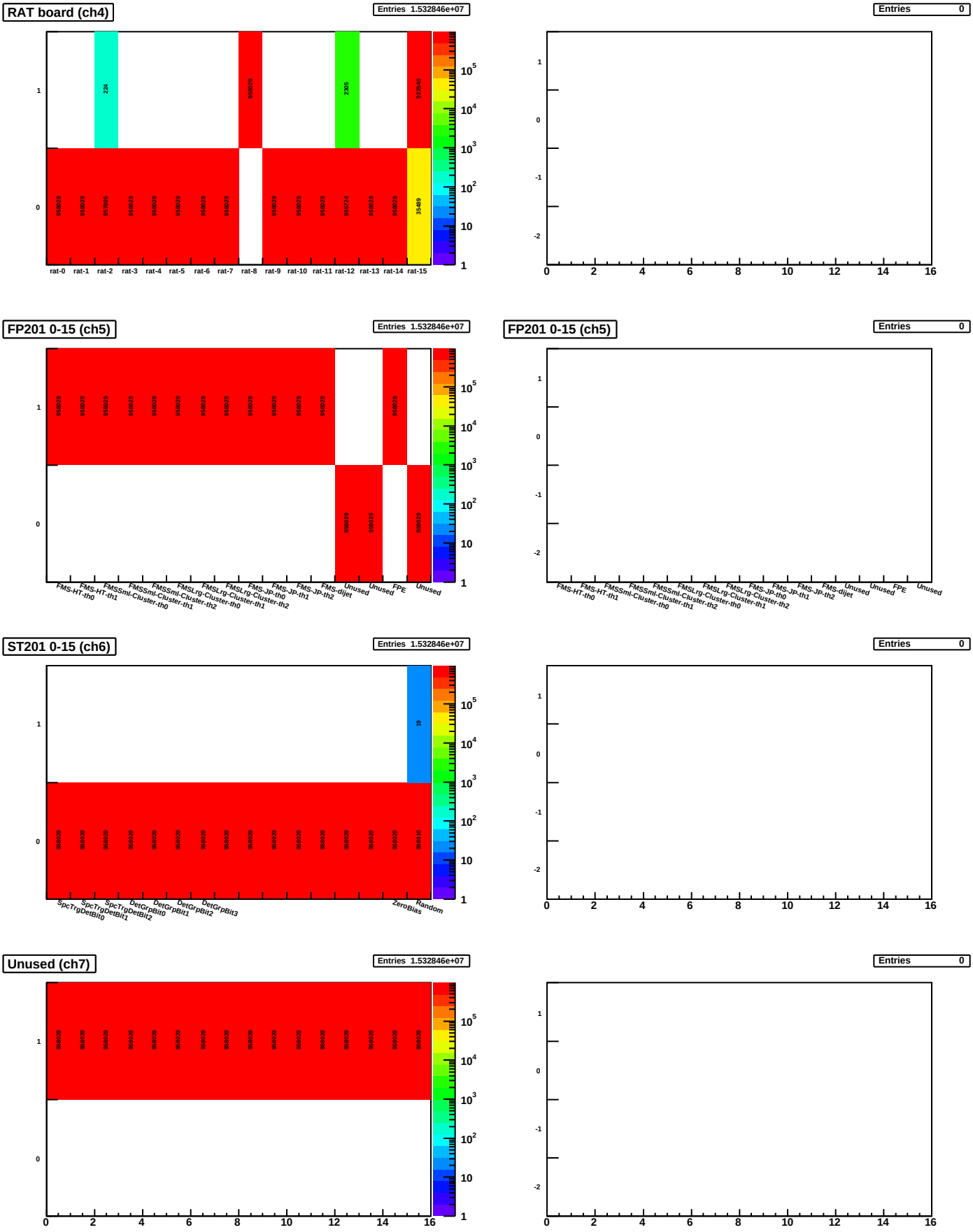

VT201 0-15 (ch1)
Entries 3475264
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1.532846e+07


Entries 0


EM201 0-15 (ch3)
Entries 1.532846e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


EM201 0-15 (ch3)
Entries 8898
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


RAT board (ch4)
Entries 1.532846e+07
Entries 0
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15
FP201 0-15 (ch5)
Entries 1.532846e+07
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused
ST201 0-15 (ch6)
Entries 1.532846e+07
Entries 0
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random
Unused (ch7)
Entries 1.532846e+07
Entries 0