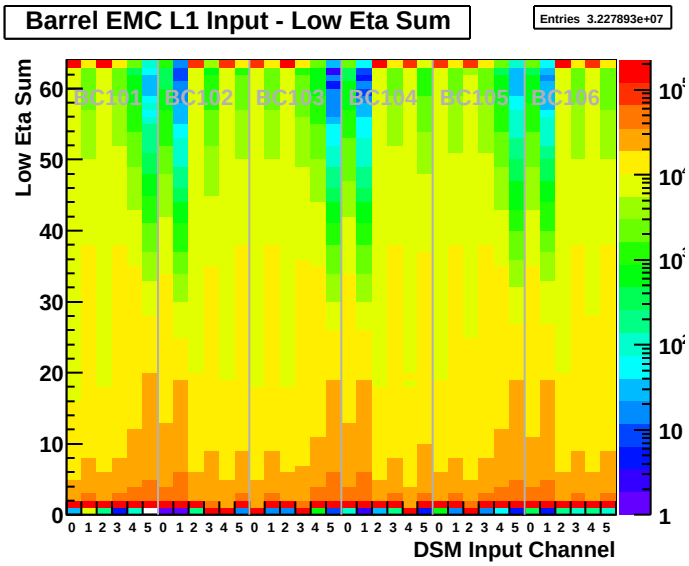
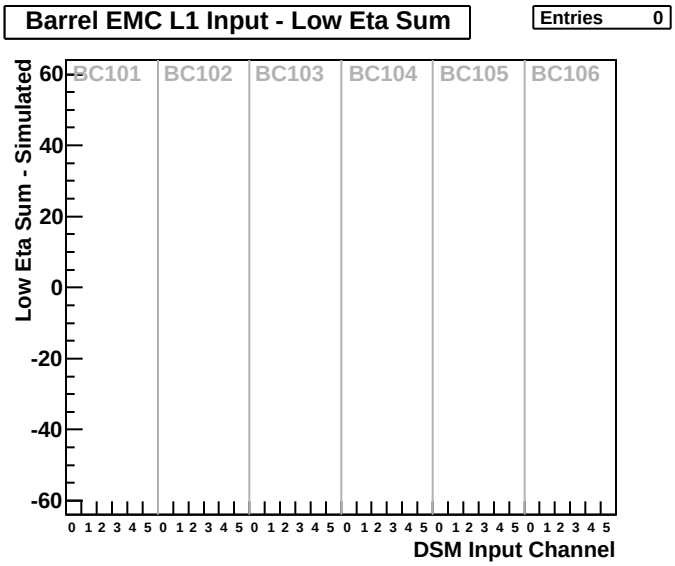
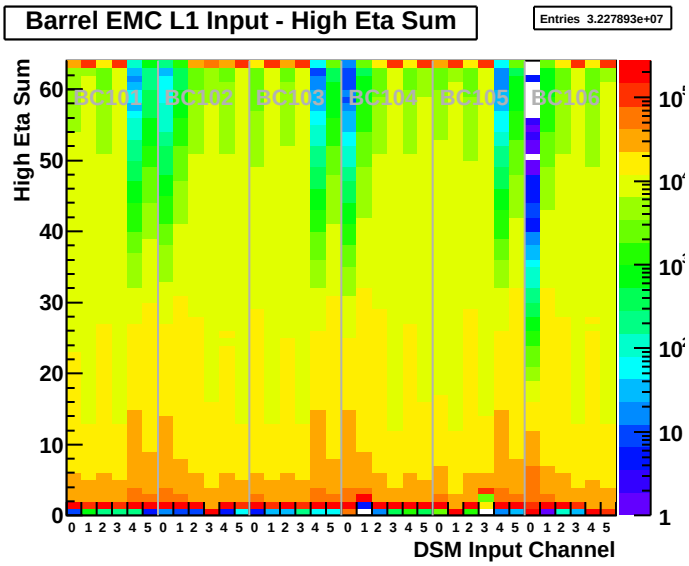
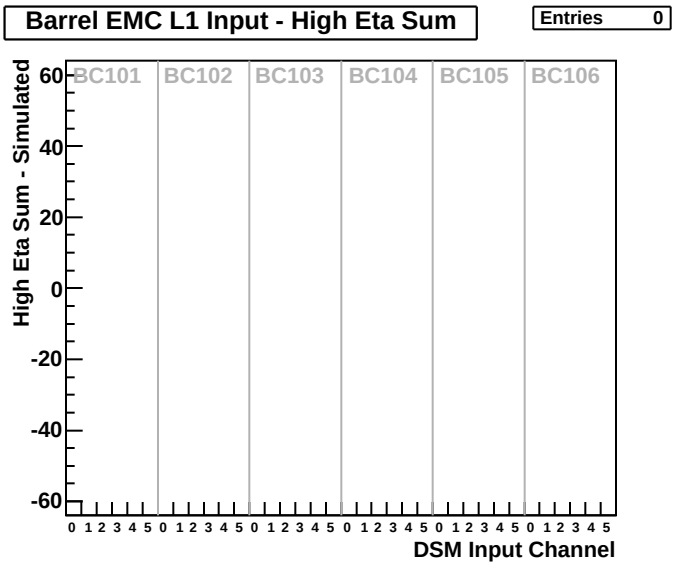
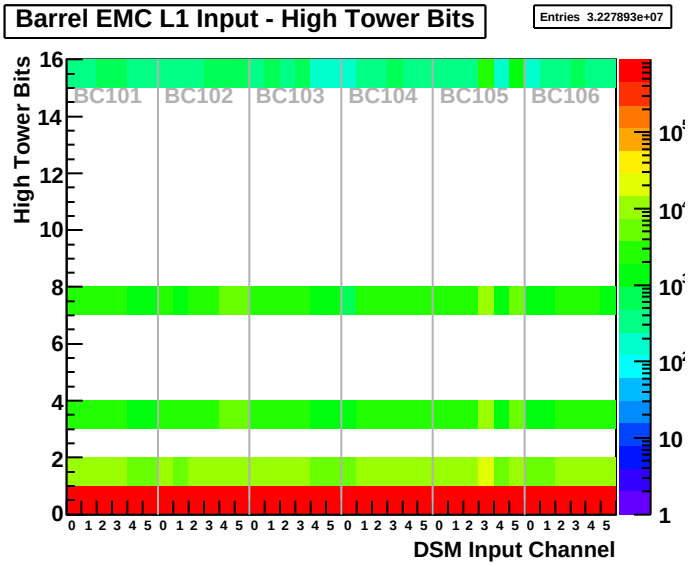
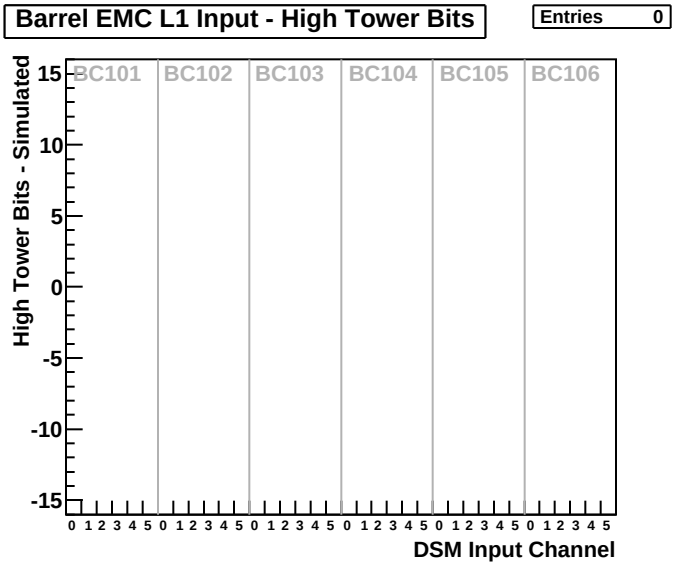

Barrel EMC L1 Input - Low Eta Sum
Entries 3.227893e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

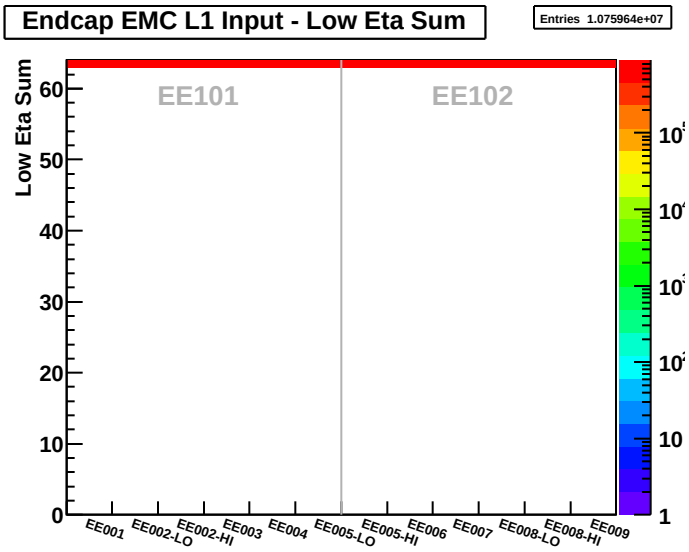

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

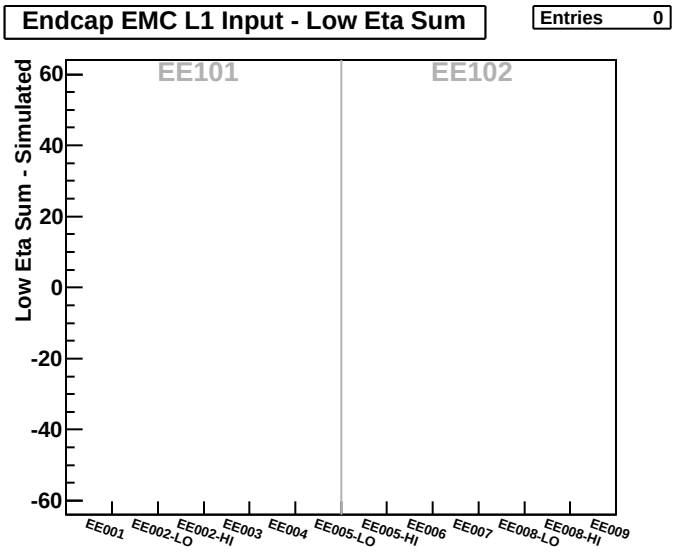

Barrel EMC L1 Input - High Eta Sum
Entries 3.227893e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

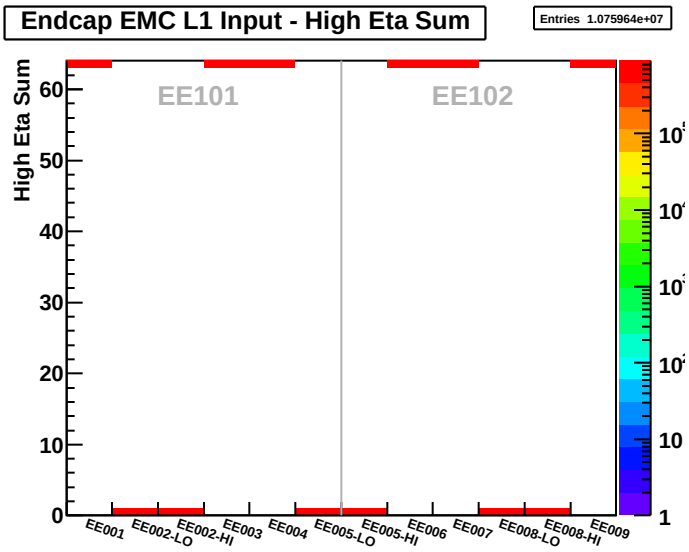

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

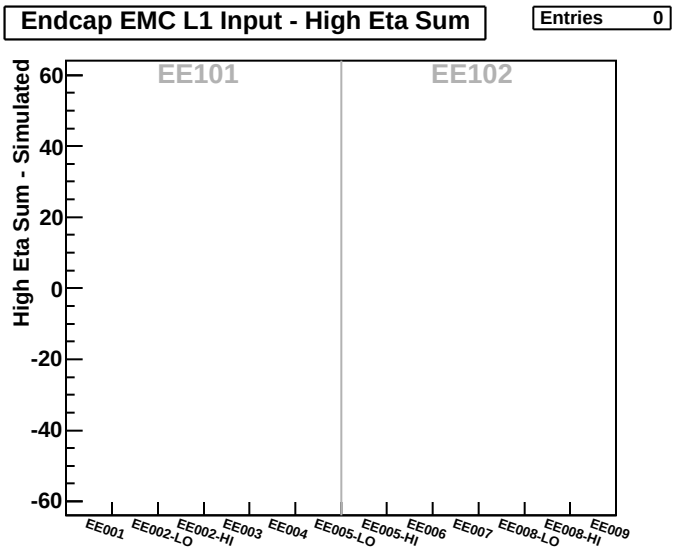

Barrel EMC L1 Input - High Tower Bits
Entries 3.227893e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

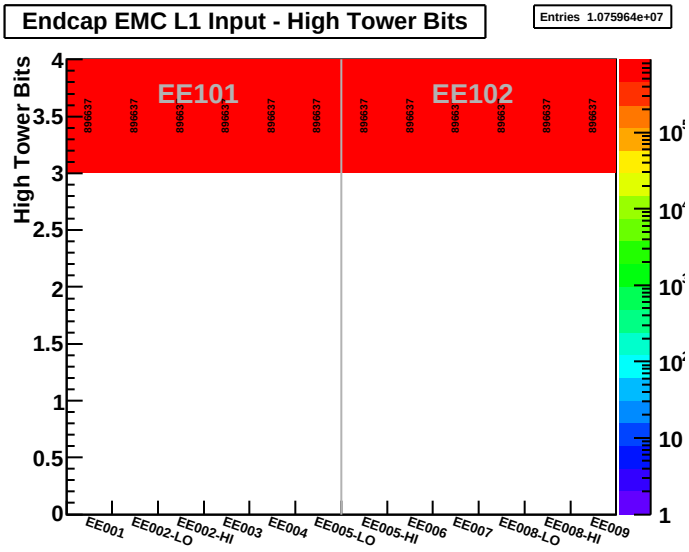

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

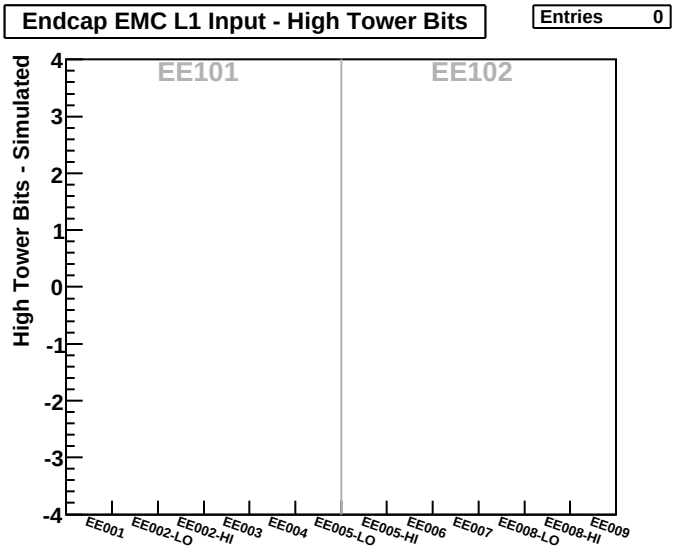

Endcap EMC L1 Input - Low Eta Sum
Entries 1.075964e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

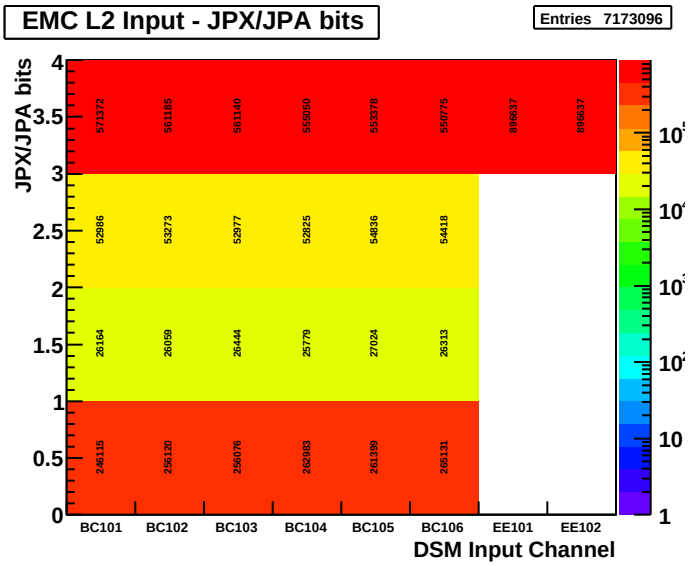

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

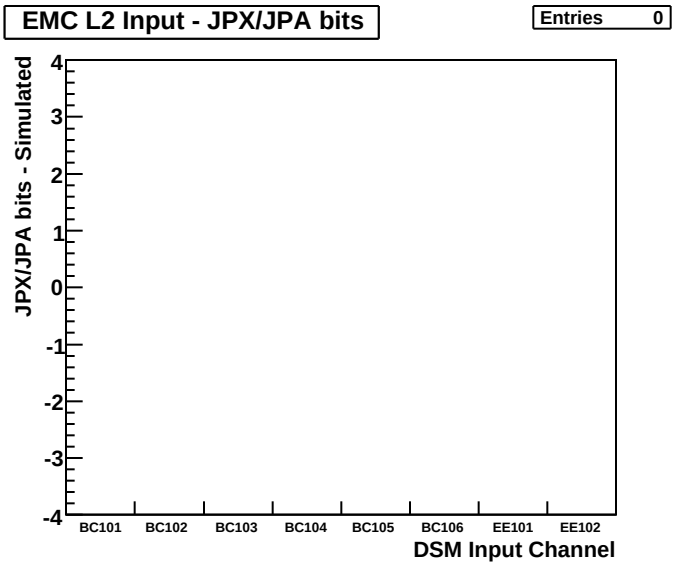

Endcap EMC L1 Input - High Eta Sum
Entries 1.075964e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

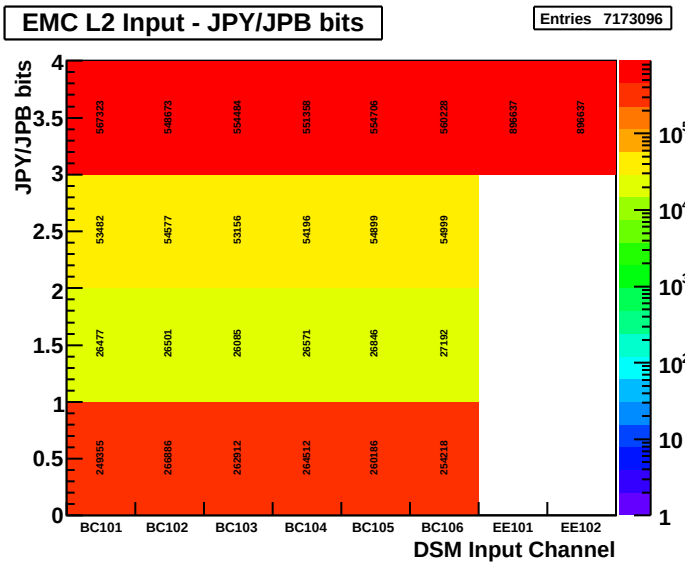

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

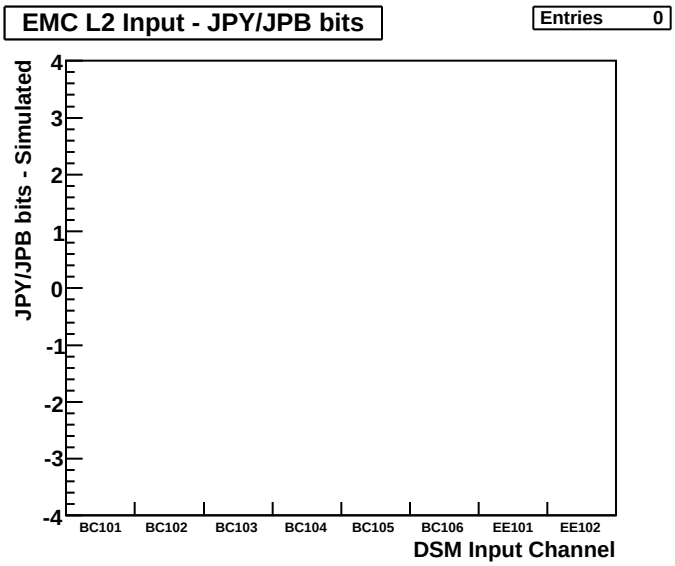

Endcap EMC L1 Input - High Tower Bits
Entries 1.075964e+07
High Tower Bits
EE101
EE102
896537
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

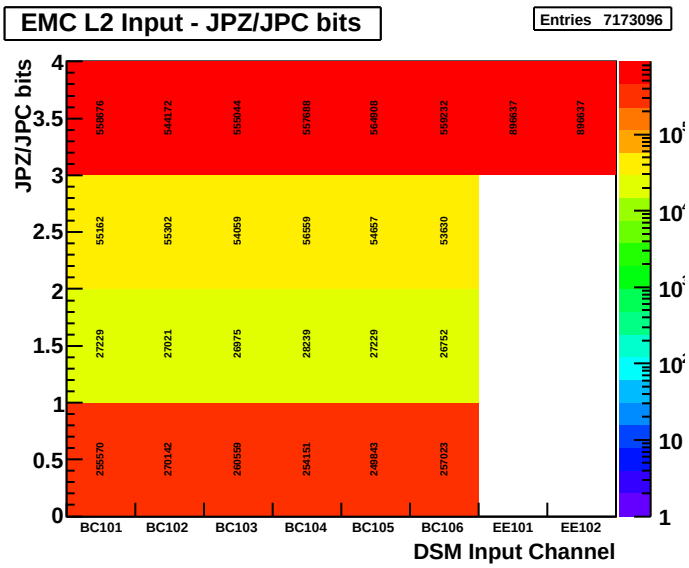

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

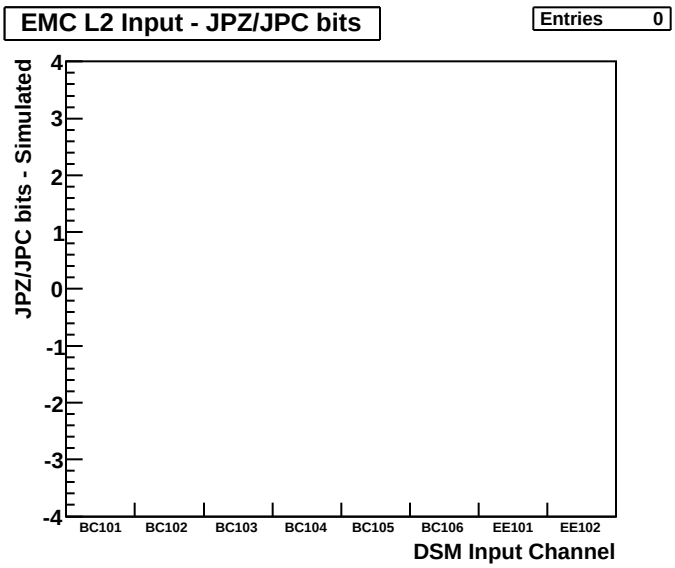

EMC L2 Input - JPX/JPA bits
Entries 7173096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
571372 561185 561140 555050 553378 550775 896637 896637
52986 53273 52977 52825 54036 54418
26164 26059 26444 25779 27024 26313
246115 256120 256076 262083 261399 265131

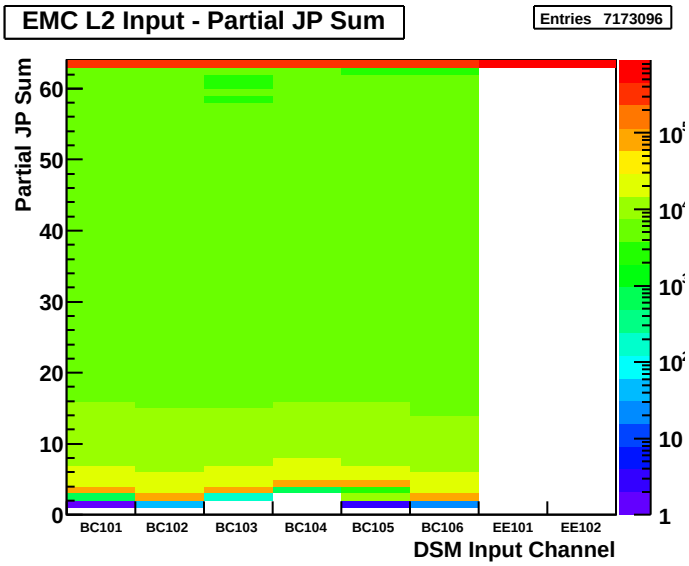

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

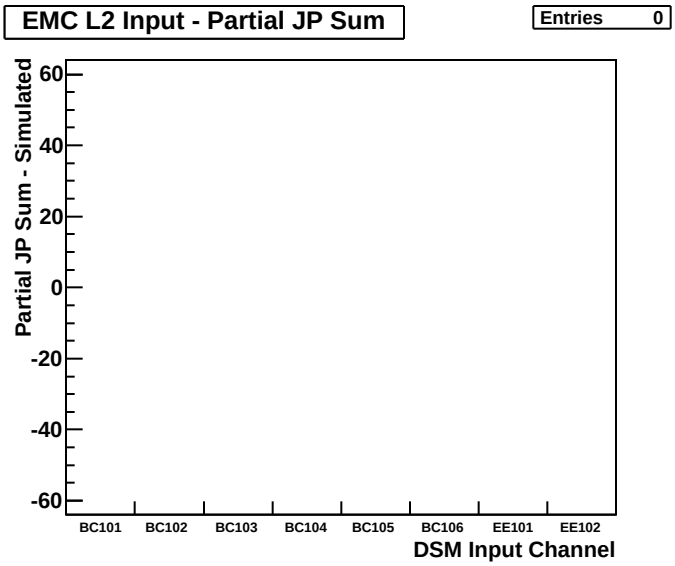

EMC L2 Input - JPY/JPB bits
Entries 7173096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
567323 548673 554484 551358 554706 560228 896637 896637
53482 54577 53156 54196 54899 54999
26477 26501 26085 26571 26846 27192
248355 266886 263912 264512 260186 254218

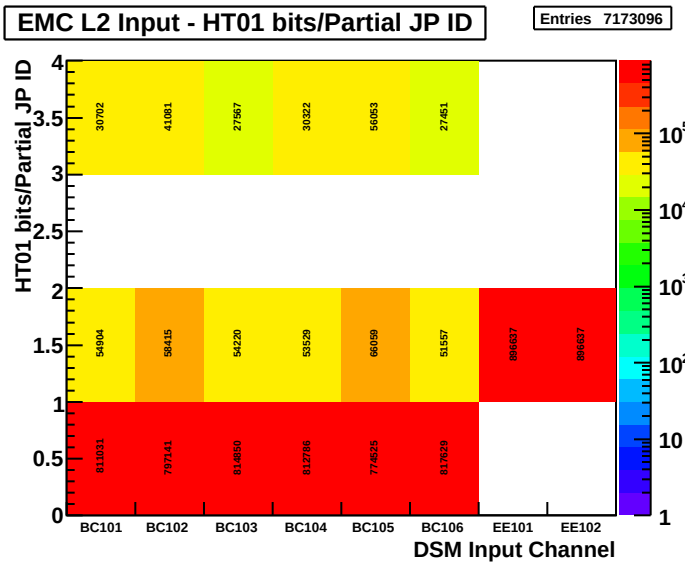

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

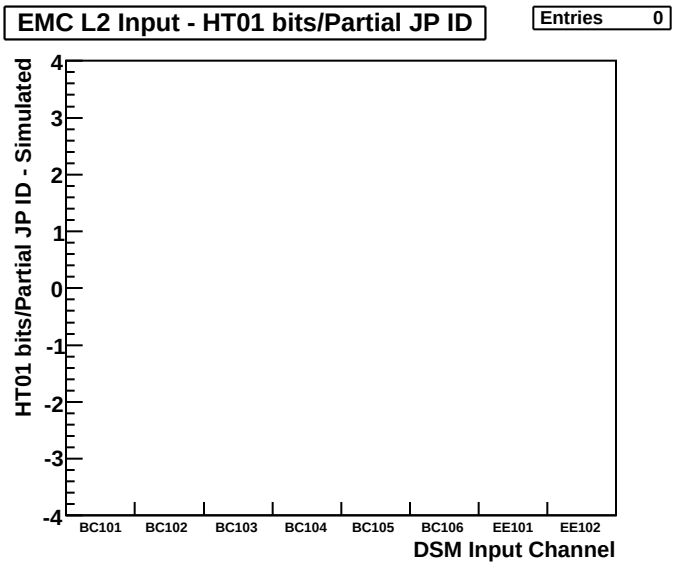

EMC L2 Input - JPZ/JPC bits
Entries 7173096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
558676 544172 555044 557688 564908 559232 896637 896637
55162 55302 54059 56559 54657 53630
27229 27021 26975 28239 27229 26752
255570 270142 260559 254151 249843 257023

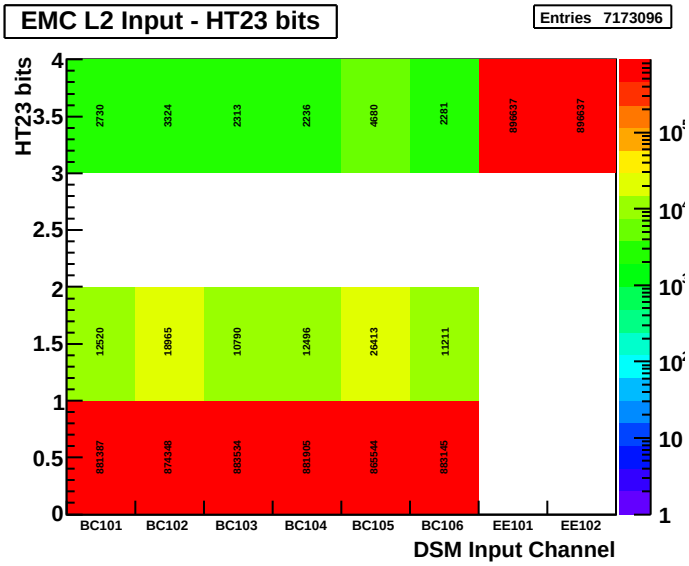

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

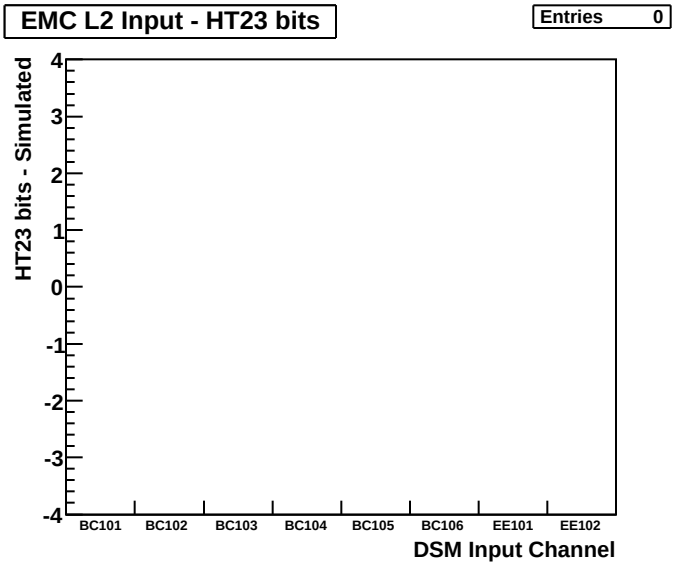

EMC L2 Input - Partial JP Sum
Entries 7173096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

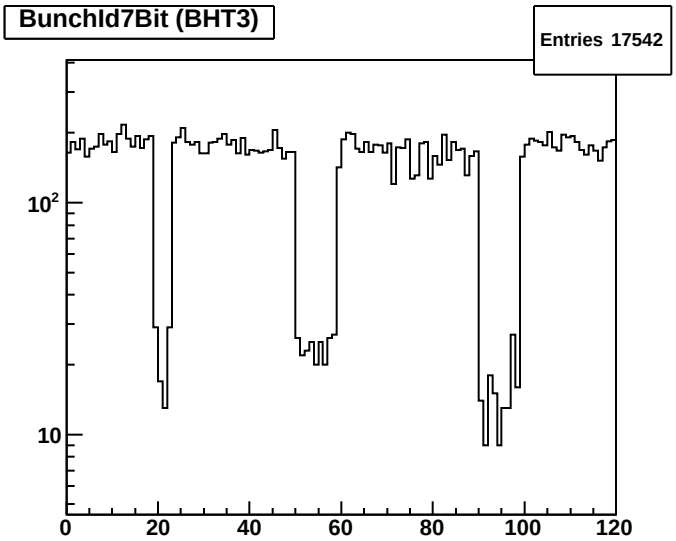

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

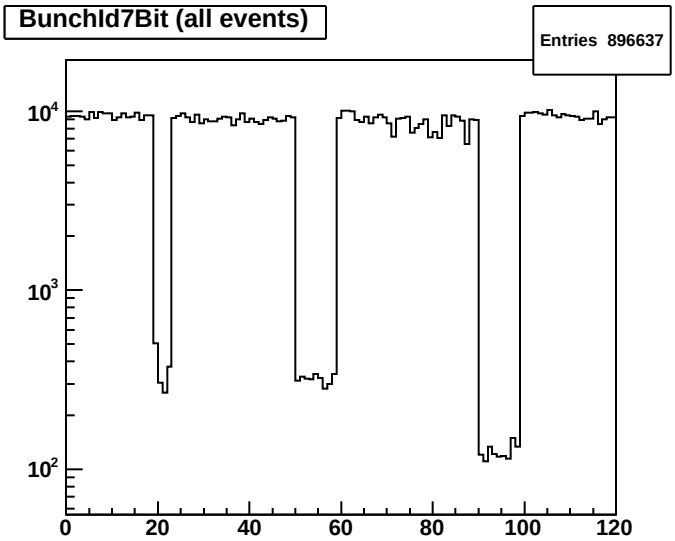

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7173096
HT01 bits/Partial JP ID
30702 41061 27567 30322 56053 27451
54904 58415 54220 53529 66059 51557 896637 896637
811031 797141 814850 812786 774525 817629
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

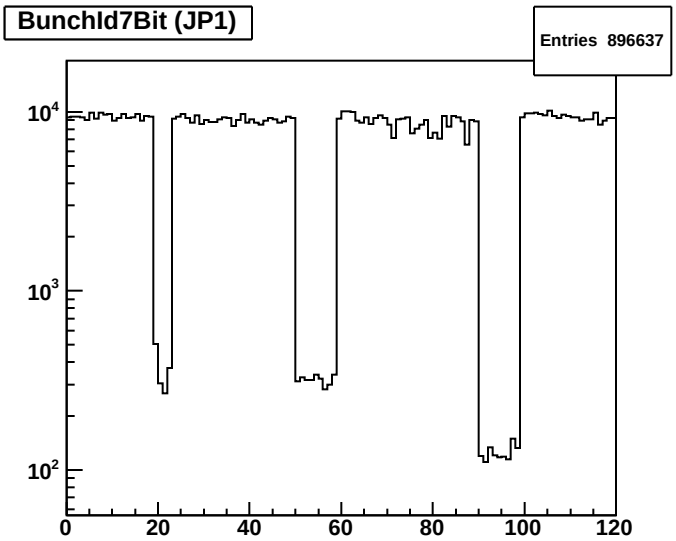

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

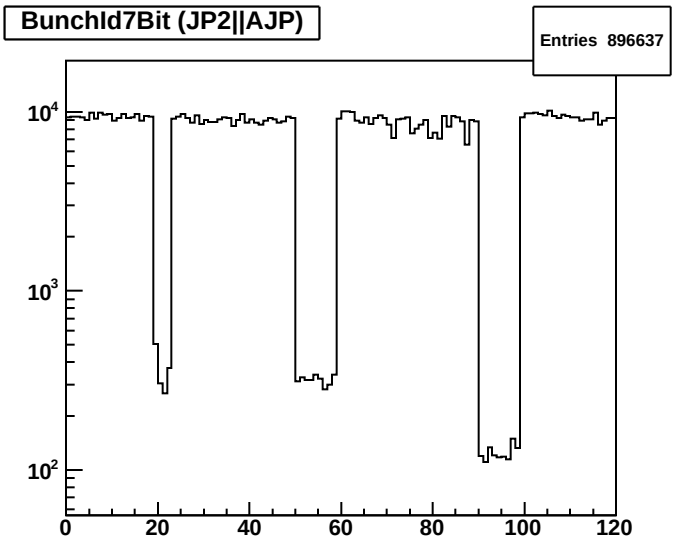

EMC L2 Input - HT23 bits
Entries 7173096
HT23 bits
2730 3324 2313 2236 4680 2281 896637 896637
12520 18965 10790 12496 26413 11211
881387 874348 883534 881905 865544 883145
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

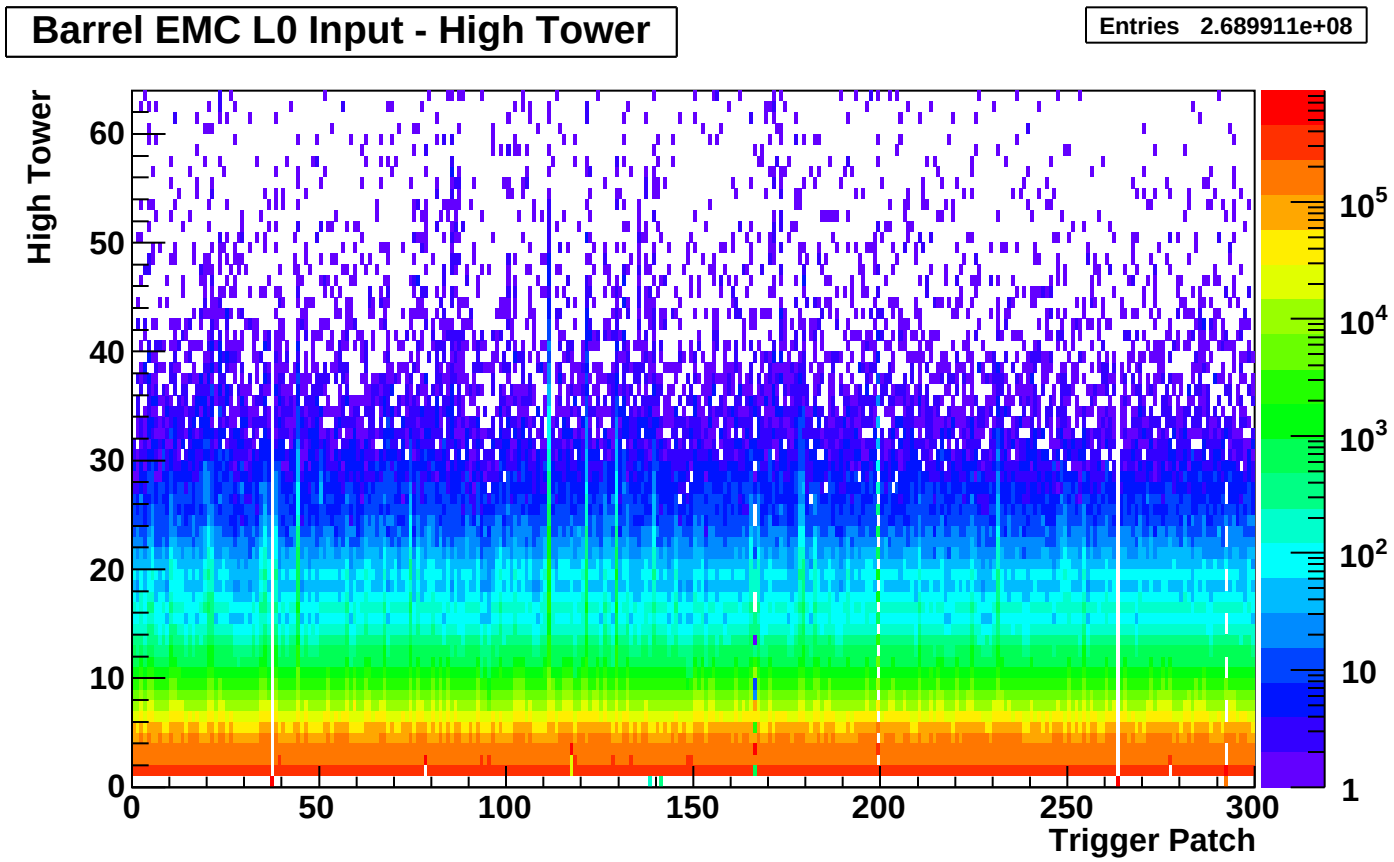

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

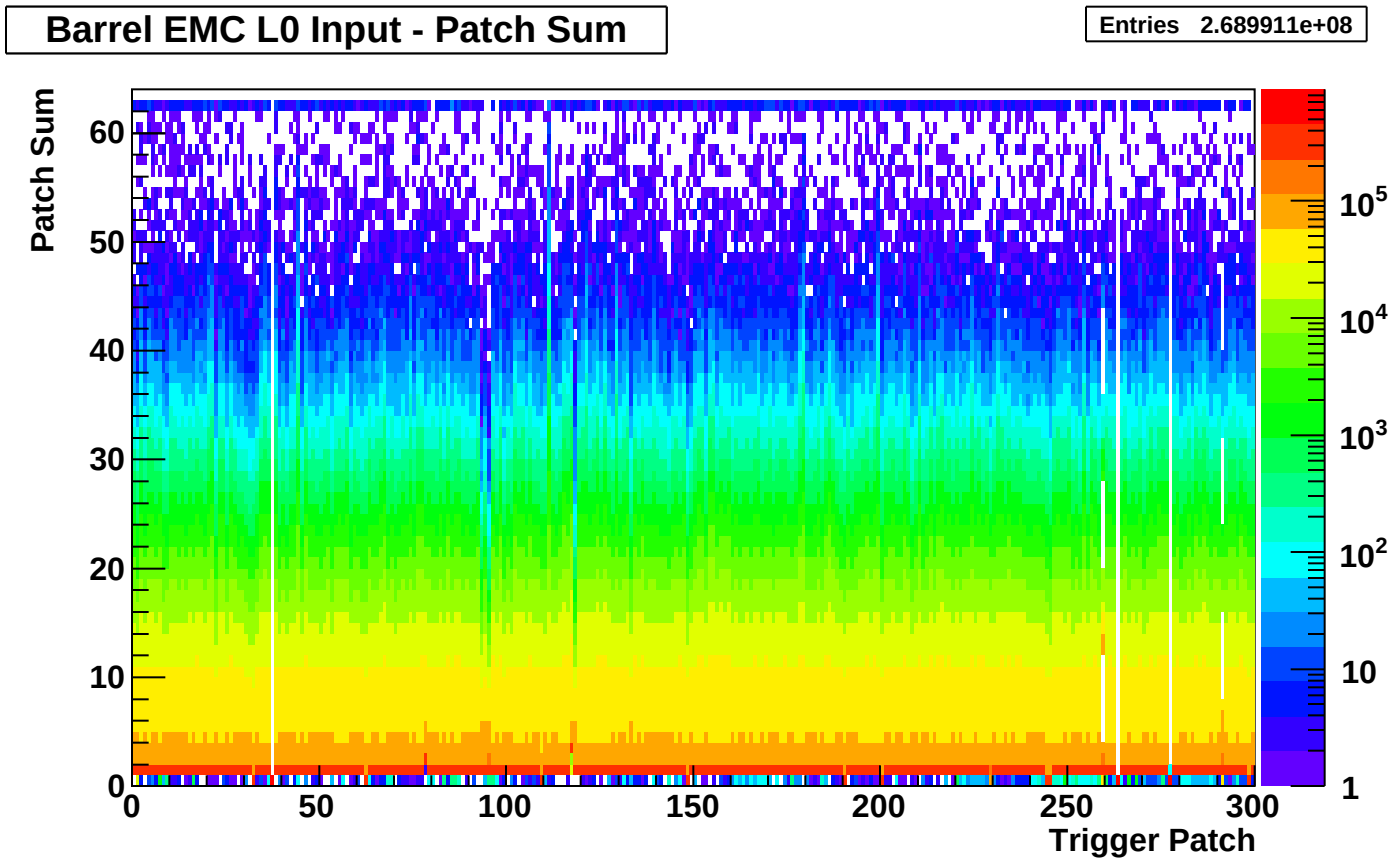

BunchId7Bit (BHT3)
Entries 17542
10^2
10
0
20
40
60
80
100
120

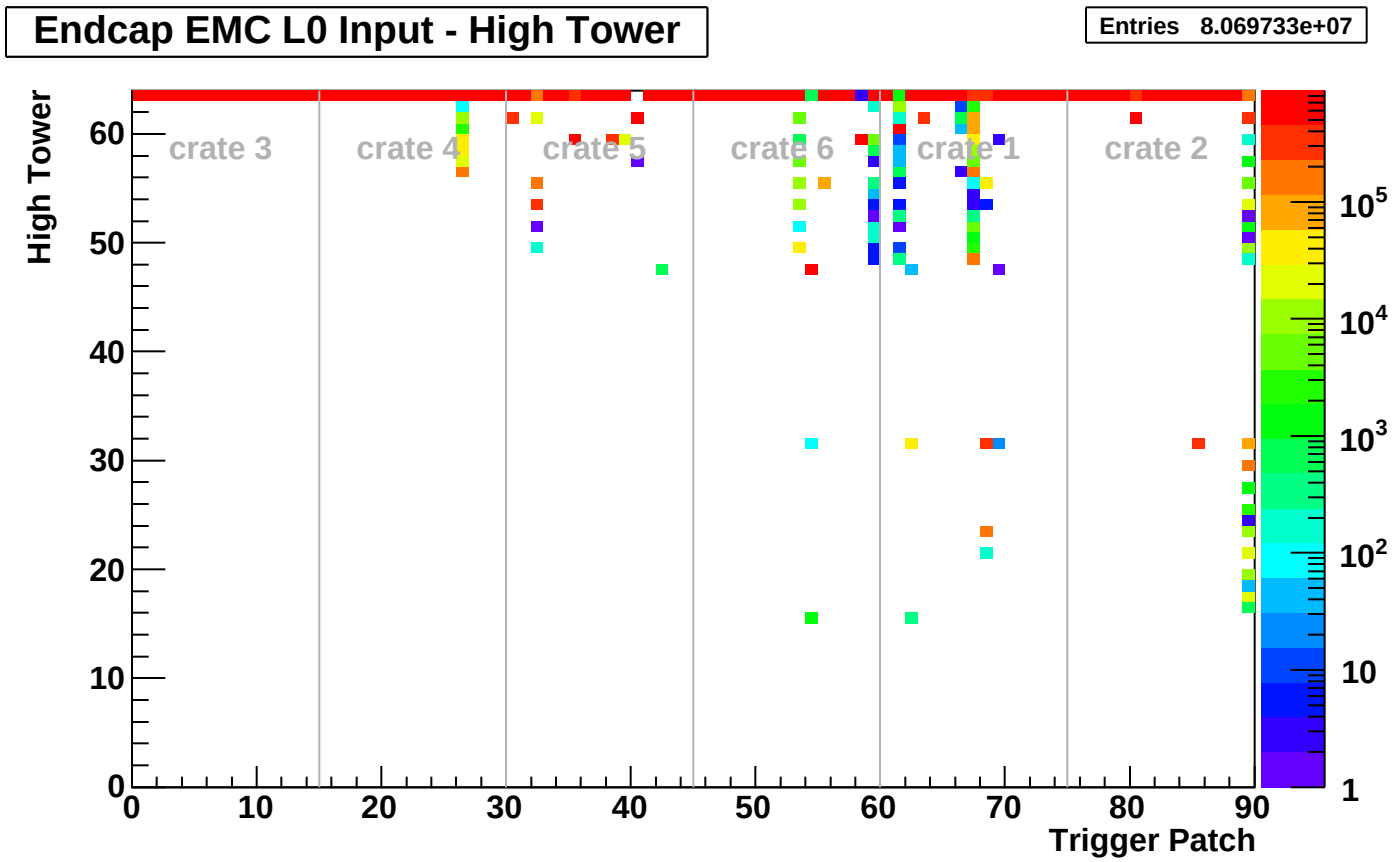

BunchId7Bit (all events)
Entries 896637
10^4
10^3
10^2
0
20
40
60
80
100
120

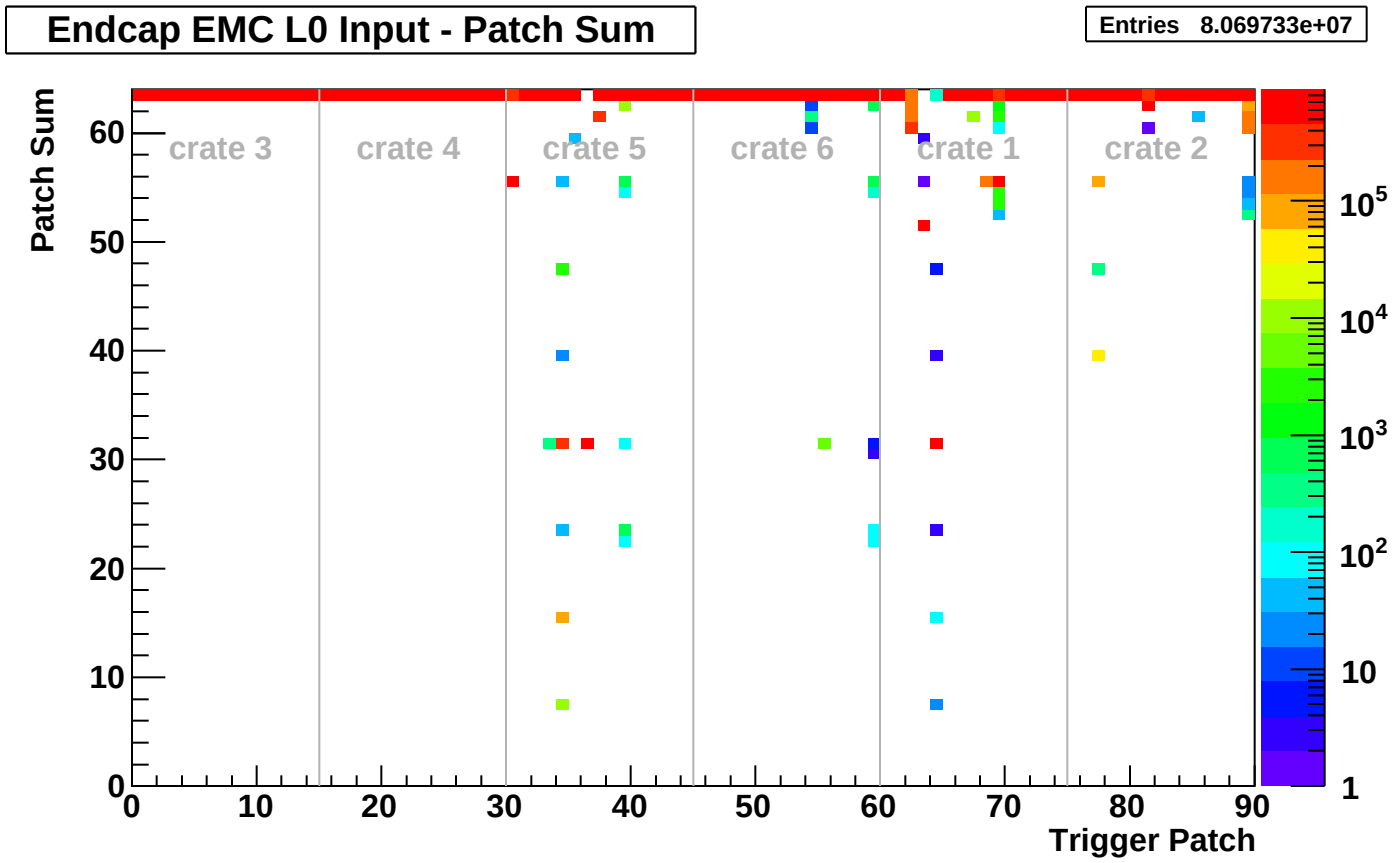

BunchId7Bit (JP1)
Entries 896637
10^4
10^3
10^2
0
20
40
60
80
100
120

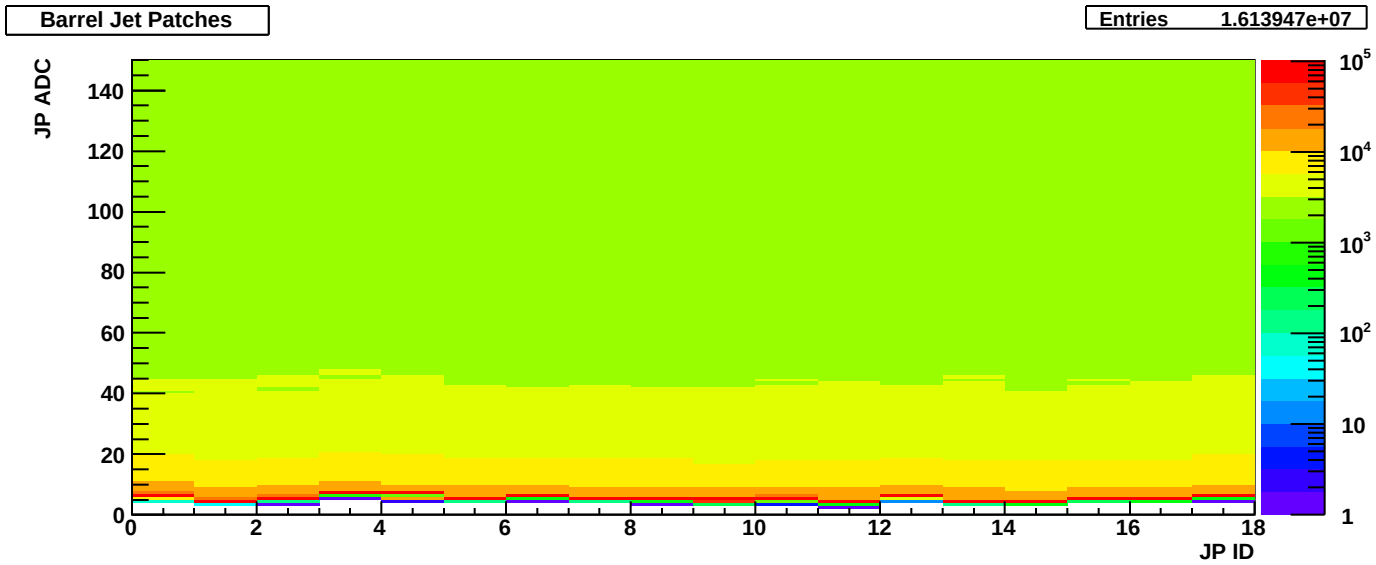

BunchId7Bit (JP2||AJP)
Entries 896637
10^4
10^3
10^2
0
20
40
60
80
100
120

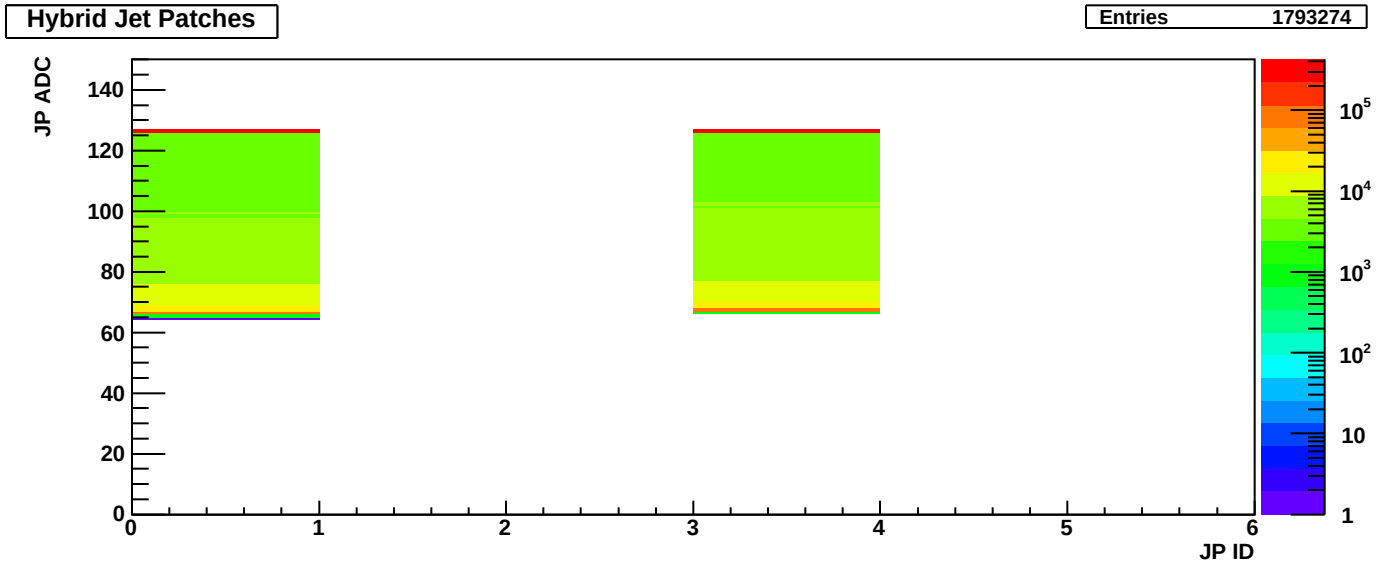

Barrel EMC L0 Input - High Tower
Entries 2.689911e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

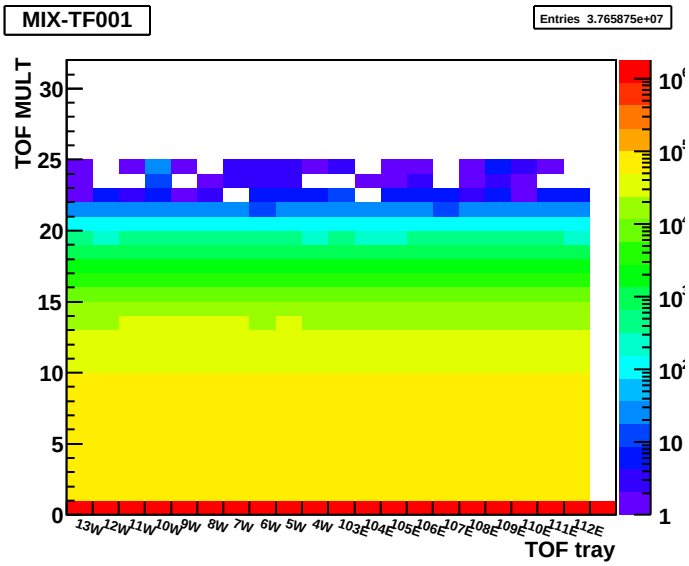

Barrel EMC L0 Input - Patch Sum
Entries 2.689911e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

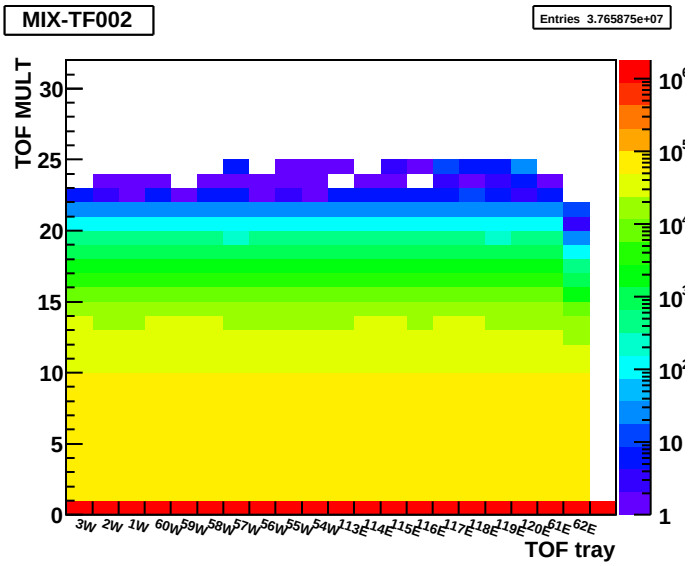

Endcap EMC L0 Input - High Tower
Entries 8.069733e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

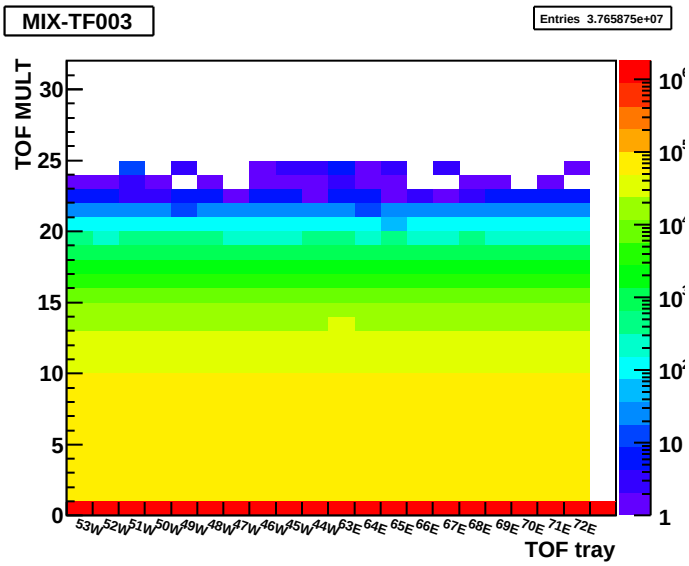

Endcap EMC L0 Input - Patch Sum
Entries 8.069733e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

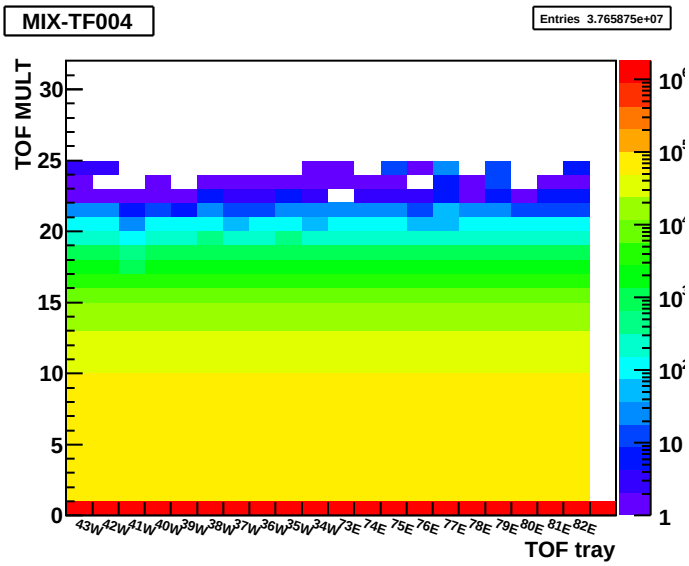

Barrel Jet Patches
Entries 1.613947e+07
JP ADC
JP ID

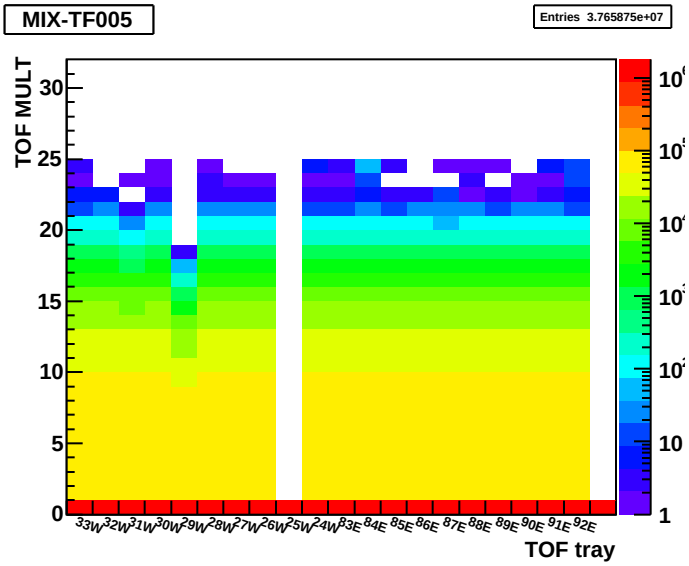

Hybrid Jet Patches
Entries 1793274
JP ADC
JP ID

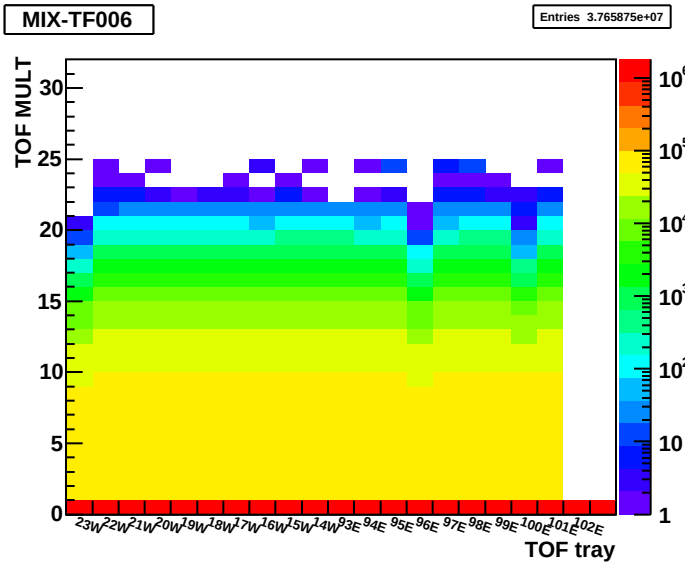

MIX-TF001
Entries 3.765875e+07
TOF MULT
TOF tray

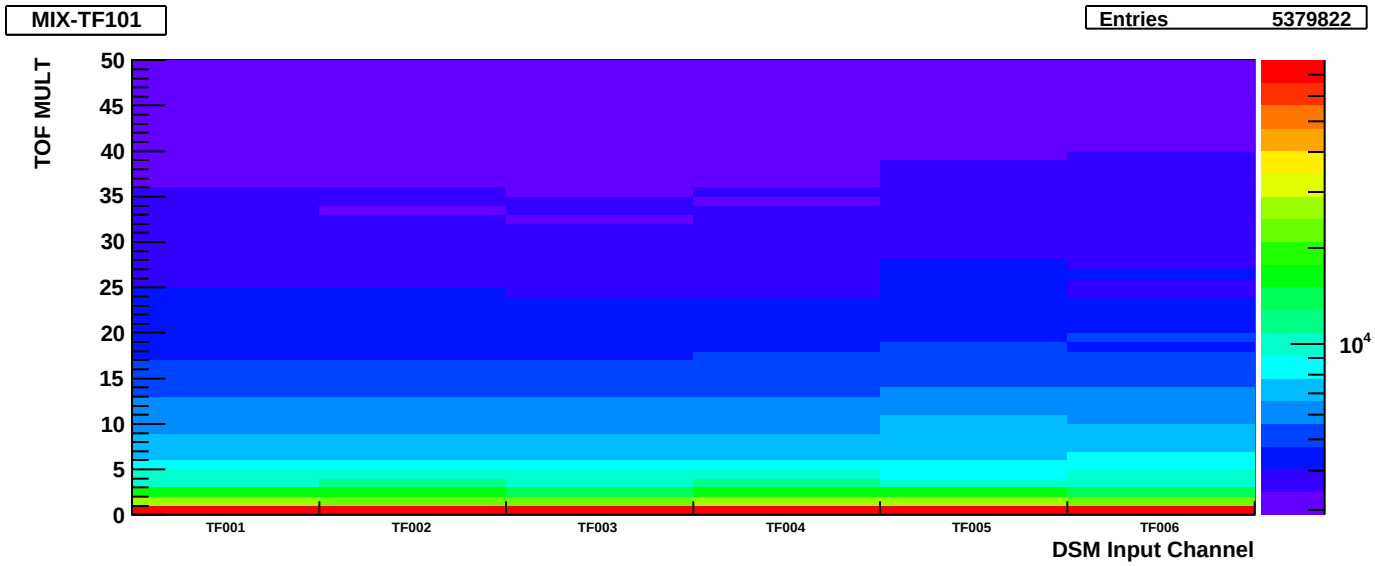

MIX-TF002
Entries 3.765875e+07
TOF MULT
TOF tray

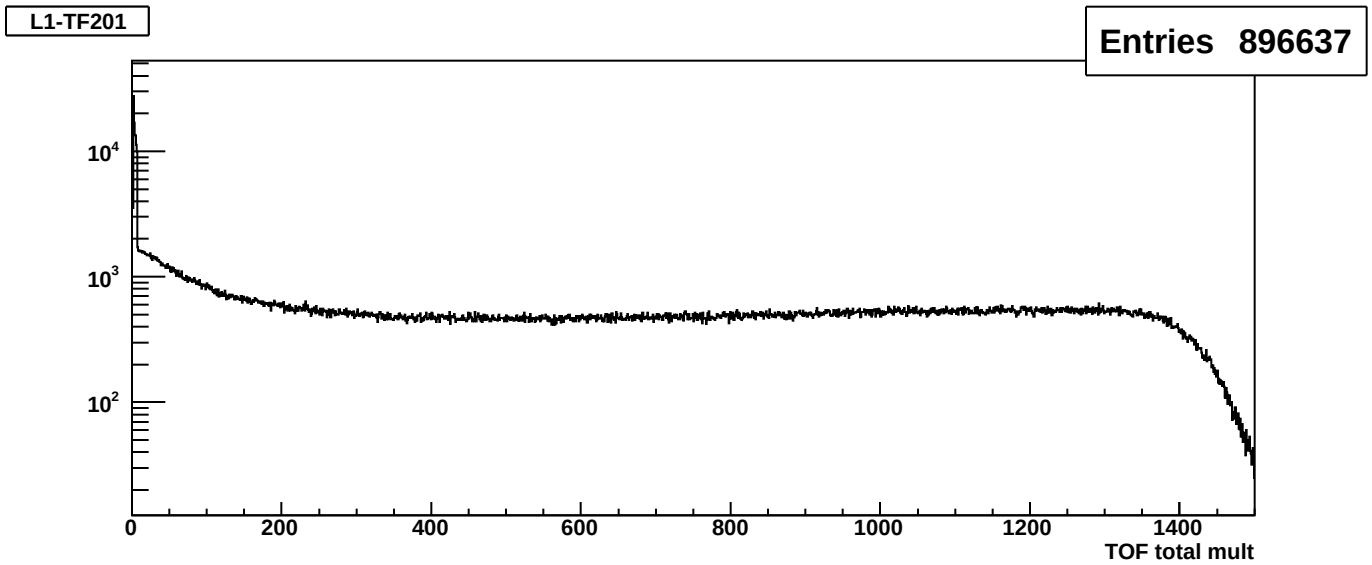

MIX-TF003
Entries 3.765875e+07
TOF MULT
TOF tray

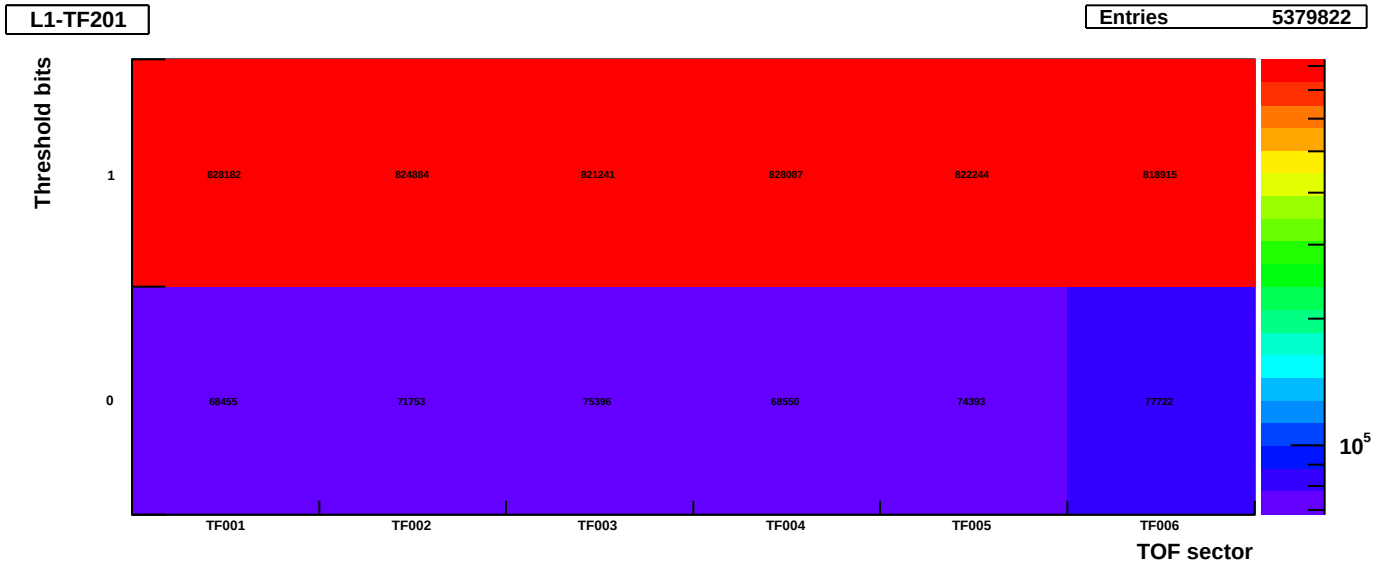

MIX-TF004
Entries 3.765875e+07
TOF MULT
TOF tray

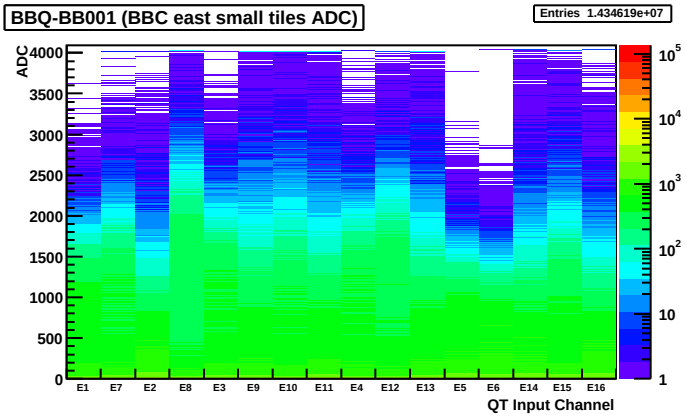

MIX-TF005
Entries 3.765875e+07
TOF MULT
TOF tray

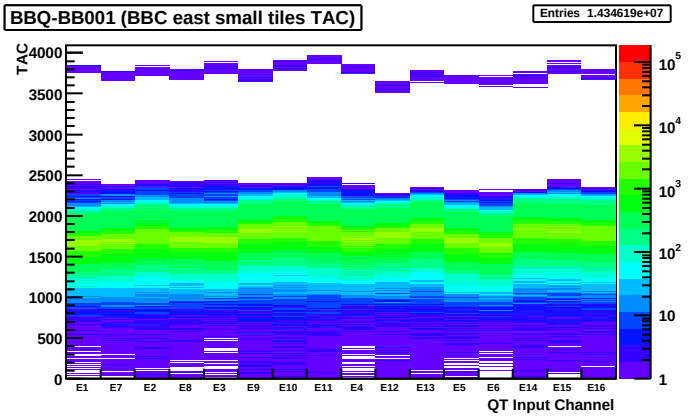

MIX-TF006
Entries 3.765875e+07
TOF MULT
TOF tray

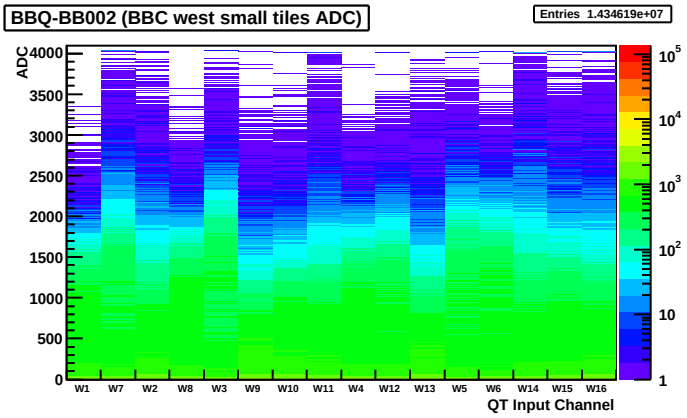

MIX-TF101
Entries 5379822
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

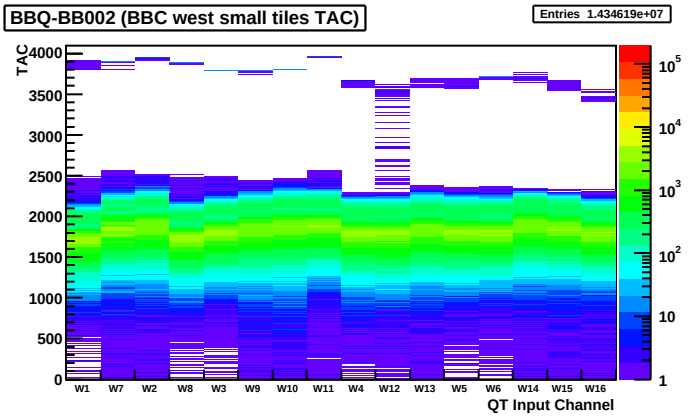

L1-TF201
Entries 896637
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

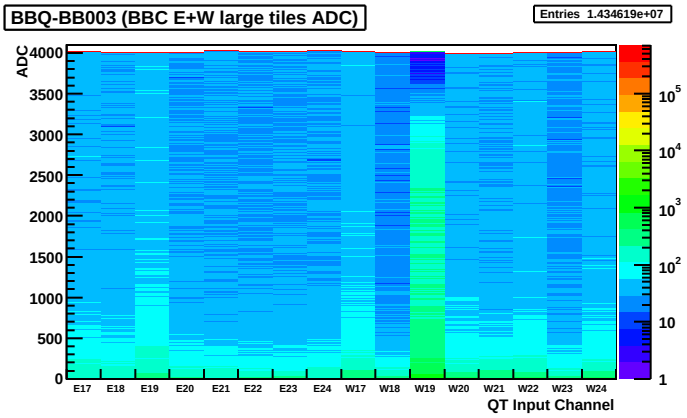

L1-TF201
Entries 5379822
Threshold bits
1
0
828182
824884
821241
828087
822244
818915
68455
71753
75396
68550
74393
77722
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

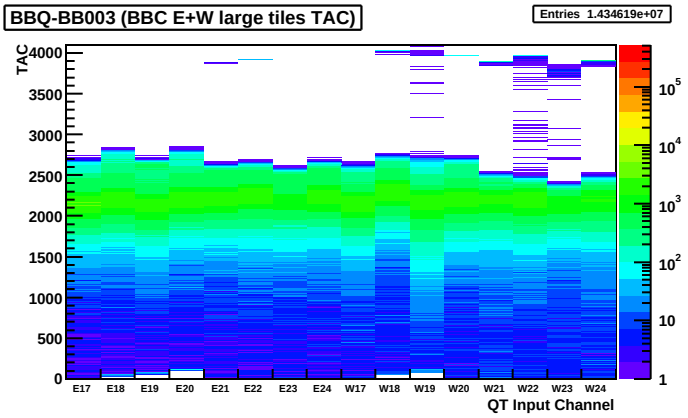

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.434619e+07
ADC
QT Input Channel

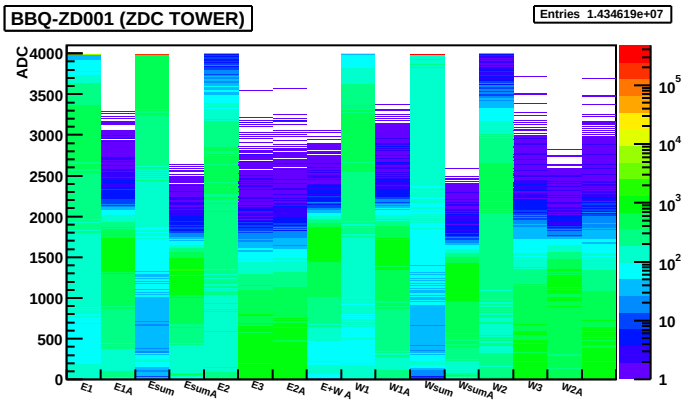

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.434619e+07
TAC
QT Input Channel

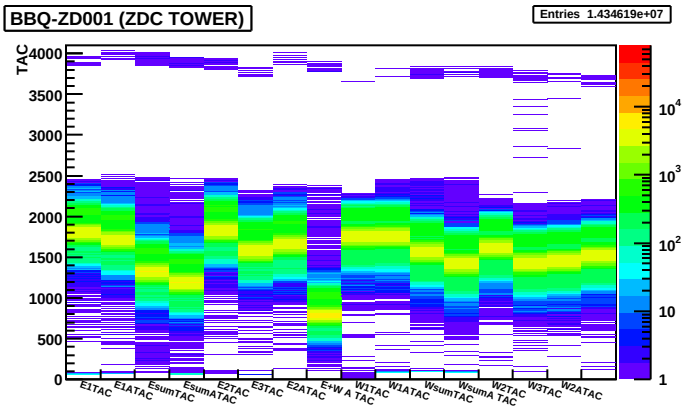

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.434619e+07
ADC
QT Input Channel

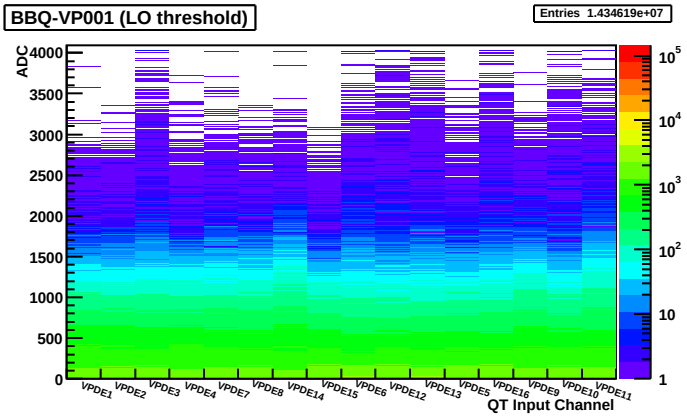

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.434619e+07
TAC
QT Input Channel

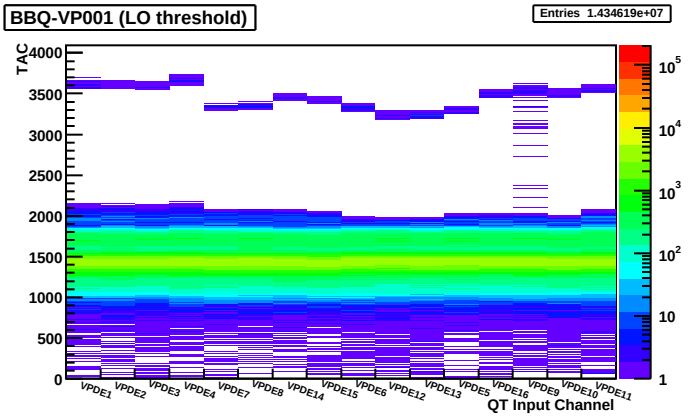

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.434619e+07
ADC
QT Input Channel

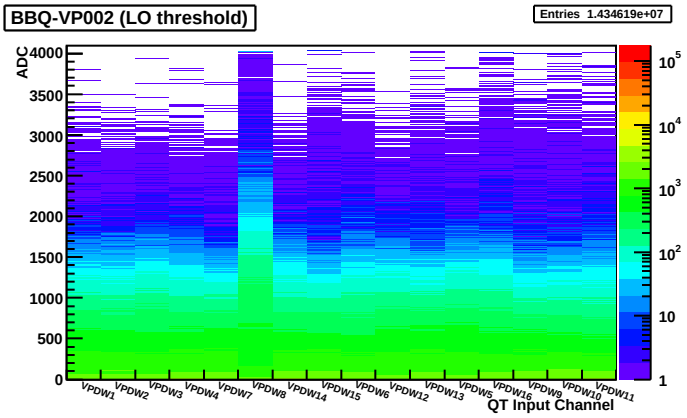

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.434619e+07
TAC
QT Input Channel

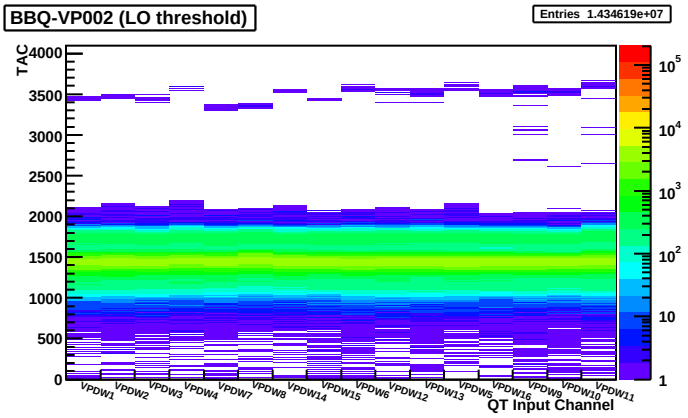

BBQ-ZD001 (ZDC TOWER)
Entries 1.434619e+07
ADC

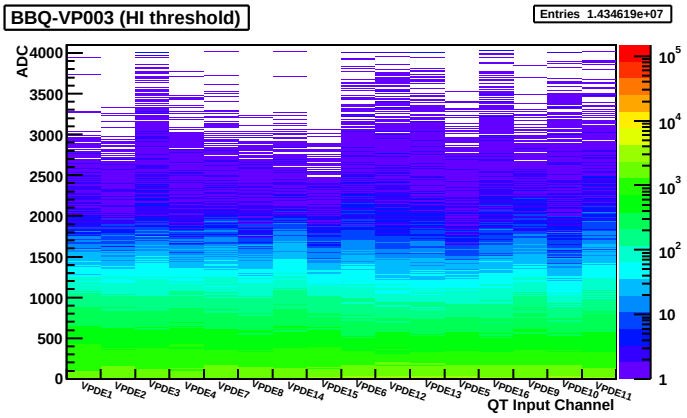

BBQ-ZD001 (ZDC TOWER)
Entries 1.434619e+07
TAC

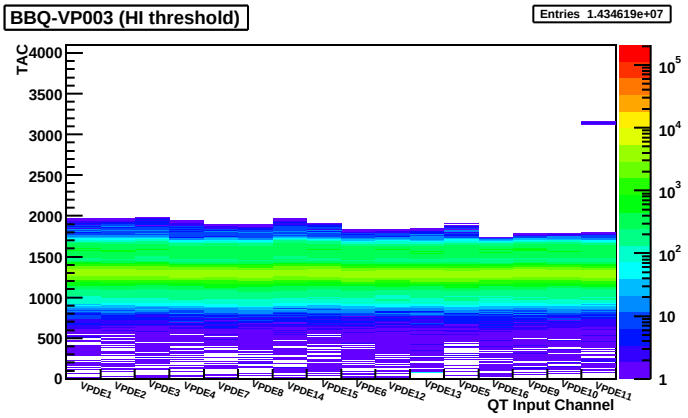

BBQ-VP001 (LO threshold)
Entries 1.434619e+07
ADC
QT Input Channel

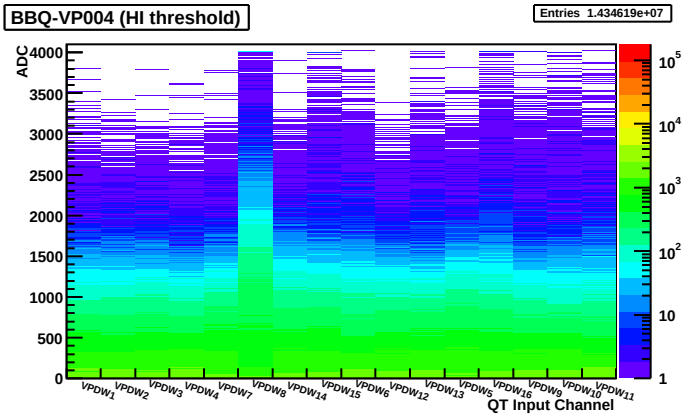

BBQ-VP001 (LO threshold)
Entries 1.434619e+07
TAC
QT Input Channel

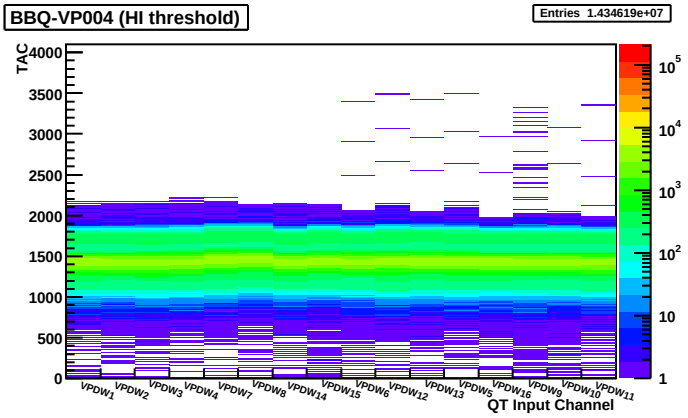

BBQ-VP002 (LO threshold)
Entries 1.434619e+07
ADC
QT Input Channel

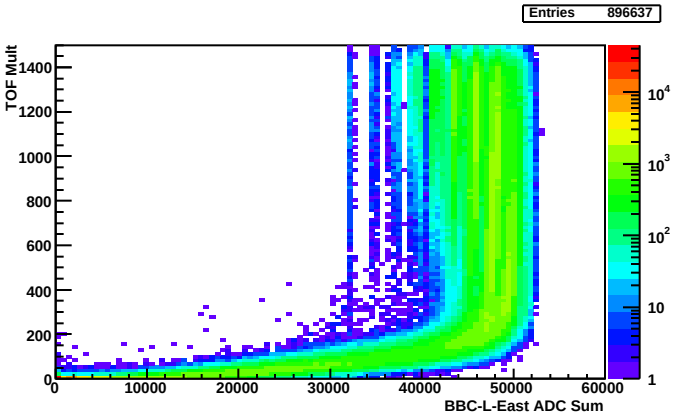

BBQ-VP002 (LO threshold)
Entries 1.434619e+07
TAC
QT Input Channel

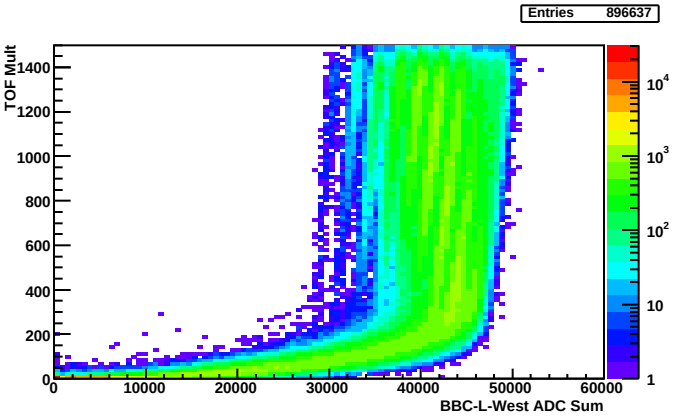

BBQ-VP003 (HI threshold)
Entries 1.434619e+07
ADC
QT Input Channel

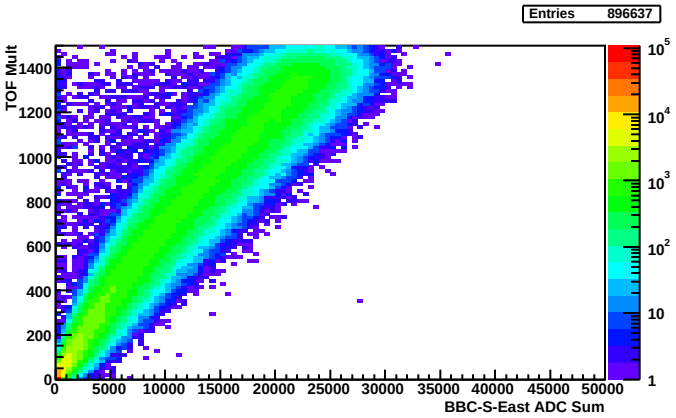

BBQ-VP003 (HI threshold)
Entries 1.434619e+07
TAC
QT Input Channel

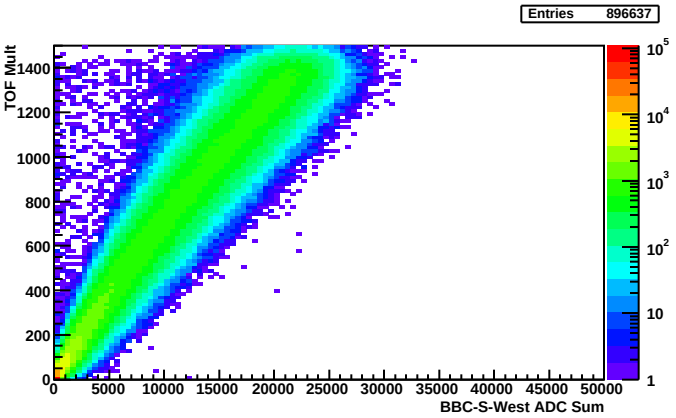

BBQ-VP004 (HI threshold)
Entries 1.434619e+07
ADC
QT Input Channel

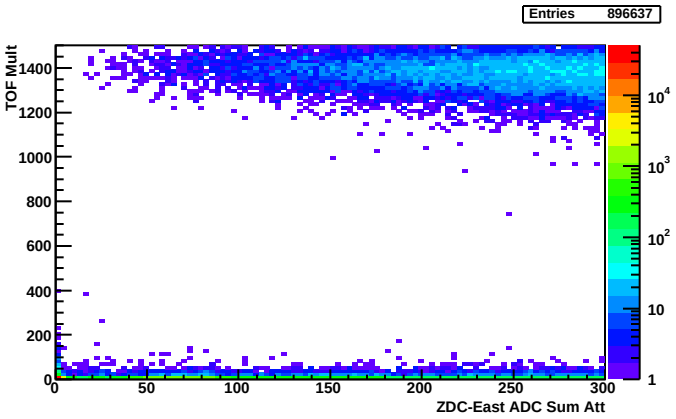

BBQ-VP004 (HI threshold)
Entries 1.434619e+07
TAC
QT Input Channel

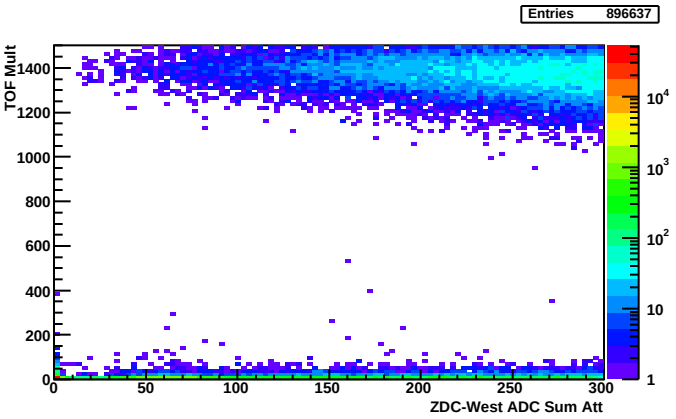

Entries 896637
TOF Mult
BBC-L-East ADC Sum

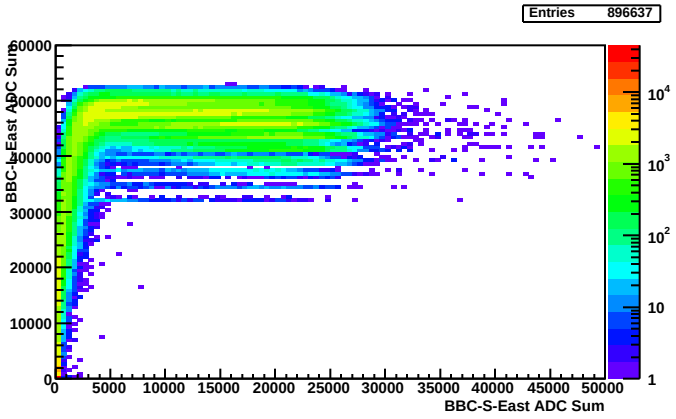

Entries 896637
TOF Mult
BBC-L-West ADC Sum

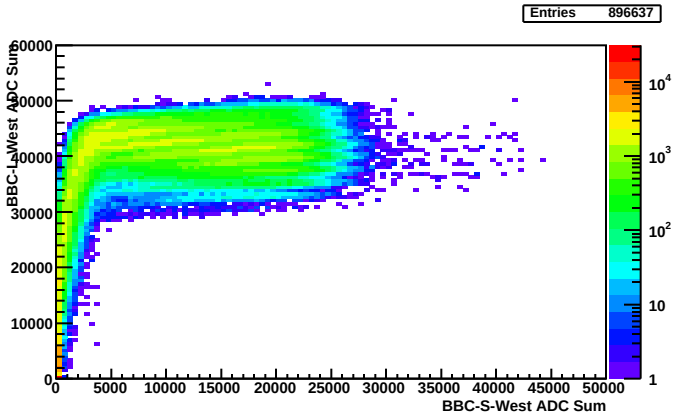

Entries 896637
TOF Mult
BBC-S-East ADC Sum

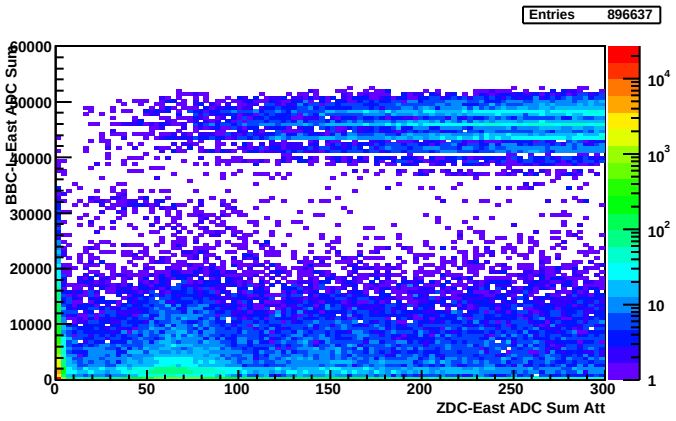

Entries 896637
TOF Mult
BBC-S-West ADC Sum

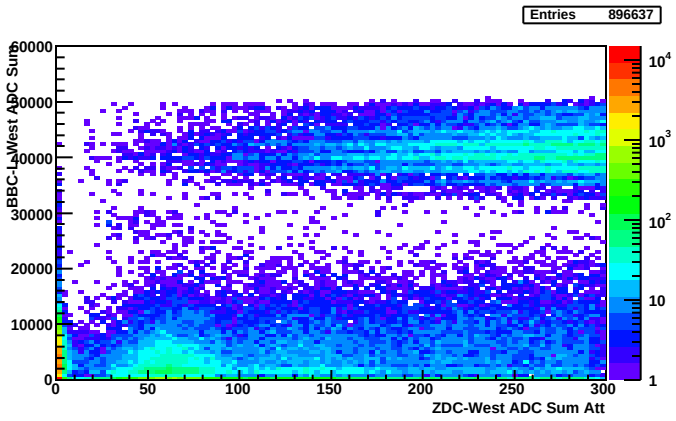

Entries 896637
TOF Mult
ZDC-East ADC Sum Att

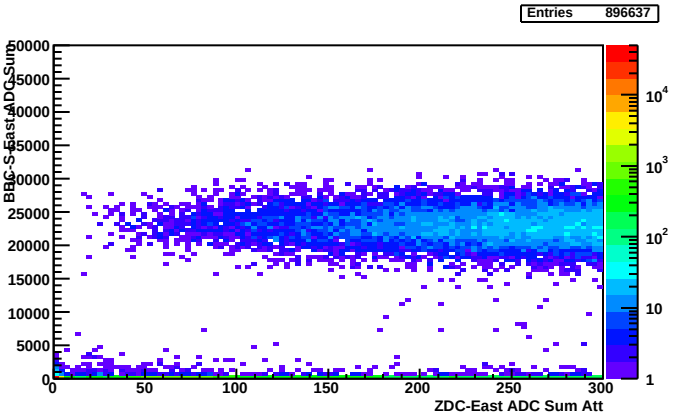

Entries 896637
TOF Mult
ZDC-West ADC Sum Att


Entries 896637
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 896637
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 896637
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 896637
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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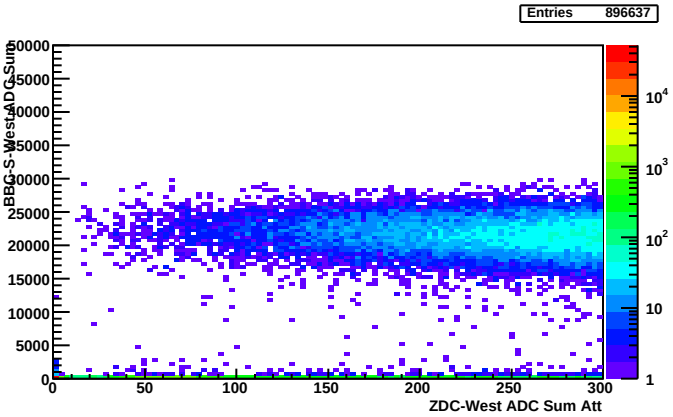
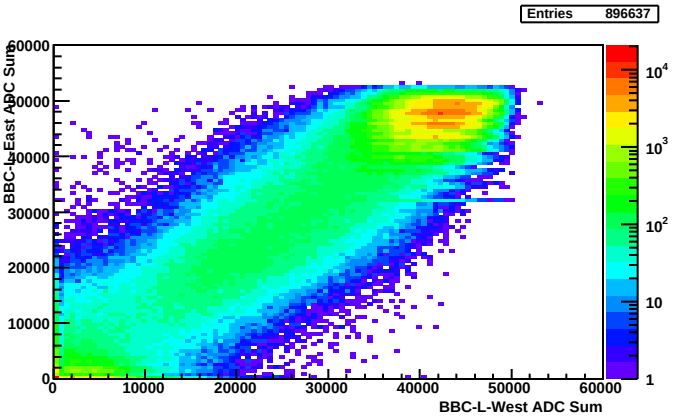
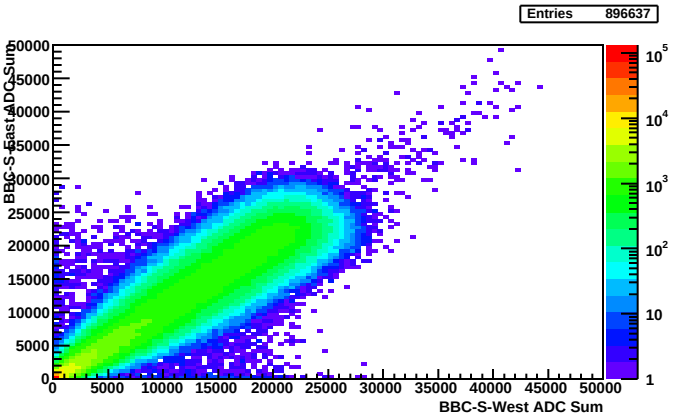
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 896637
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 896637
BBC-L-East ADC Sum
BBC-L-West ADC Sum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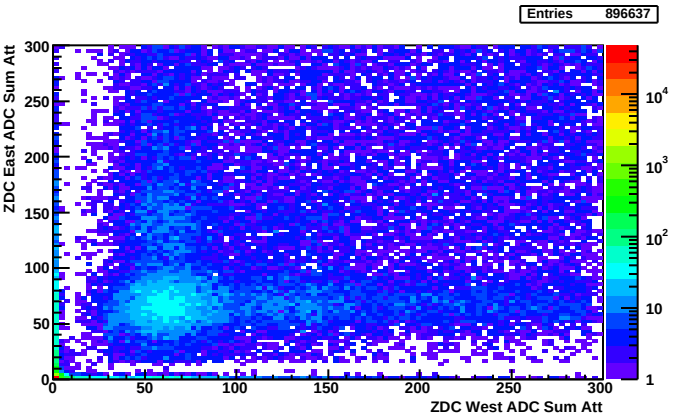
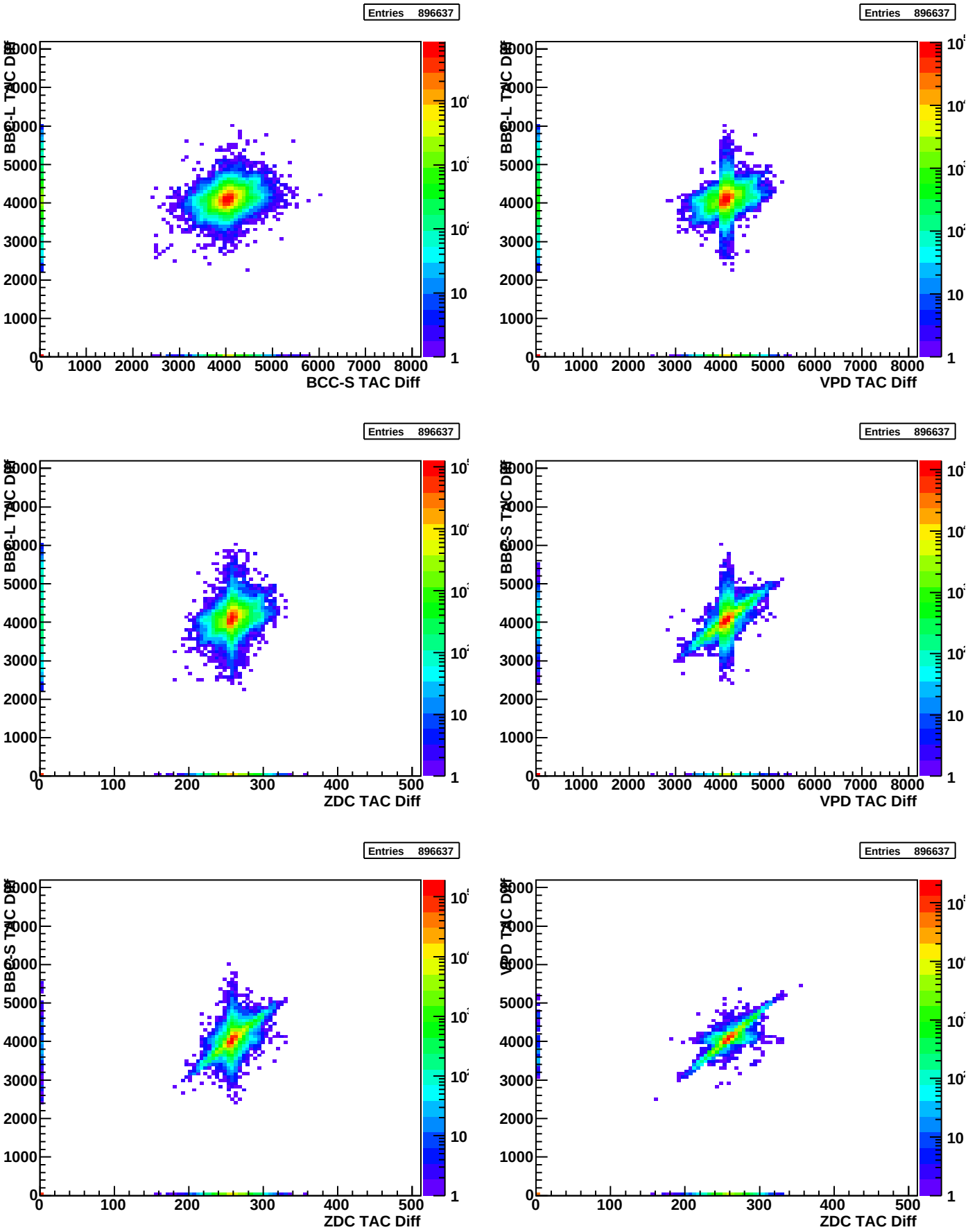
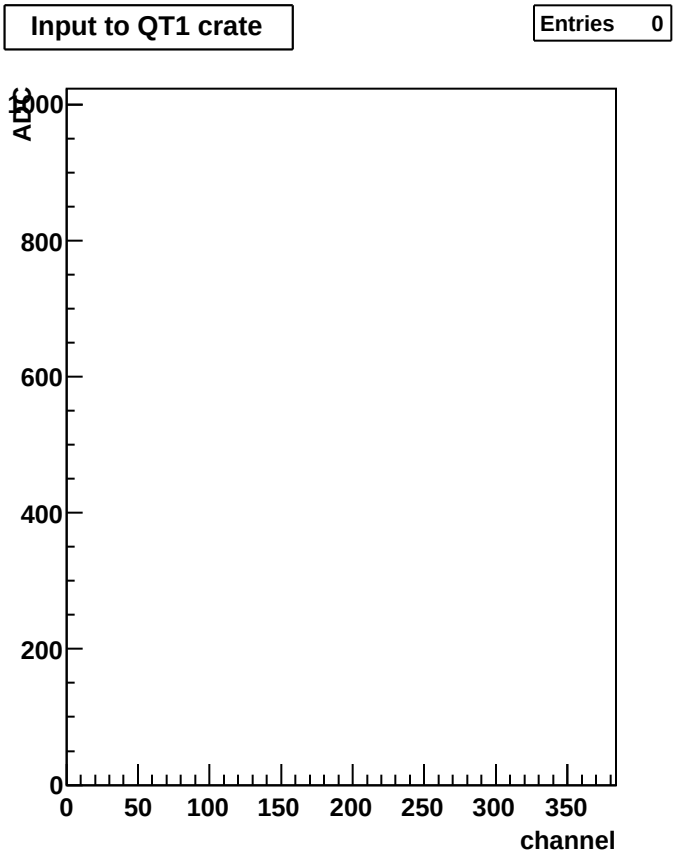
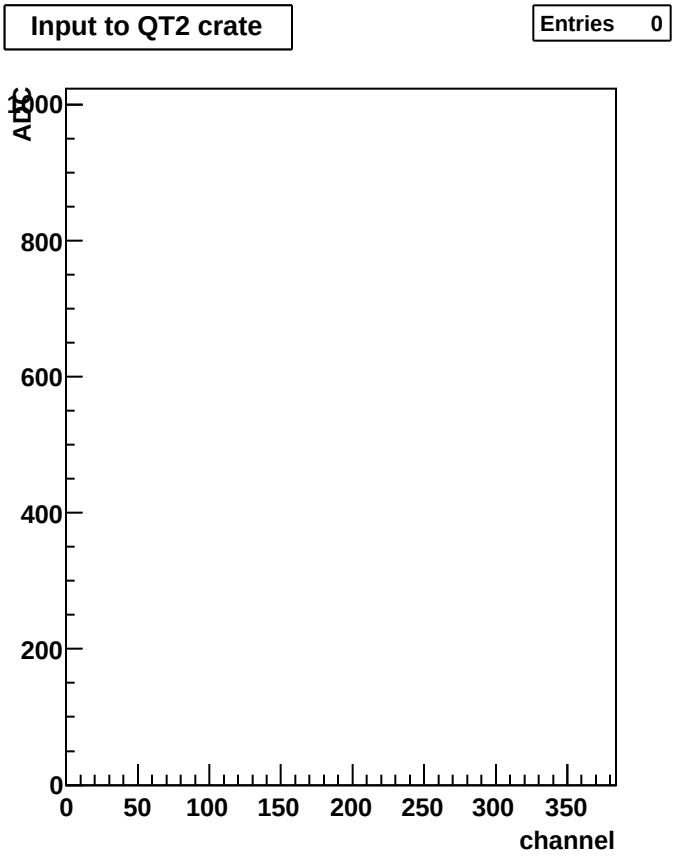
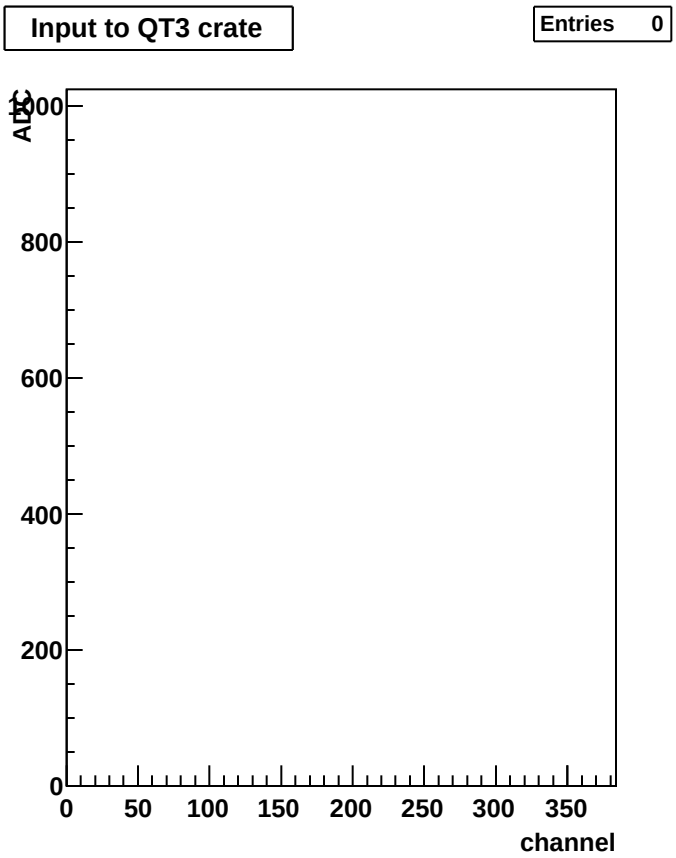
BBC-S-East ADC Sum
BBC-S-West ADC Sum

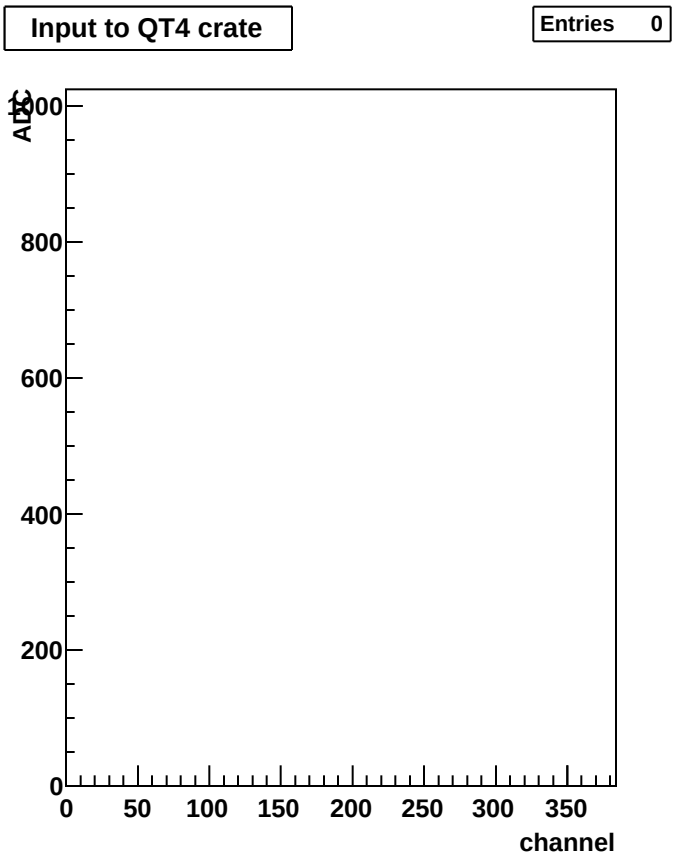

Entries 896637
ZDC East ADC Sum Att
ZDC West ADC Sum Att

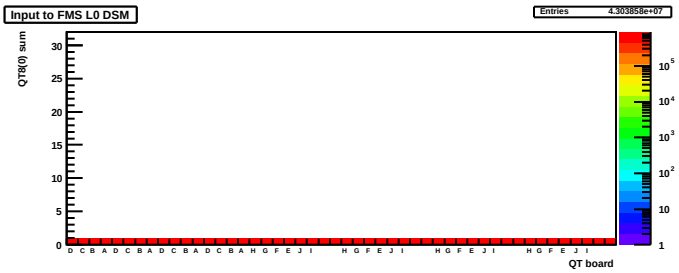

Entries 896637
BBC-L TAC Diff
BCC-S TAC Diff
Entries 896637
BBC-L TAC Diff
VPD TAC Diff
Entries 896637
BBC-L TAC Diff
ZDC TAC Diff
Entries 896637
BBC-S TAC Diff
VPD TAC Diff
Entries 896637
BBC-S TAC Diff
ZDC TAC Diff
Entries 896637
VPD TAC Diff
ZDC TAC Diff

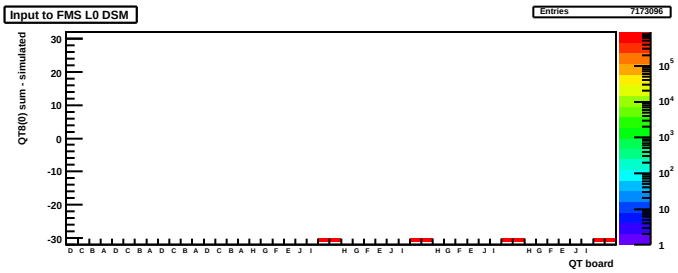

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

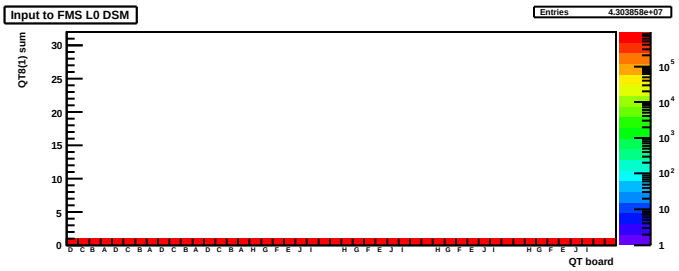

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

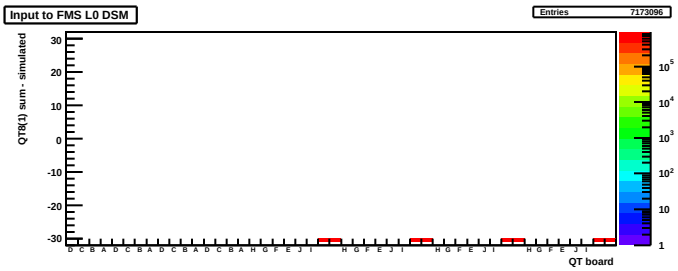

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

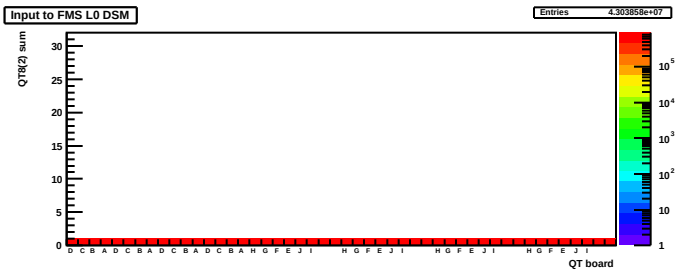

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

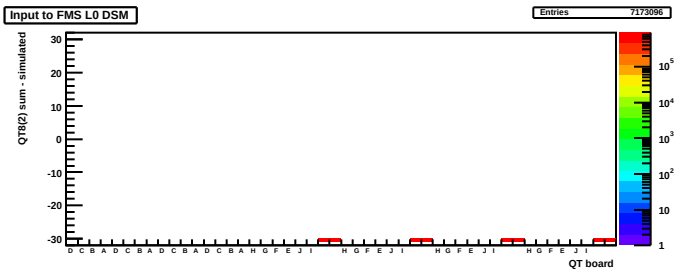

Input to FMS L0 DSM
Entries 4.303858e+07
QT8(0) sum
QT board

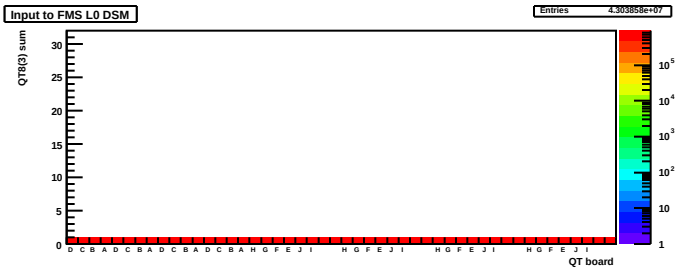

Input to FMS L0 DSM
Entries 7173096
QT8(0) sum - simulated
QT board

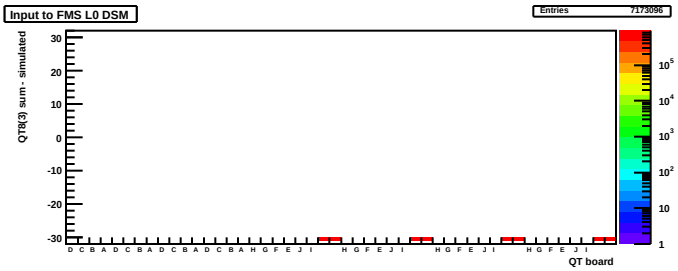

Input to FMS L0 DSM
Entries 4.303858e+07
QT8(1) sum
QT board

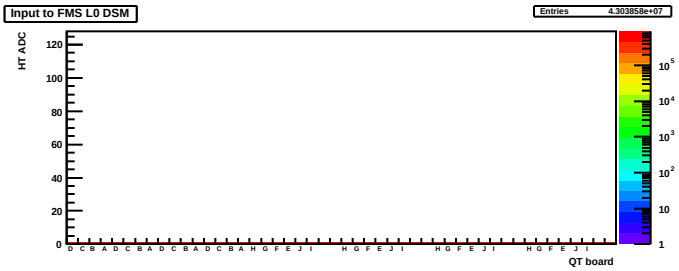

Input to FMS L0 DSM
Entries 7173096
QT8(1) sum - simulated
QT board

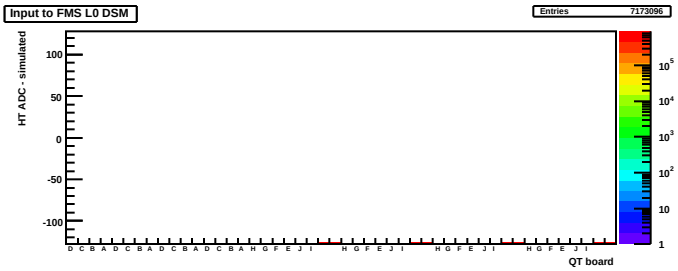

Input to FMS L0 DSM
Entries 4.303858e+07
QT8(2) sum
QT board

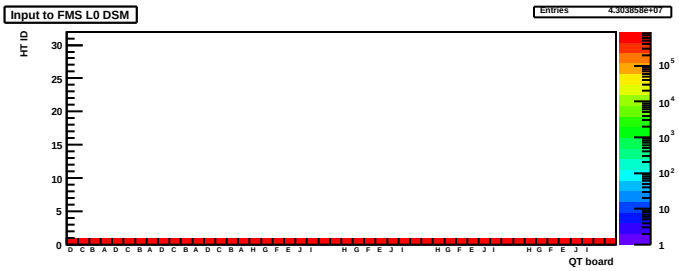

Input to FMS L0 DSM
Entries 7173096
QT8(2) sum - simulated
QT board

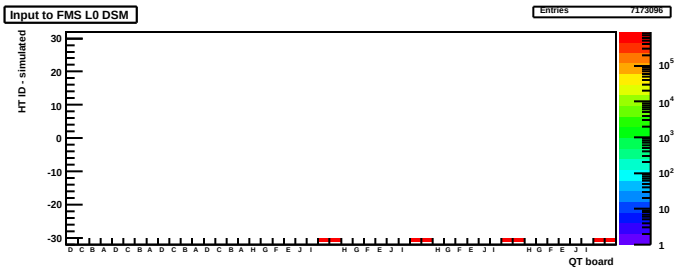

Input to FMS L0 DSM
Entries 4.303858e+07
QT8(3) sum
QT board

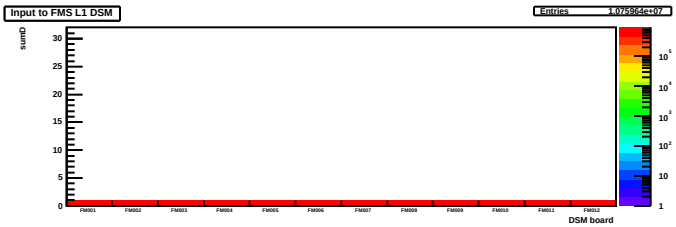

Input to FMS L0 DSM
Entries 7173096
QT8(3) sum - simulated
QT board

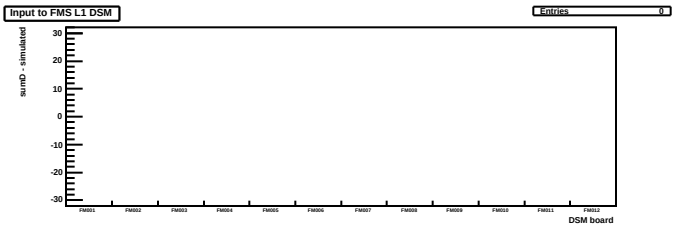

Input to FMS L0 DSM
Entries 4.303858e+07
HT ADC
QT board

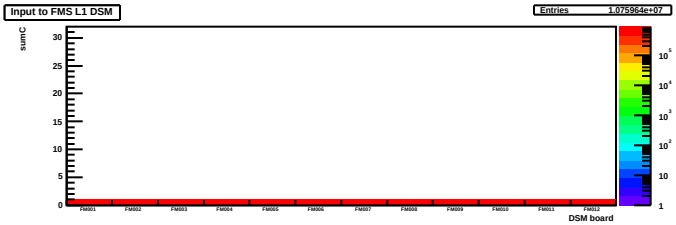

Input to FMS L0 DSM
Entries 7173096
HT ADC - simulated
QT board

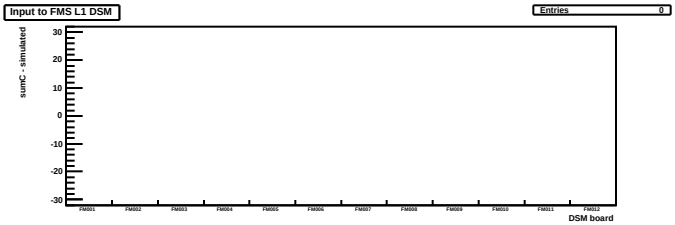

Input to FMS L0 DSM
Entries 4.303858e+07
HT ID
QT board

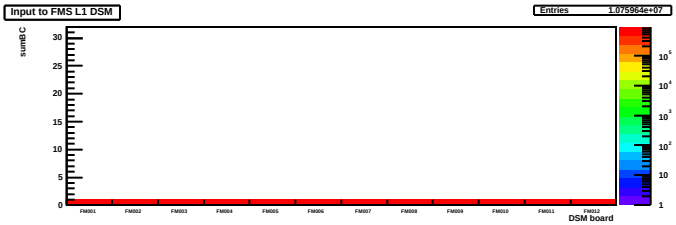

Input to FMS L0 DSM
Entries 7173096
HT ID - simulated
QT board

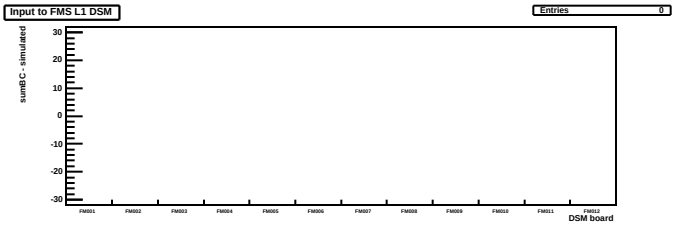

Input to FMS L1 DSM
Entries 1.075964e+07
sumD
DSM board

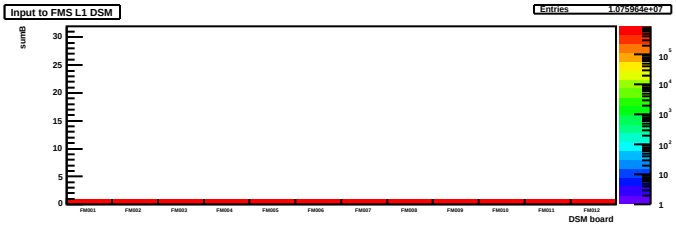

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

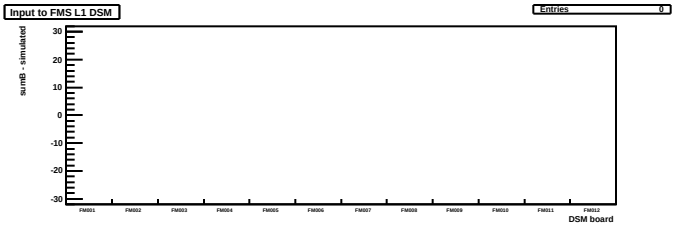

Input to FMS L1 DSM
Entries 1.075964e+07
sumC
DSM board

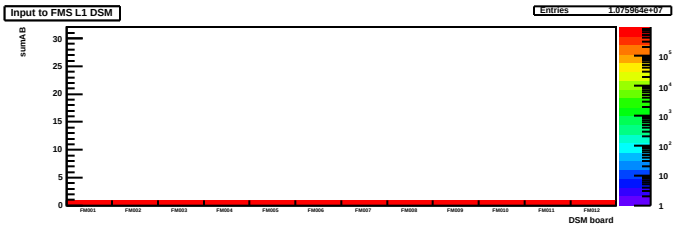

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

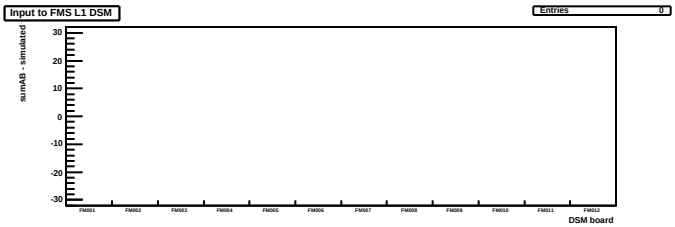

Input to FMS L1 DSM
Entries 1.075964e+07
sumBC
DSM board

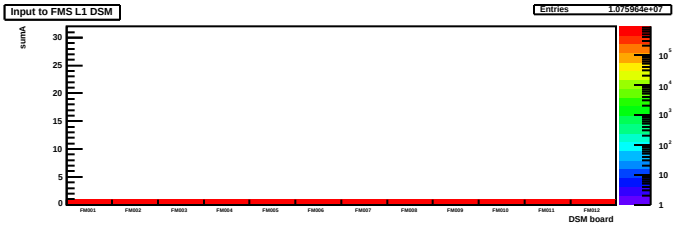

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

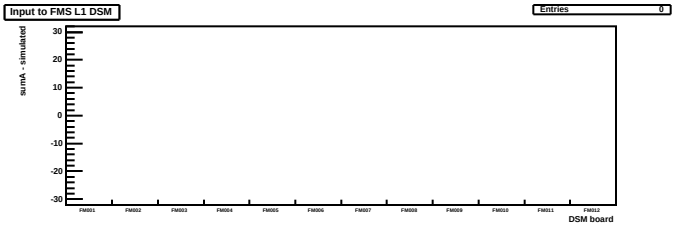

Input to FMS L1 DSM
Entries 1.075964e+07
sumB
DSM board

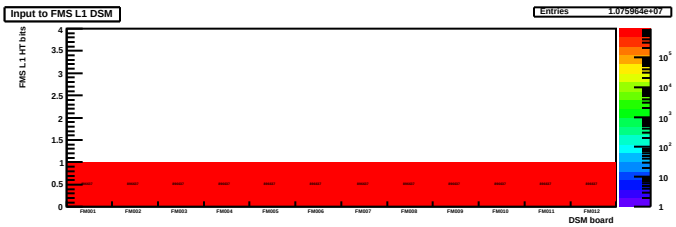

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

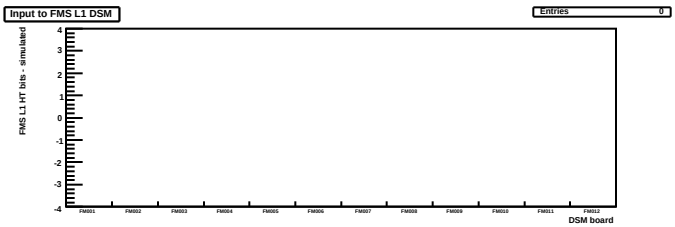

Input to FMS L1 DSM
Entries 1.075964e+07
sumAB
DSM board

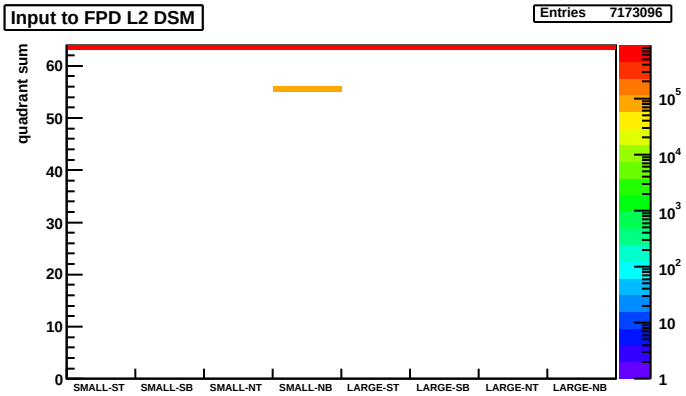

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

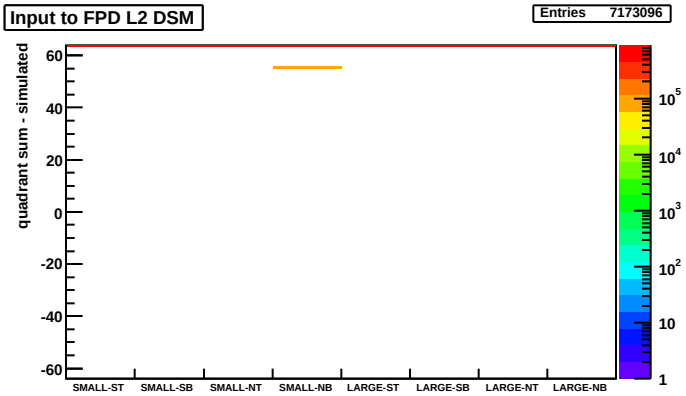

Input to FMS L1 DSM
Entries 1.075964e+07
sumA
DSM board

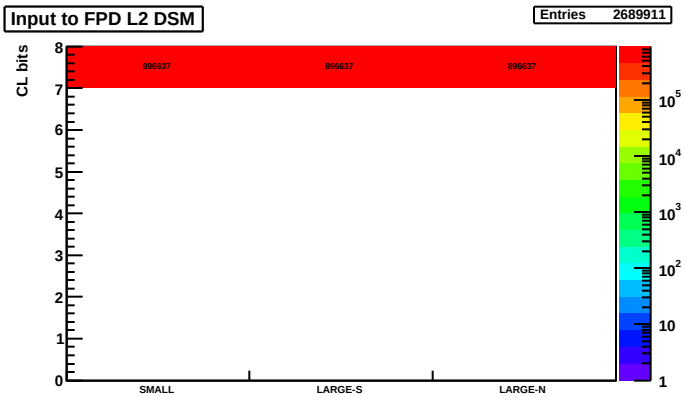

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

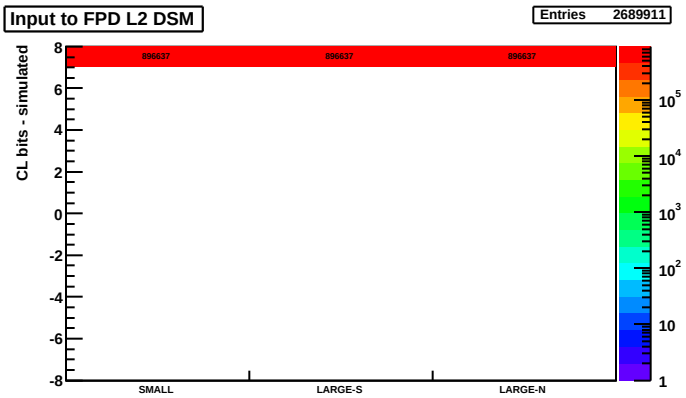

Input to FMS L1 DSM
Entries 1.075964e+07
FMS L1 HT bits
DSM board

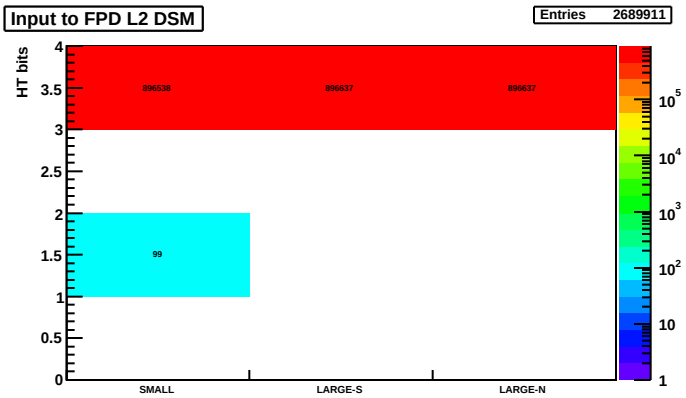

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

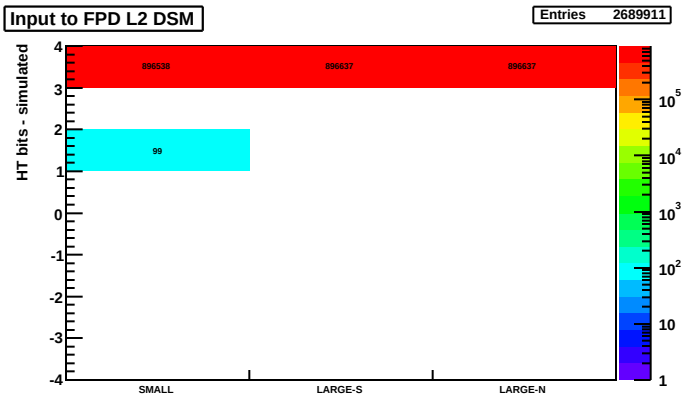

Input to FPD L2 DSM
Entries 7173096
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

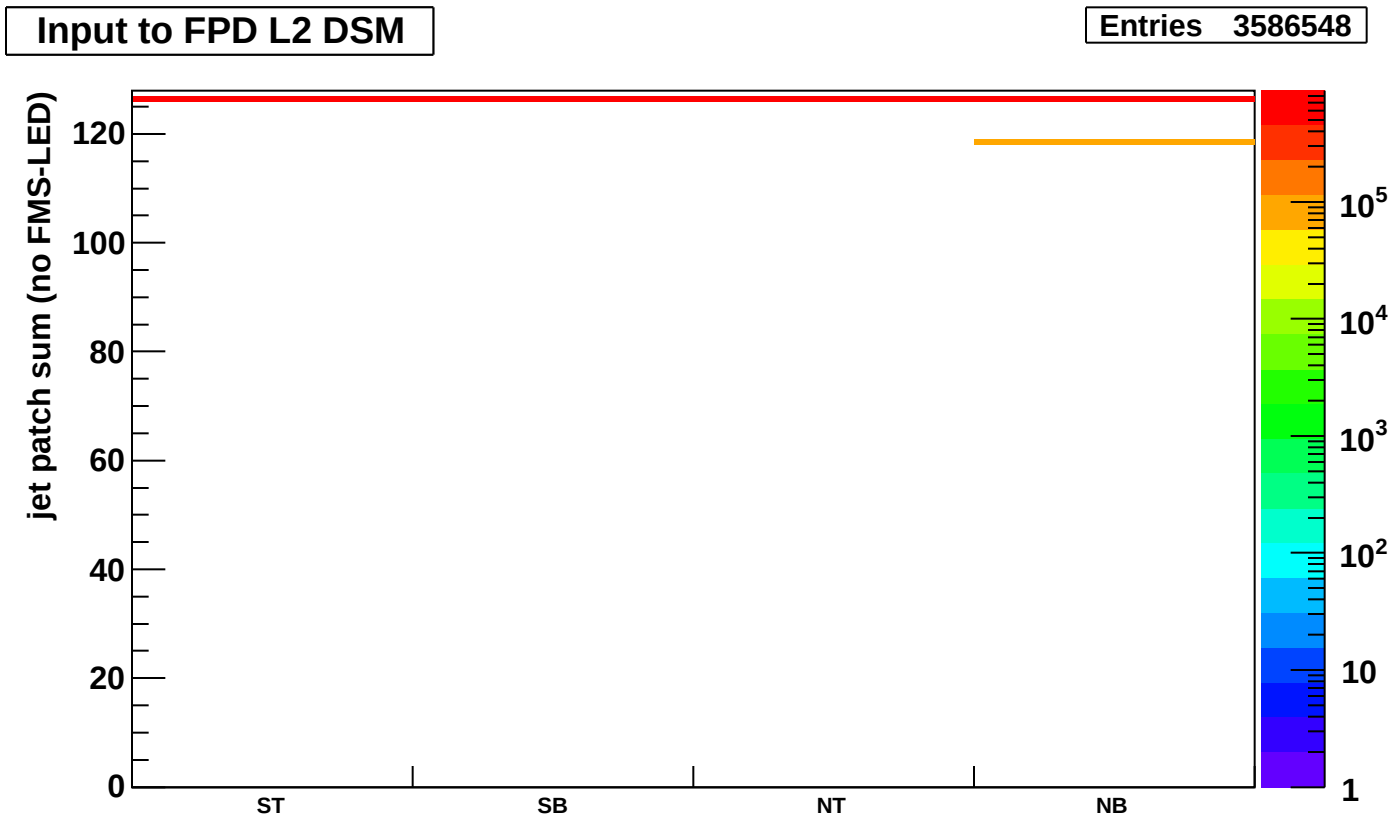

Input to FPD L2 DSM
Entries 7173096
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

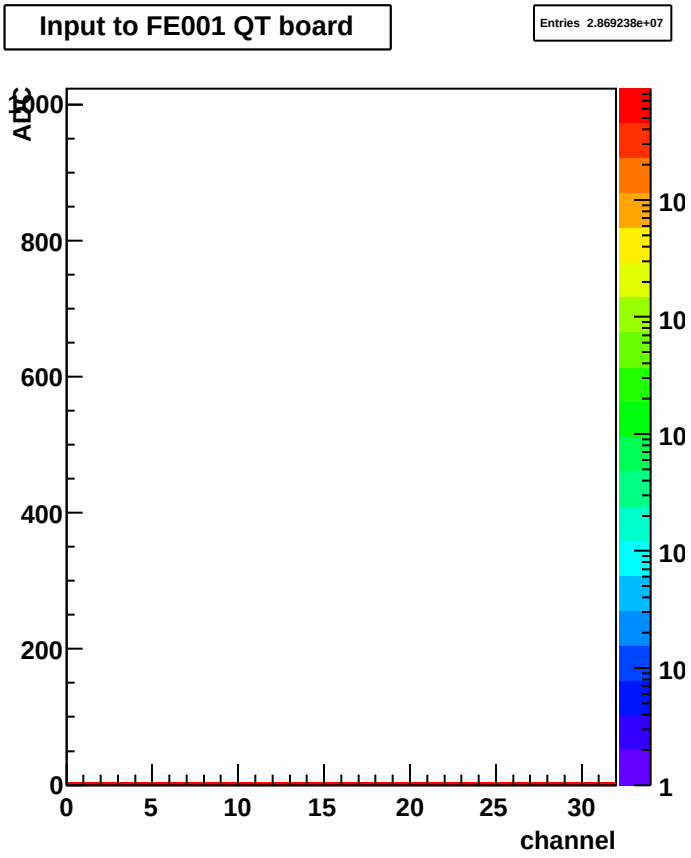

Input to FPD L2 DSM
Entries 2689911
CL bits
896637
896637
896637
SMALL
LARGE-S
LARGE-N

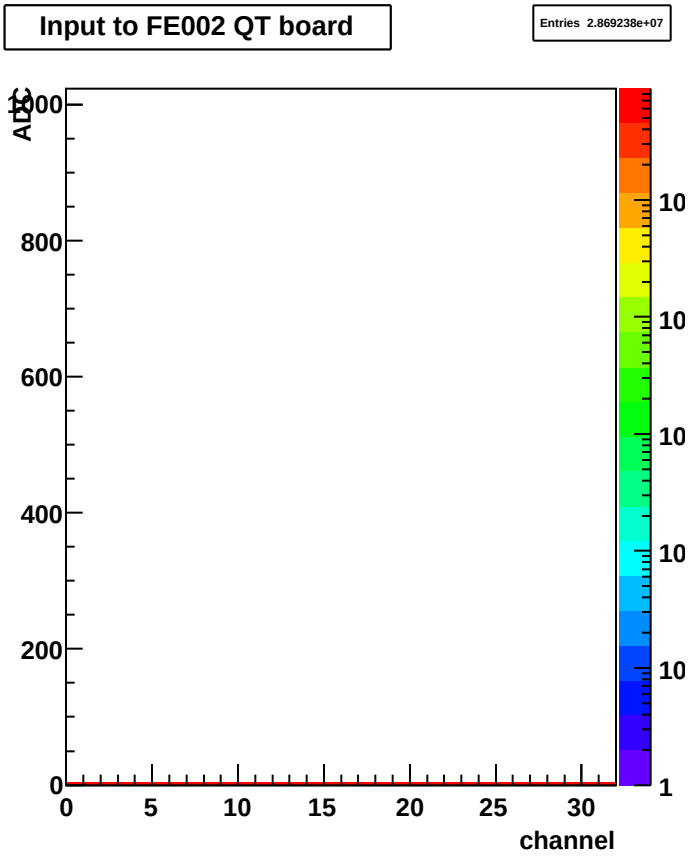

Input to FPD L2 DSM
Entries 2689911
CL bits - simulated
896637
896637
896637
SMALL
LARGE-S
LARGE-N

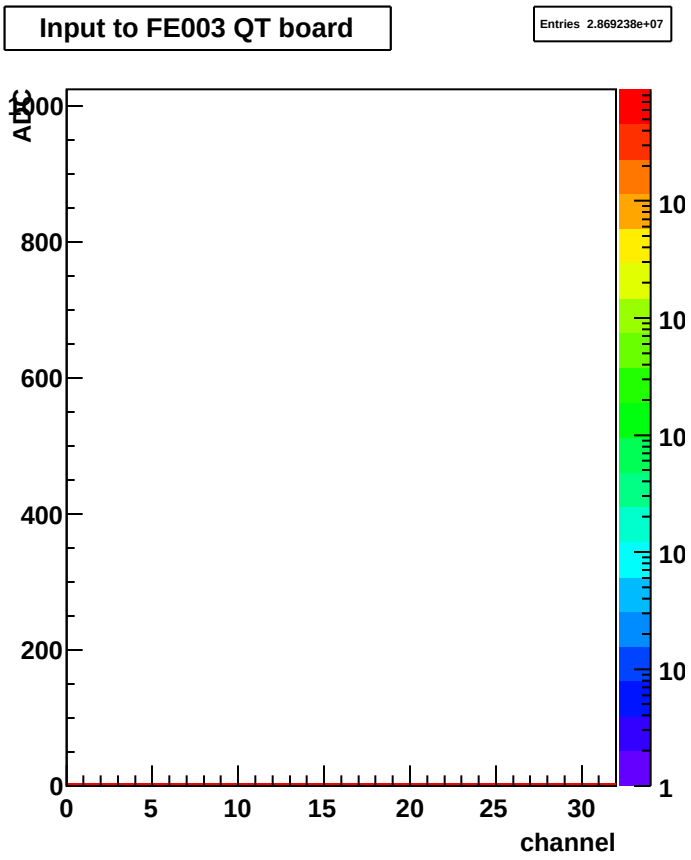

Input to FPD L2 DSM
Entries 2689911
HT bits
896538
896637
896637
99
SMALL
LARGE-S
LARGE-N

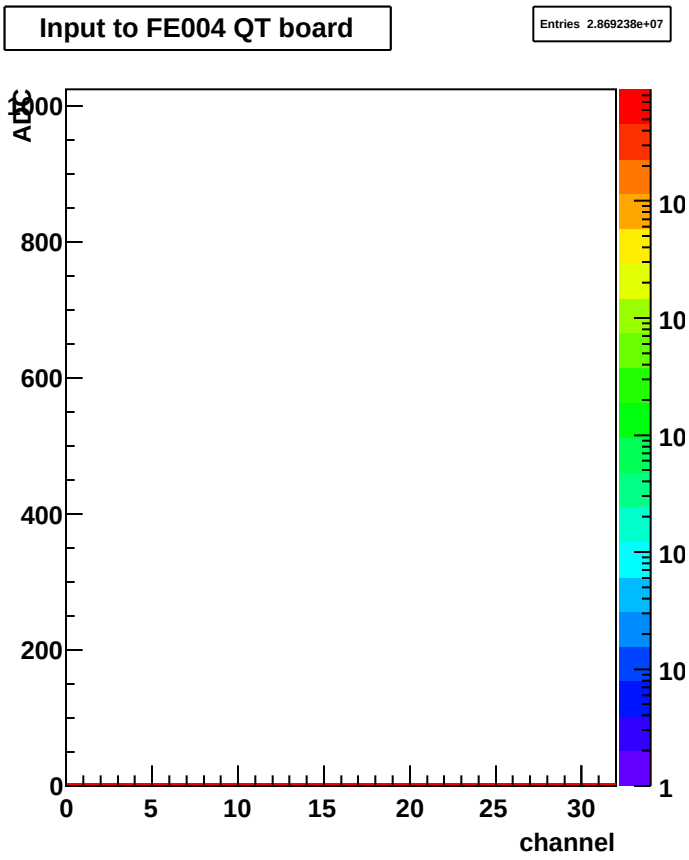

Input to FPD L2 DSM
Entries 2689911
HT bits - simulated
896538
896637
896637
99
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3586548
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

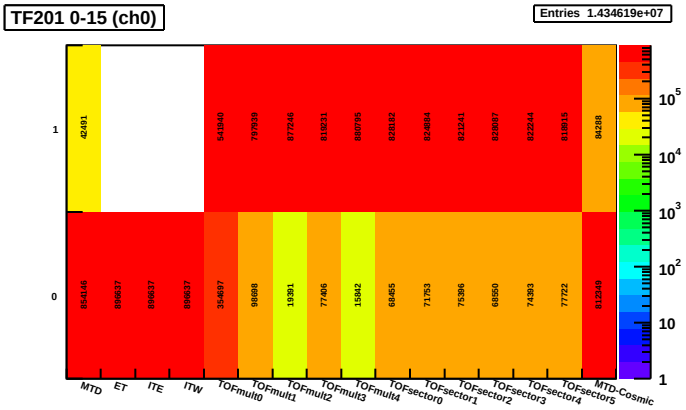

Input to FE001 QT board
Entries 2.869238e+07
ADC
channel


Input to FE002 QT board
Entries 2.869238e+07
ADC
channel

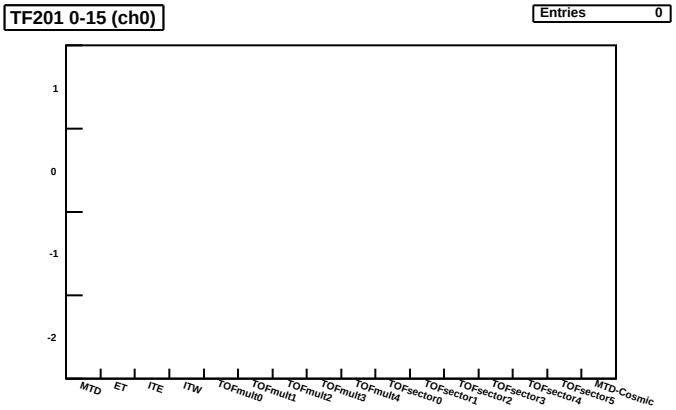

Input to FE003 QT board
Entries 2.869238e+07
ADC
channel


Input to FE004 QT board
Entries 2.869238e+07
ADC
channel

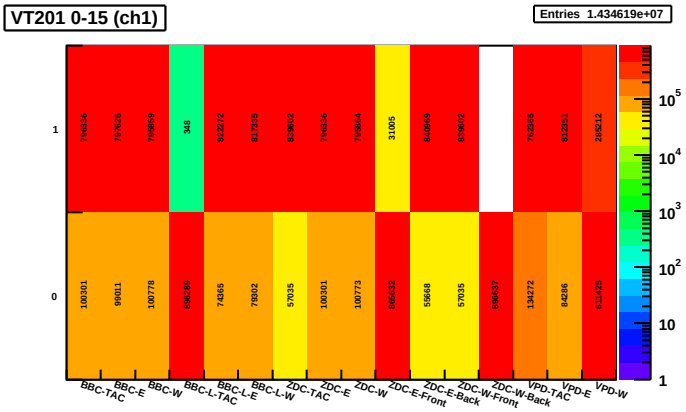
TF201 0-15 (ch0)



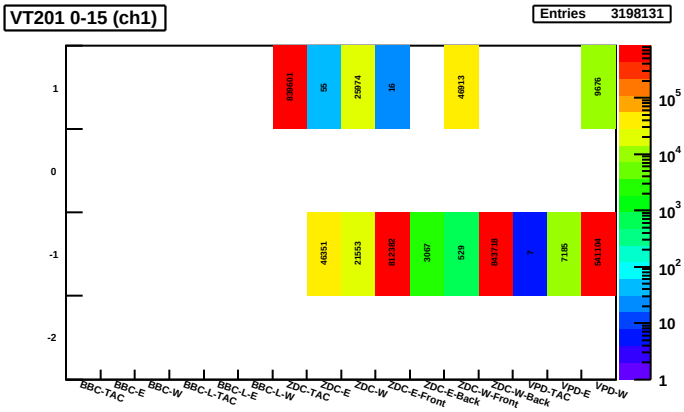
TF201 0-15 (ch0)



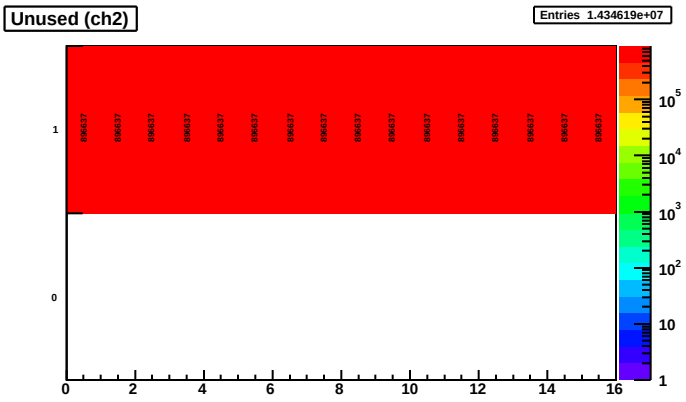
VT201 0-15 (ch1)

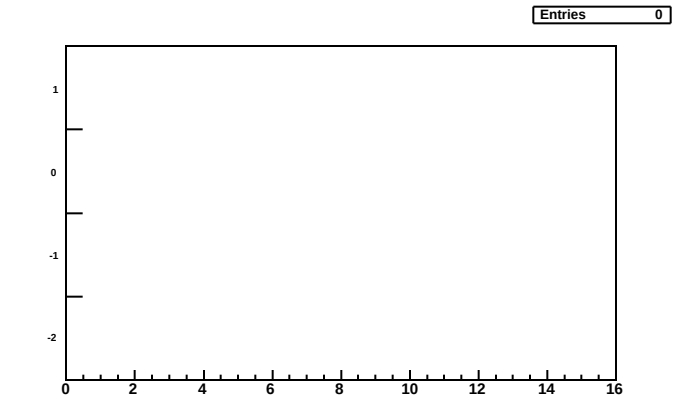


VT201 0-15 (ch1)

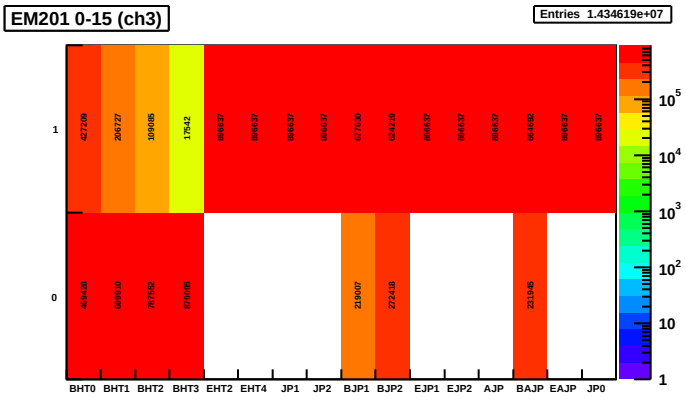


Unused (ch2)

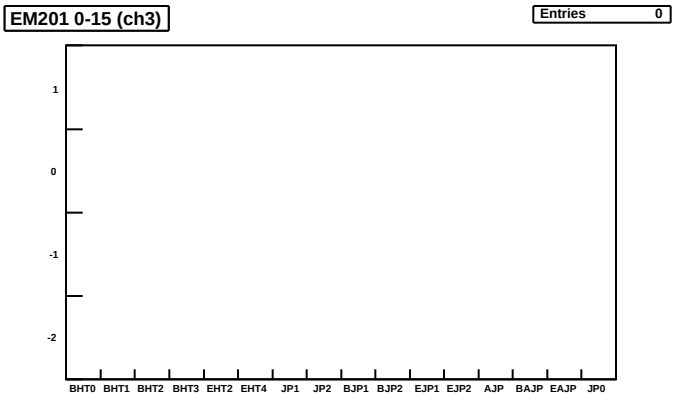




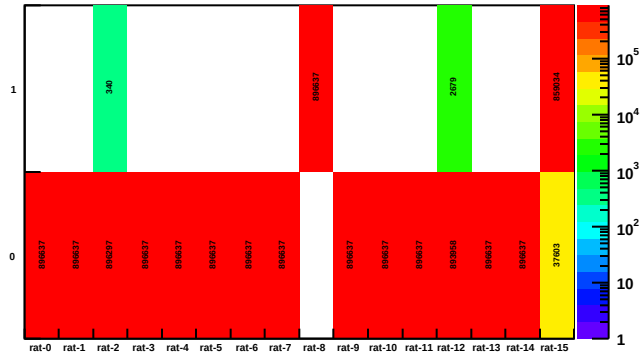
EM201 0-15 (ch3)

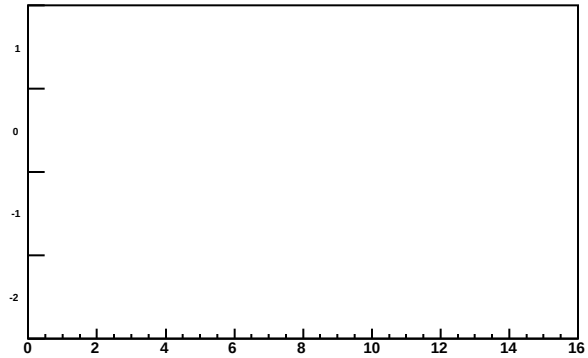


EM201 0-15 (ch3)

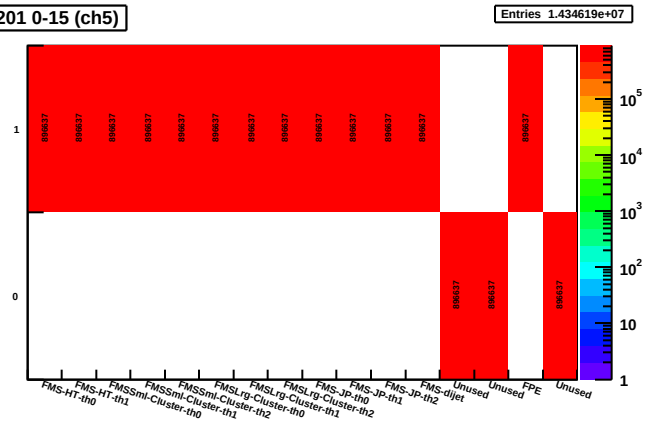


RAT board (ch4)

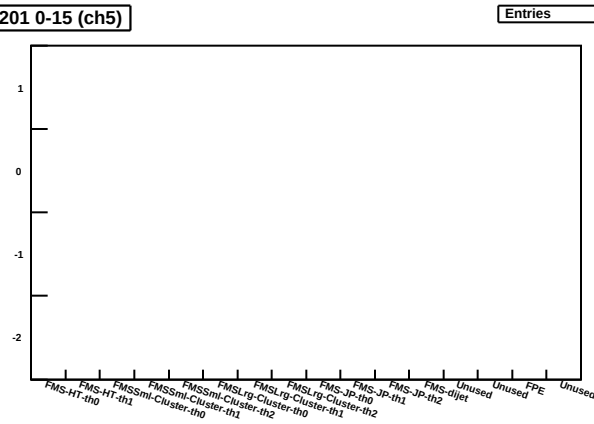




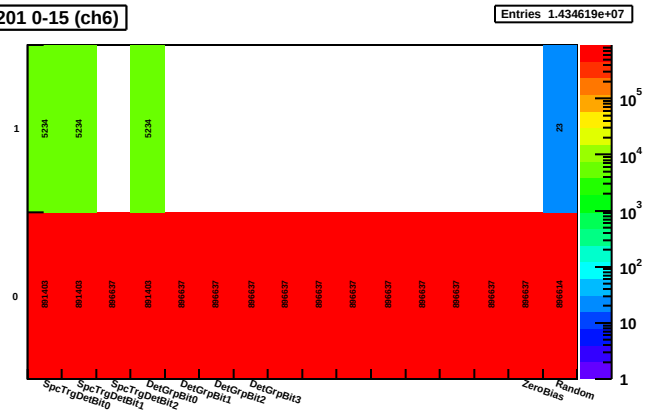
FP201 0-15 (ch5)

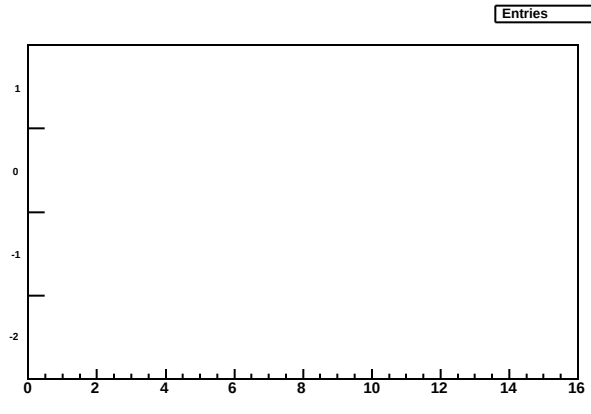
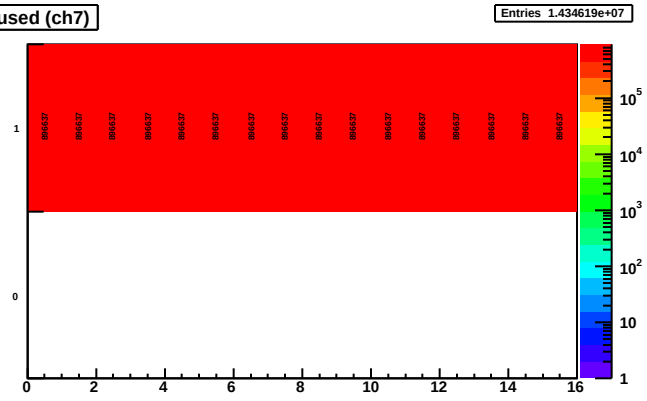
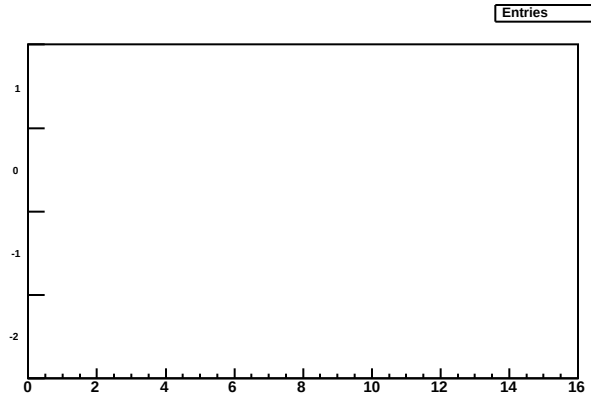


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)