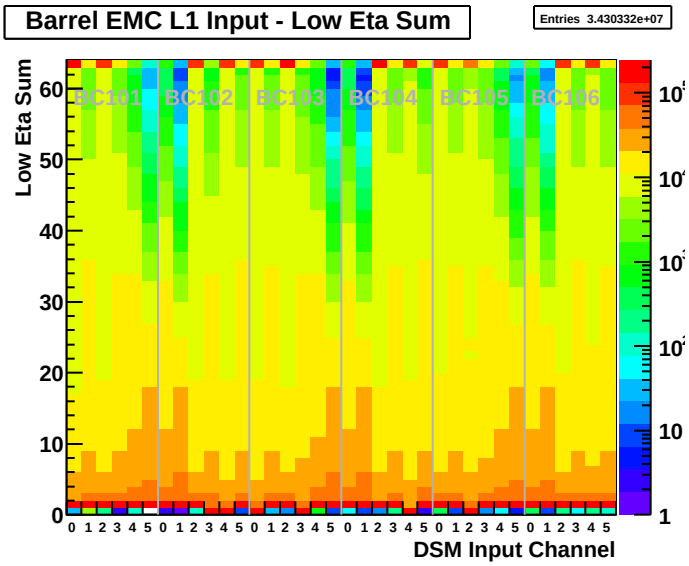
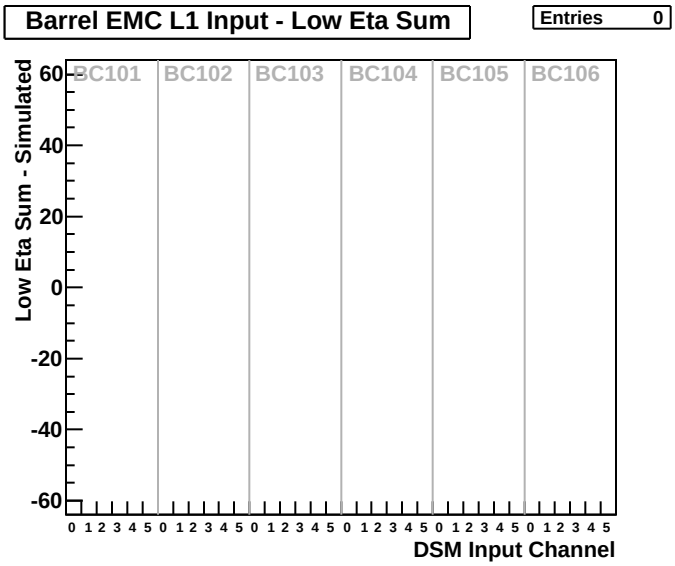
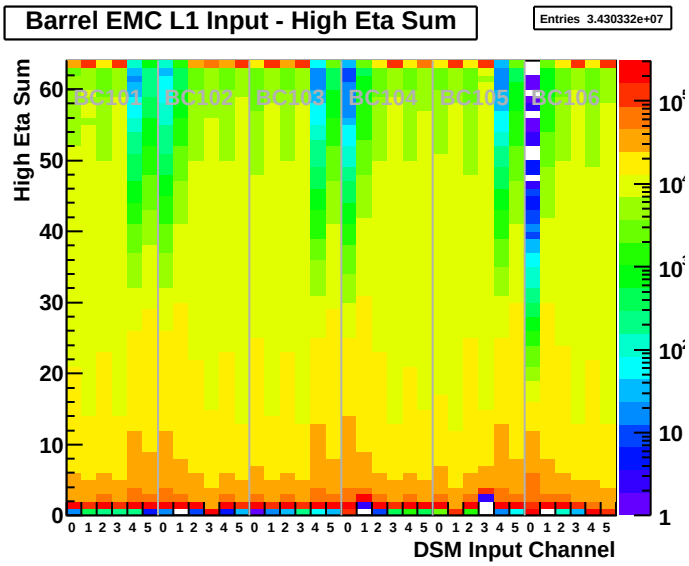
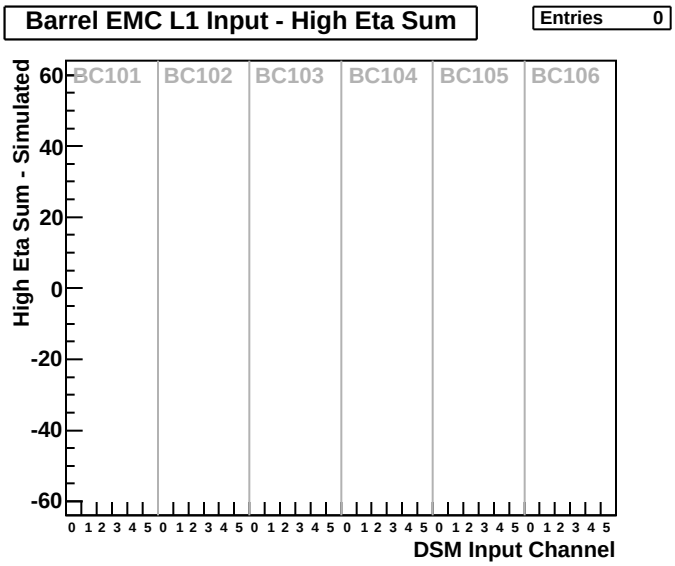
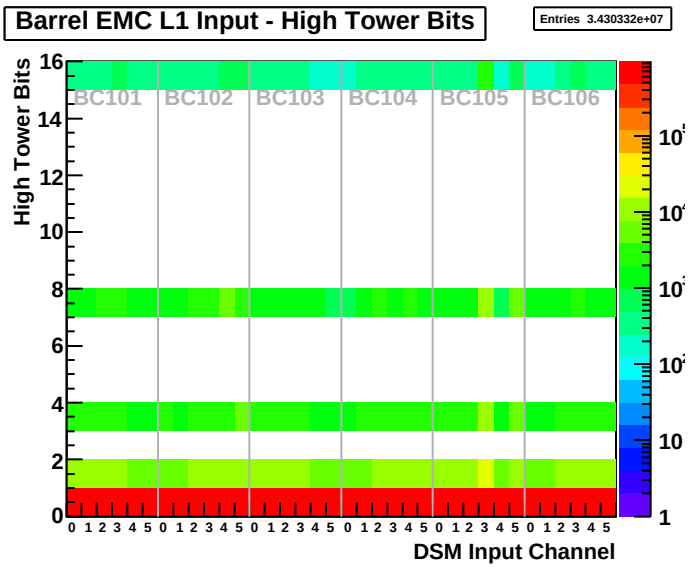
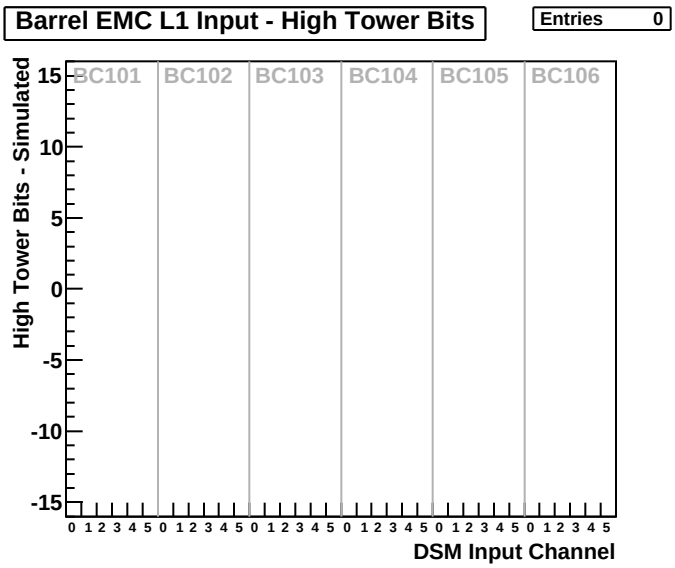

Barrel EMC L1 Input - Low Eta Sum
Entries 3.430332e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

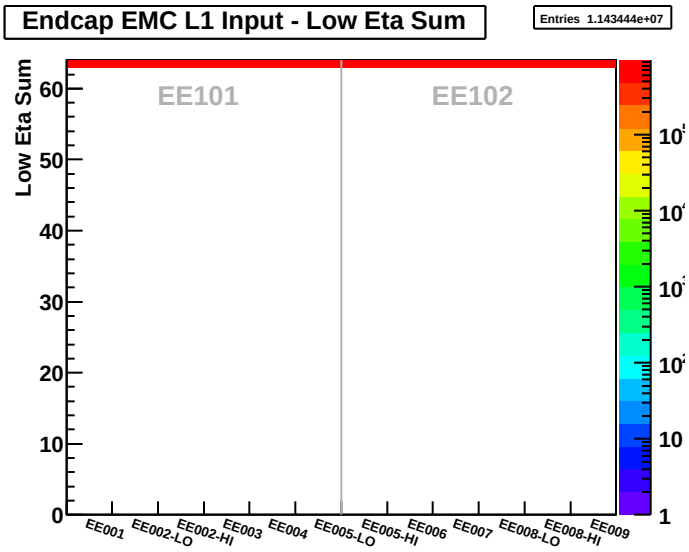

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

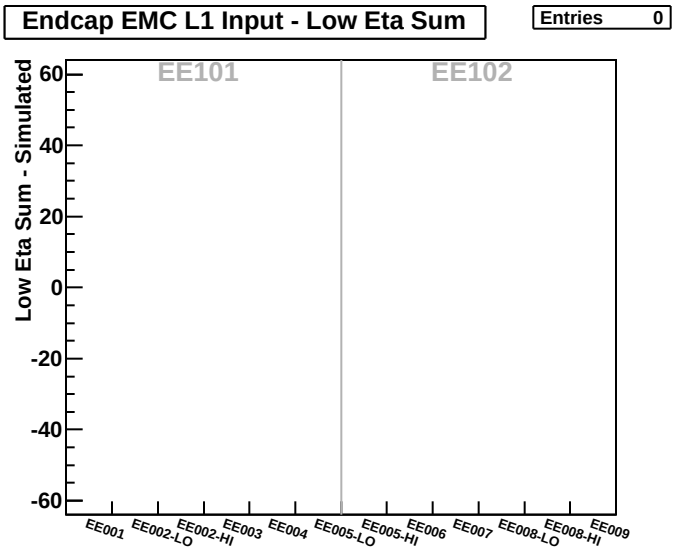

Barrel EMC L1 Input - High Eta Sum
Entries 3.430332e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

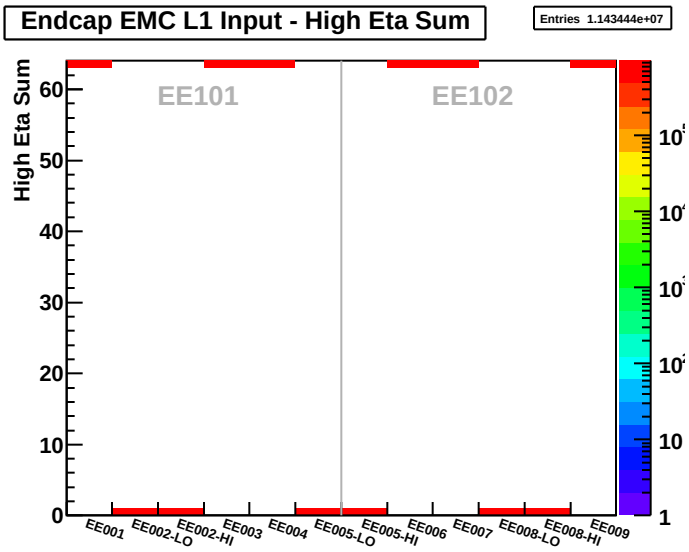

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

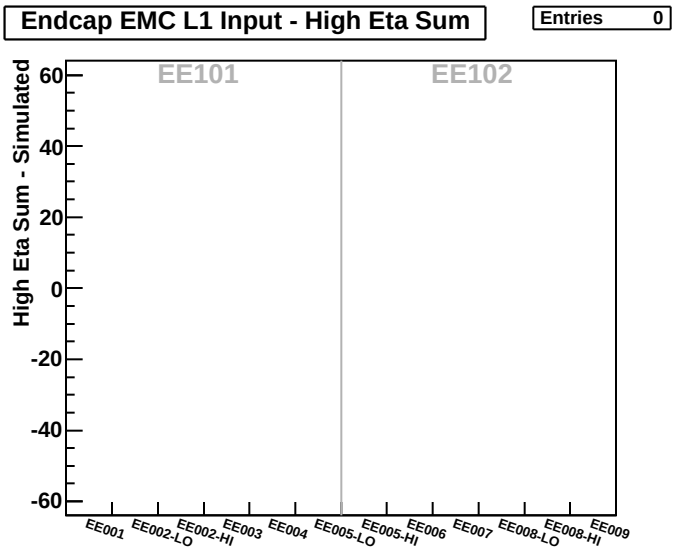

Barrel EMC L1 Input - High Tower Bits
Entries 3.430332e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

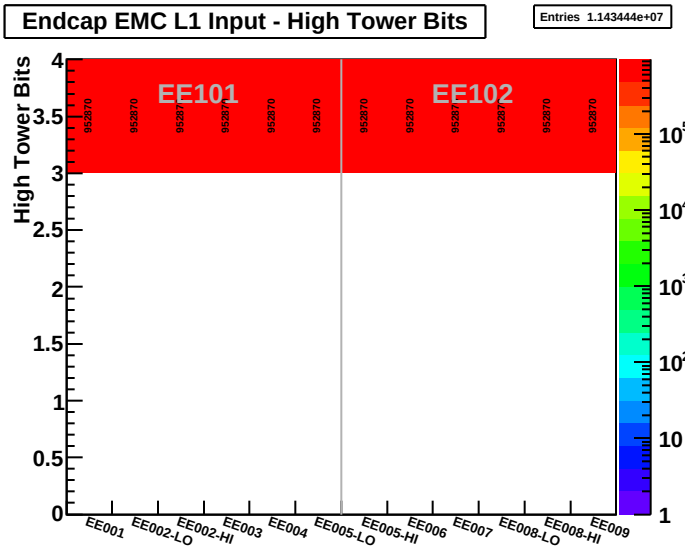

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

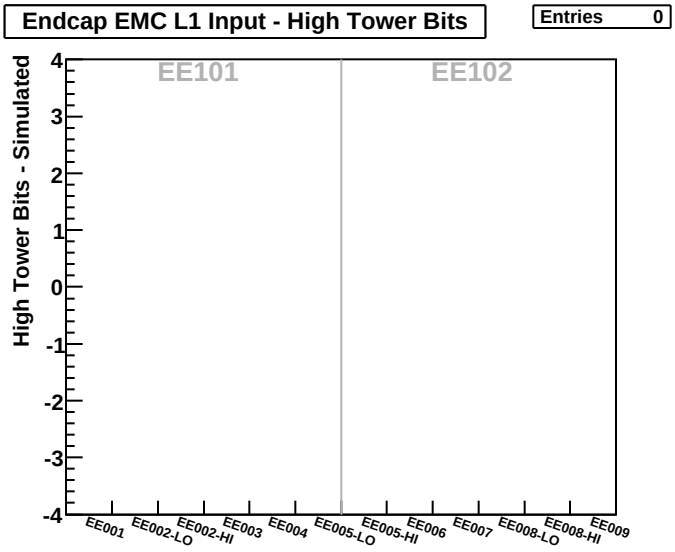

Endcap EMC L1 Input - Low Eta Sum
Entries 1.143444e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

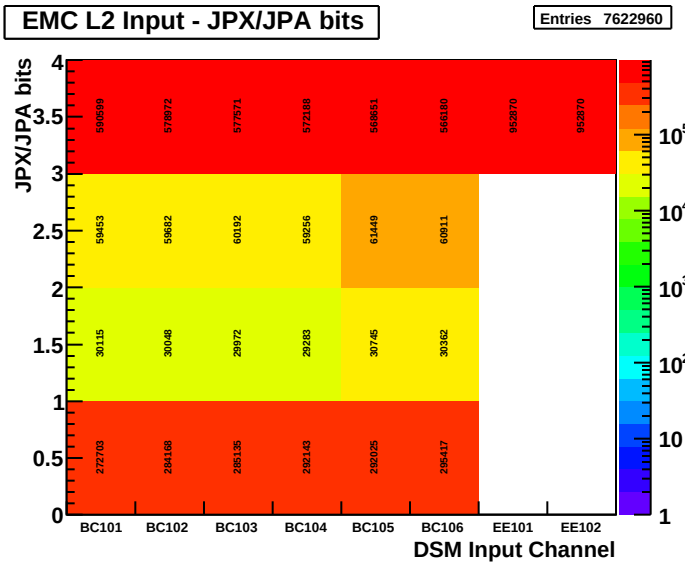

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

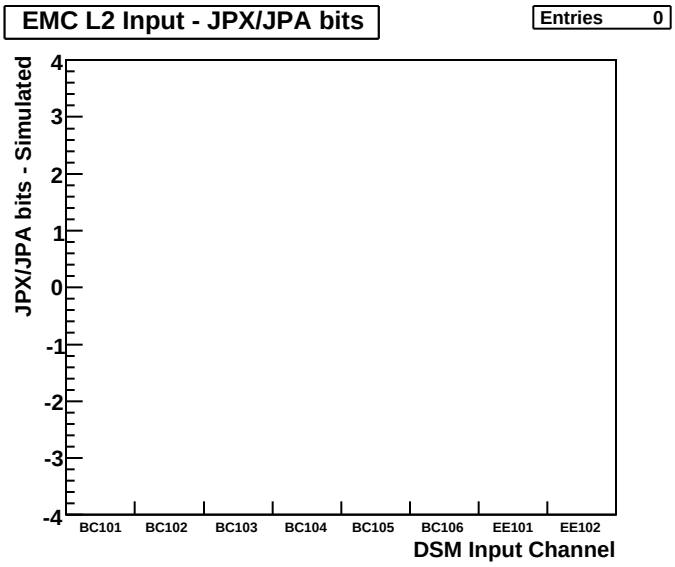

Endcap EMC L1 Input - High Eta Sum
Entries 1.143444e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

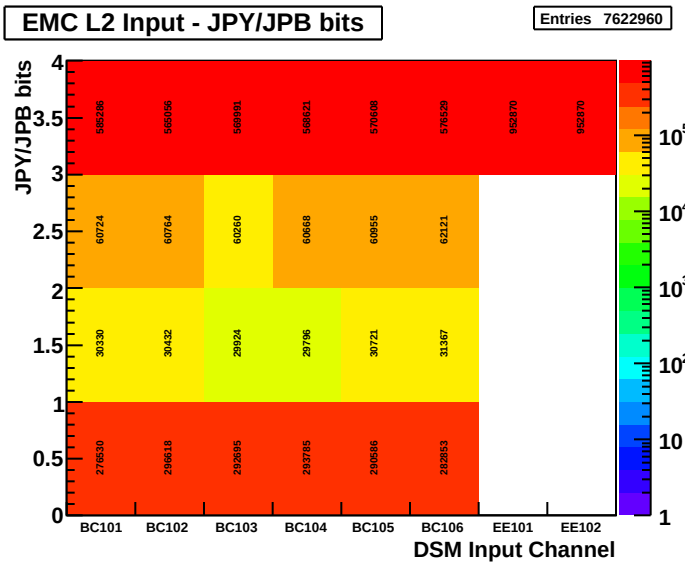

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

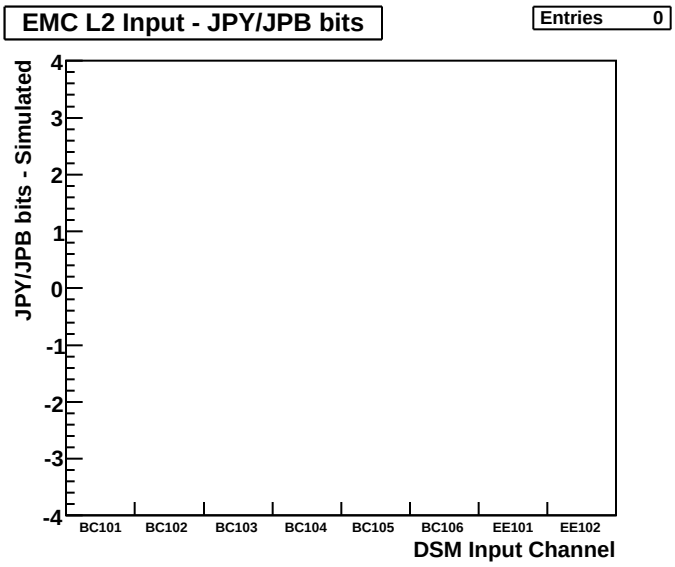

Endcap EMC L1 Input - High Tower Bits
Entries 1.143444e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

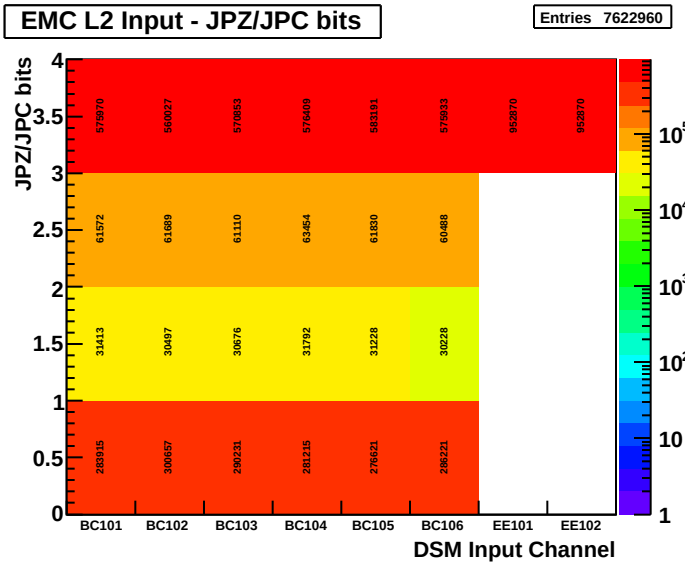

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

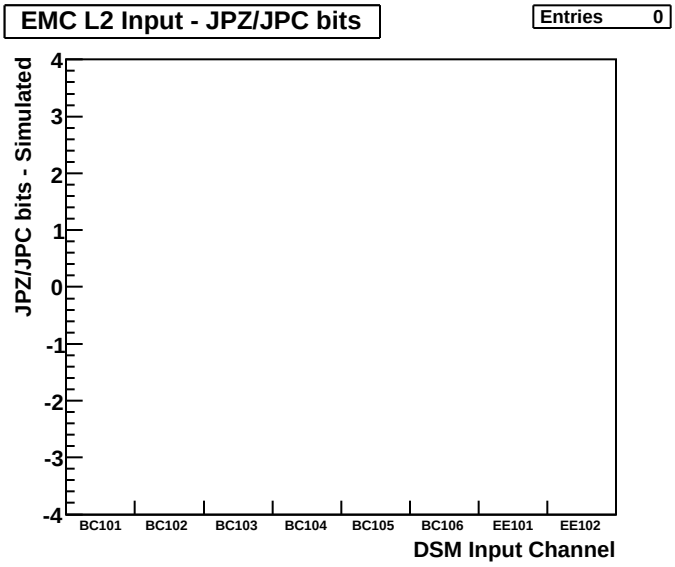

EMC L2 Input - JPX/JPA bits
Entries 7622960
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
590599 578872 577571 572188 568851 566180 952870 952870
59453 59682 60192 59256 61449 60911
30115 30048 29972 29283 30745 30362
272703 284168 285135 292143 292025 295417

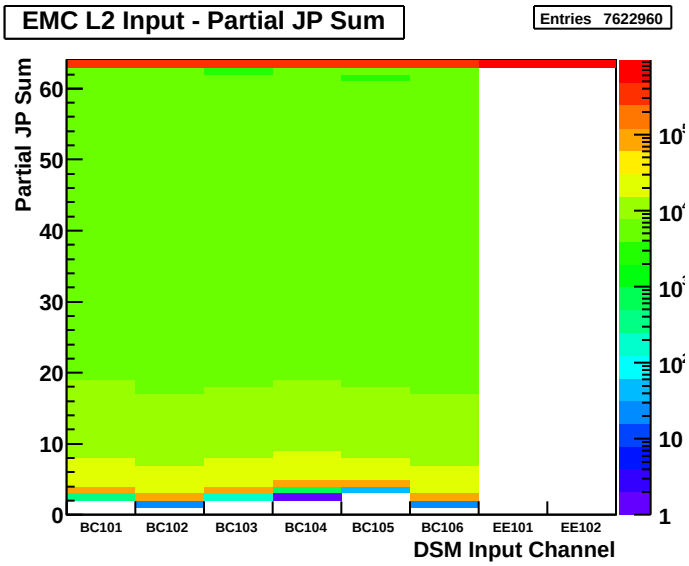

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

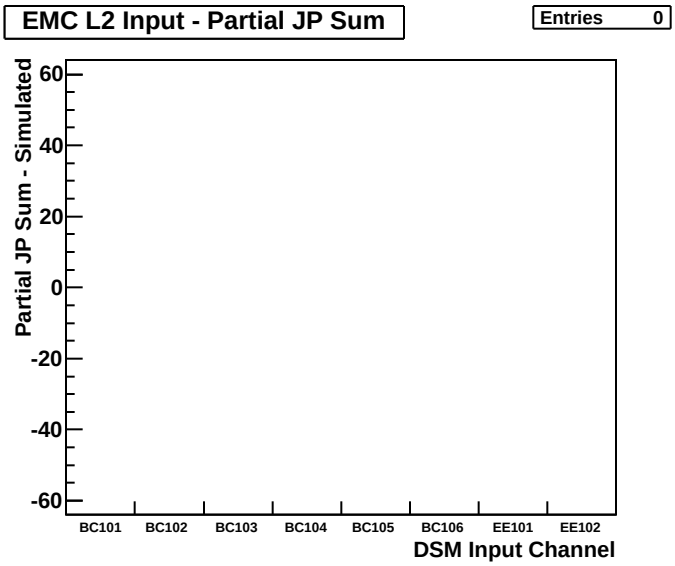

EMC L2 Input - JPY/JPB bits
Entries 7622960
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
565286 565056 560931 568621 570608 576529 952870 952870
60724 60764 60260 60568 60955 62121
30330 30432 29924 29796 30721 31367
276530 290618 293595 293785 290586 292853

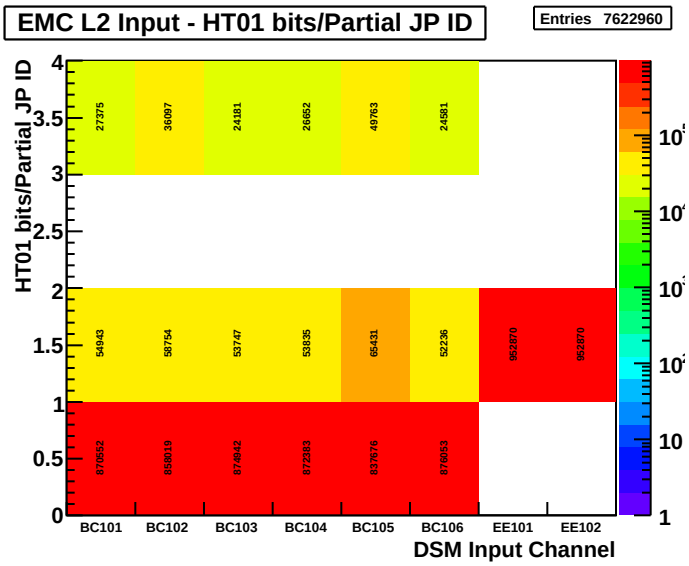

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

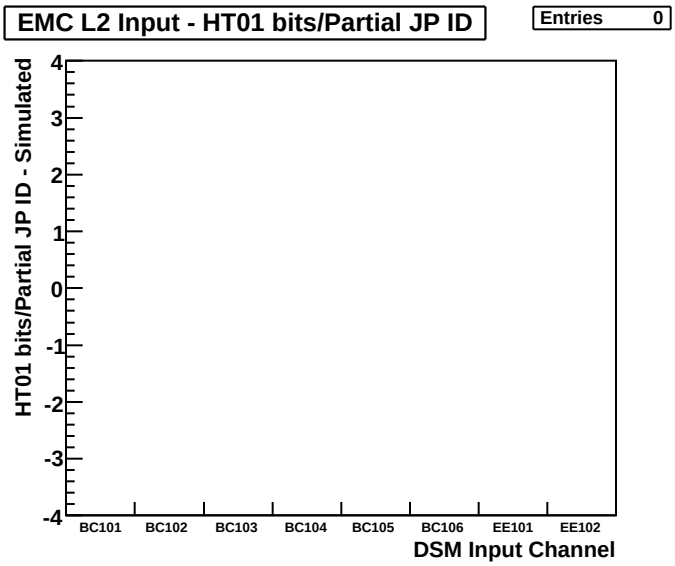

EMC L2 Input - JPZ/JPC bits
Entries 7622960
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
575970 560027 570853 576409 583191 575933 952870 952870
61572 61689 61110 63454 61830 60488
31413 30497 30676 31792 31228 30228
283915 300657 299231 281215 276621 286221

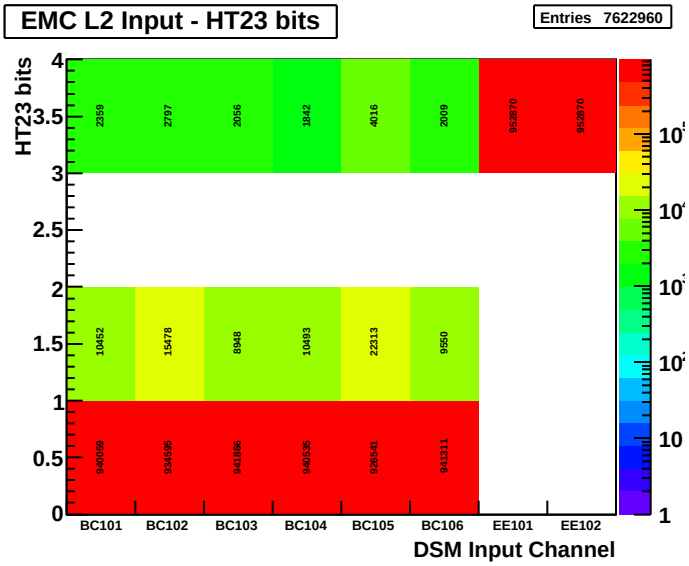

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

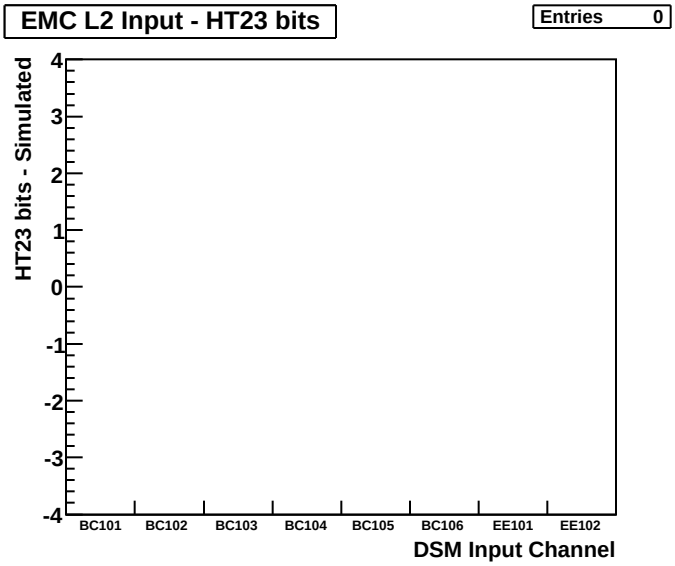

EMC L2 Input - Partial JP Sum
Entries 7622960
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

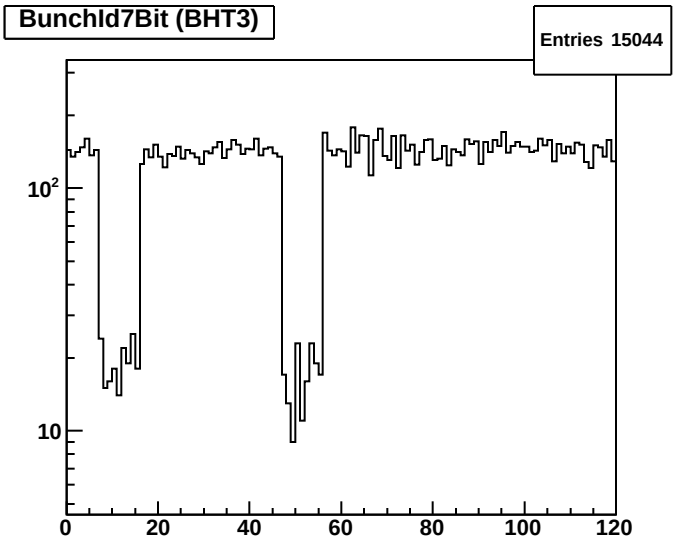

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

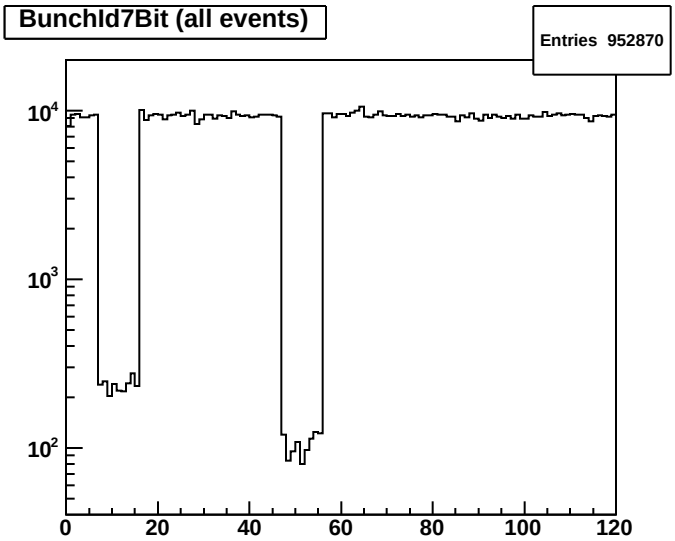

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7622960
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

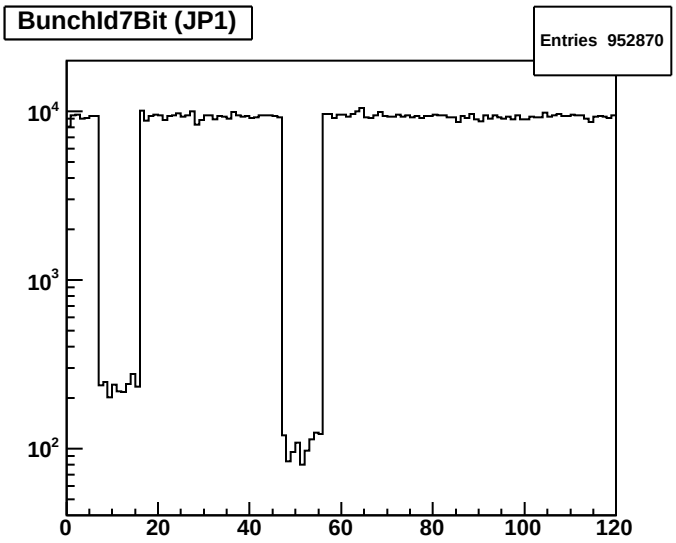

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

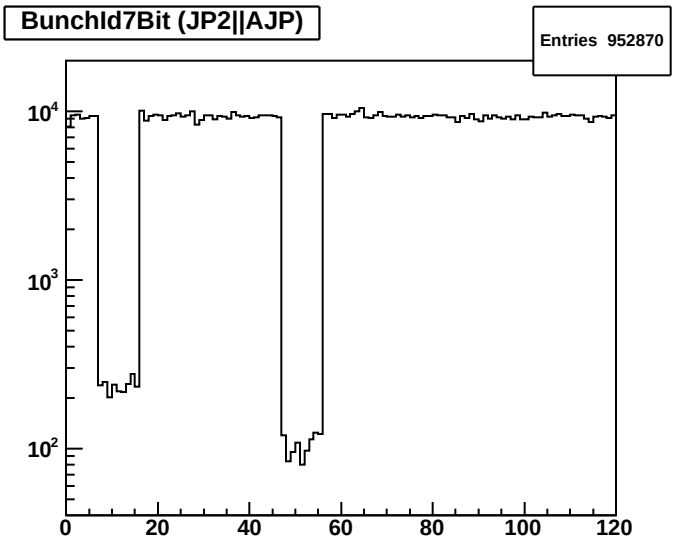

EMC L2 Input - HT23 bits
Entries 7622960
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

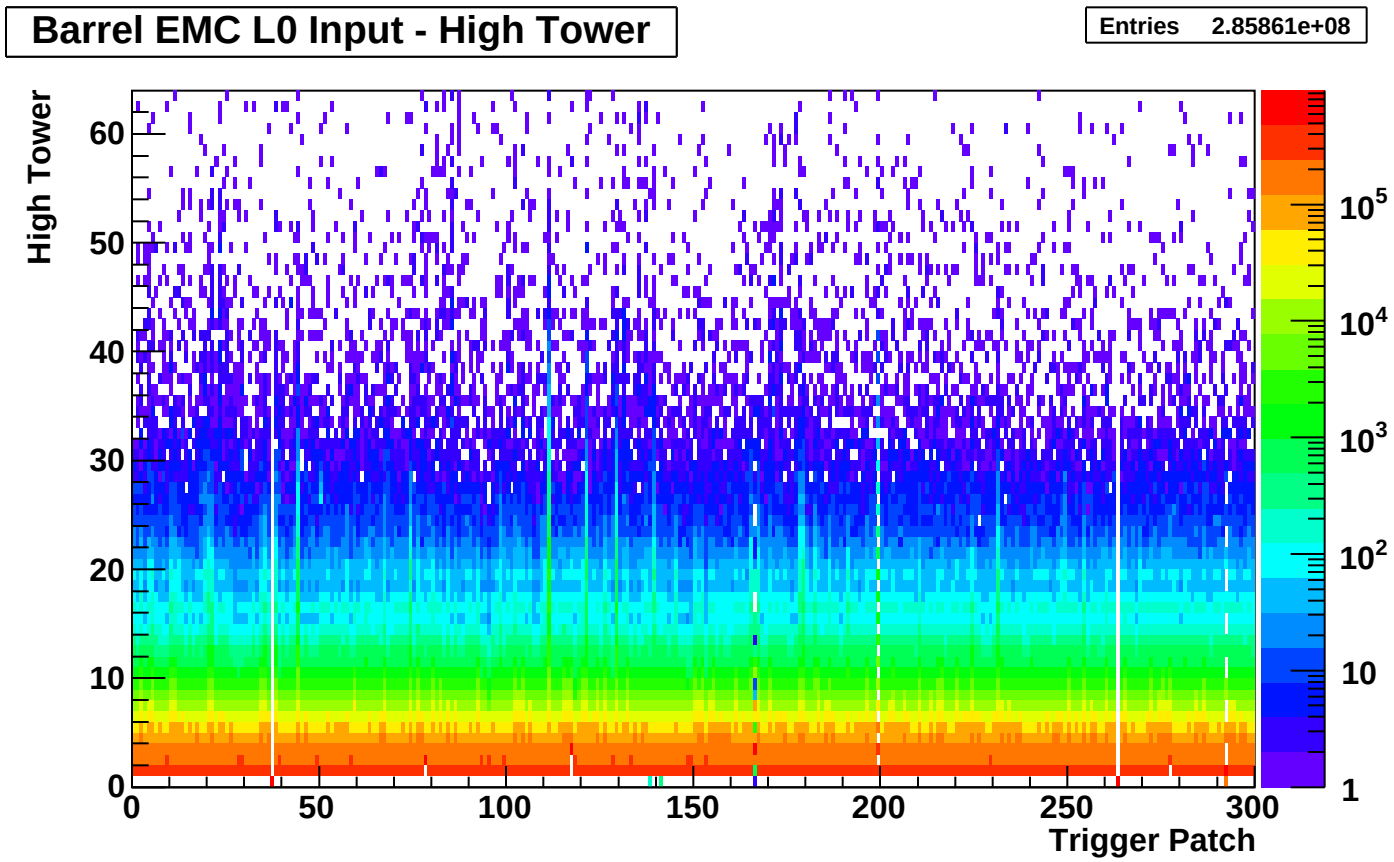

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

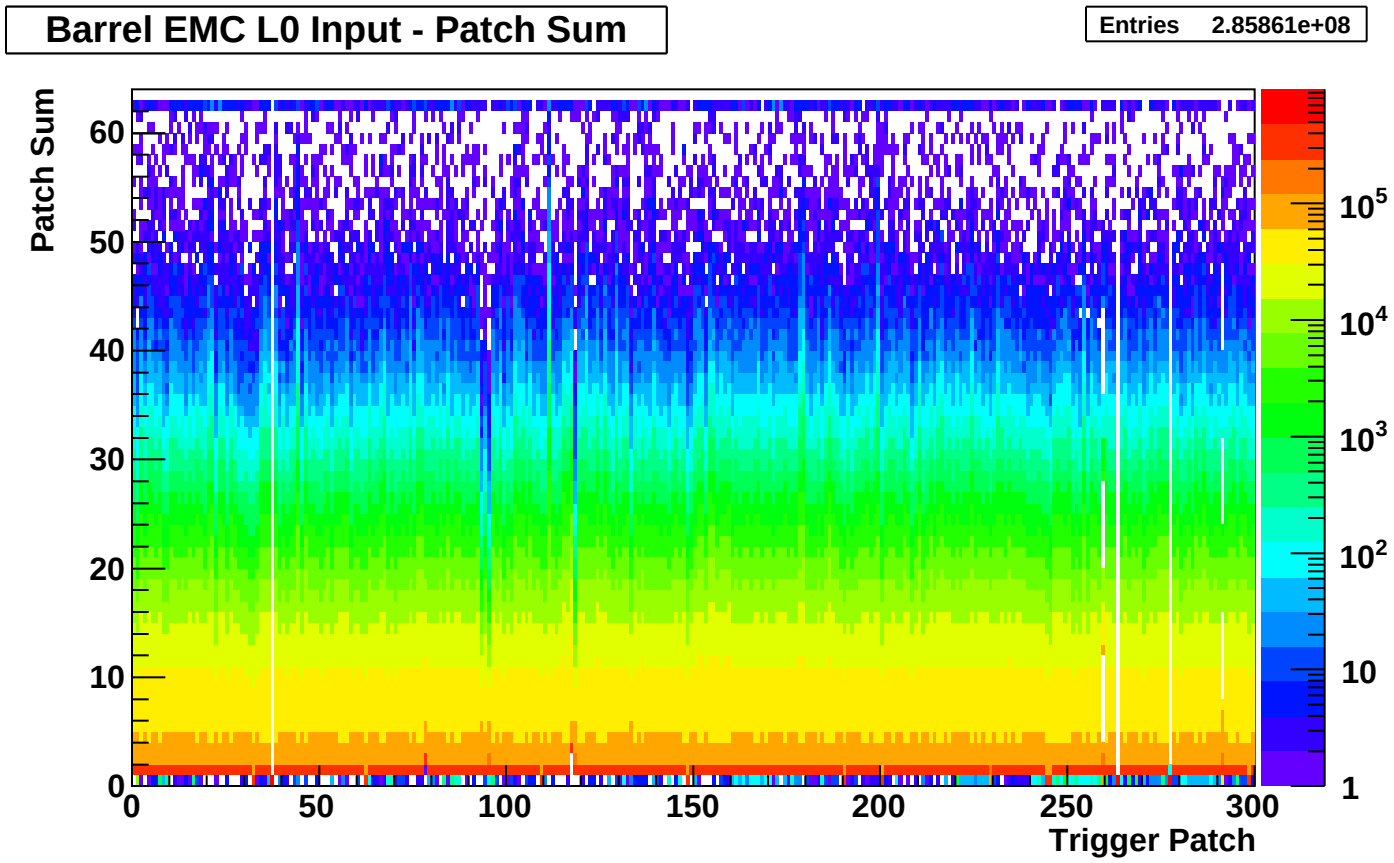

BunchId7Bit (BHT3)
Entries 15044
10^2
10
0
20
40
60
80
100
120

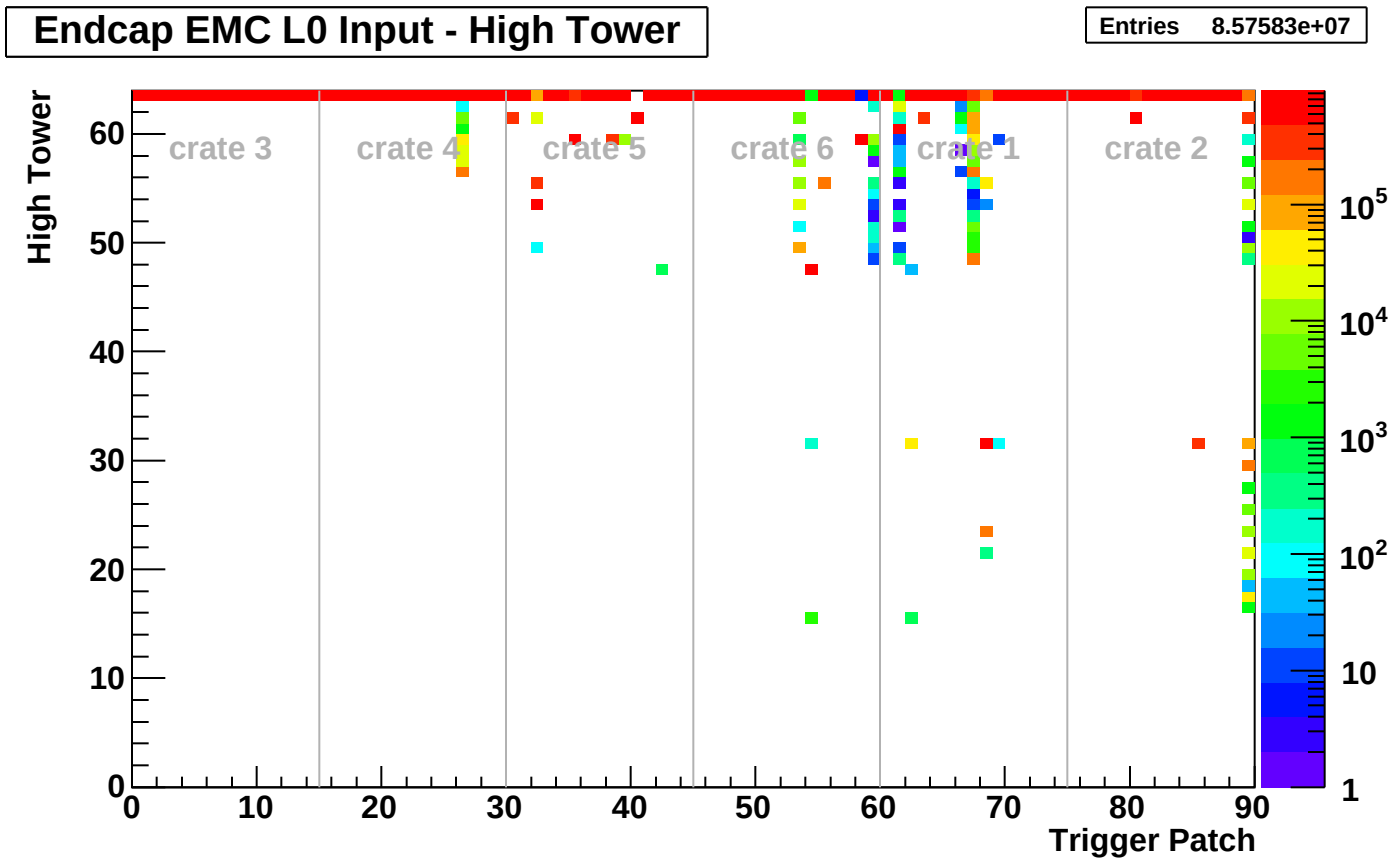

BunchId7Bit (all events)
Entries 952870
10^4
10^3
10^2
0
20
40
60
80
100
120

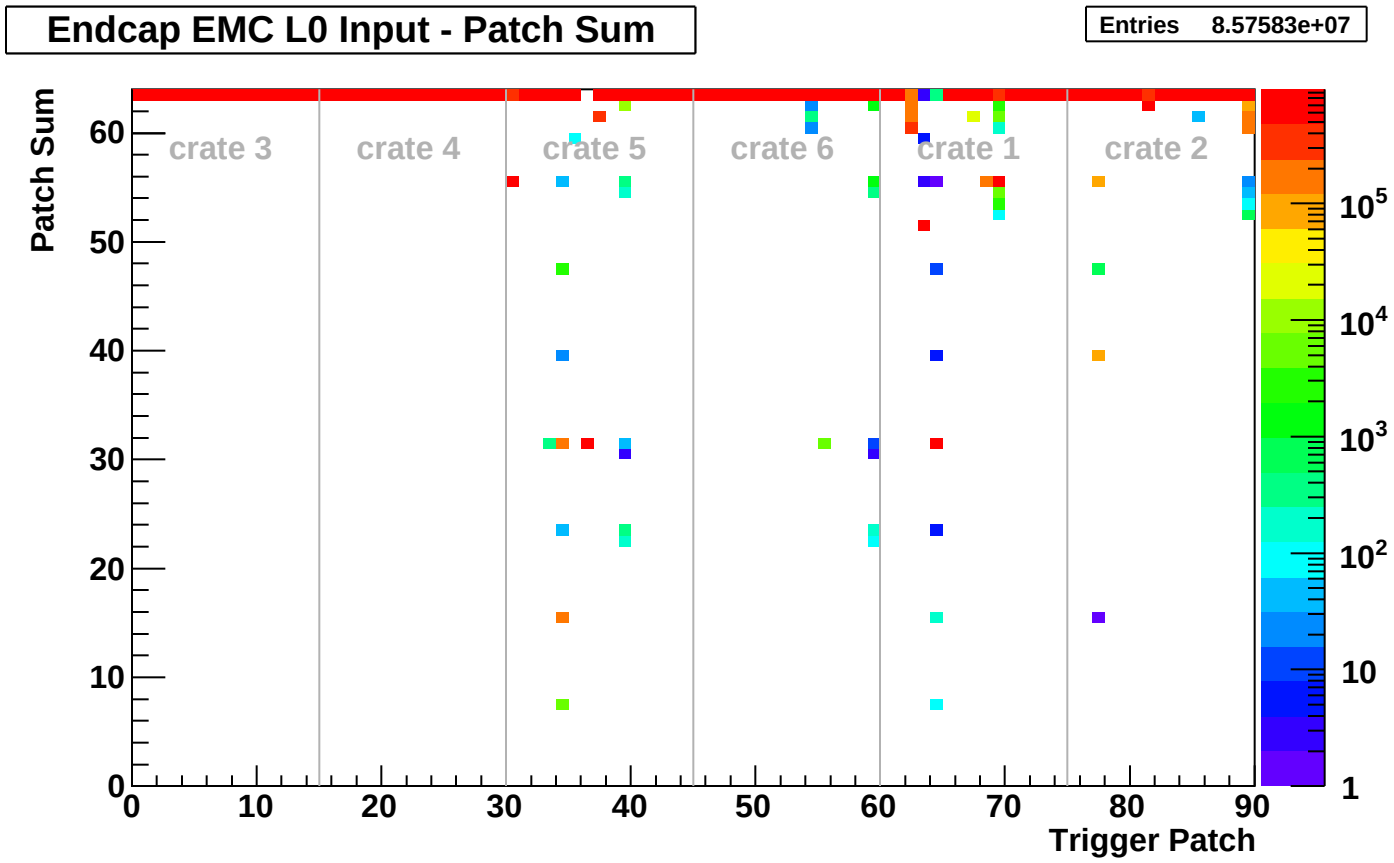

BunchId7Bit (JP1)
Entries 952870
10^4
10^3
10^2
0
20
40
60
80
100
120

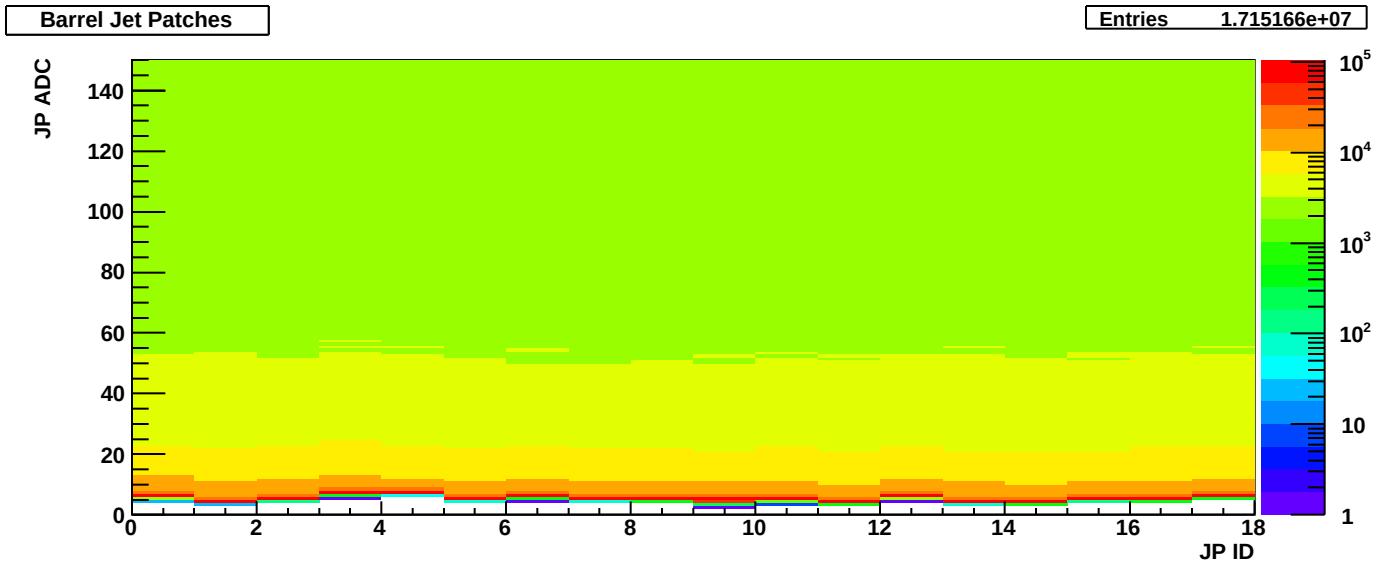

BunchId7Bit (JP2||AJP)
Entries 952870
10^4
10^3
10^2
0
20
40
60
80
100
120

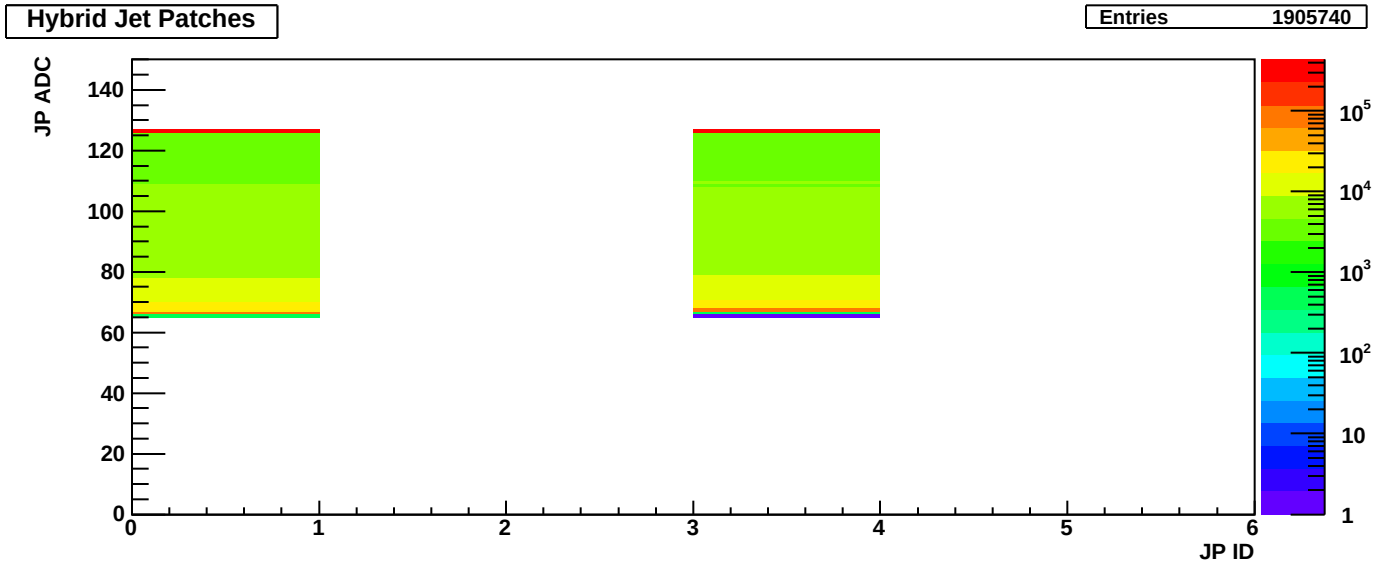

Barrel EMC L0 Input - High Tower
Entries 2.85861e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

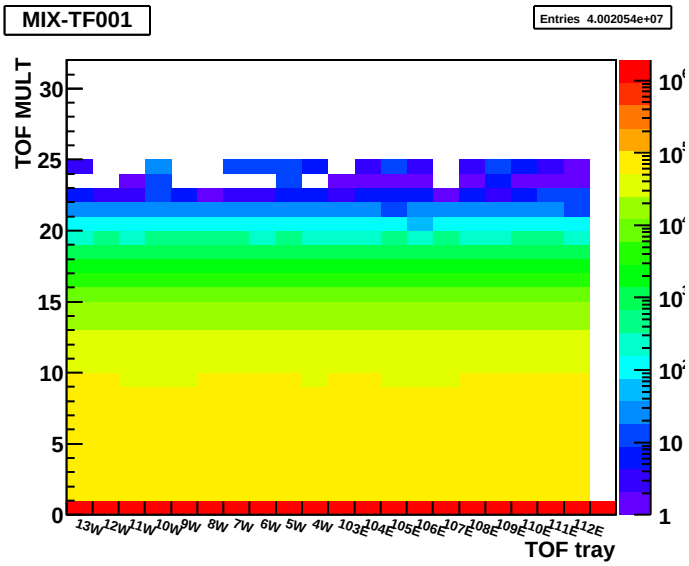

Barrel EMC L0 Input - Patch Sum
Entries 2.85861e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

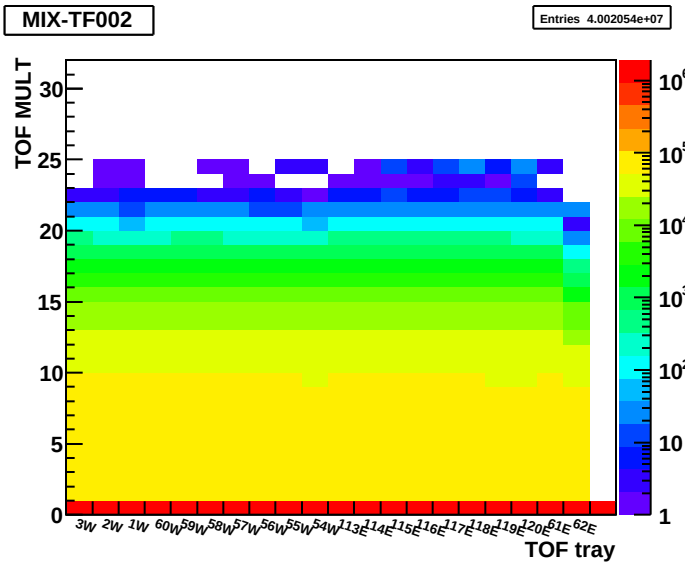

Endcap EMC L0 Input - High Tower
Entries 8.57583e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

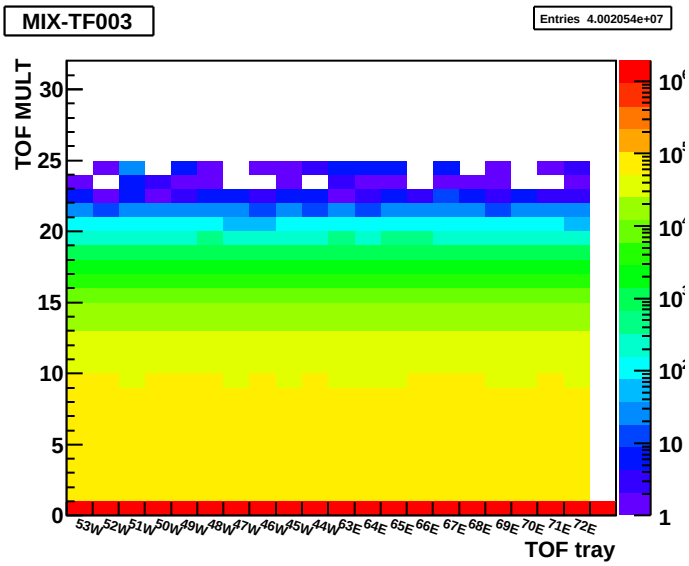

Endcap EMC L0 Input - Patch Sum
Entries 8.57583e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

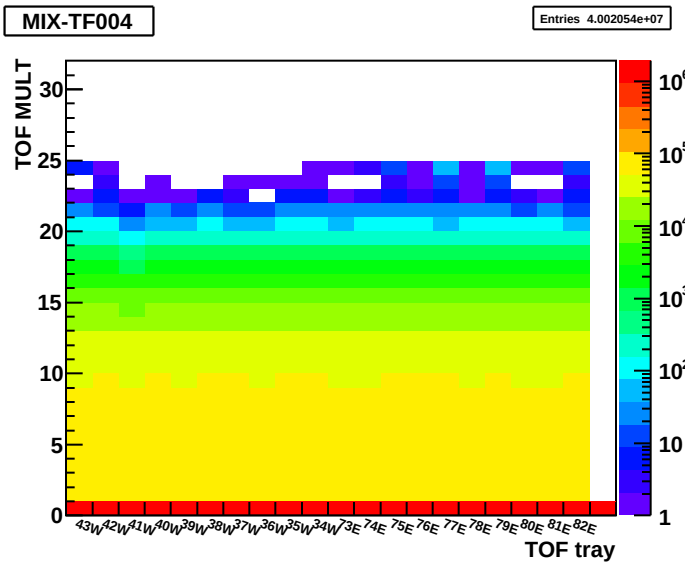

Barrel Jet Patches
Entries 1.715166e+07
JP ADC
JP ID

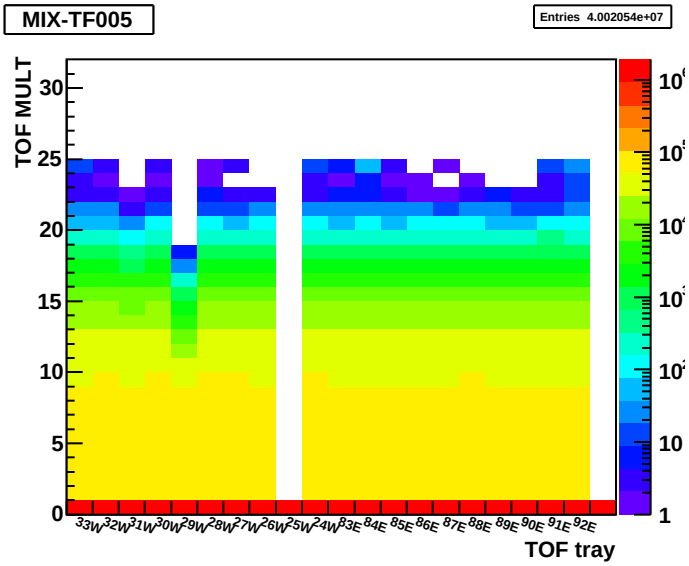

Hybrid Jet Patches
Entries 1905740
JP ADC
JP ID

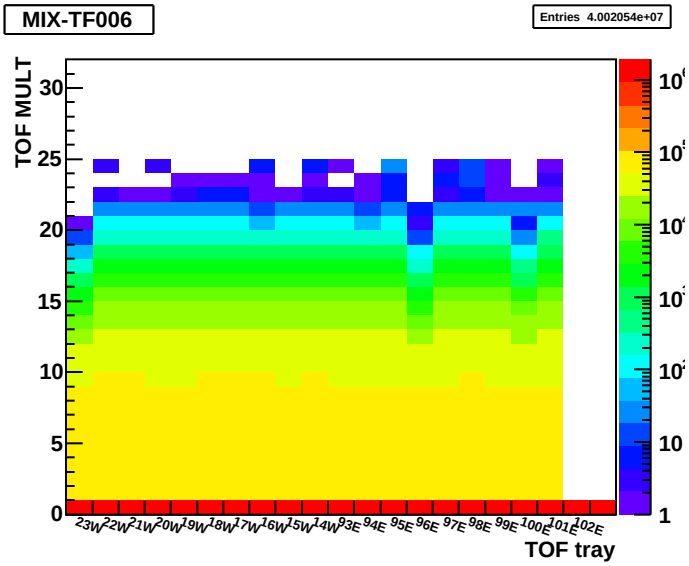

MIX-TF001
Entries 4.002054e+07
TOF MULT
TOF tray

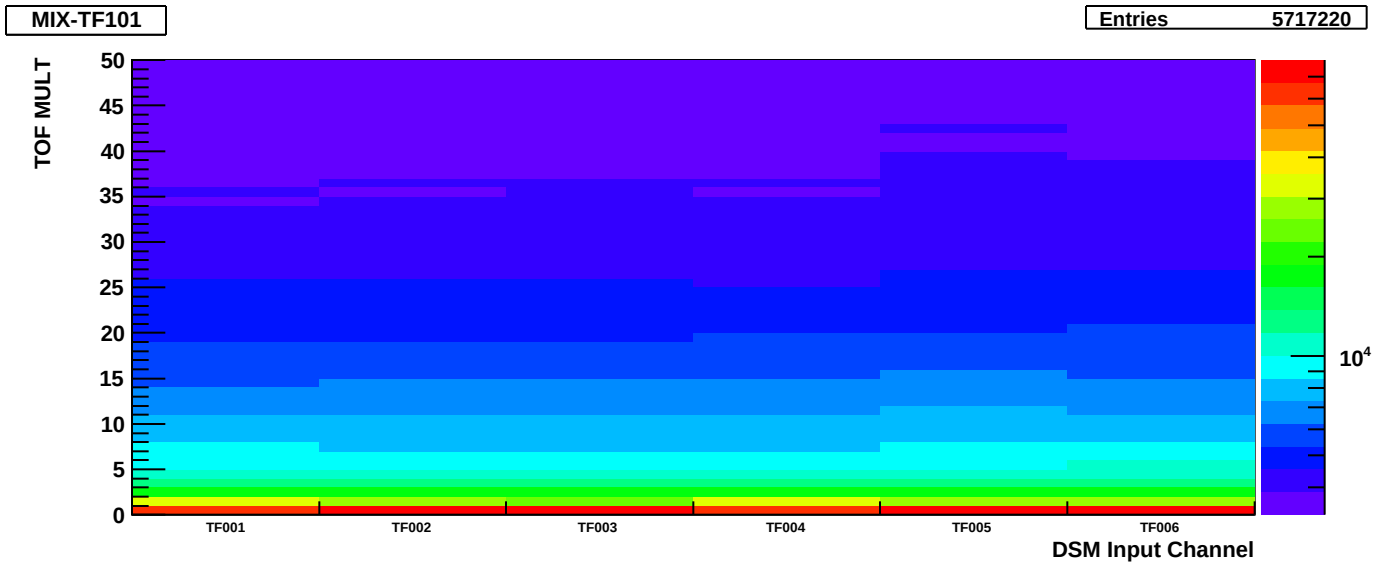

MIX-TF002
Entries 4.002054e+07
TOF MULT
TOF tray

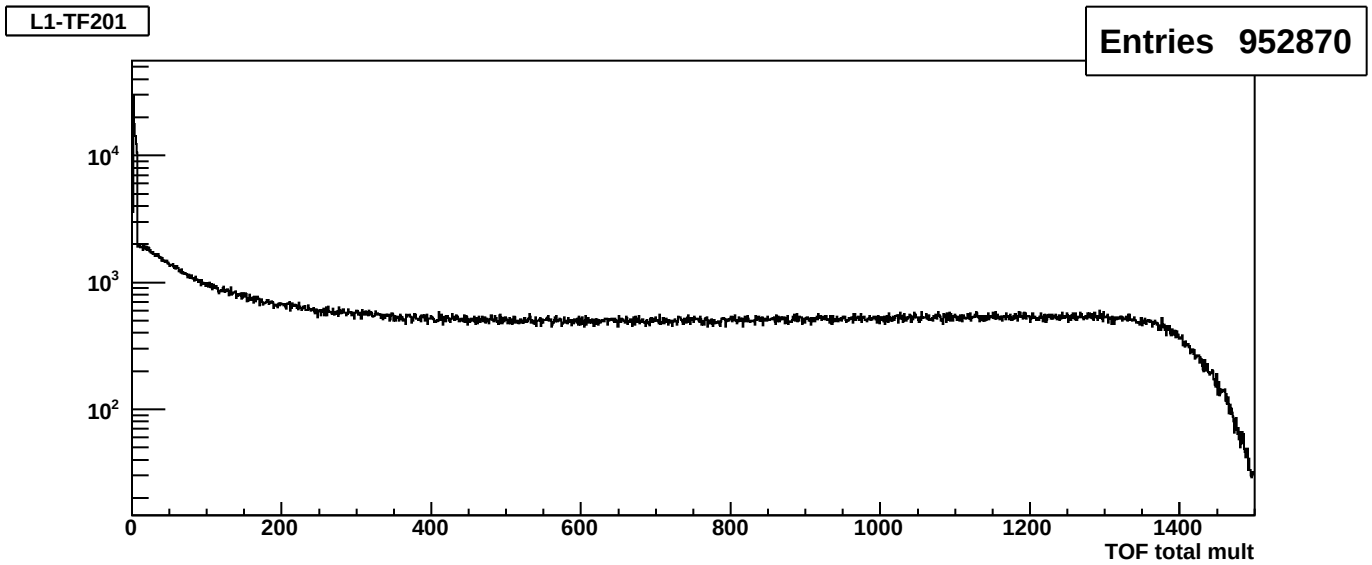

MIX-TF003
Entries 4.002054e+07
TOF MULT
TOF tray

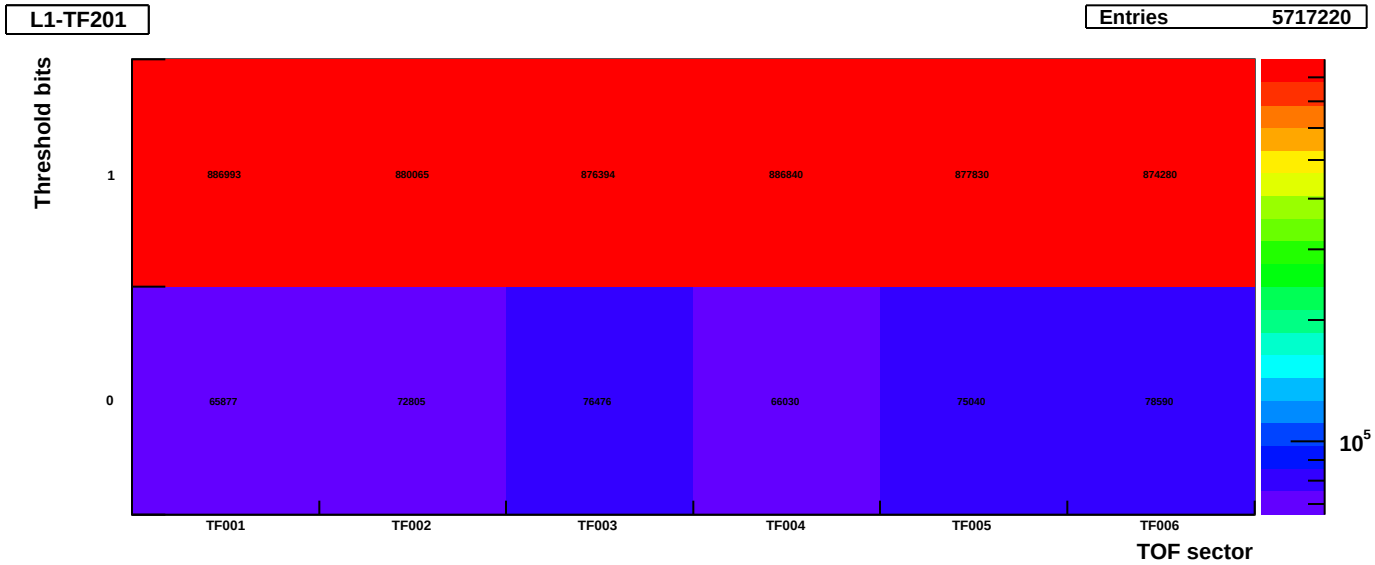

MIX-TF004
Entries 4.002054e+07
TOF MULT
TOF tray

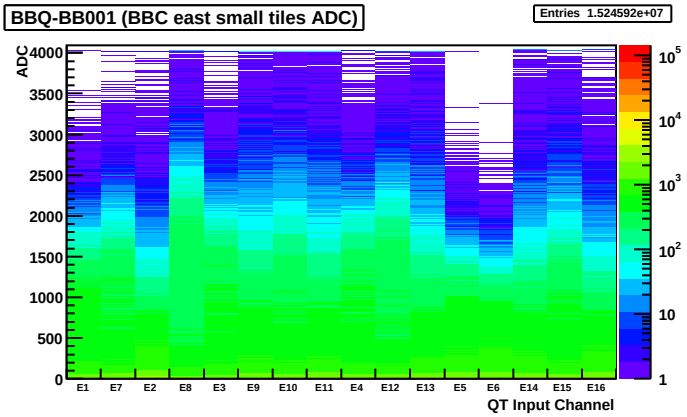

MIX-TF005
Entries 4.002054e+07
TOF MULT
TOF tray

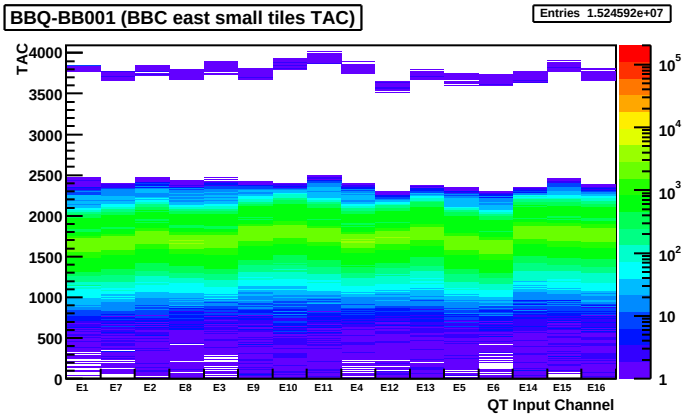

MIX-TF006
Entries 4.002054e+07
TOF MULT
TOF tray

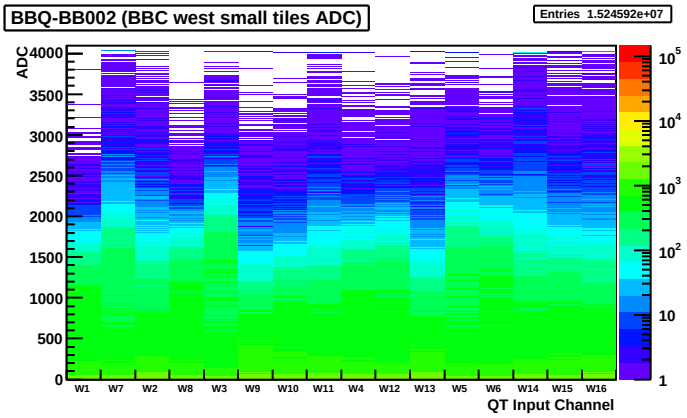

MIX-TF101
Entries 5717220
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

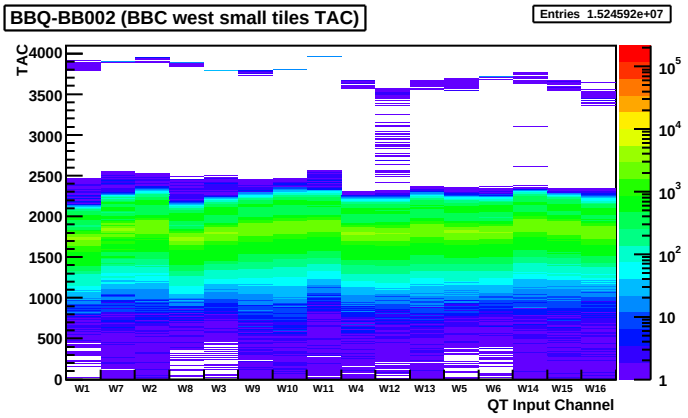

L1-TF201
Entries 952870
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

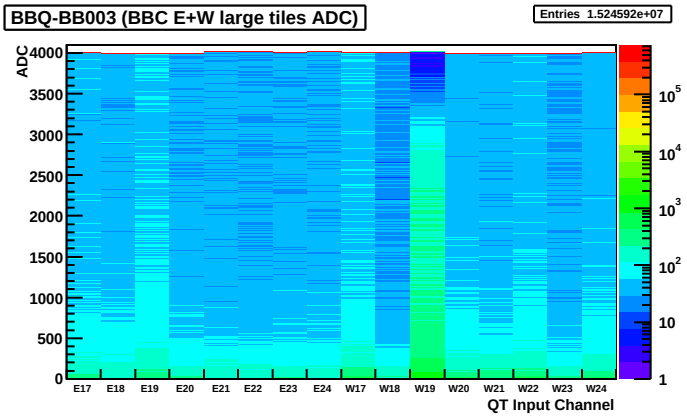

L1-TF201
Entries 5717220
Threshold bits
1
0
886993
880065
876394
886840
877830
874280
65877
72805
76476
66030
75040
78590
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

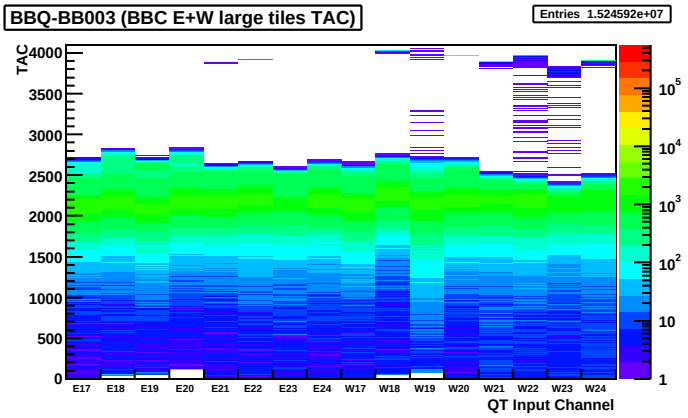

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.524592e+07
ADC
QT Input Channel

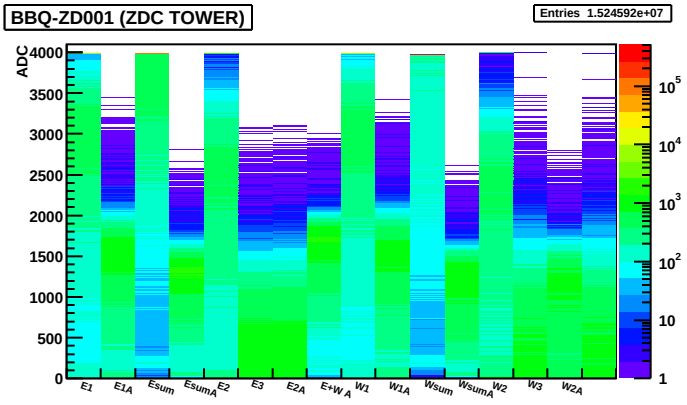

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.524592e+07
TAC
QT Input Channel

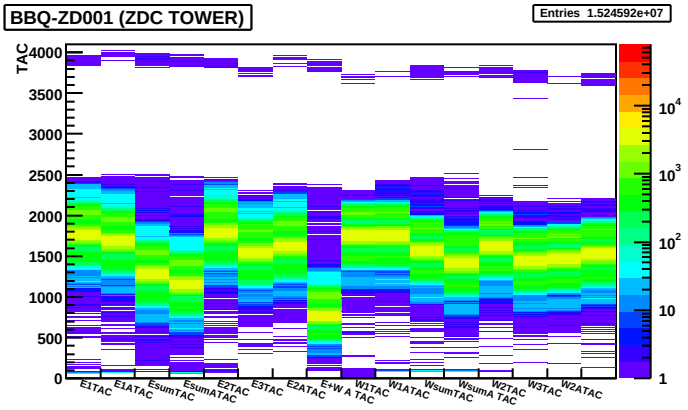

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.524592e+07
ADC
QT Input Channel

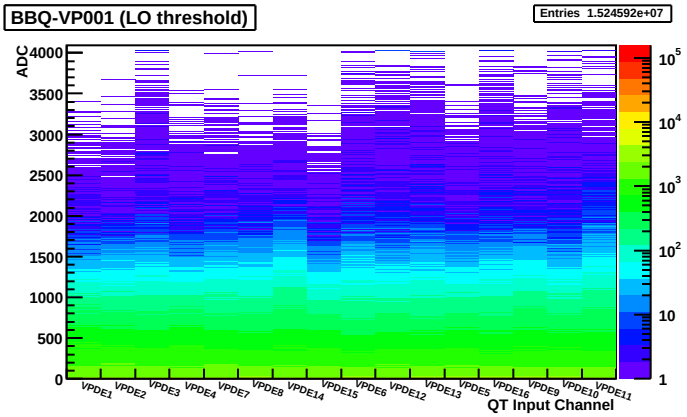

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.524592e+07
TAC
QT Input Channel

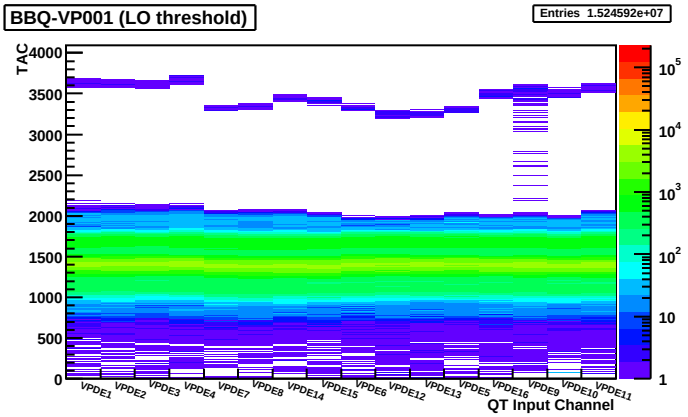

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.524592e+07
ADC
QT Input Channel

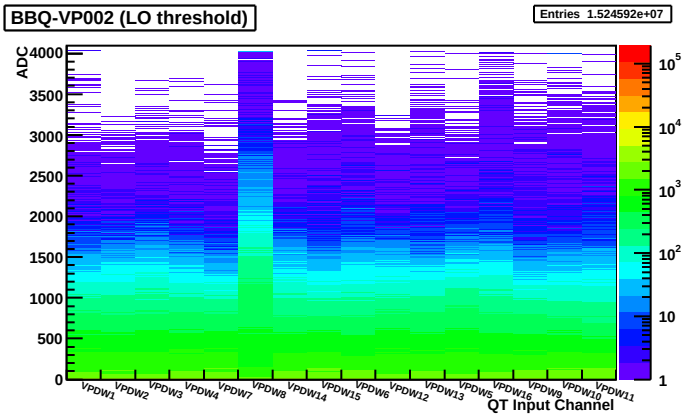

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.524592e+07
TAC
QT Input Channel

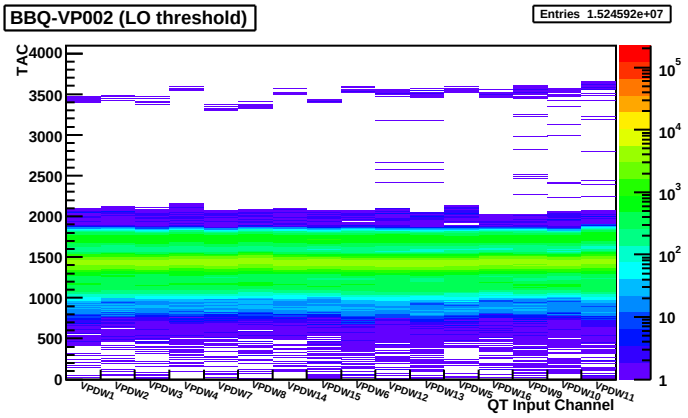

BBQ-ZD001 (ZDC TOWER)
Entries 1.524592e+07
ADC

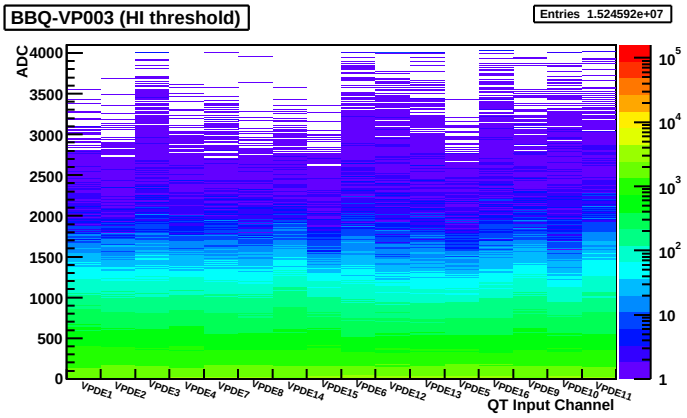

BBQ-ZD001 (ZDC TOWER)
Entries 1.524592e+07
TAC

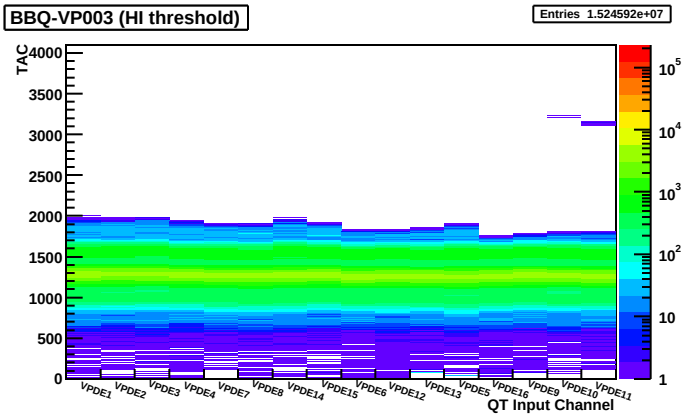

BBQ-VP001 (LO threshold)
Entries 1.524592e+07
ADC
QT Input Channel

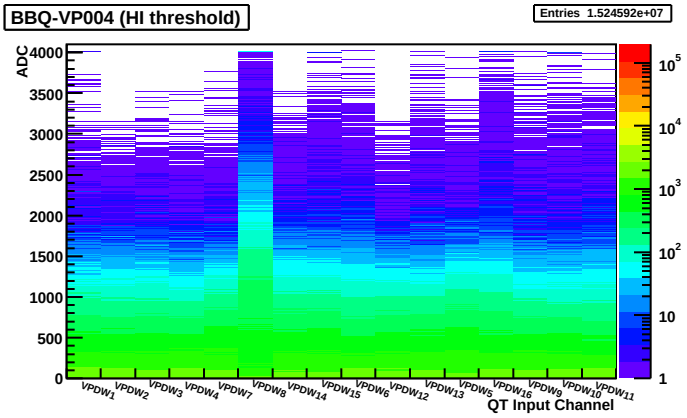

BBQ-VP001 (LO threshold)
Entries 1.524592e+07
TAC
QT Input Channel

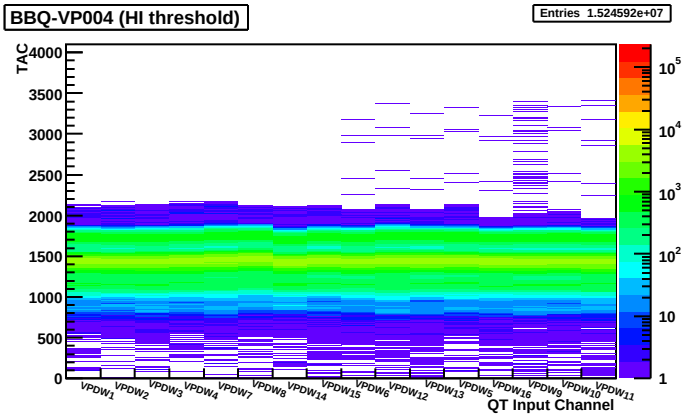

BBQ-VP002 (LO threshold)
Entries 1.524592e+07
ADC
QT Input Channel

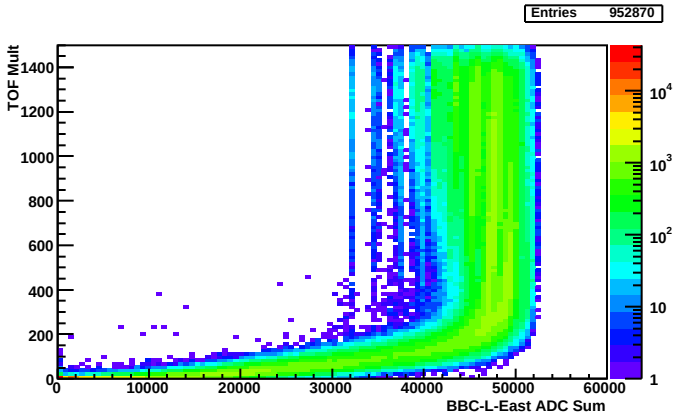

BBQ-VP002 (LO threshold)
Entries 1.524592e+07
TAC
QT Input Channel

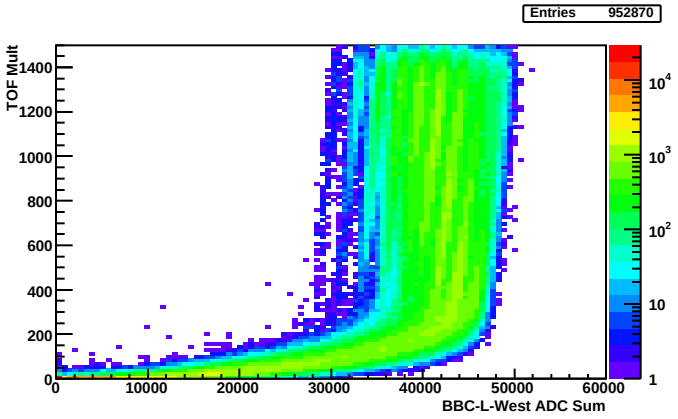

BBQ-VP003 (HI threshold)
Entries 1.524592e+07
ADC
QT Input Channel

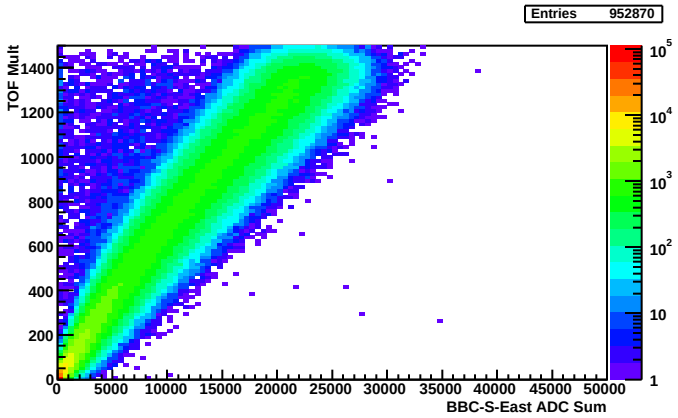

BBQ-VP003 (HI threshold)
Entries 1.524592e+07
TAC
QT Input Channel

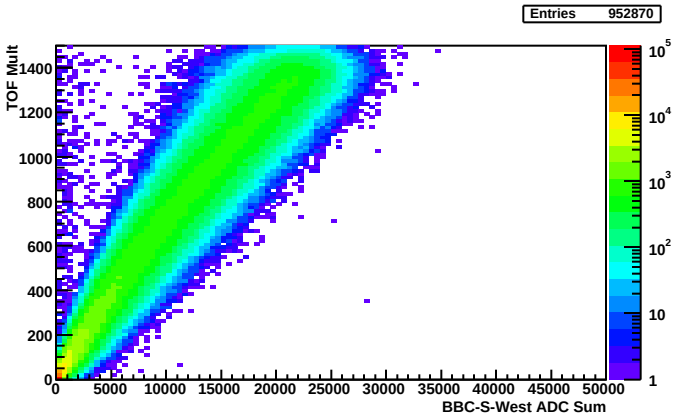

BBQ-VP004 (HI threshold)
Entries 1.524592e+07
ADC
QT Input Channel

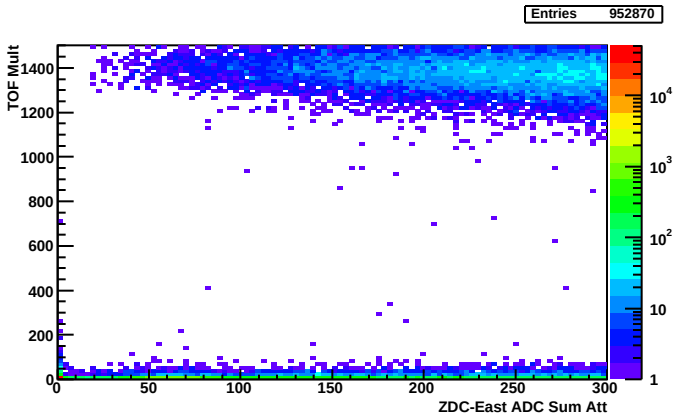

BBQ-VP004 (HI threshold)
Entries 1.524592e+07
TAC
QT Input Channel

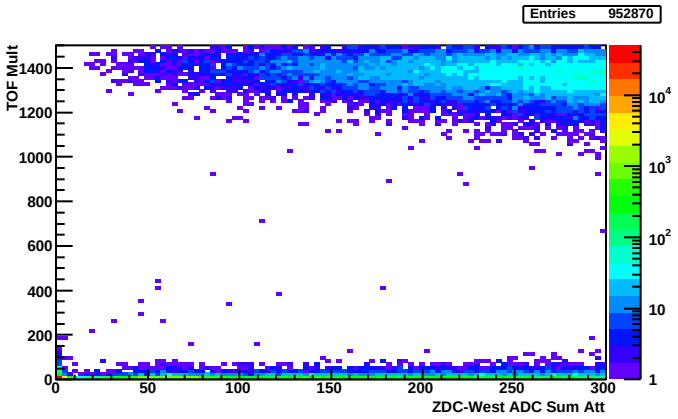

Entries 952870
TOF Mult
BBC-L-East ADC Sum

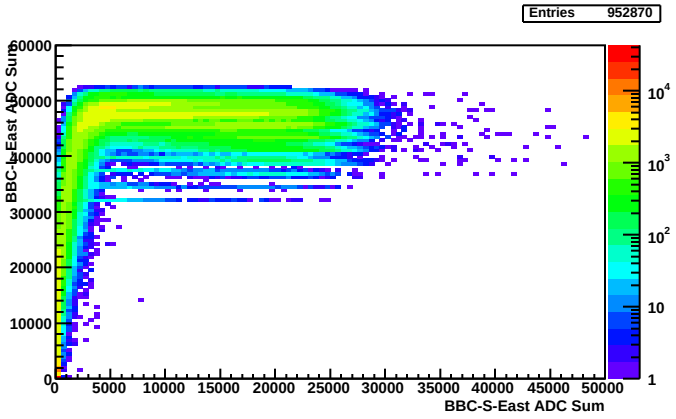

Entries 952870
TOF Mult
BBC-L-West ADC Sum

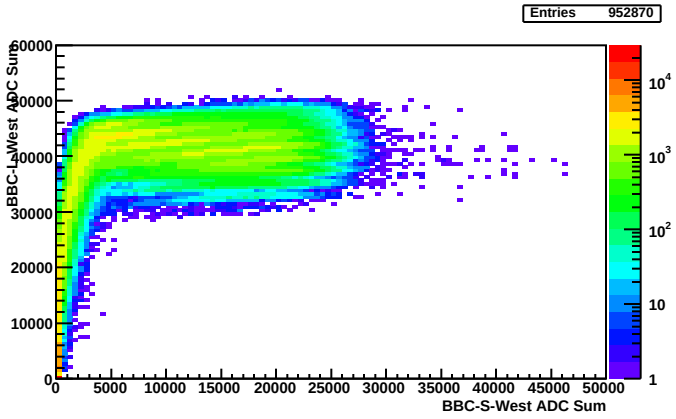

Entries 952870
TOF Mult
BBC-S-East ADC Sum

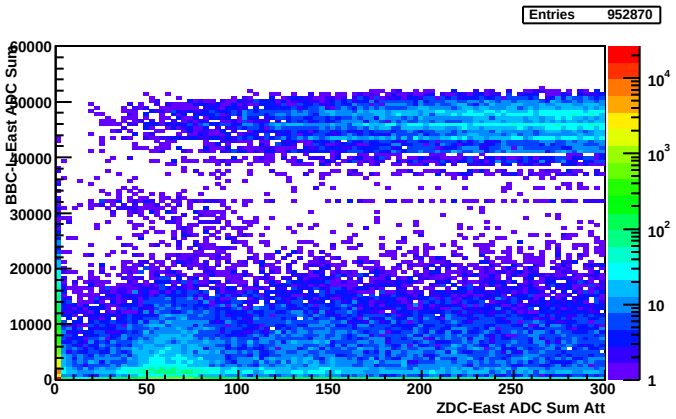

Entries 952870
TOF Mult
BBC-S-West ADC Sum

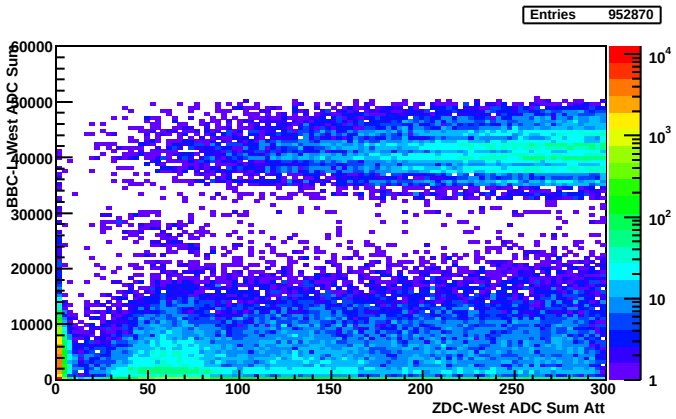

Entries 952870
TOF Mult
ZDC-East ADC Sum Att

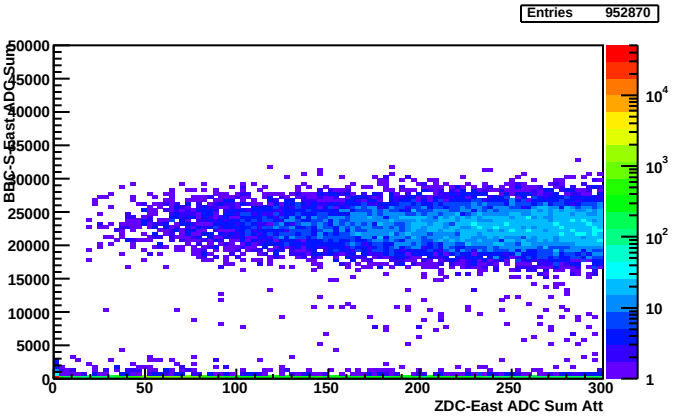

Entries 952870
TOF Mult
ZDC-West ADC Sum Att


Entries 952870
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 952870
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 952870
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 952870
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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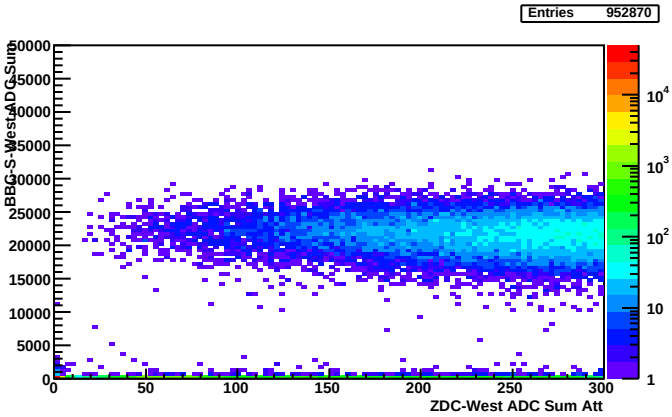
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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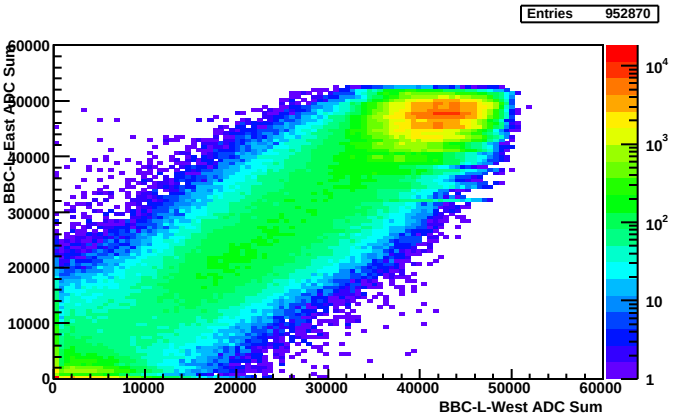
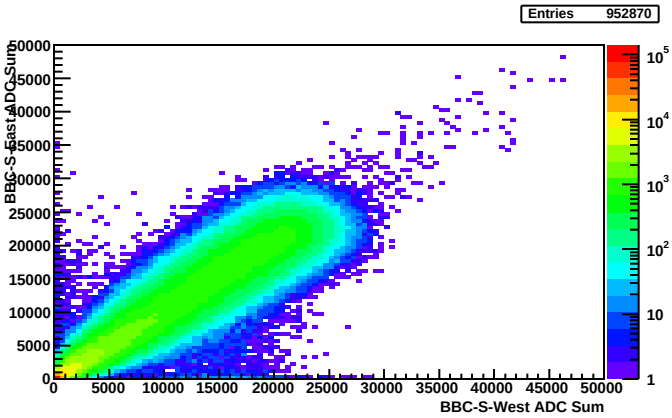
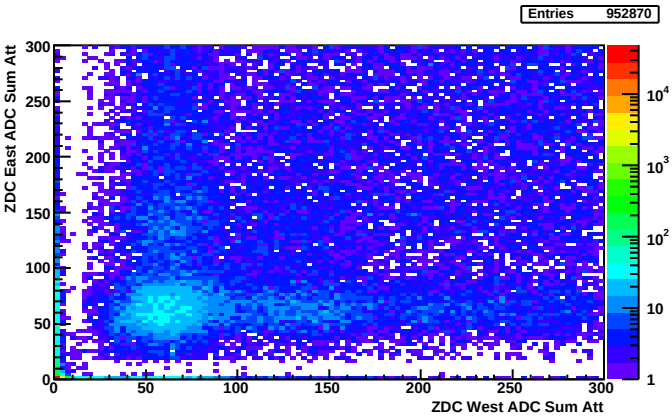
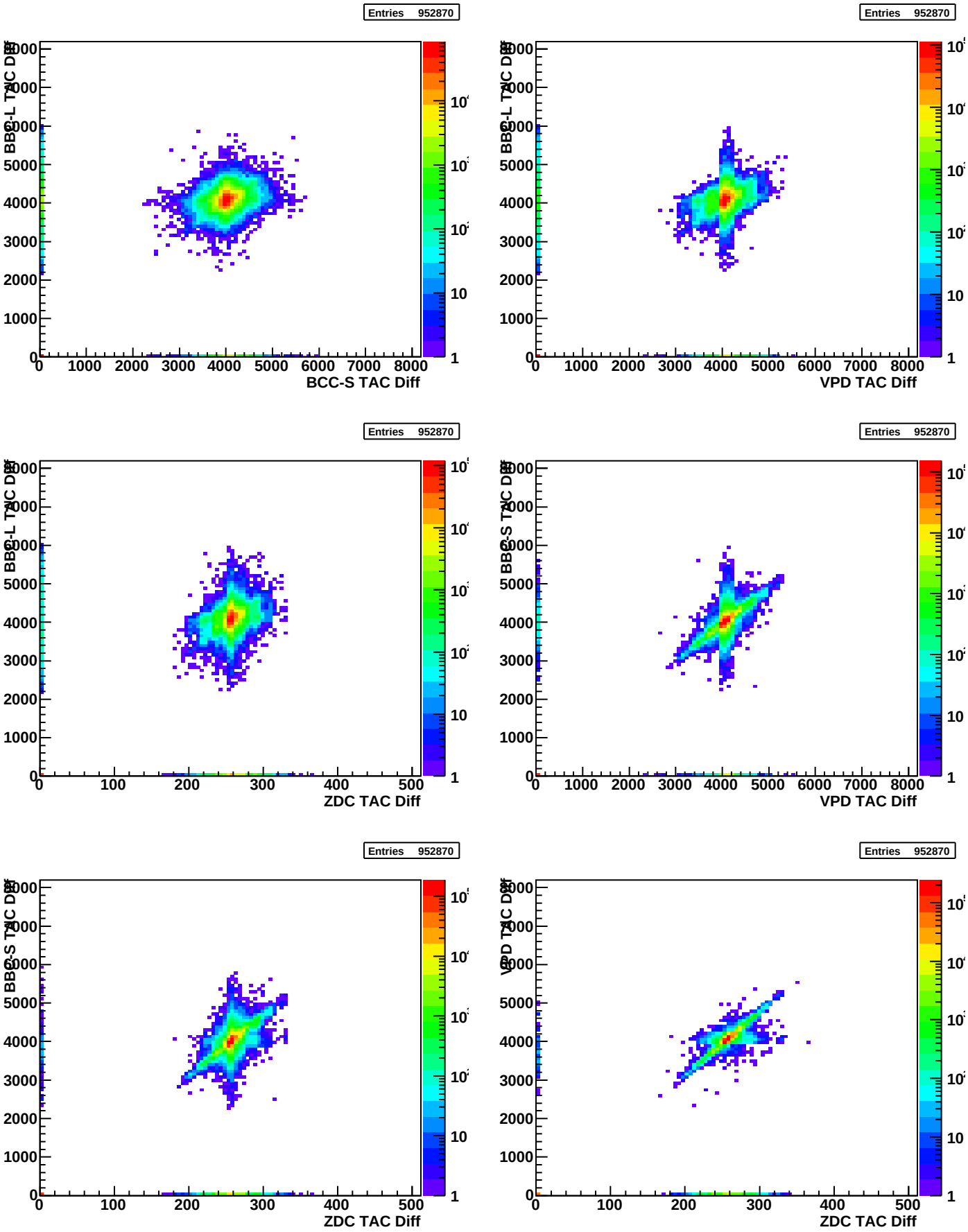
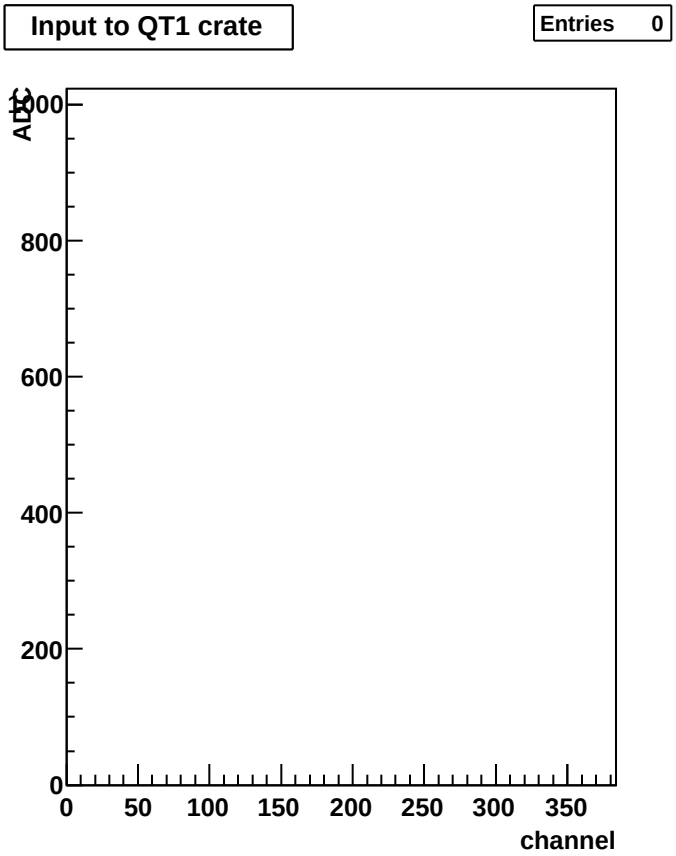
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

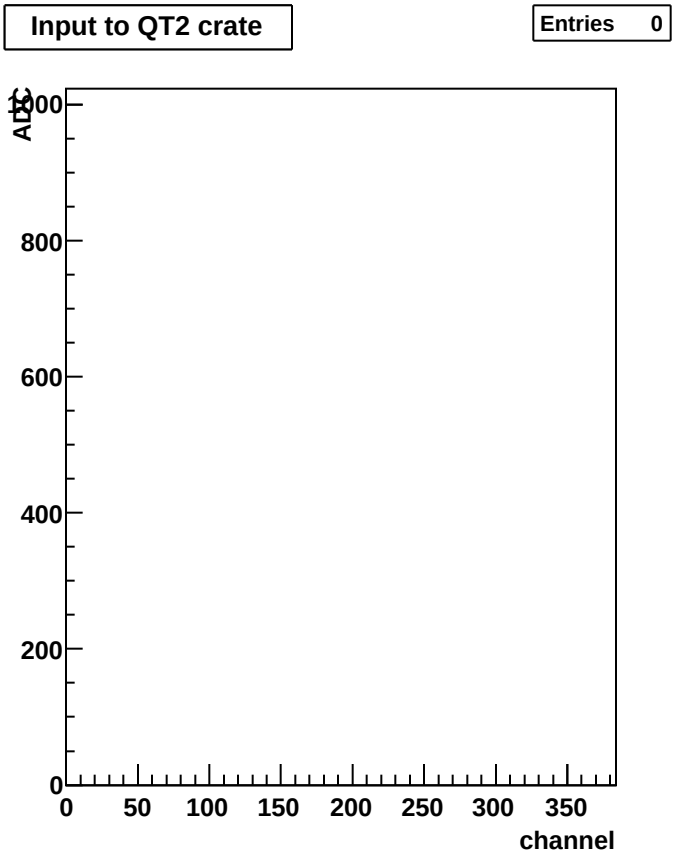

Entries 952870
BBC-L-East ADC Sum
BBC-L-West ADC Sum

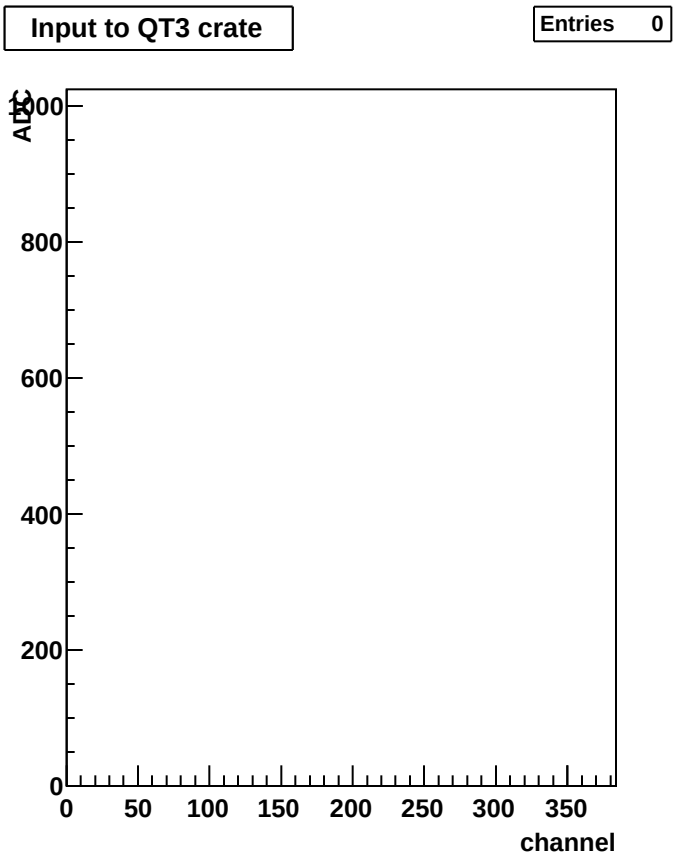

Entries 952870
BBC-S-East ADC Sum
BBC-S-West ADC Sum

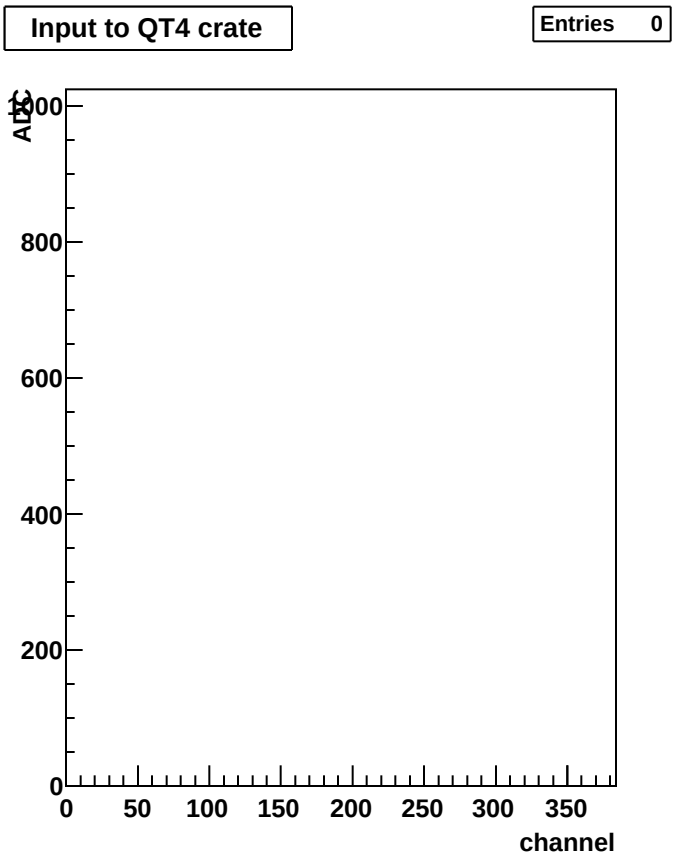

Entries 952870
ZDC East ADC Sum Att
ZDC West ADC Sum Att

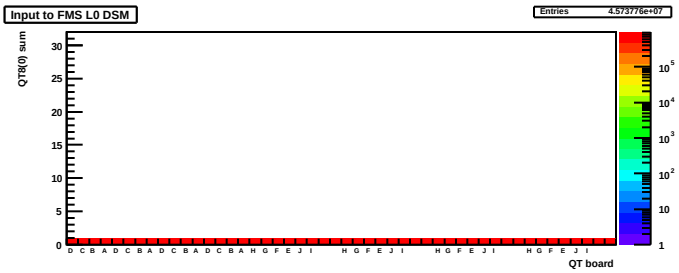

Entries 952870
BBC-L TAC Diff
BCC-S TAC Diff
Entries 952870
BBC-L TAC Diff
VPD TAC Diff
Entries 952870
BBC-L TAC Diff
ZDC TAC Diff
Entries 952870
BBC-S TAC Diff
VPD TAC Diff
Entries 952870
BBC-S TAC Diff
ZDC TAC Diff
Entries 952870
VPD TAC Diff
ZDC TAC Diff

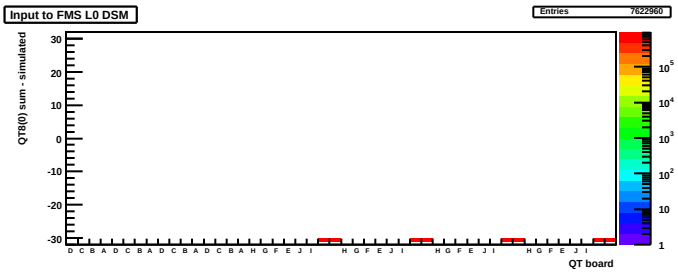

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

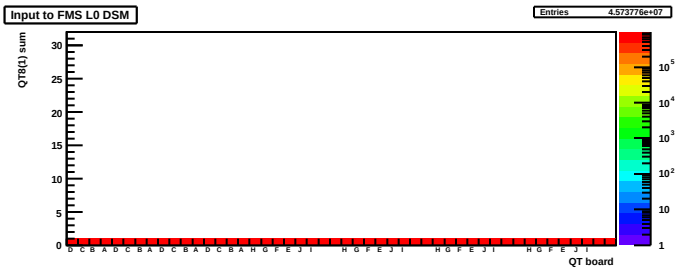

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

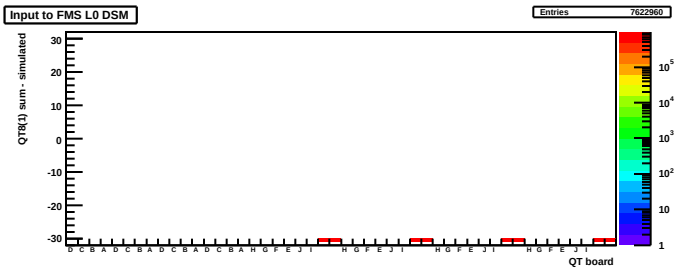

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

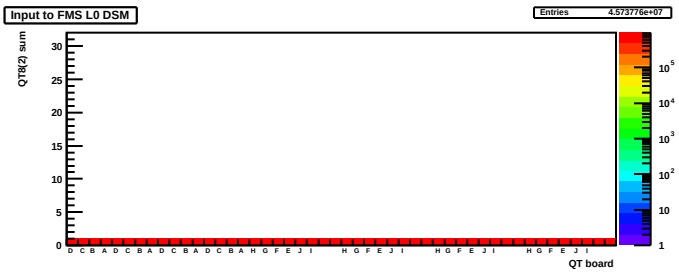

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

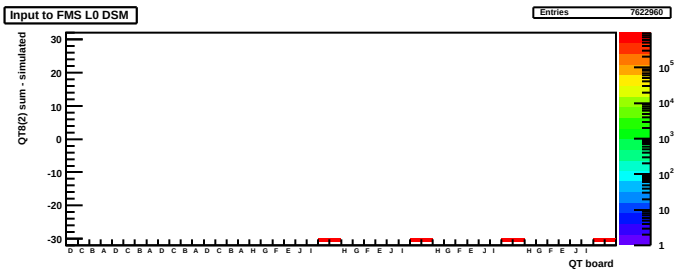

Input to FMS L0 DSM
Entries 4.573776e+07
QT8(0) sum
QT board

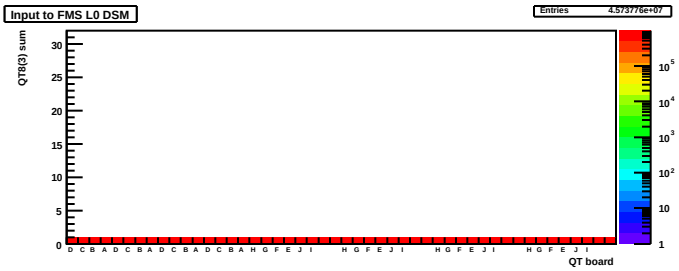

Input to FMS L0 DSM
Entries 7622960
QT8(0) sum - simulated
QT board

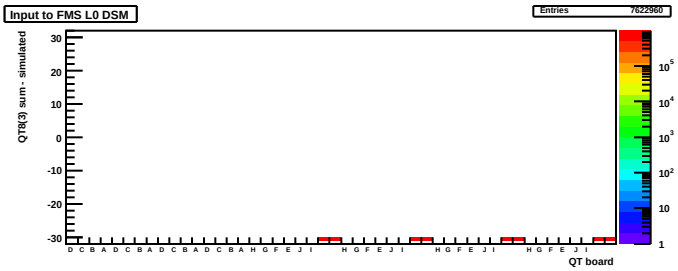

Input to FMS L0 DSM
Entries 4.573776e+07
QT8(1) sum
QT board

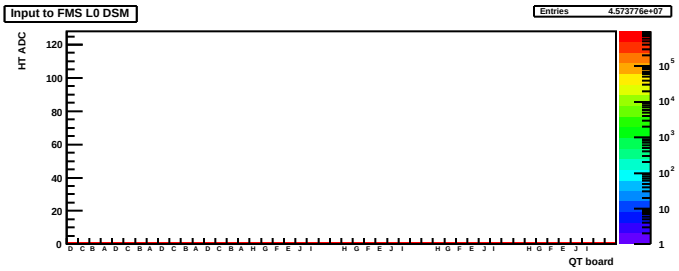

Input to FMS L0 DSM
Entries 7622960
QT8(1) sum - simulated
QT board

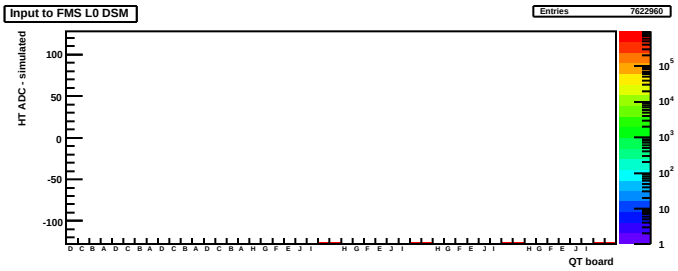

Input to FMS L0 DSM
Entries 4.573776e+07
QT8(2) sum
QT board

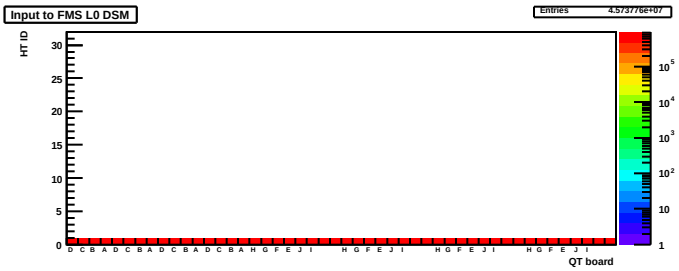

Input to FMS L0 DSM
Entries 7622960
QT8(2) sum - simulated
QT board

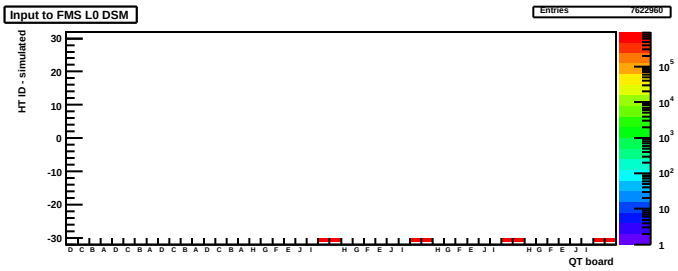

Input to FMS L0 DSM
Entries 4.573776e+07
QT8(3) sum
QT board

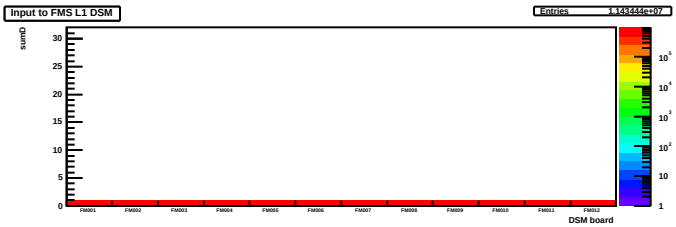

Input to FMS L0 DSM
Entries 7622960
QT8(3) sum - simulated
QT board

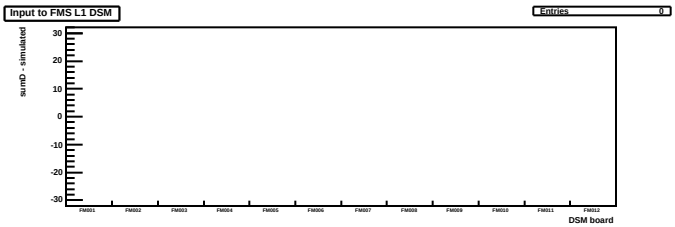

Input to FMS L0 DSM
Entries 4.573776e+07
HT ADC
QT board

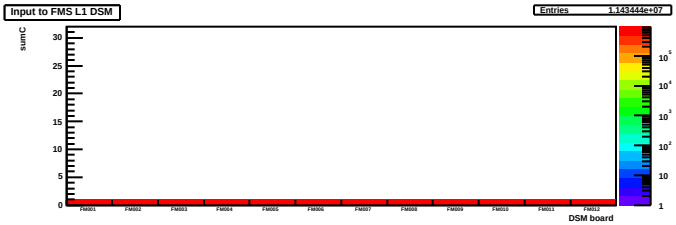

Input to FMS L0 DSM
Entries 7622960
HT ADC - simulated
QT board

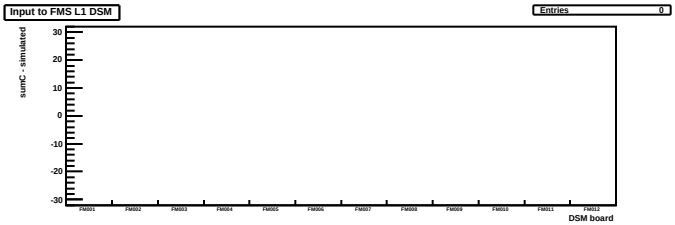

Input to FMS L0 DSM
Entries 4.573776e+07
HT ID
QT board

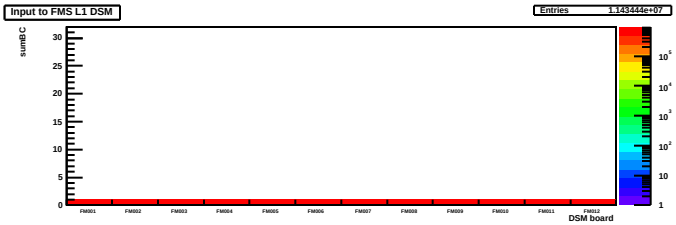

Input to FMS L0 DSM
Entries 7622960
HT ID - simulated
QT board

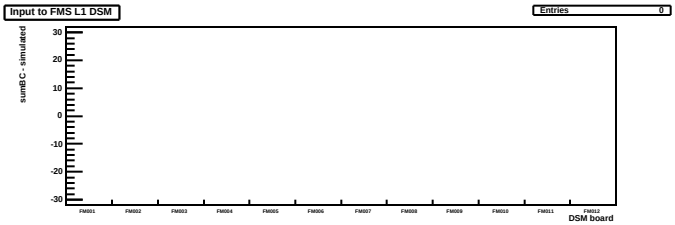

Input to FMS L1 DSM
Entries 1.143444e+07
sumD
DSM board

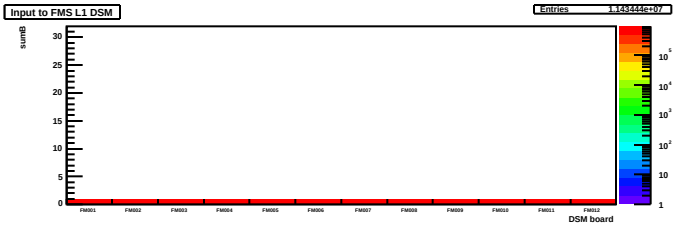

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

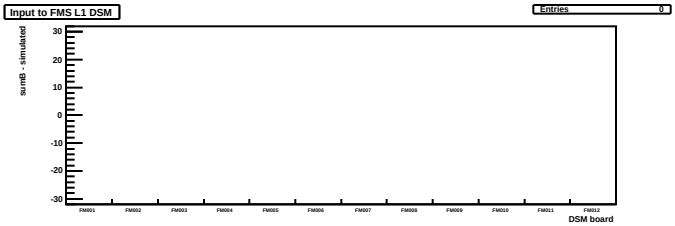

Input to FMS L1 DSM
Entries 1.143444e+07
sumC
DSM board

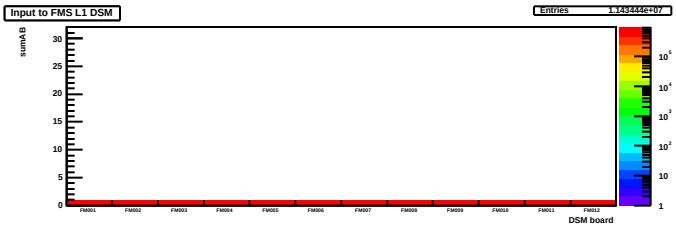

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

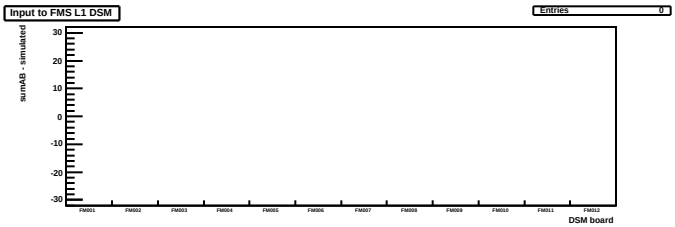

Input to FMS L1 DSM
Entries 1.143444e+07
sumBC
DSM board

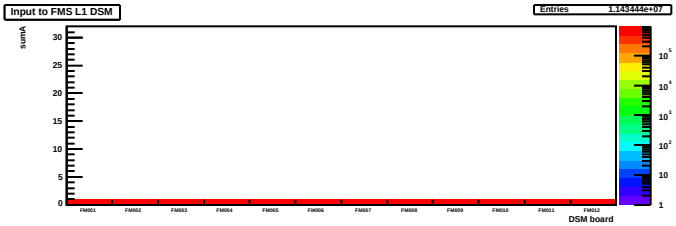

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

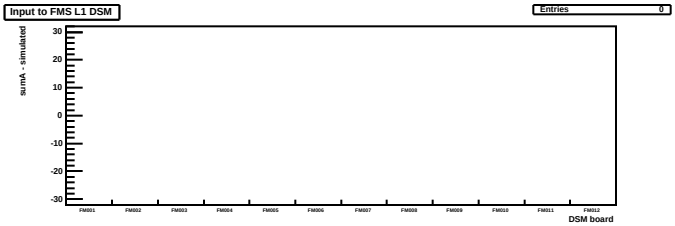

Input to FMS L1 DSM
Entries 1.143444e+07
sumB
DSM board

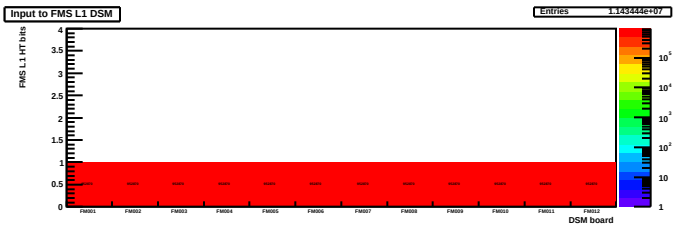

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

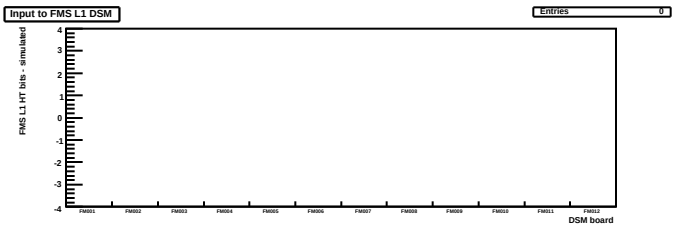

Input to FMS L1 DSM
Entries 1.143444e+07
sumAB
DSM board

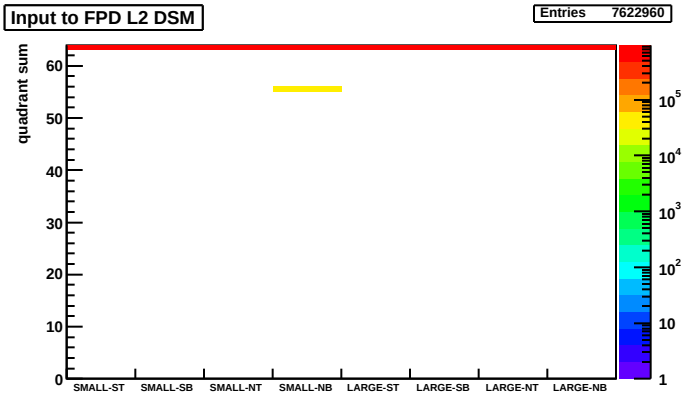

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

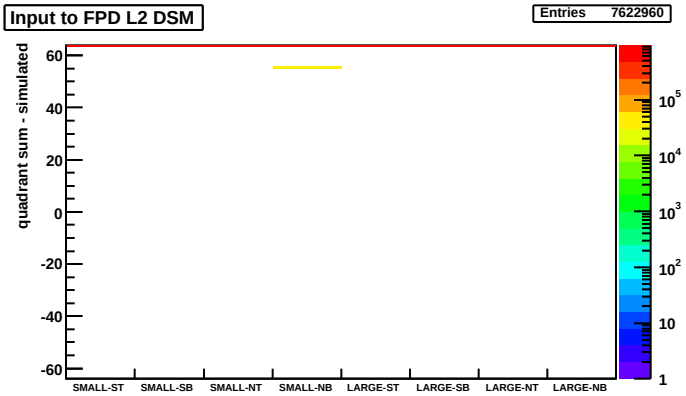

Input to FMS L1 DSM
Entries 1.143444e+07
sumA
DSM board

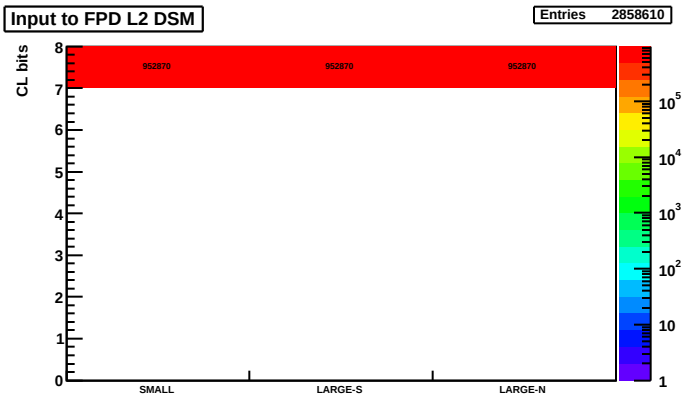

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

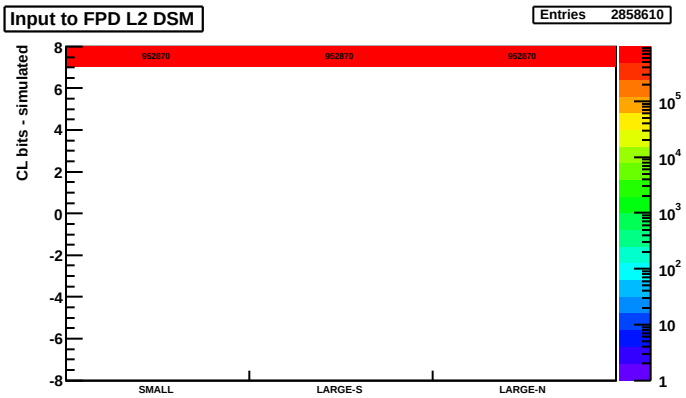

Input to FMS L1 DSM
Entries 1.143444e+07
FMS L1 HT bits
DSM board

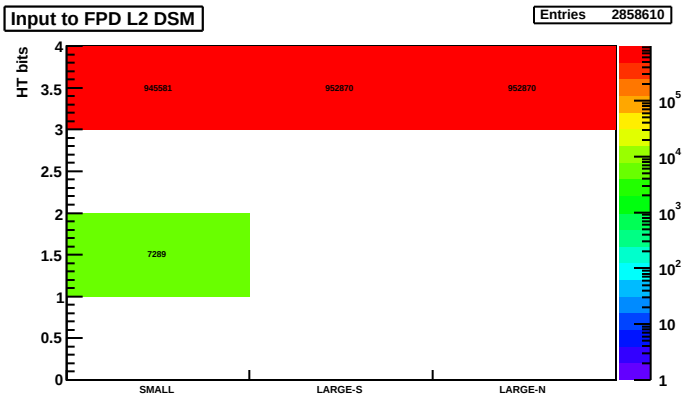

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

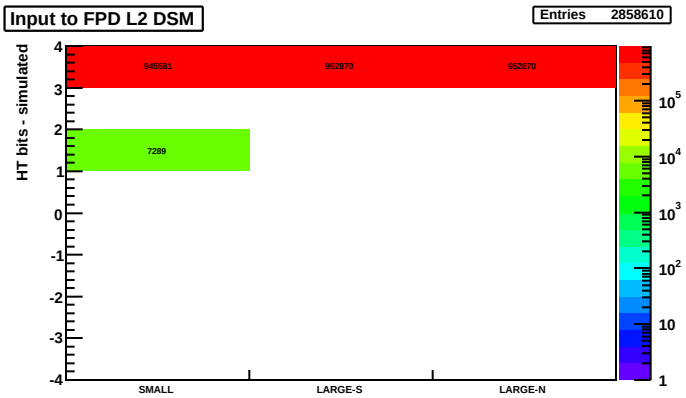

Input to FPD L2 DSM
Entries 7622960
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

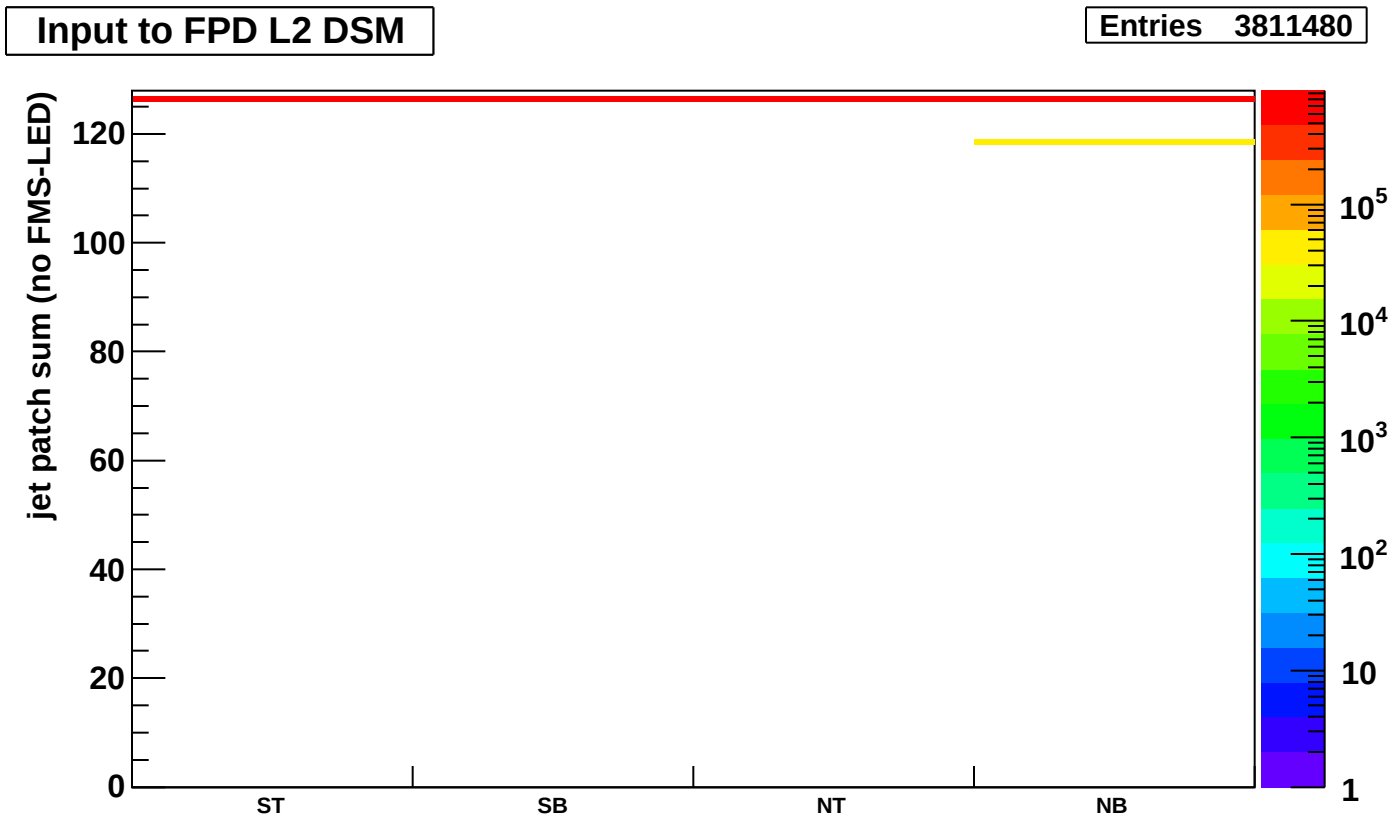

Input to FPD L2 DSM
Entries 7622960
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

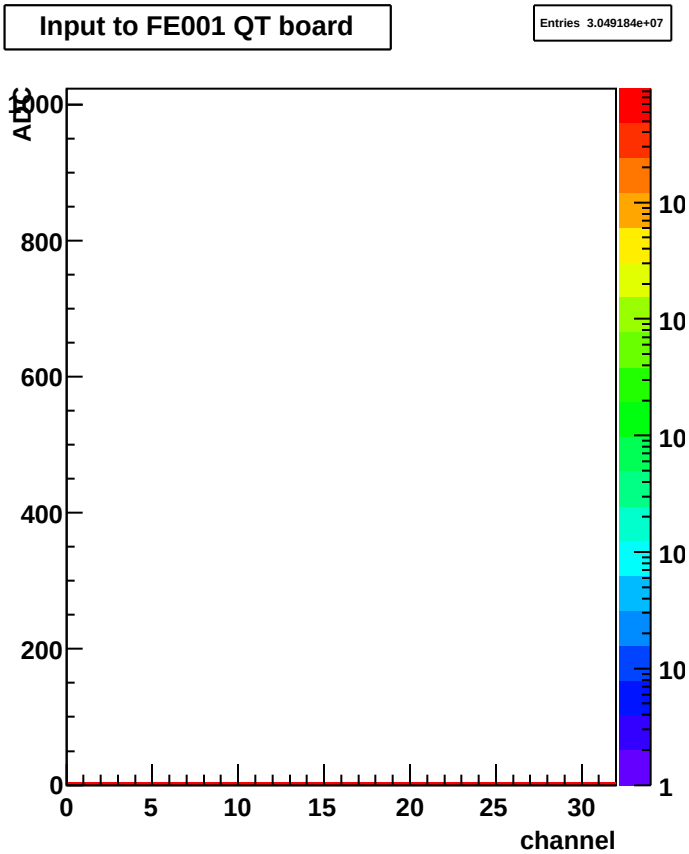

Input to FPD L2 DSM
Entries 2858610
CL bits
952870
952870
952870
SMALL LARGE-S LARGE-N

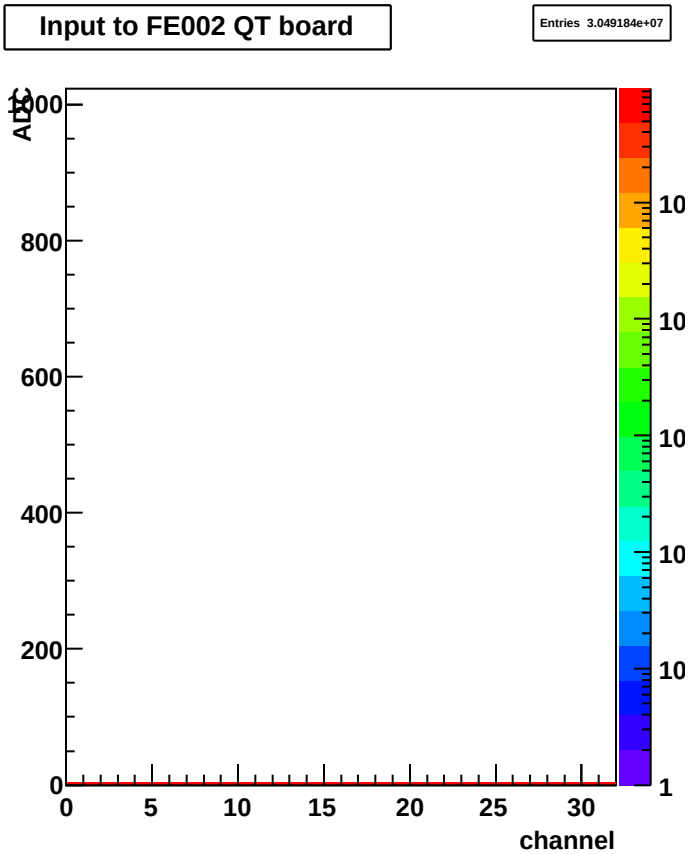

Input to FPD L2 DSM
Entries 2858610
CL bits - simulated
952870
952870
952870
SMALL LARGE-S LARGE-N

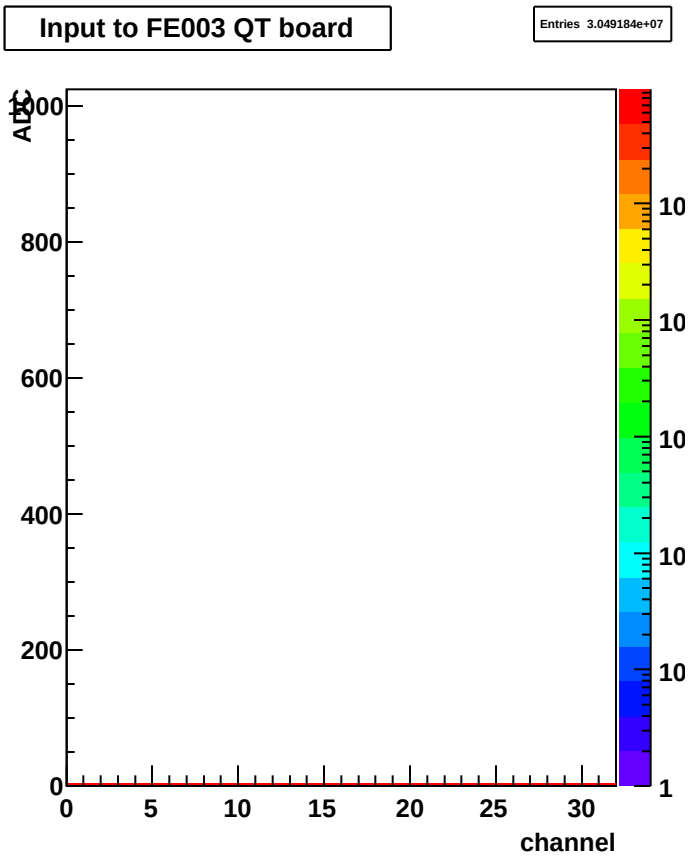

Input to FPD L2 DSM
Entries 2858610
HT bits
945581
952870
952870
7289
SMALL LARGE-S LARGE-N

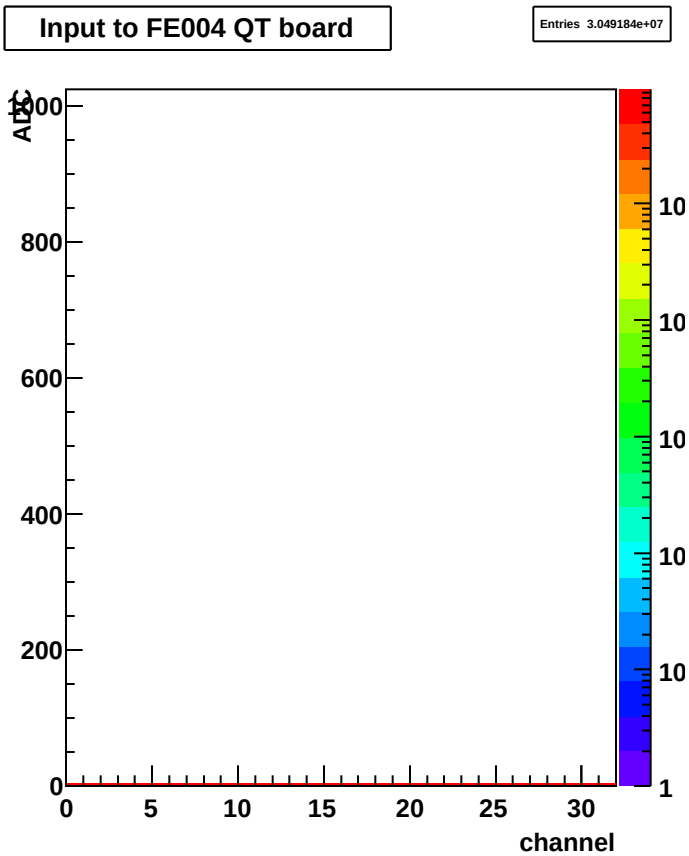

Input to FPD L2 DSM
Entries 2858610
HT bits - simulated
945581
952870
952870
7289
SMALL LARGE-S LARGE-N

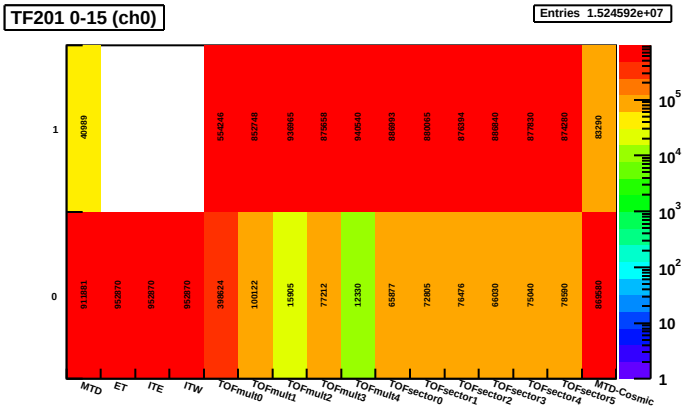

Input to FPD L2 DSM
Entries 3811480
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

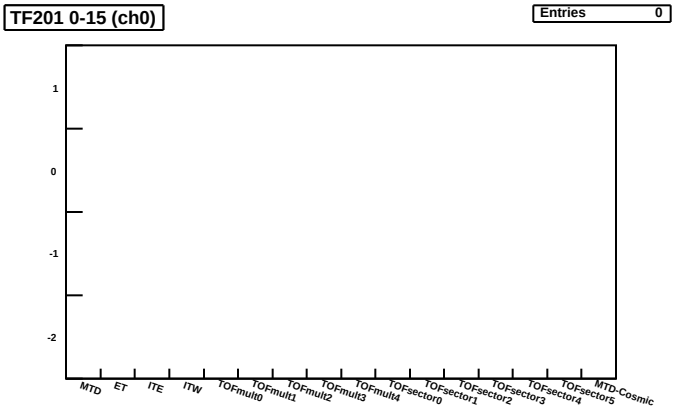

Input to FE001 QT board
Entries 3.049184e+07
ADC
channel

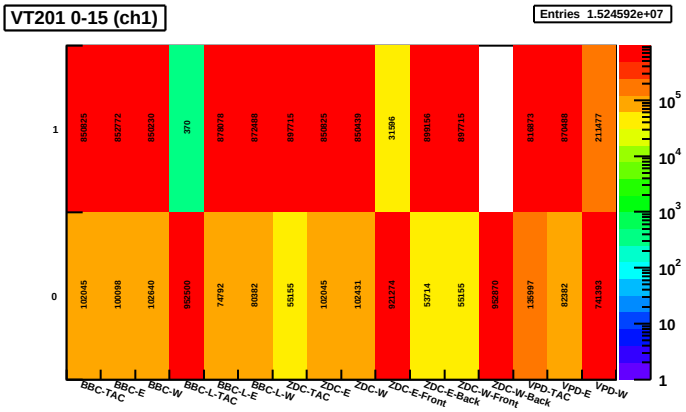

Input to FE002 QT board
Entries 3.049184e+07
ADC
channel

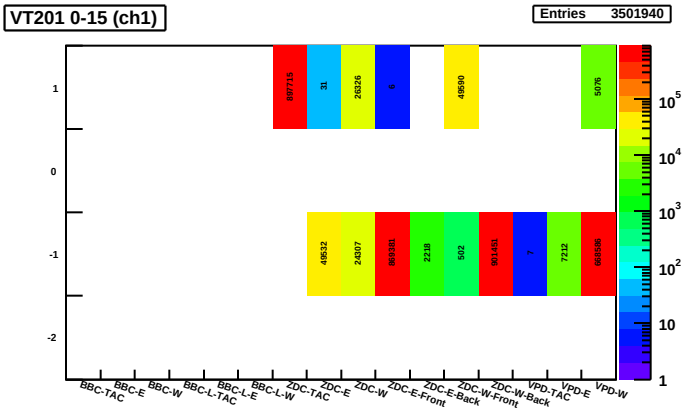

Input to FE003 QT board
Entries 3.049184e+07
ADC
channel

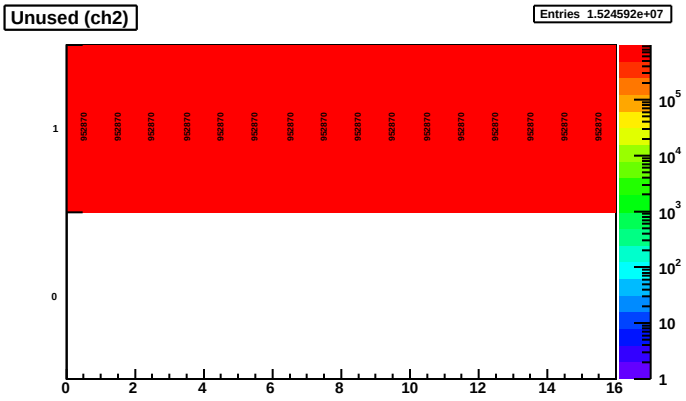

Input to FE004 QT board
Entries 3.049184e+07
ADC
channel

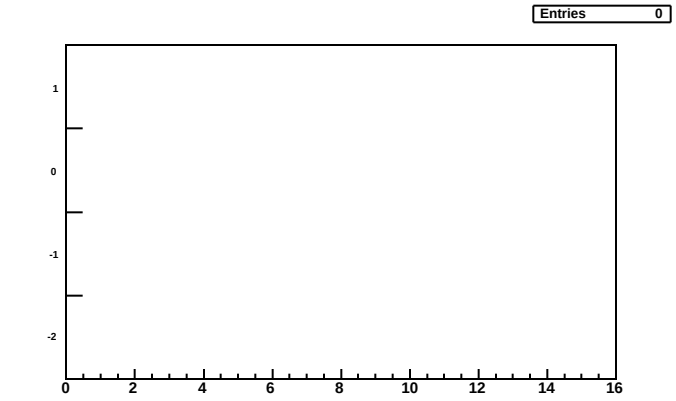

TF201 0-15 (ch0)
Entries 1.524592e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

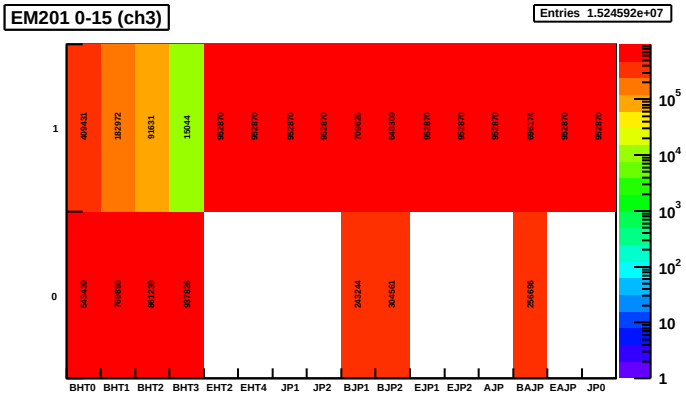

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

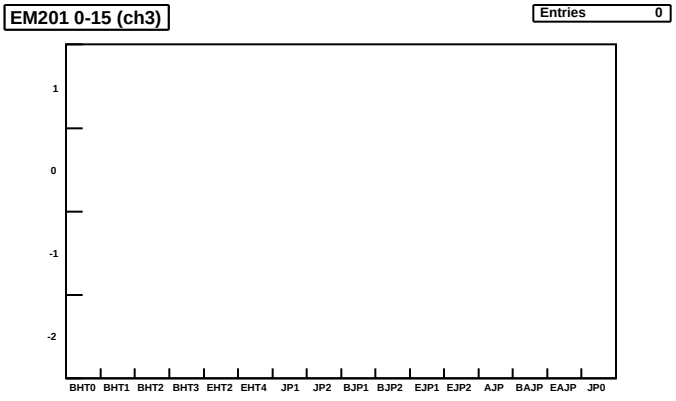

VT201 0-15 (ch1)
Entries 1.524592e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 3501940
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

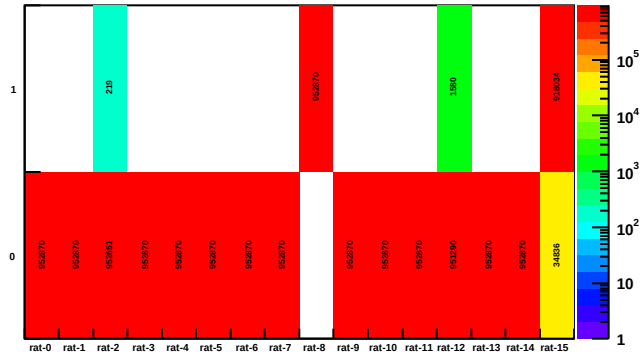

Unused (ch2)
Entries 1.524592e+07

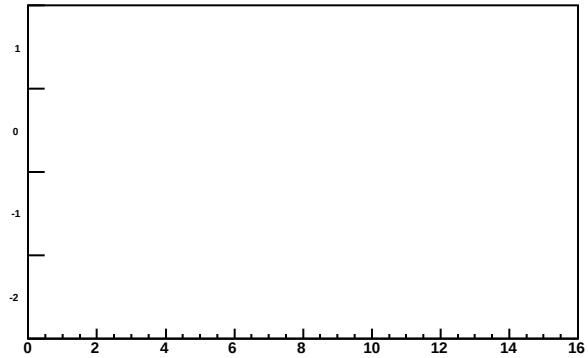

Entries 0


EM201 0-15 (ch3)
Entries 1.524592e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

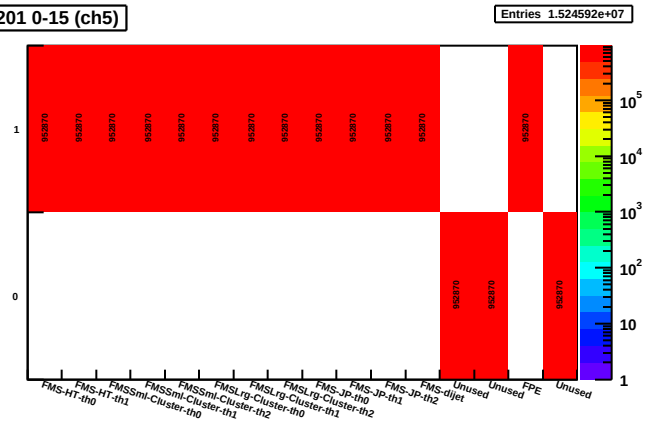

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

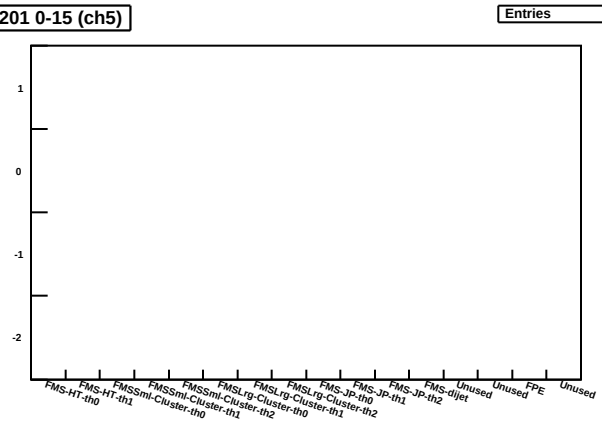




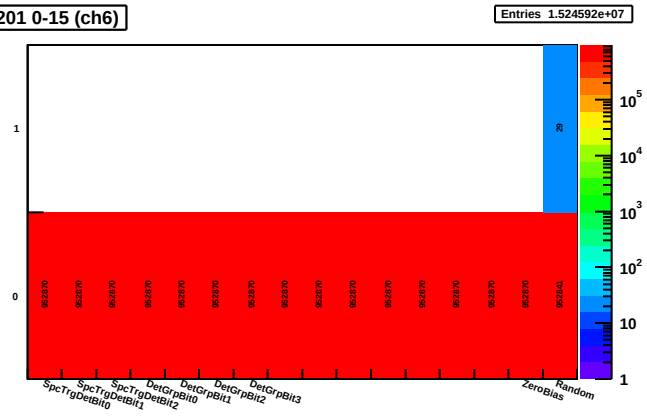
FP201 0-15 (ch5)

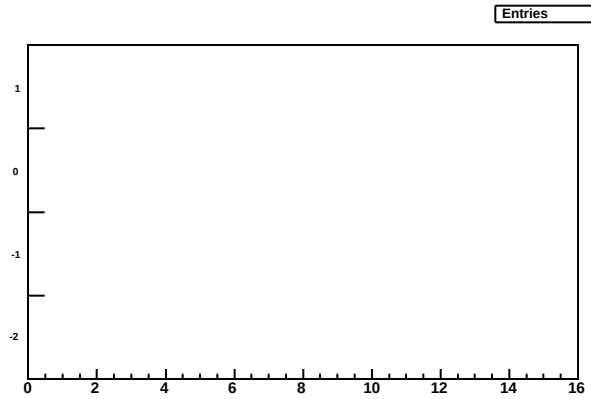
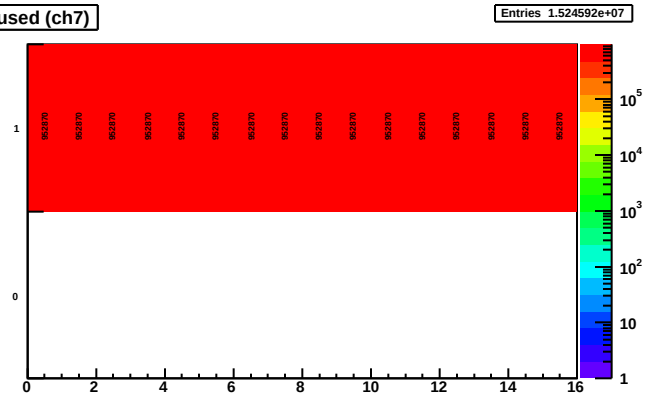
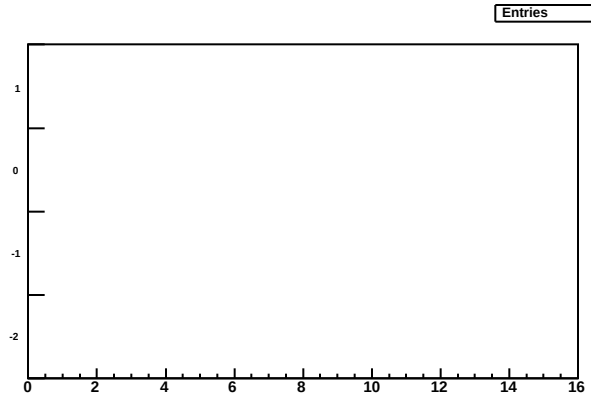


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)