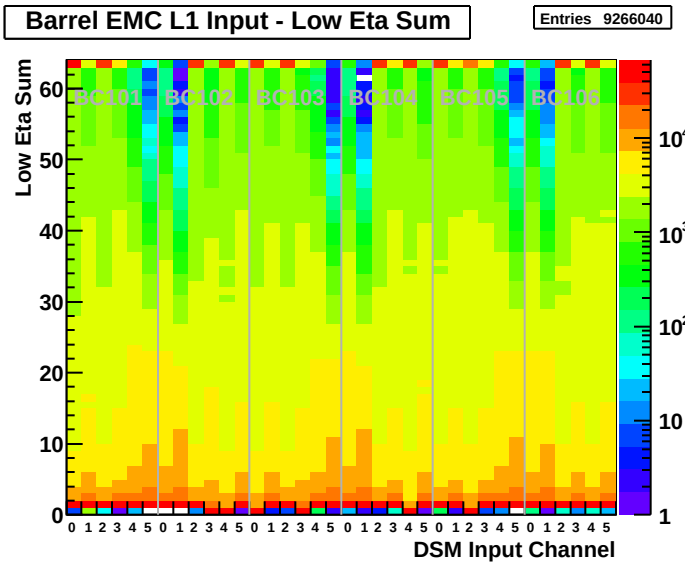
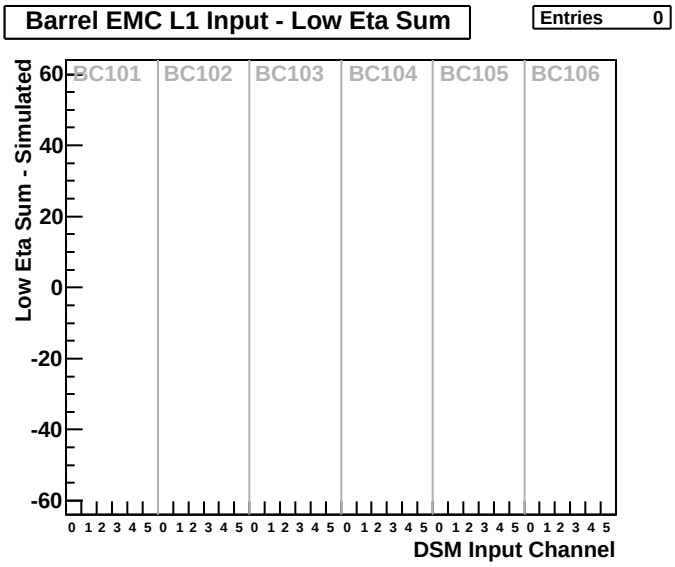
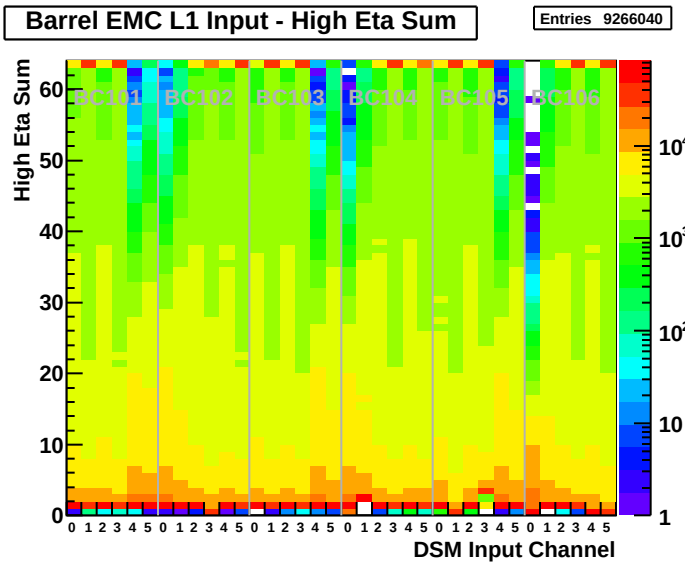
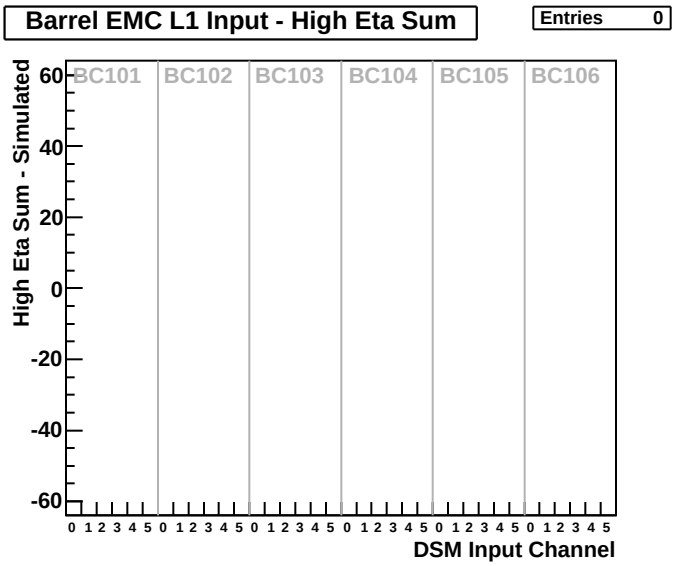
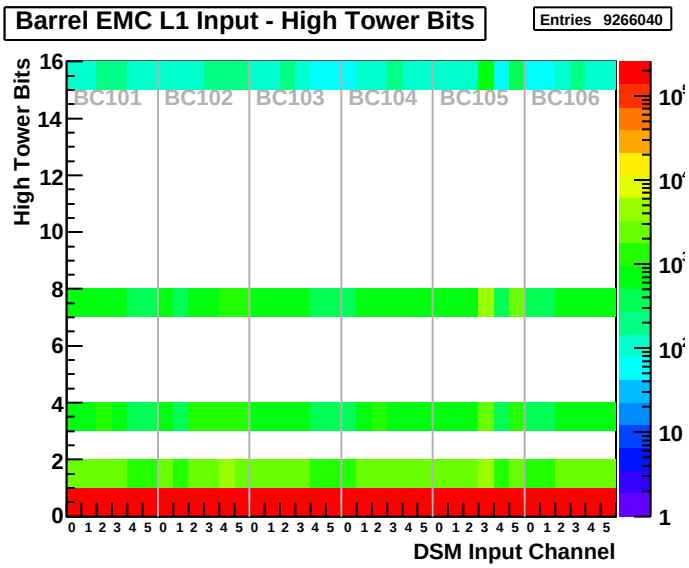
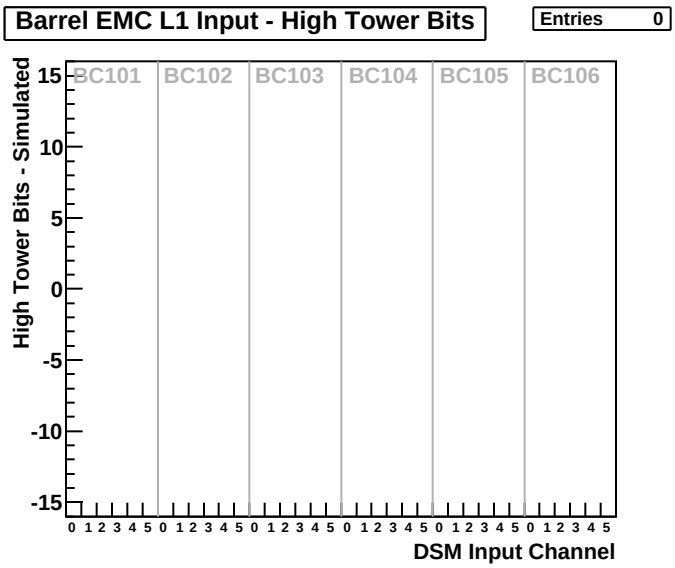

Barrel EMC L1 Input - Low Eta Sum
Entries 9266040
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

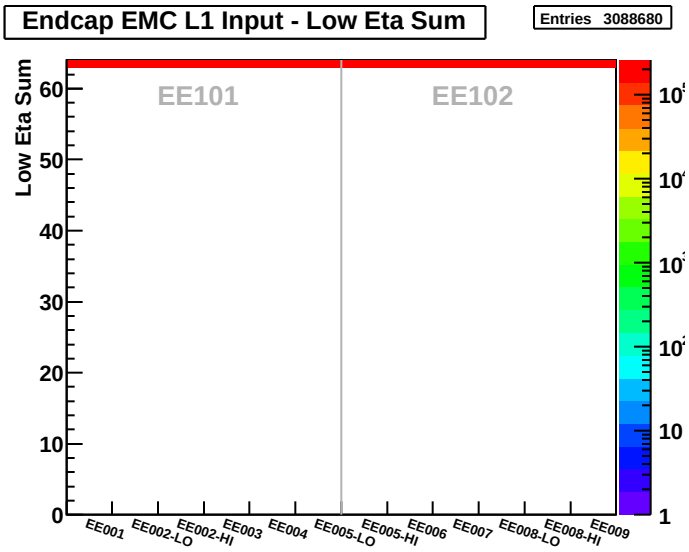

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

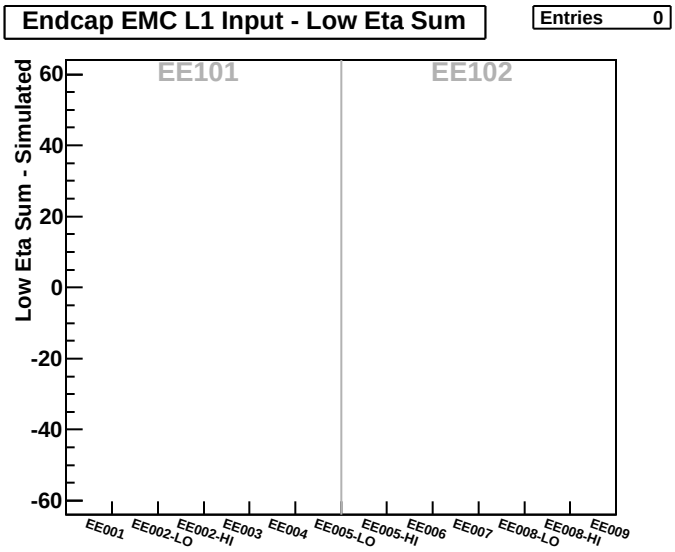

Barrel EMC L1 Input - High Eta Sum
Entries 9266040
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

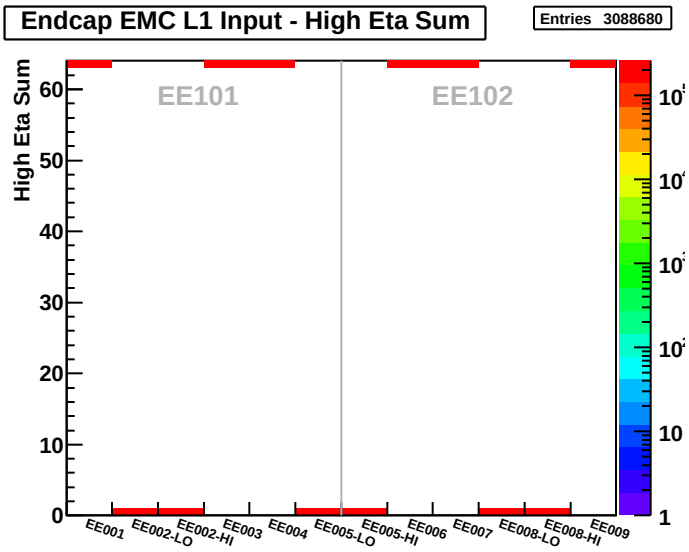

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

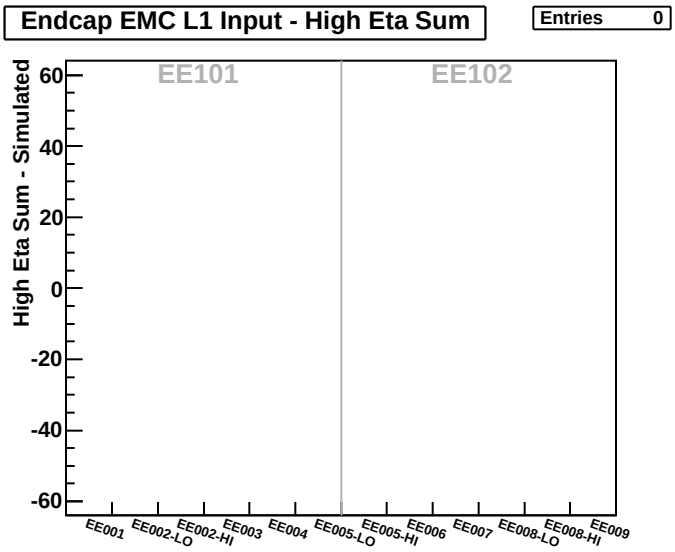

Barrel EMC L1 Input - High Tower Bits
Entries 9266040
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

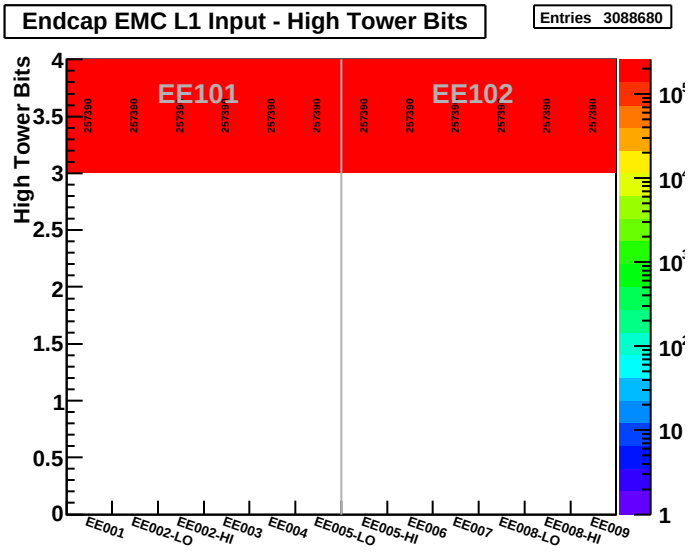

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

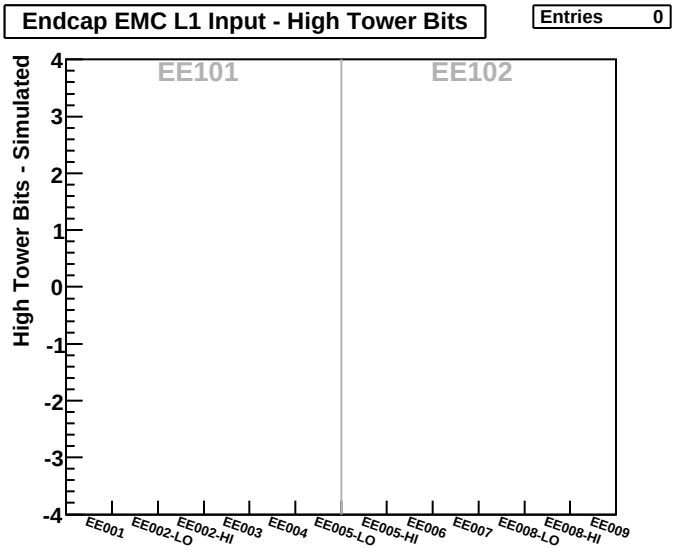

Endcap EMC L1 Input - Low Eta Sum
Entries 3088680
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

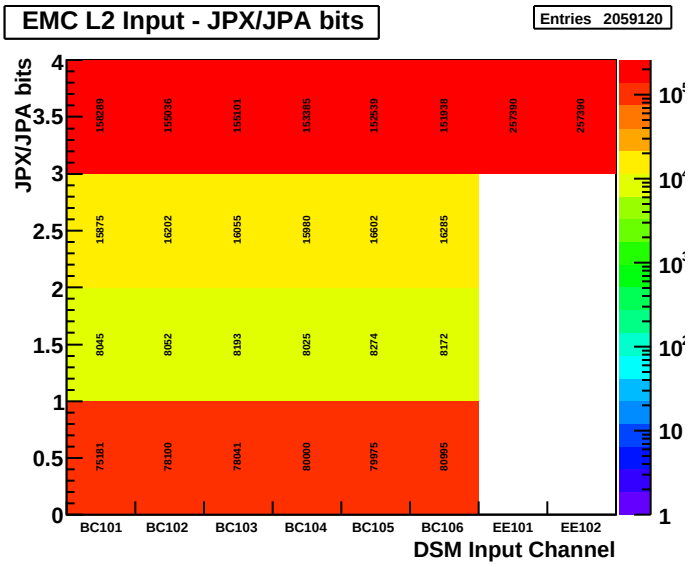

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

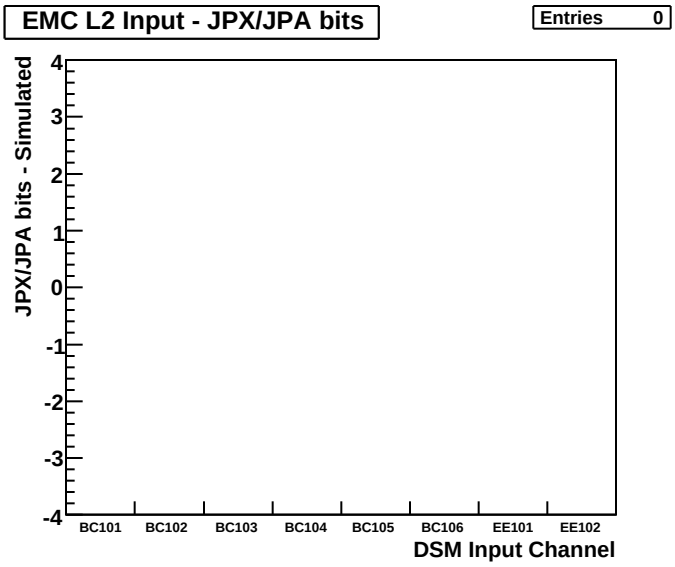

Endcap EMC L1 Input - High Eta Sum
Entries 3088680
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

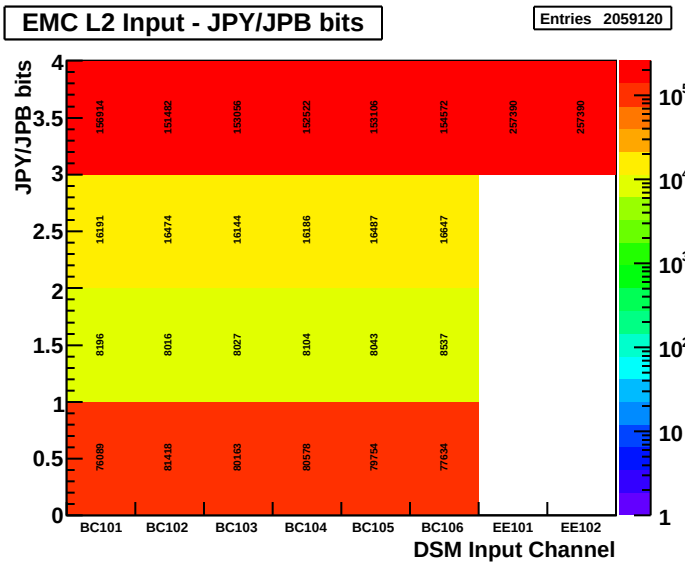

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

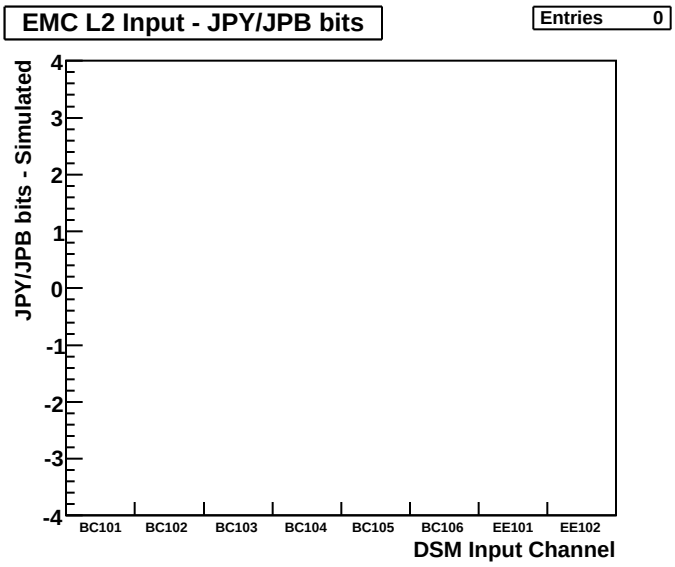

Endcap EMC L1 Input - High Tower Bits
Entries 3088680
High Tower Bits
EE101
EE102
257390
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

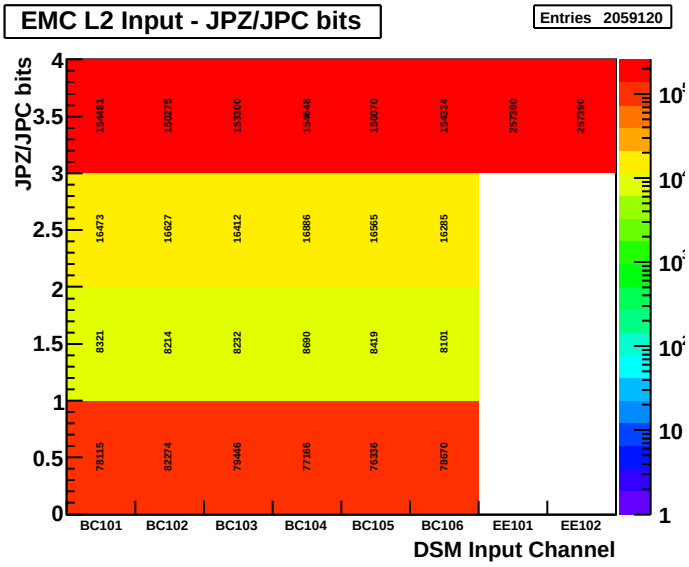

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

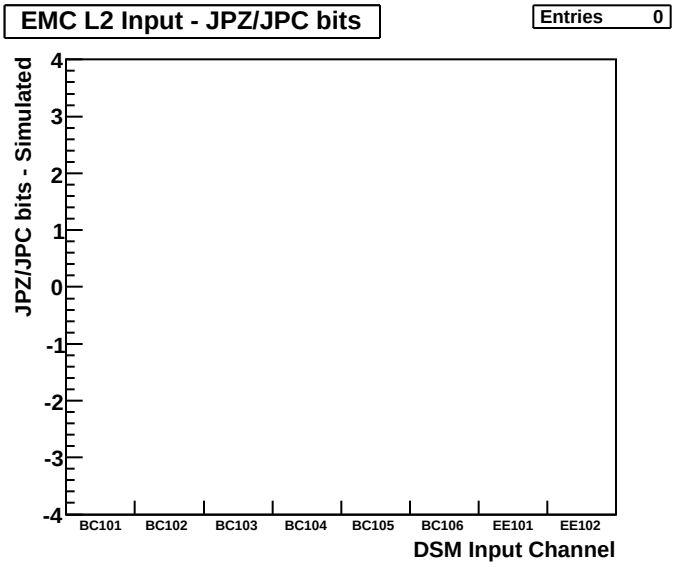

EMC L2 Input - JPX/JPA bits
Entries 2059120
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
156289 156036 155101 153385 152539 151938 257390 257390
15875 16202 16055 15980 16602 16285
8045 8052 8193 8025 8274 8172
75181 78100 78041 80000 79975 80995

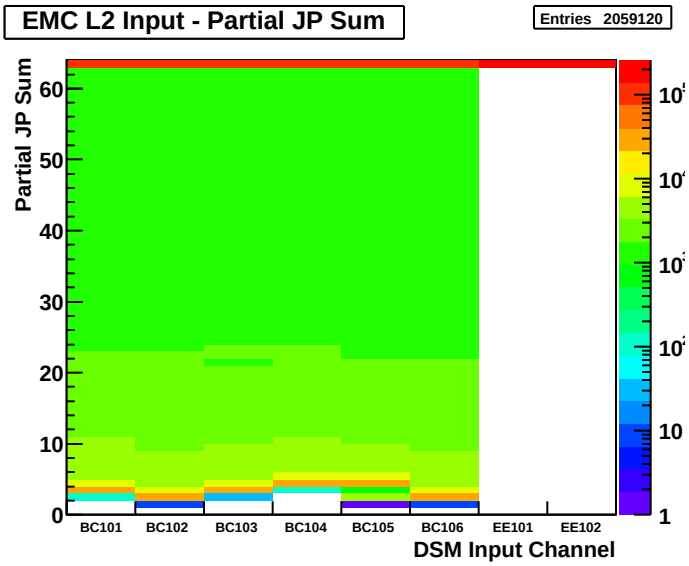

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

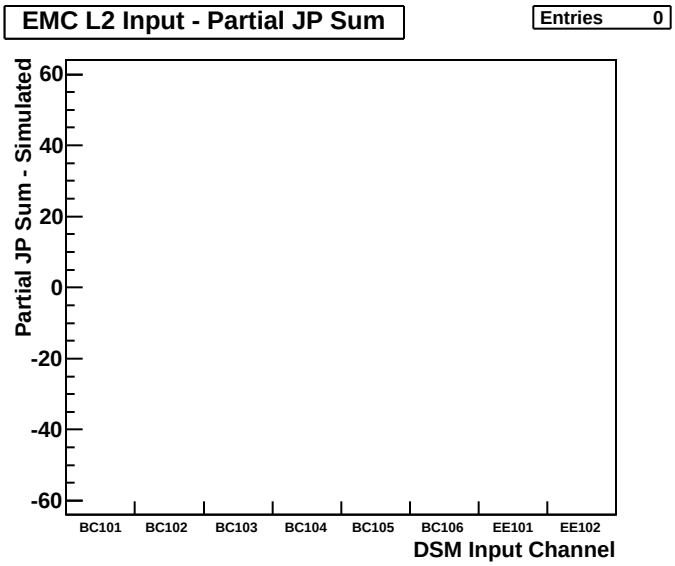

EMC L2 Input - JPY/JPB bits
Entries 2059120
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
156914 151482 153056 152522 153106 154572 257390 257390
16191 16474 16144 16186 16487 16647
8196 8016 8027 8104 8043 8537
76089 81418 80163 80578 79754 77634

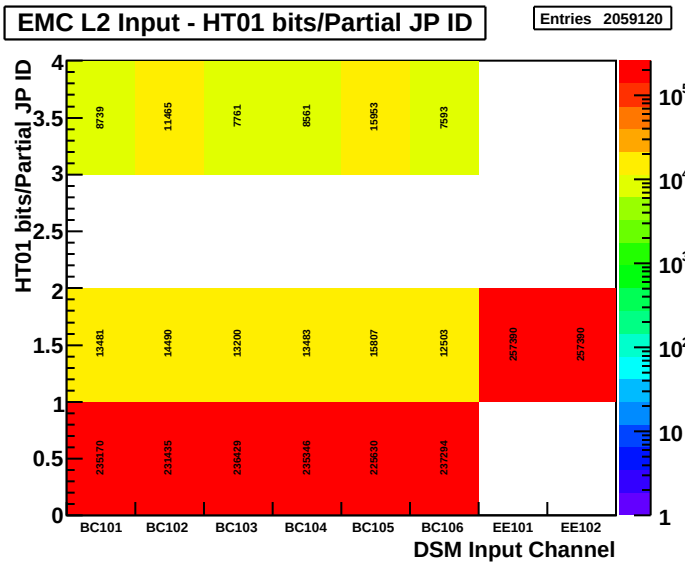

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

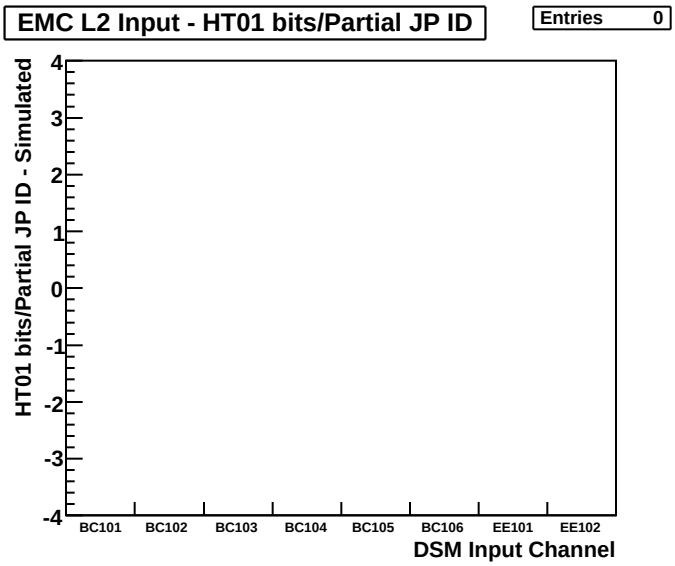

EMC L2 Input - JPZ/JPC bits
Entries 2059120
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
154481 150275 153300 156648 156070 154334 257390 257390
16473 16627 16412 16886 16565 16285
8321 8214 8232 8690 8419 8101
78115 82274 79446 77166 76336 78670

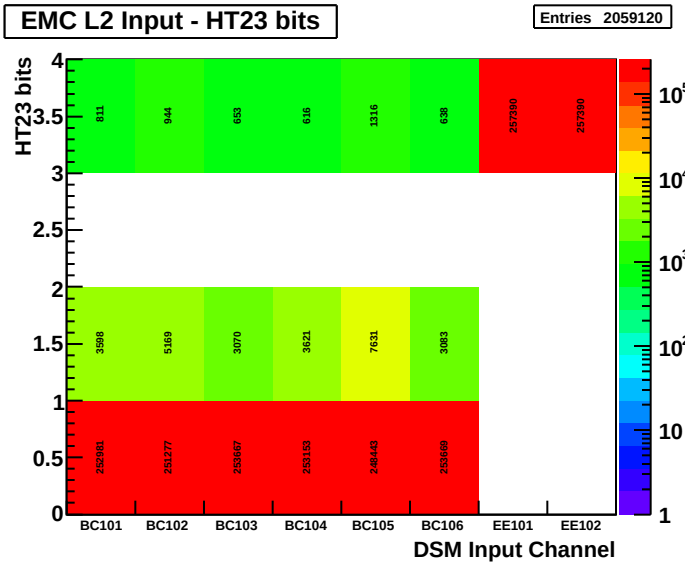

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

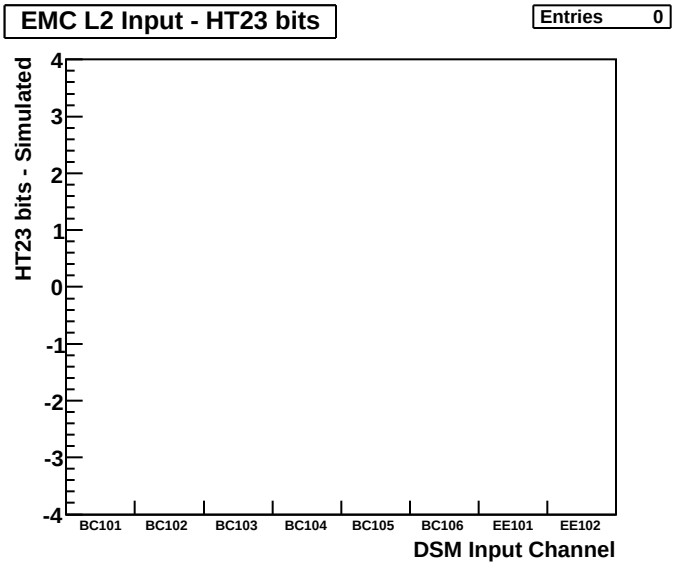

EMC L2 Input - Partial JP Sum
Entries 2059120
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

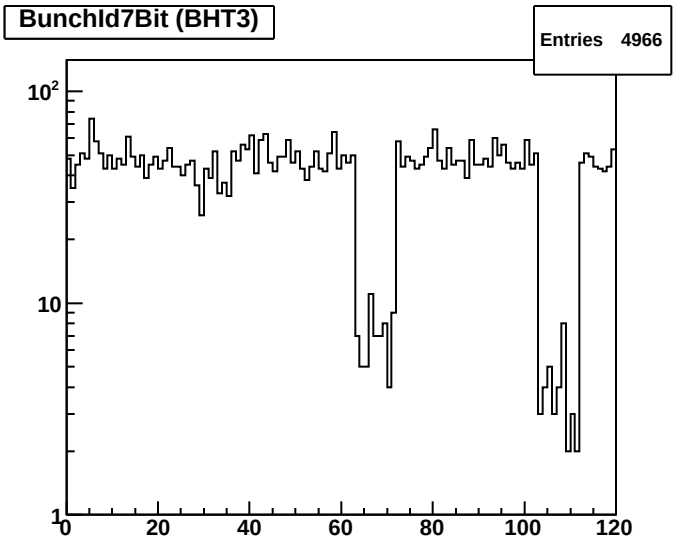

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

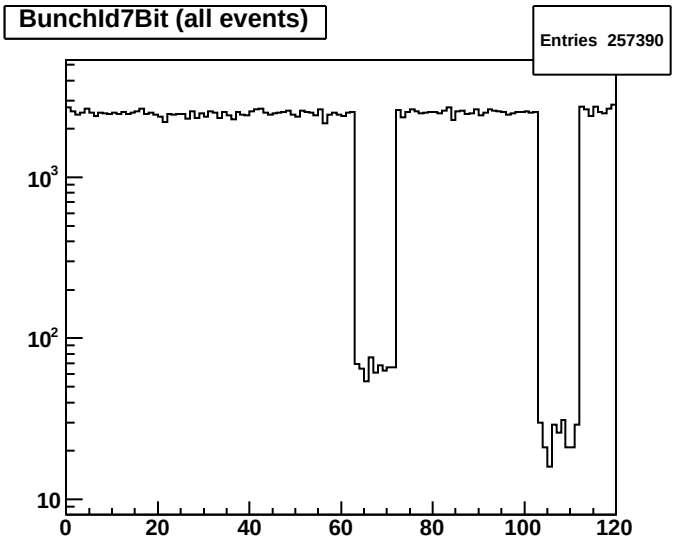

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2059120
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

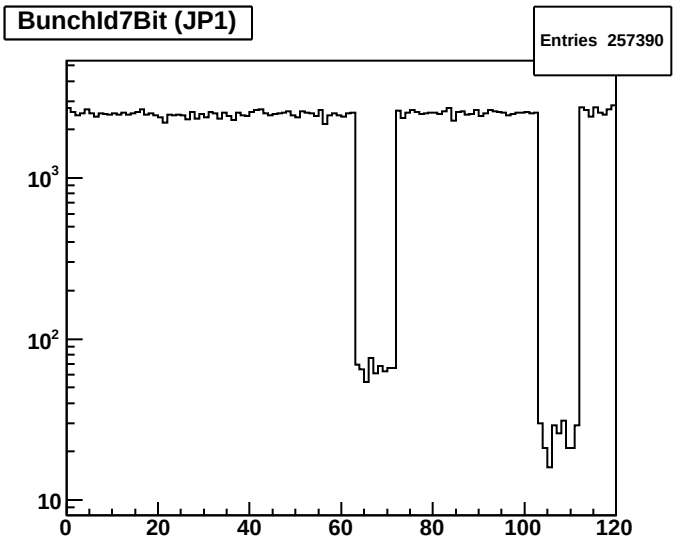

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

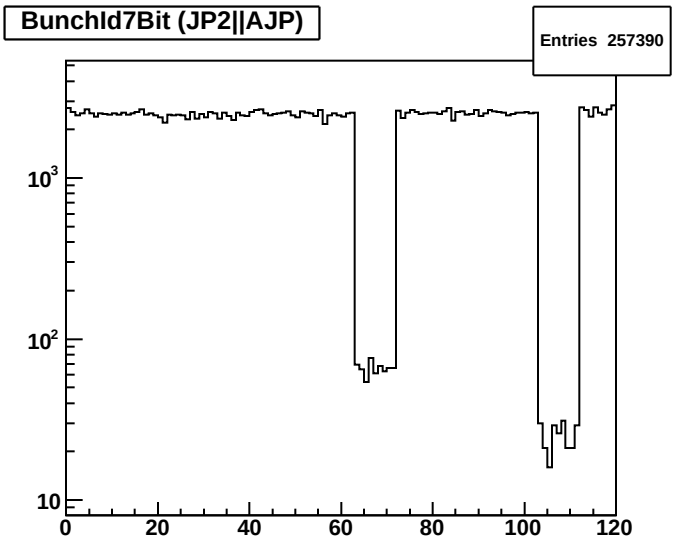

EMC L2 Input - HT23 bits
Entries 2059120
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

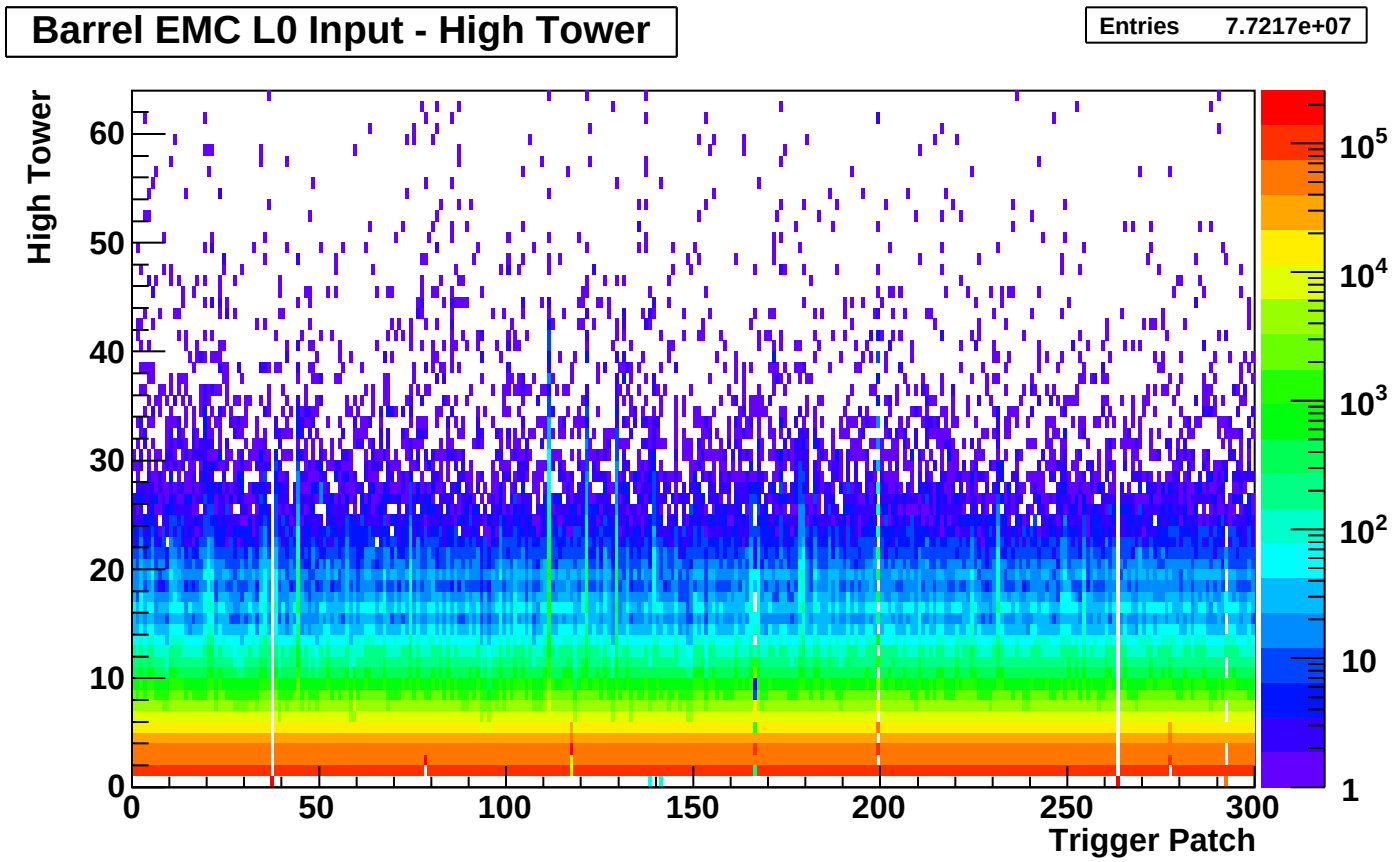

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

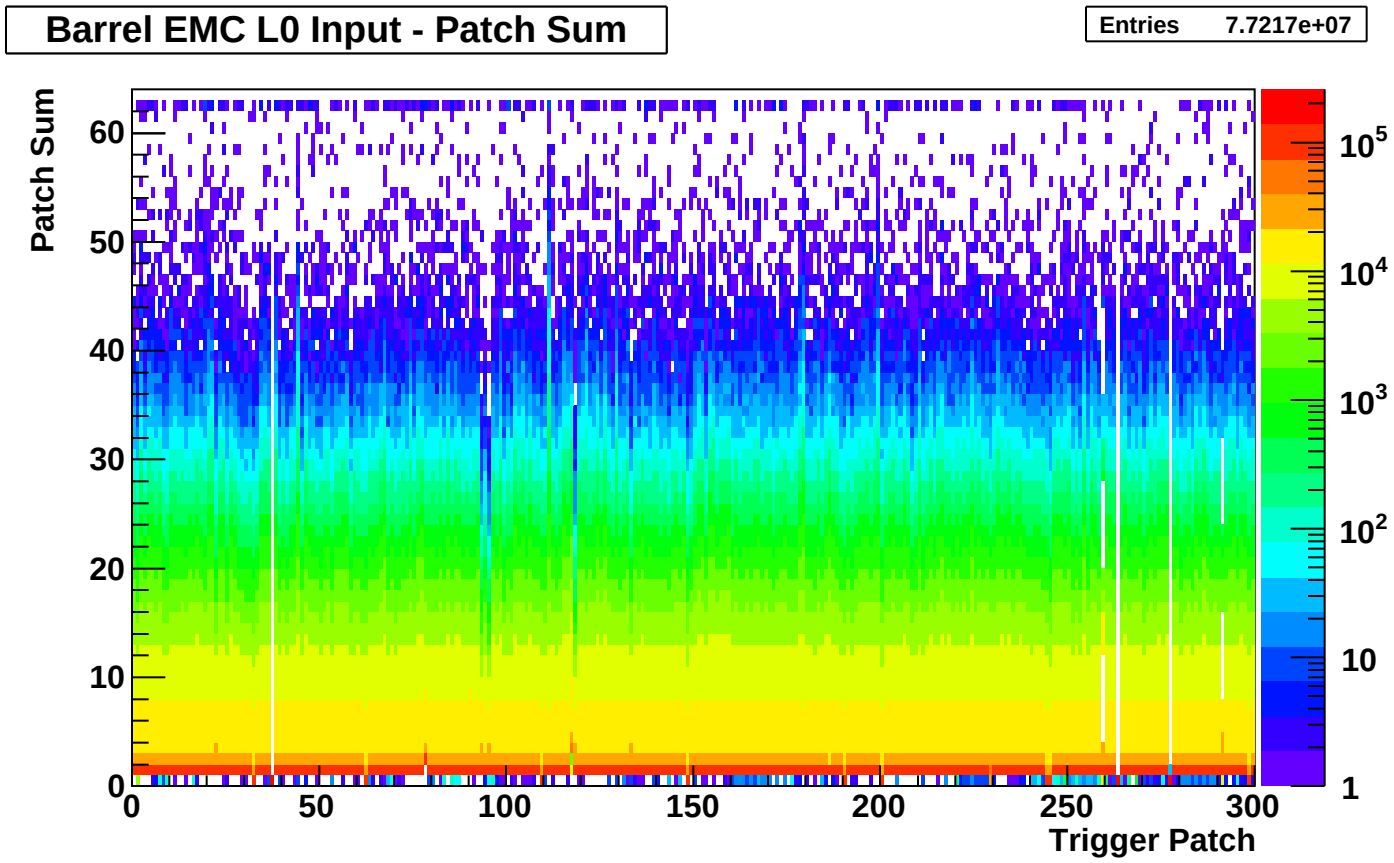

BunchId7Bit (BHT3)
Entries 4966
10^2
10
1
0
20
40
60
80
100
120

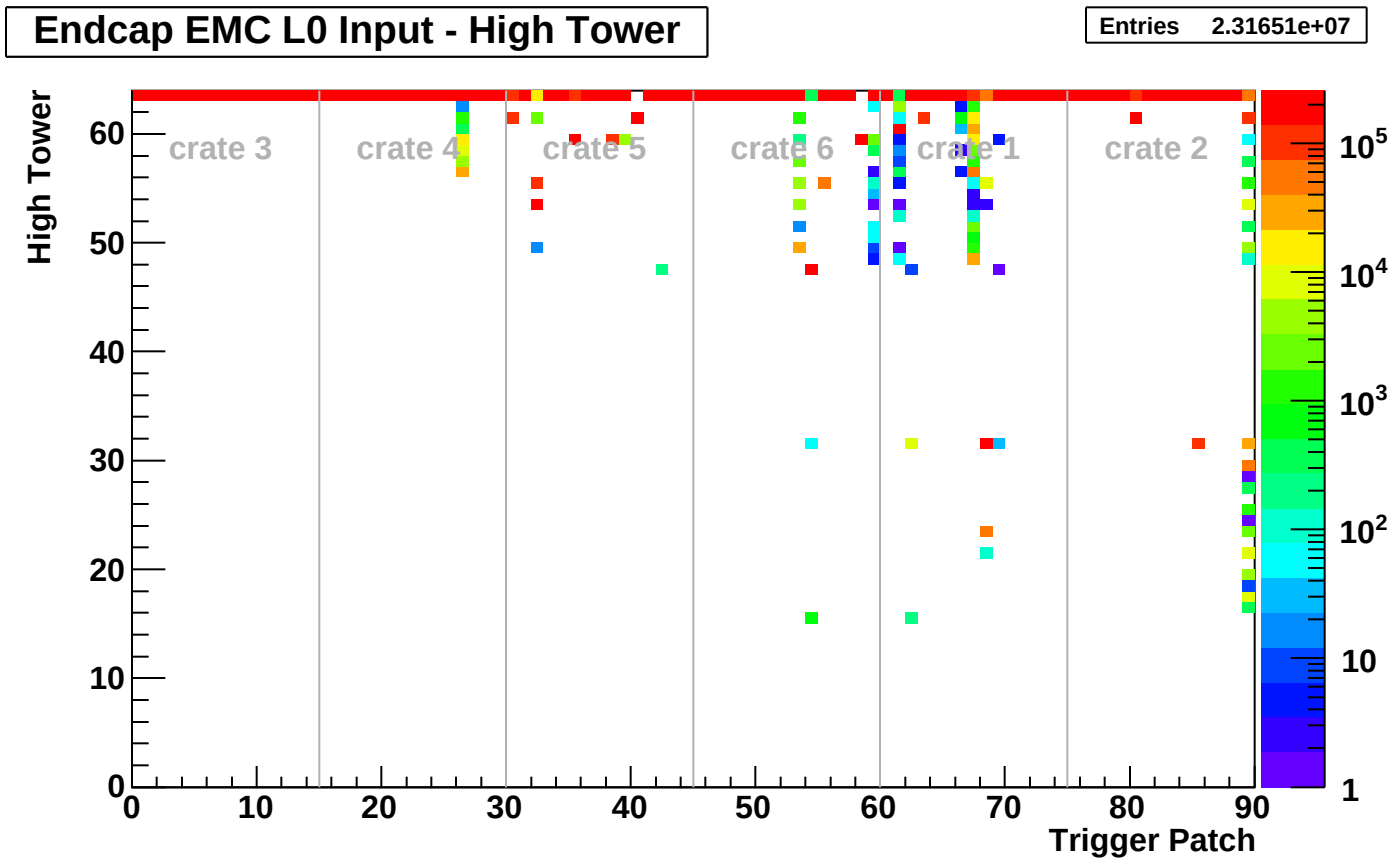

BunchId7Bit (all events)
Entries 257390
10^3
10^2
10
0
20
40
60
80
100
120

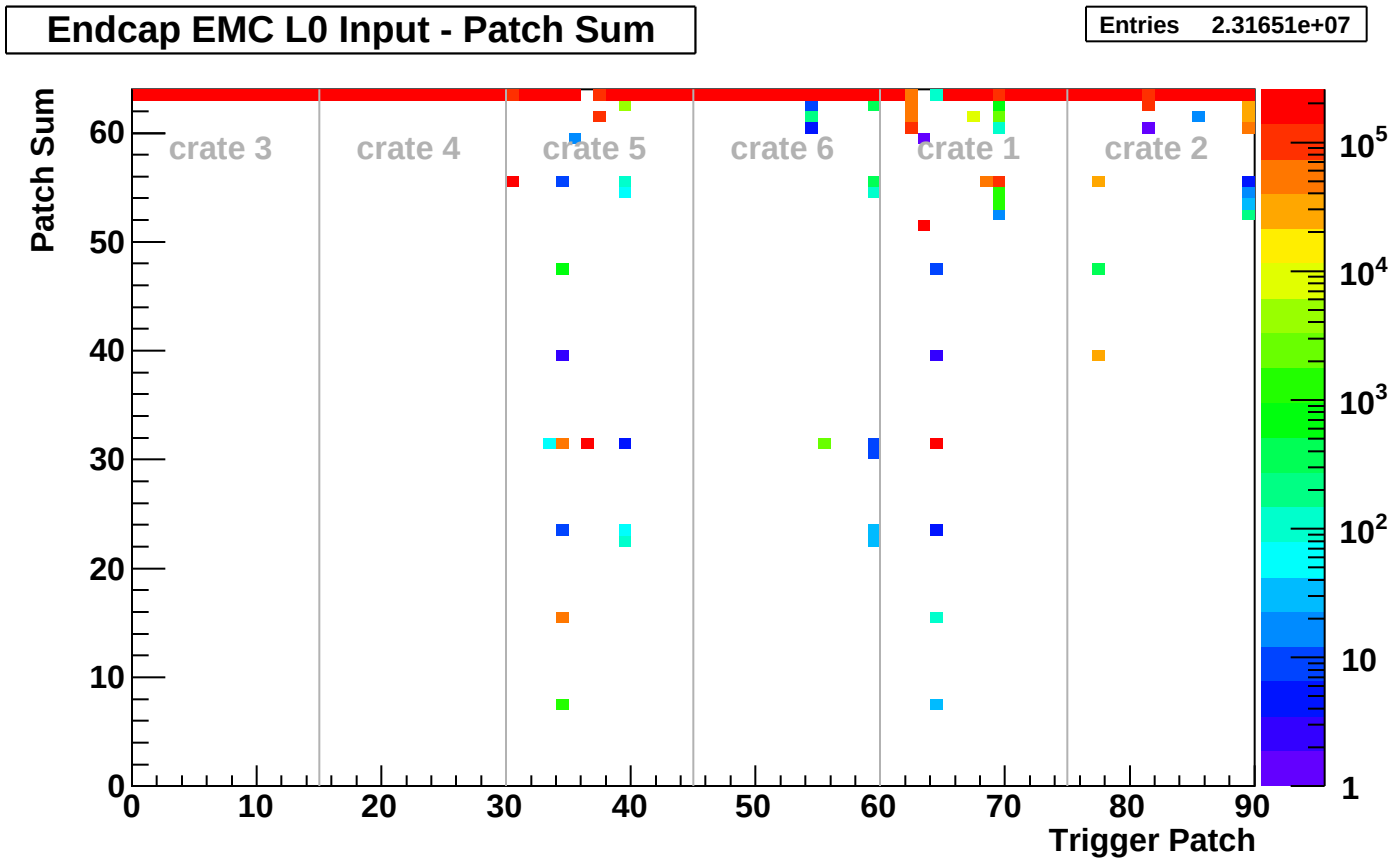

BunchId7Bit (JP1)
Entries 257390
10^3
10^2
10
0
20
40
60
80
100
120

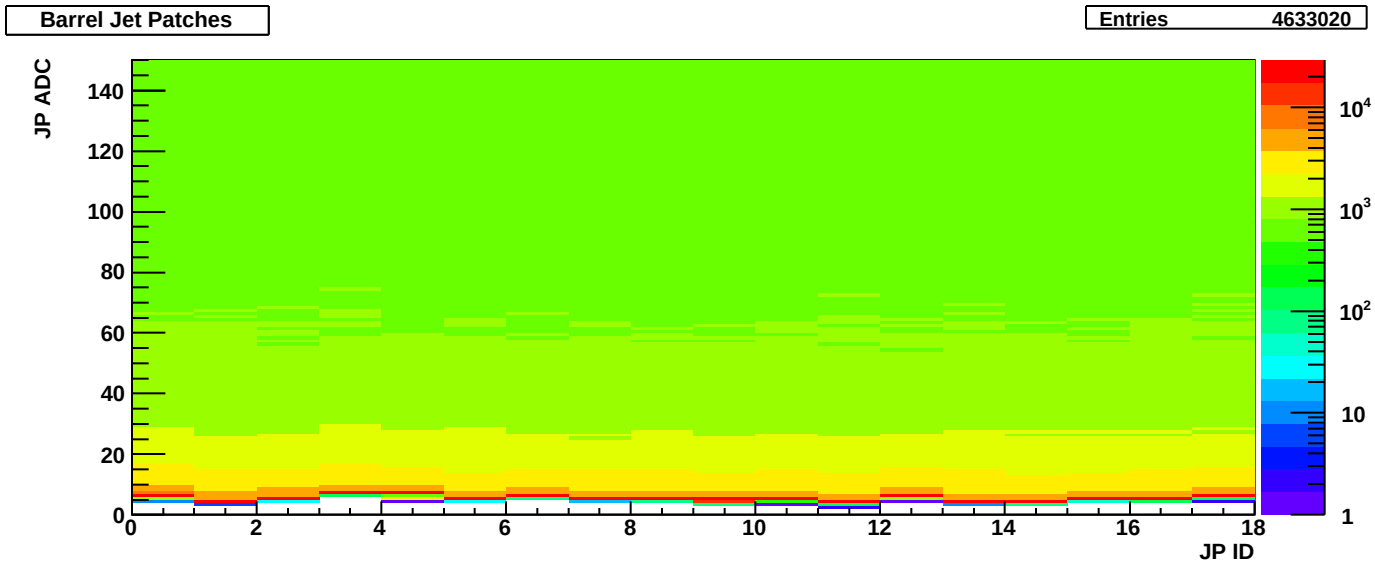

BunchId7Bit (JP2||AJP)
Entries 257390
10^3
10^2
10
0
20
40
60
80
100
120

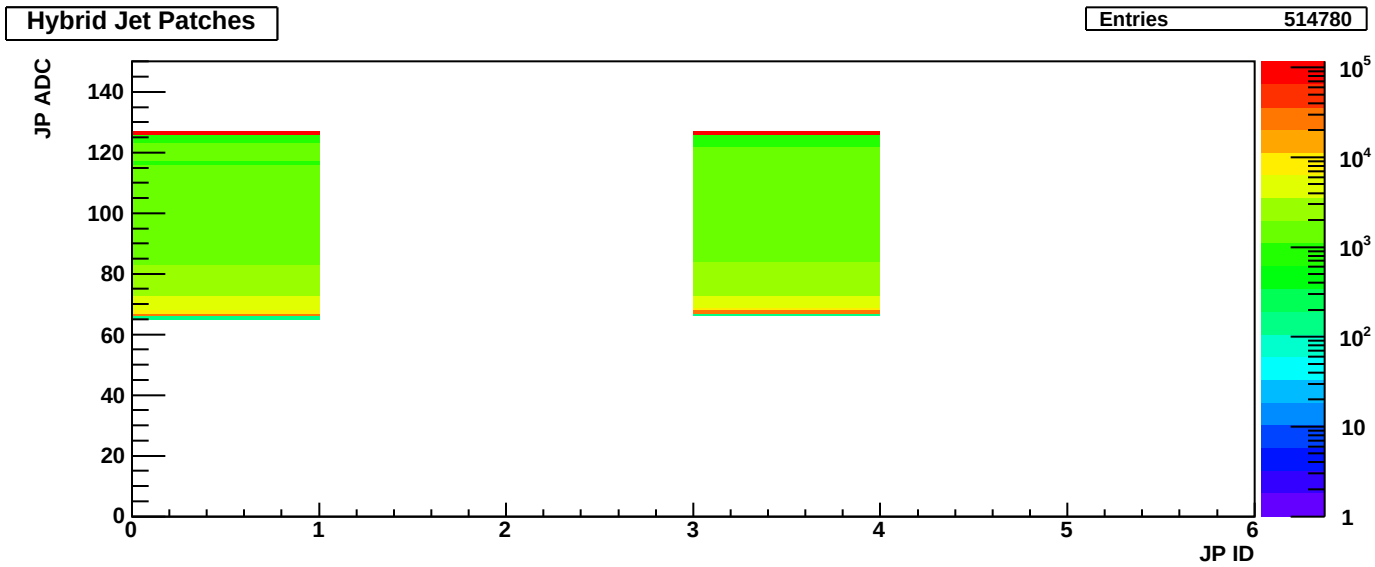

Barrel EMC L0 Input - High Tower
Entries 7.7217e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

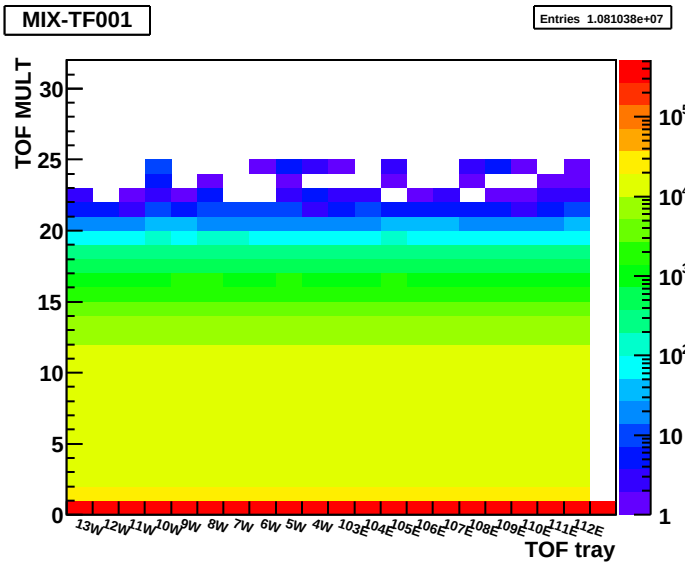

Barrel EMC L0 Input - Patch Sum
Entries 7.7217e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

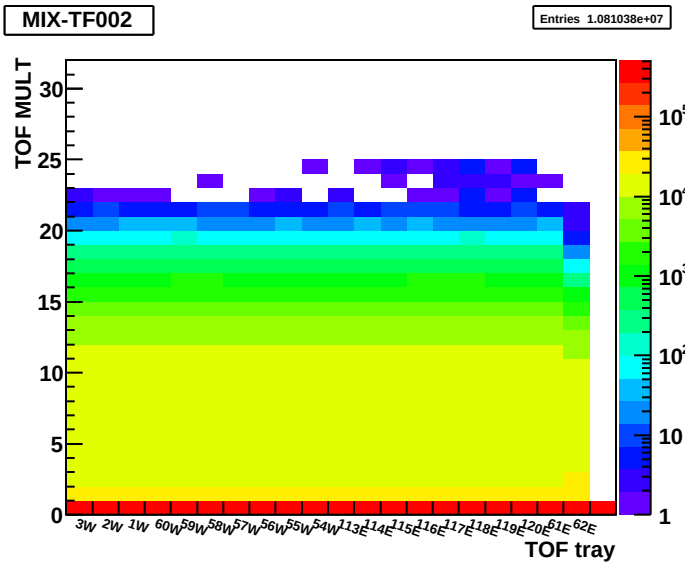

Endcap EMC L0 Input - High Tower
Entries 2.31651e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

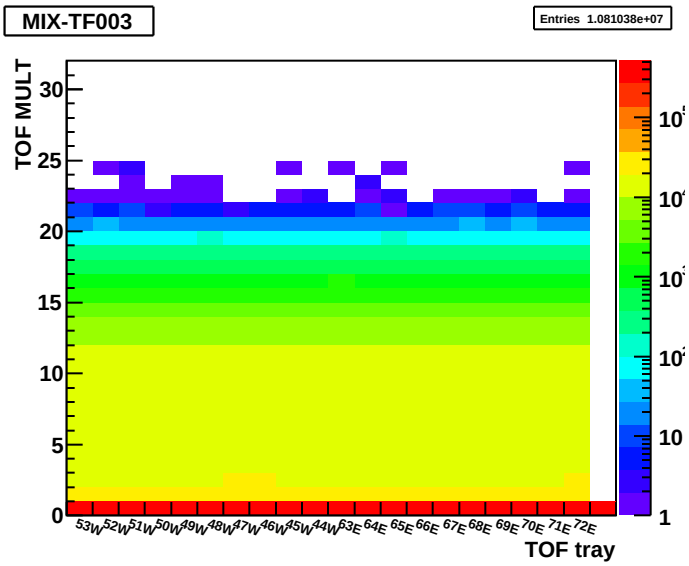

Endcap EMC L0 Input - Patch Sum
Entries 2.31651e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

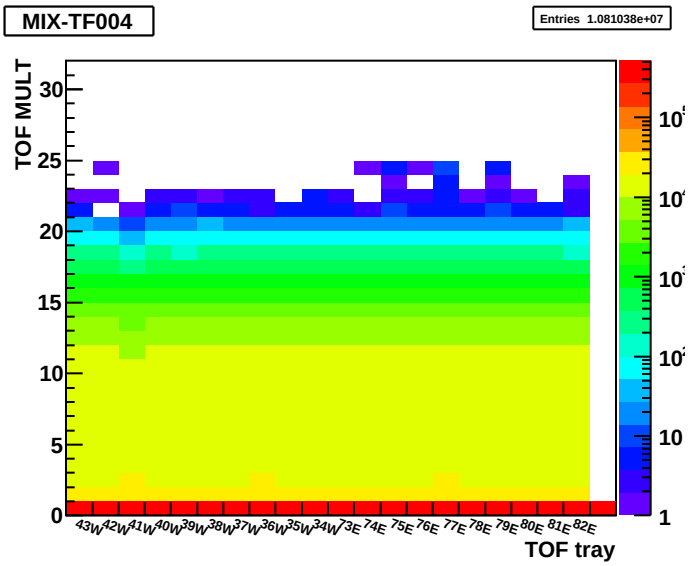

Barrel Jet Patches
Entries 4633020
JP ADC
JP ID

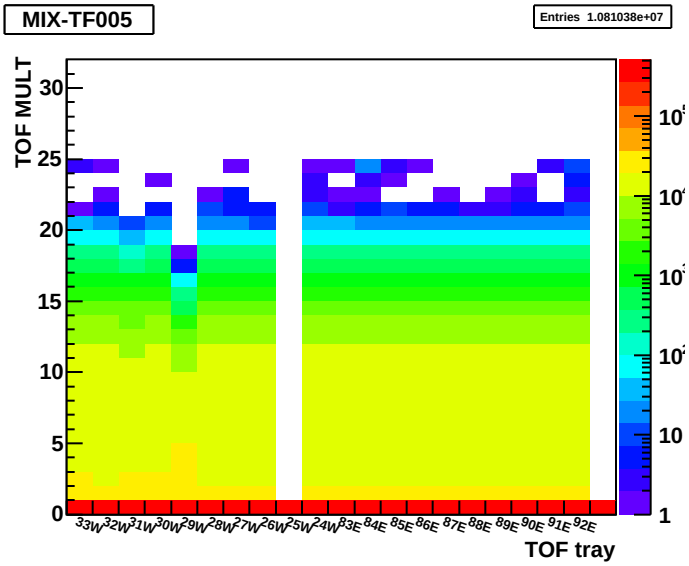

Hybrid Jet Patches
Entries 514780
JP ADC
JP ID

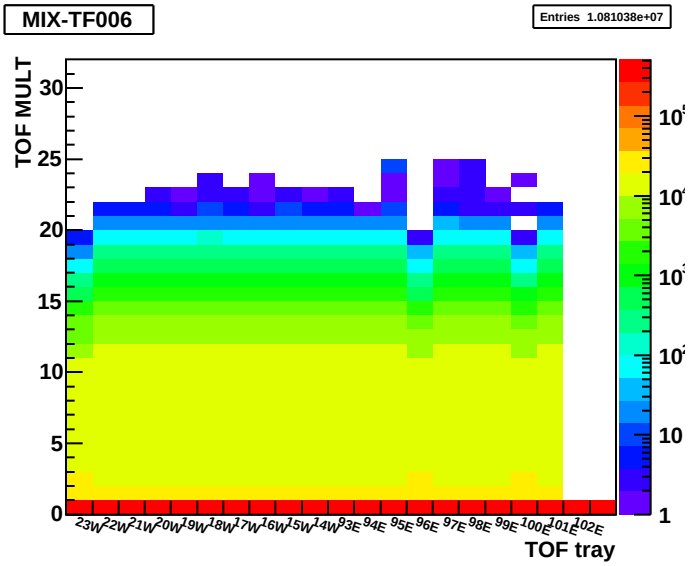

MIX-TF001
Entries 1.081038e+07
TOF MULT
TOF tray

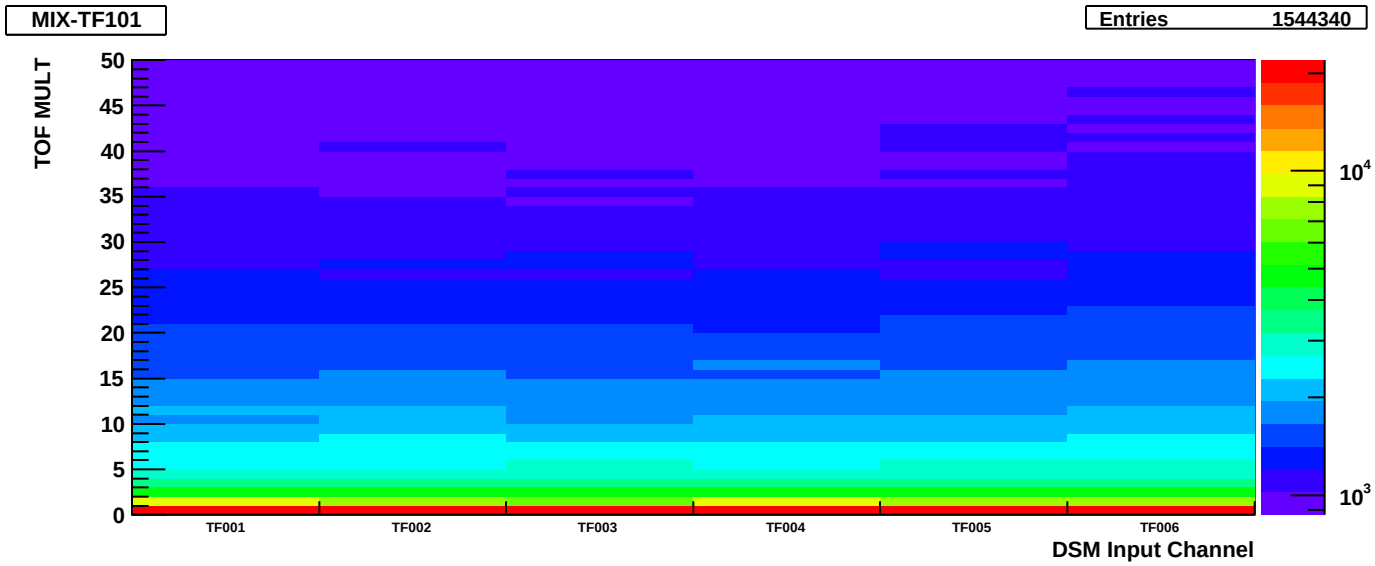

MIX-TF002
Entries 1.081038e+07
TOF MULT
TOF tray

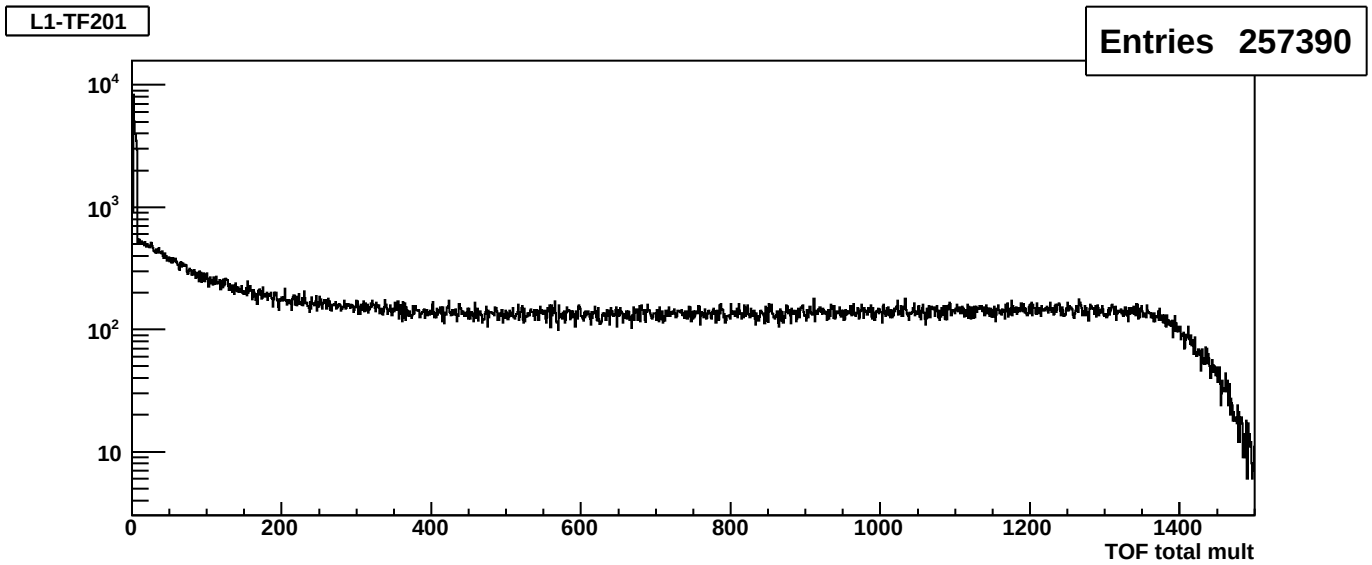

MIX-TF003
Entries 1.081038e+07
TOF MULT
TOF tray

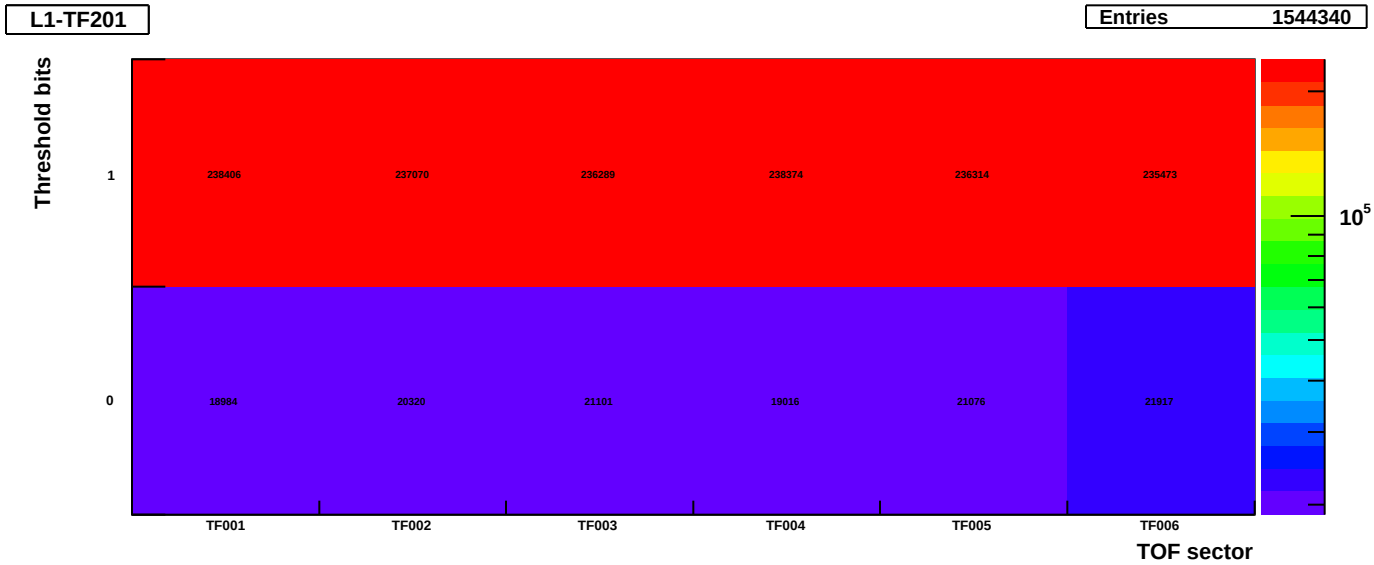

MIX-TF004
Entries 1.081038e+07
TOF MULT
TOF tray

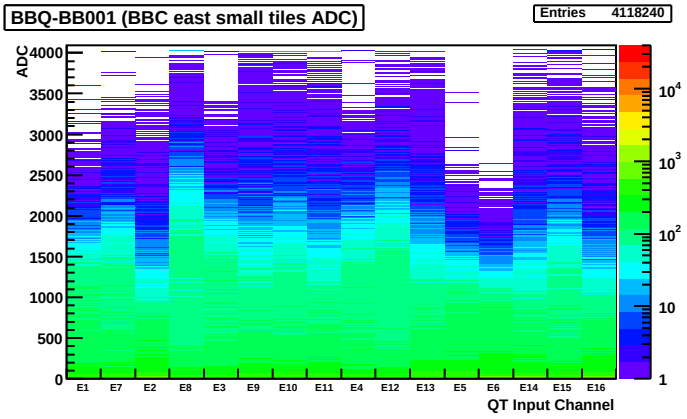

MIX-TF005
Entries 1.081038e+07
TOF MULT
TOF tray

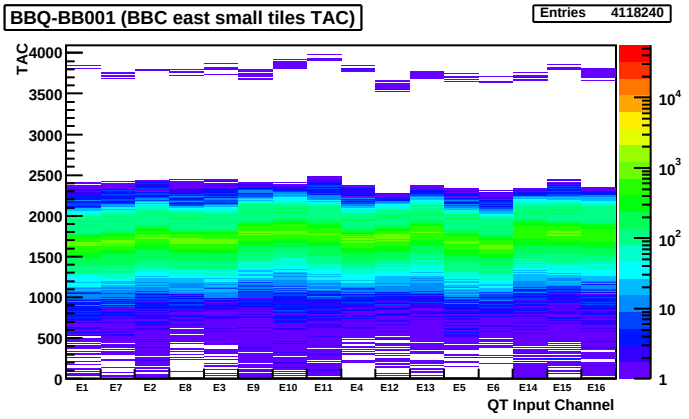

MIX-TF006
Entries 1.081038e+07
TOF MULT
TOF tray

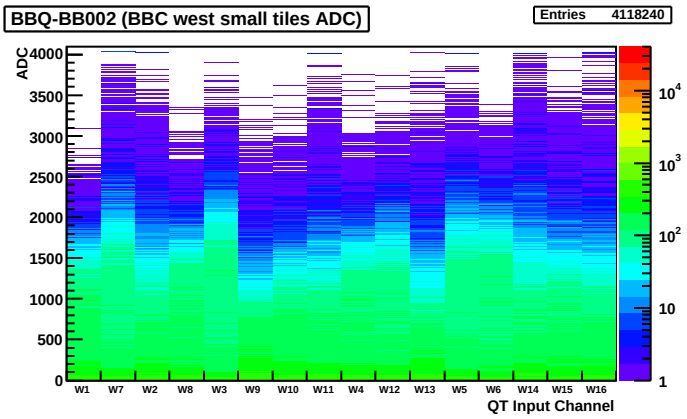

MIX-TF101
Entries 1544340
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3

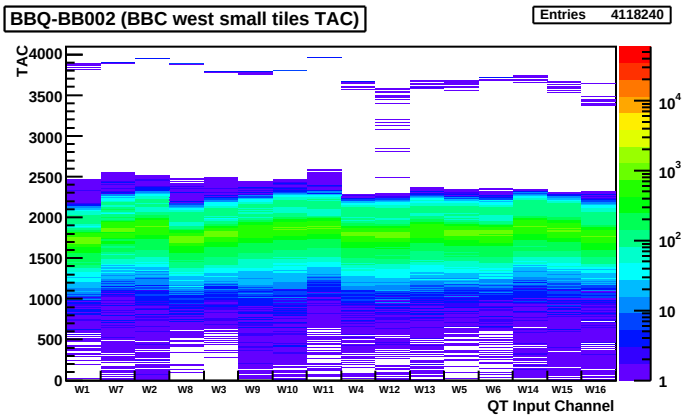

L1-TF201
Entries 257390
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

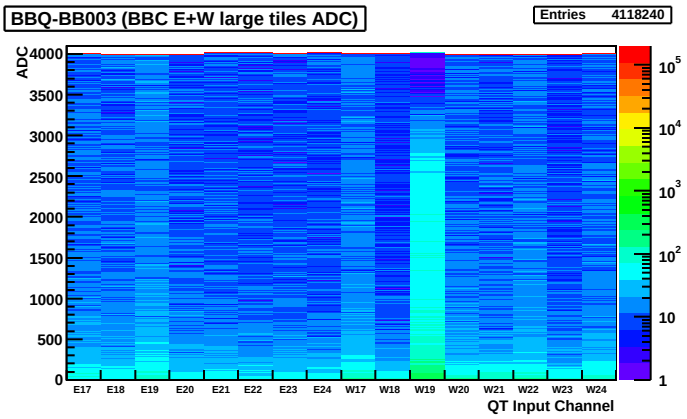

L1-TF201
Entries 1544340
Threshold bits
1
0
238406
237070
236289
238374
236314
235473
18984
20320
21101
19016
21076
21917
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

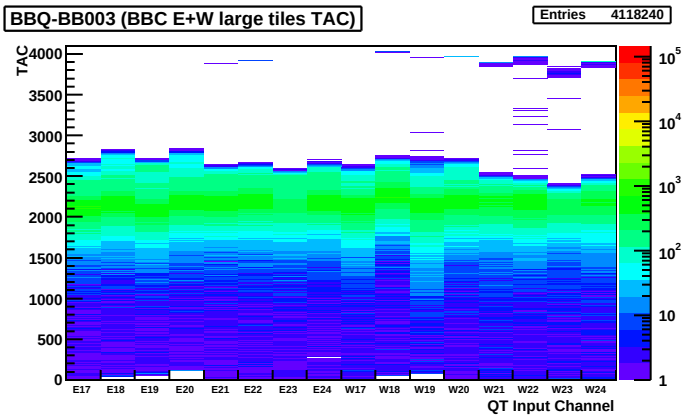

BBQ-BB001 (BBC east small tiles ADC)
Entries 4118240
ADC
QT Input Channel

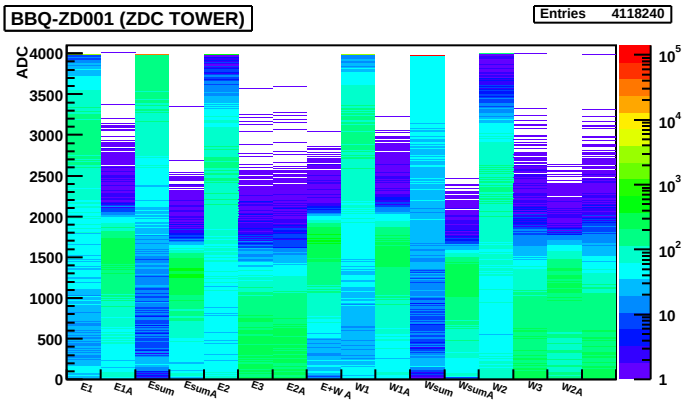

BBQ-BB001 (BBC east small tiles TAC)
Entries 4118240
TAC
QT Input Channel

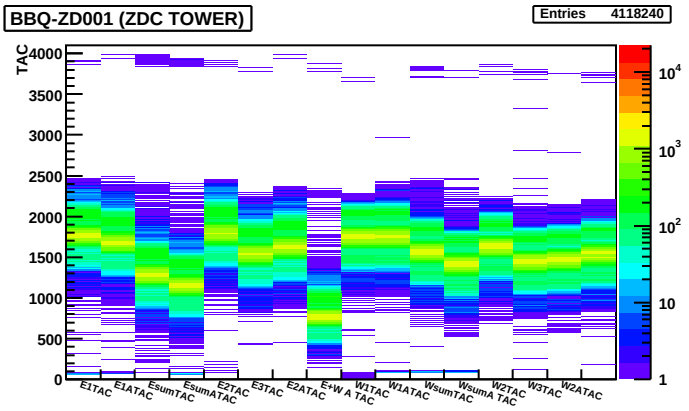

BBQ-BB002 (BBC west small tiles ADC)
Entries 4118240
ADC
QT Input Channel

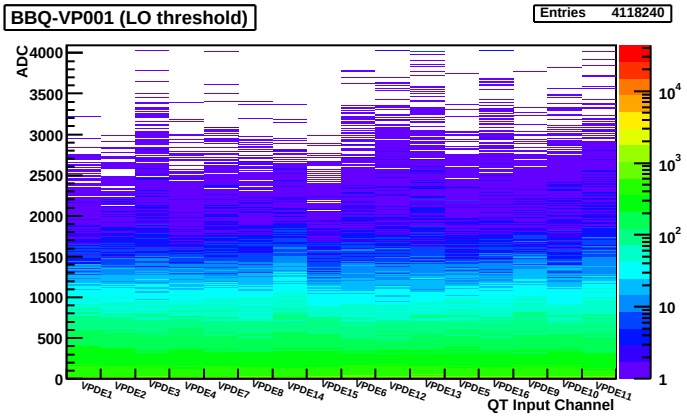

BBQ-BB002 (BBC west small tiles TAC)
Entries 4118240
TAC
QT Input Channel

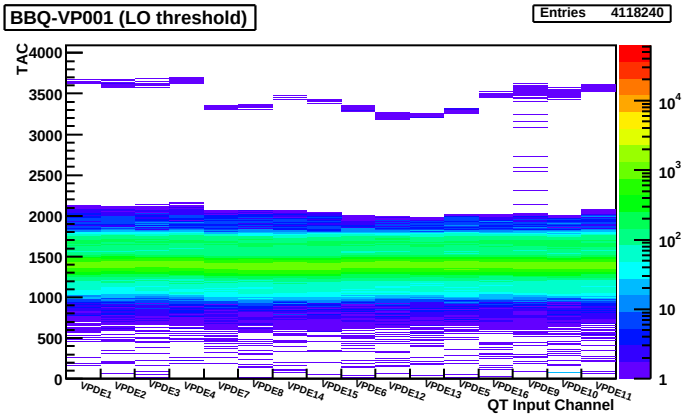

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4118240
ADC
QT Input Channel

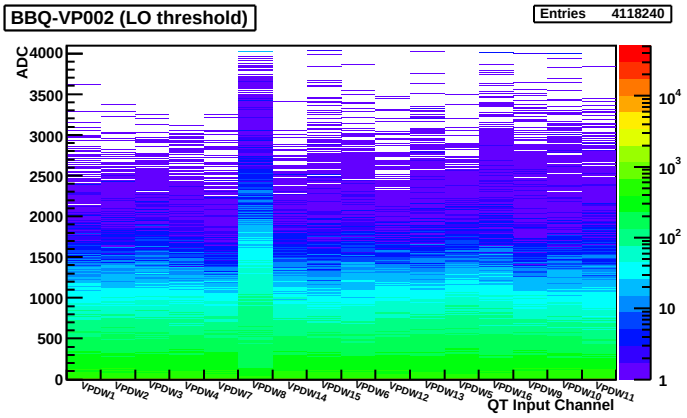

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4118240
TAC
QT Input Channel

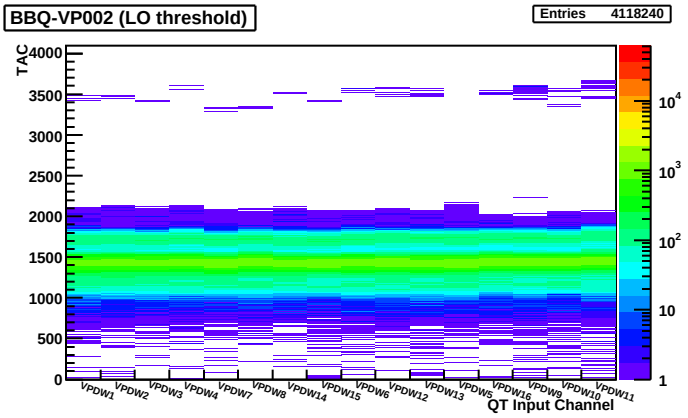

BBQ-ZD001 (ZDC TOWER)
Entries 4118240
ADC

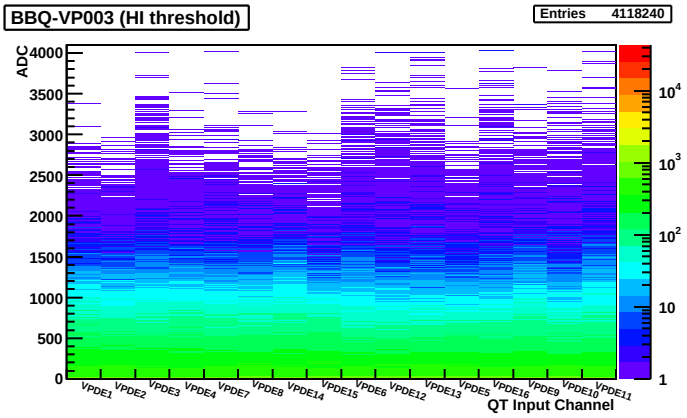

BBQ-ZD001 (ZDC TOWER)
Entries 4118240
TAC

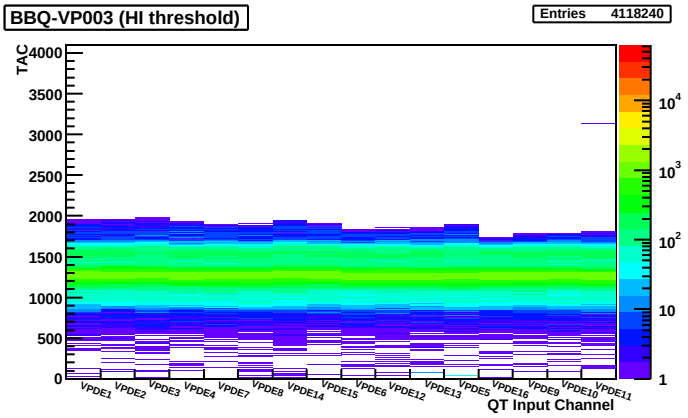

BBQ-VP001 (LO threshold)
Entries 4118240
ADC
QT Input Channel

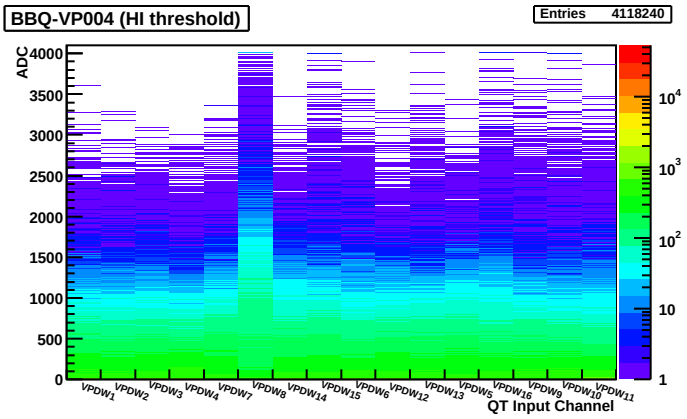

BBQ-VP001 (LO threshold)
Entries 4118240
TAC
QT Input Channel

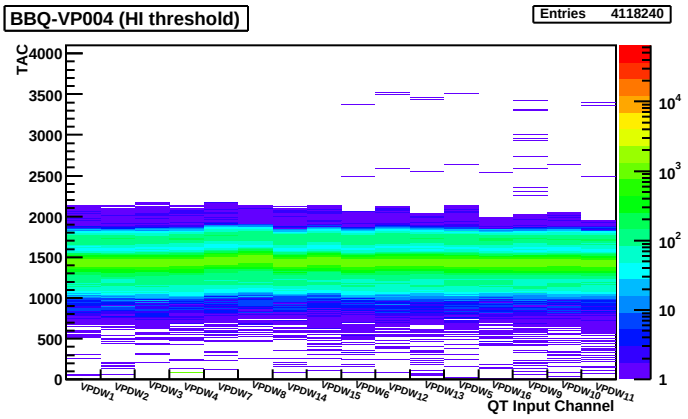

BBQ-VP002 (LO threshold)
Entries 4118240
ADC
QT Input Channel

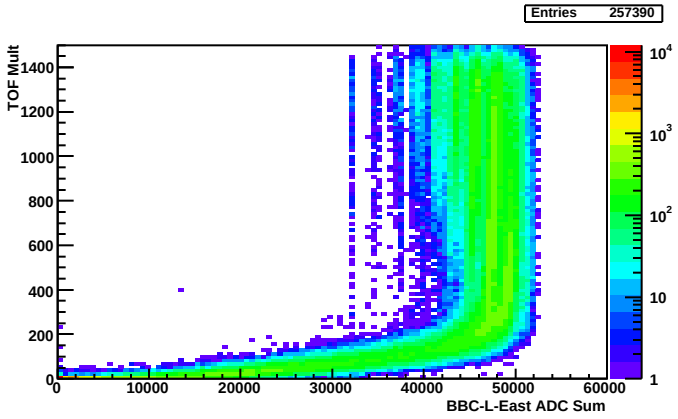

BBQ-VP002 (LO threshold)
Entries 4118240
TAC
QT Input Channel

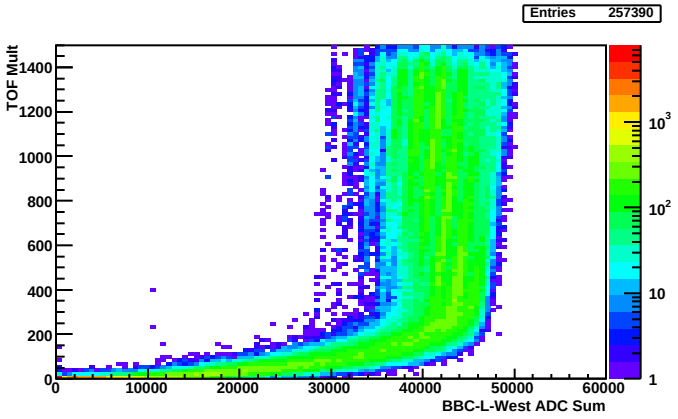

BBQ-VP003 (HI threshold)
Entries 4118240
ADC
QT Input Channel

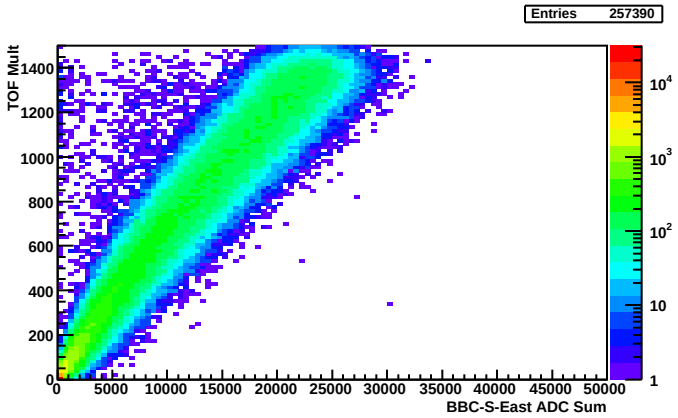

BBQ-VP003 (HI threshold)
Entries 4118240
TAC
QT Input Channel

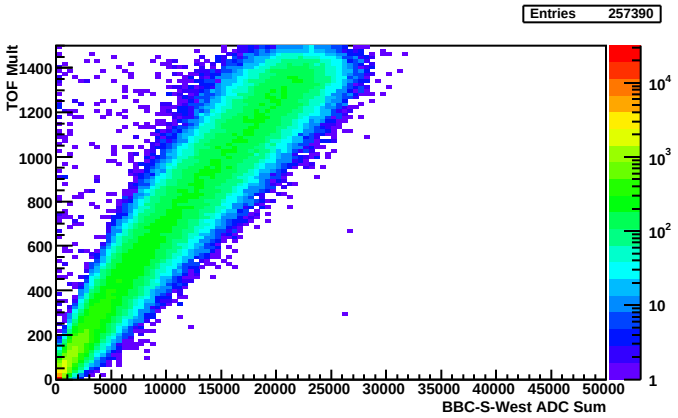

BBQ-VP004 (HI threshold)
Entries 4118240
ADC
QT Input Channel

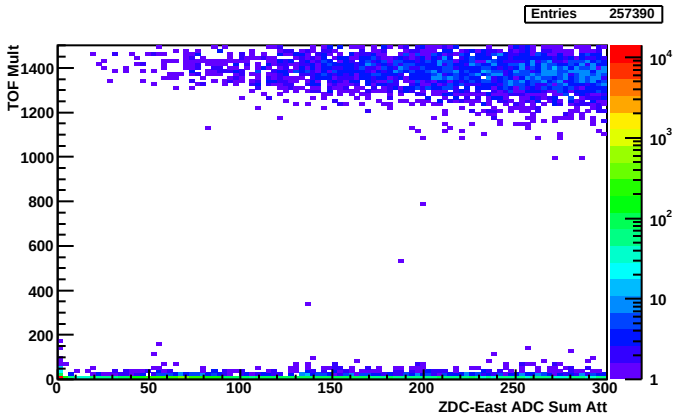

BBQ-VP004 (HI threshold)
Entries 4118240
TAC
QT Input Channel

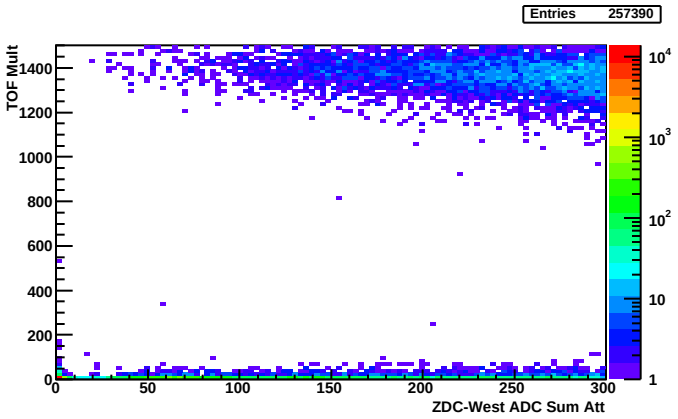

Entries 257390
TOF Mult
BBC-L-East ADC Sum

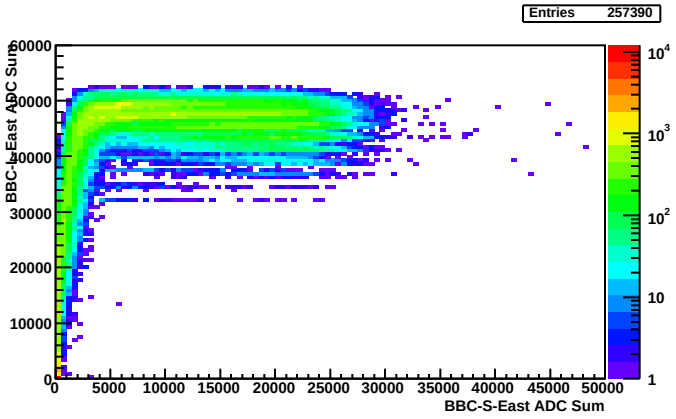

Entries 257390
TOF Mult
BBC-L-West ADC Sum

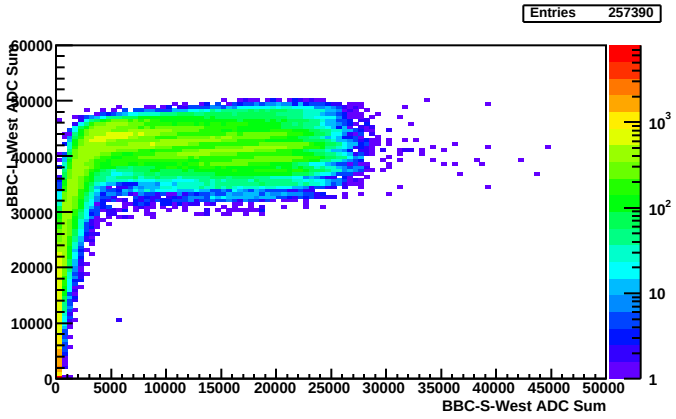

Entries 257390
TOF Mult
BBC-S-East ADC Sum

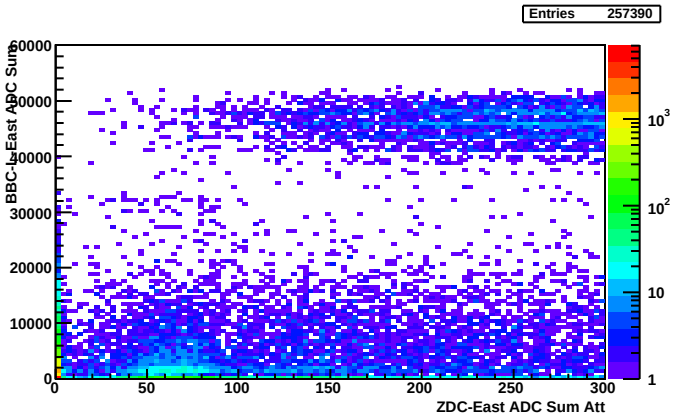

Entries 257390
TOF Mult
BBC-S-West ADC Sum

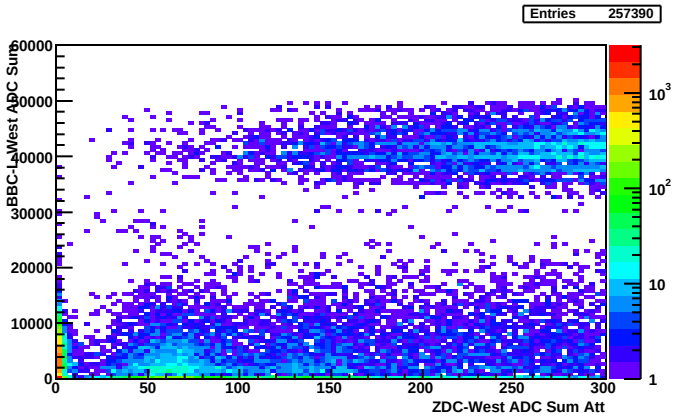

Entries 257390
TOF Mult
ZDC-East ADC Sum Att

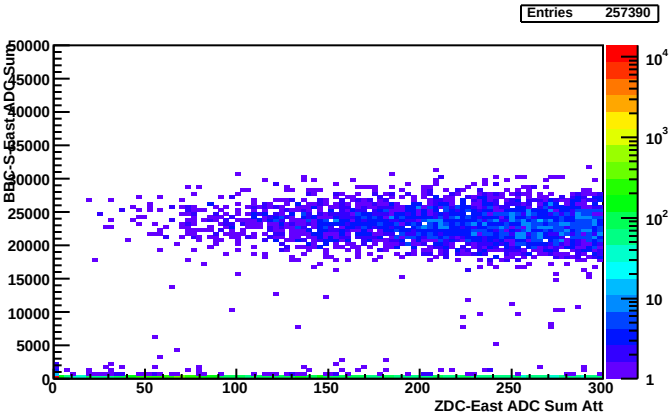

Entries 257390
TOF Mult
ZDC-West ADC Sum Att

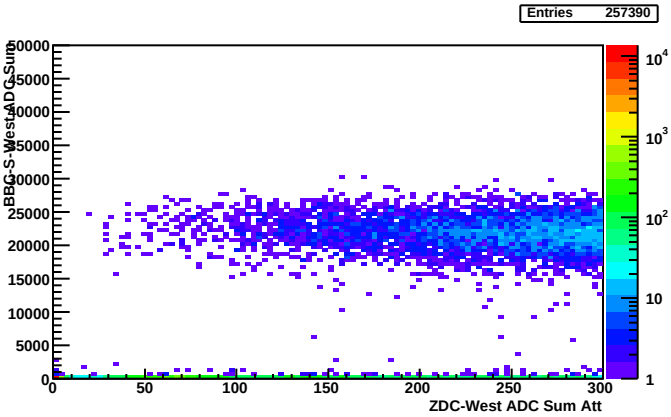

Entries 257390
BBC-L-East ADC Sum
BBC-S-East ADC Sum

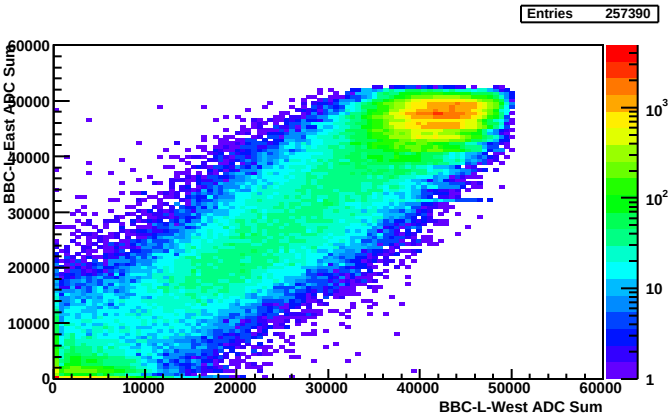

Entries 257390
BBC-L-West ADC Sum
BBC-S-West ADC Sum

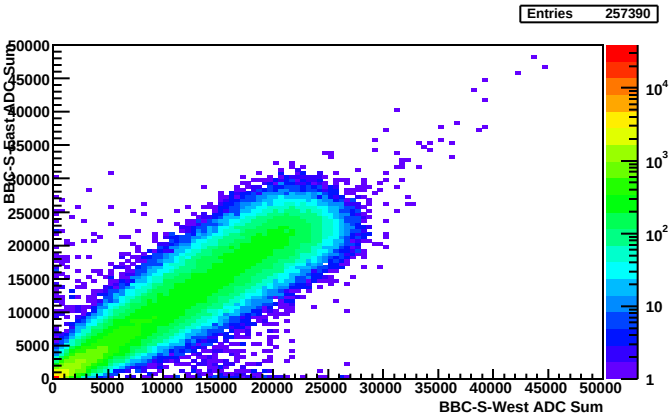

Entries 257390
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

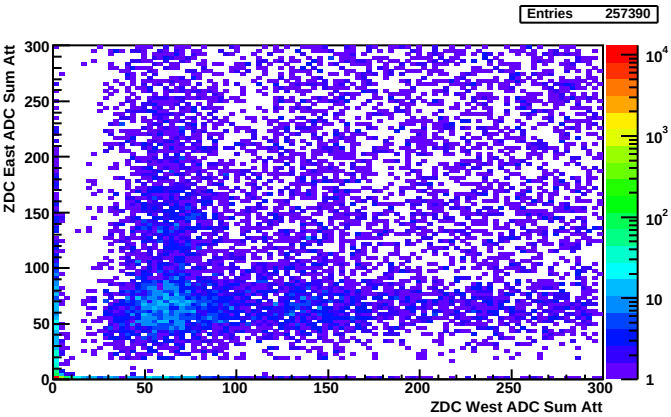

Entries 257390
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

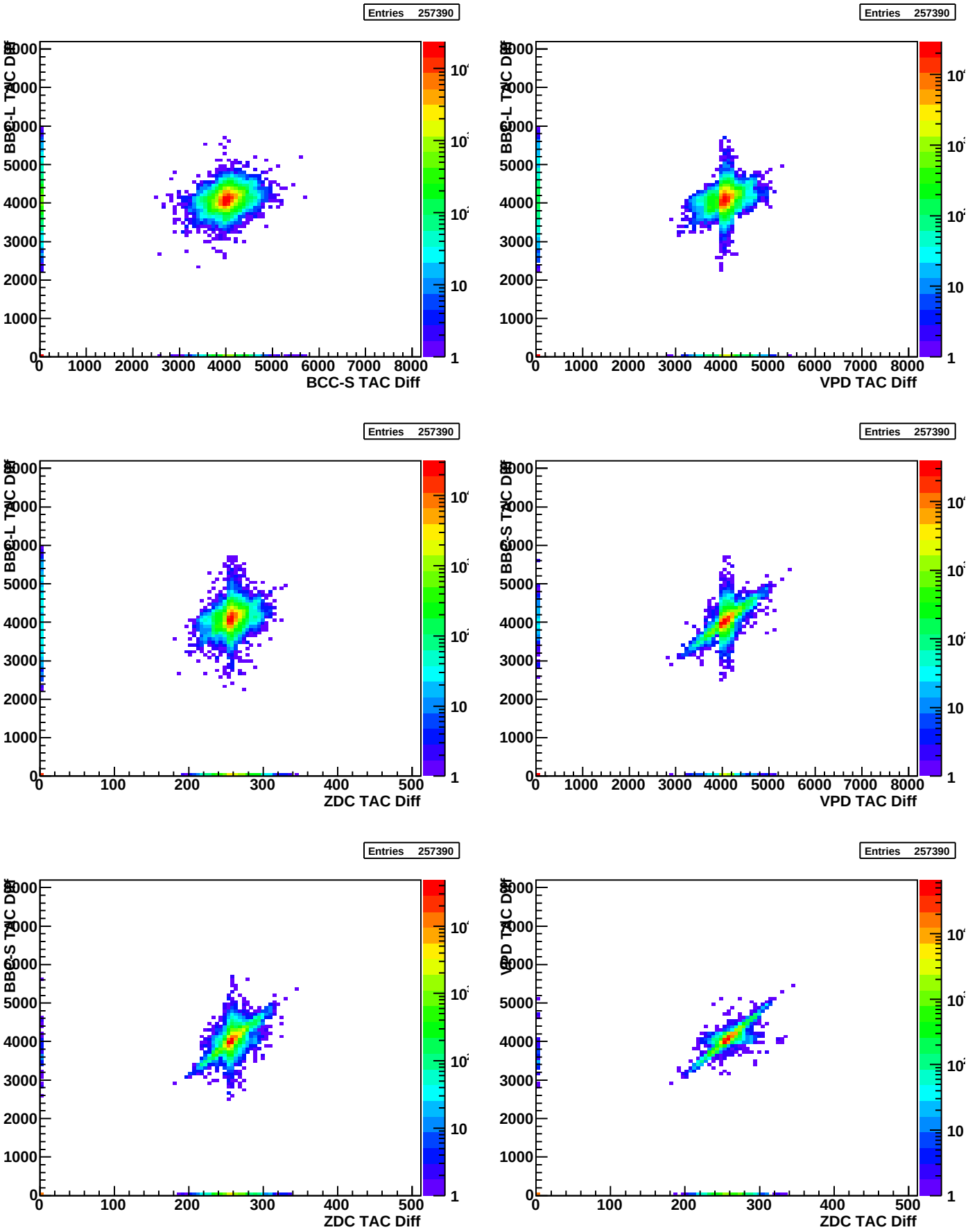

Entries 257390
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

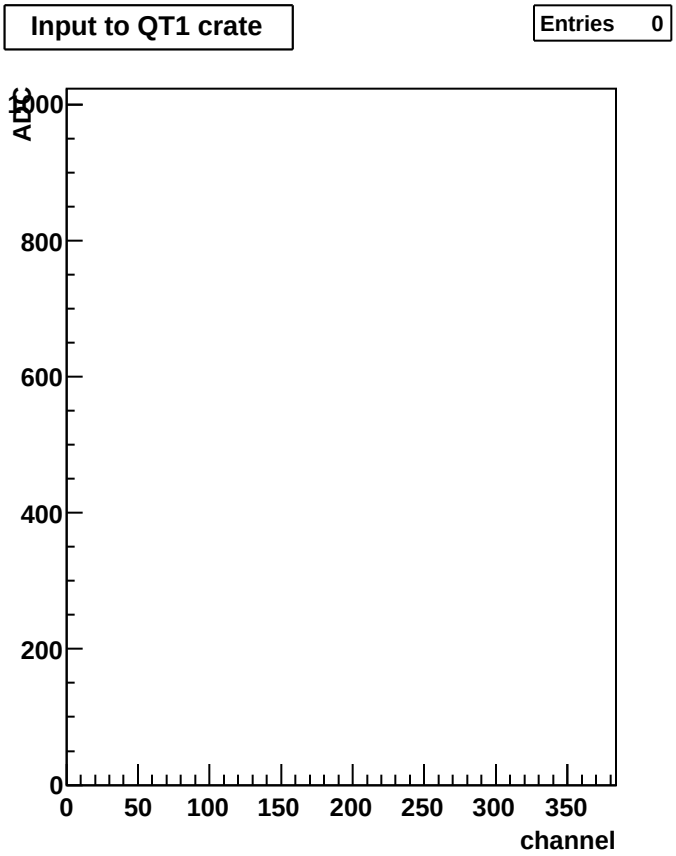

Entries 257390
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

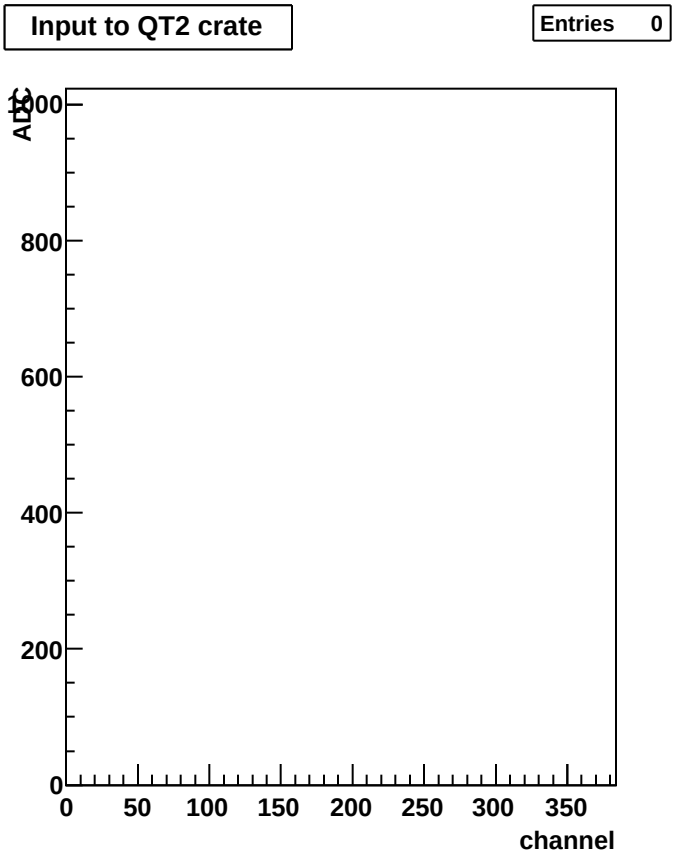

Entries 257390
BBC-L-East ADC Sum
BBC-L-West ADC Sum

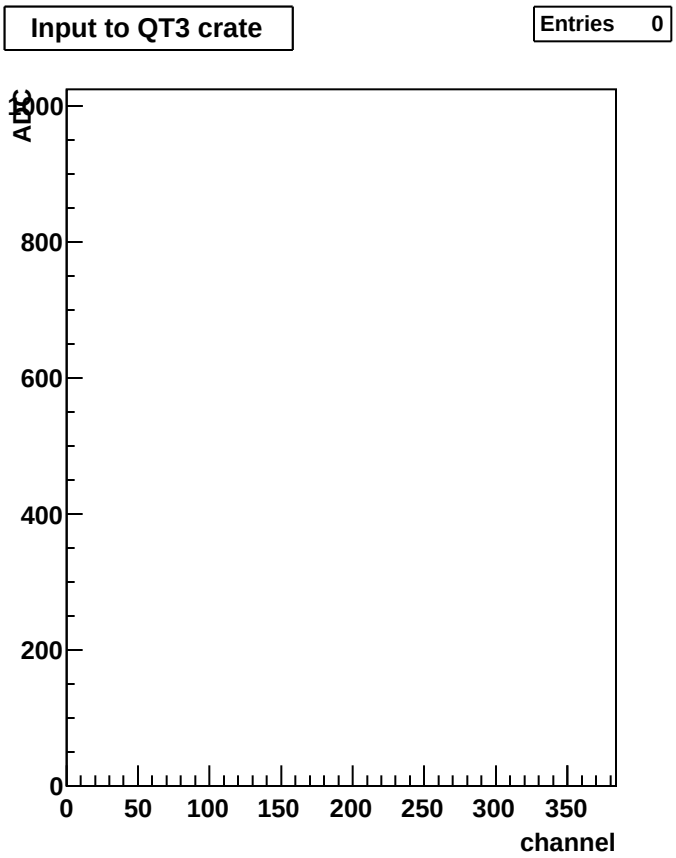

Entries 257390
BBC-S-East ADC Sum
BBC-S-West ADC Sum

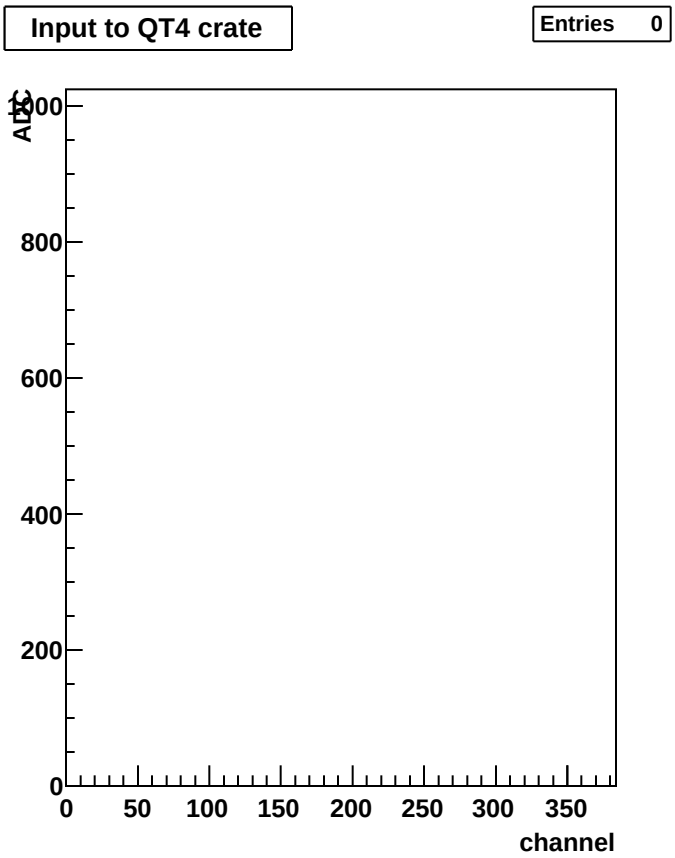

Entries 257390
ZDC East ADC Sum Att
ZDC West ADC Sum Att

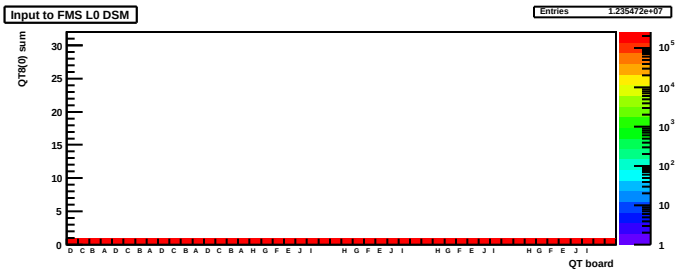

Entries 257390
BBC-L TAC Diff
BCC-S TAC Diff
Entries 257390
BBC-L TAC Diff
VPD TAC Diff
Entries 257390
BBC-L TAC Diff
ZDC TAC Diff
Entries 257390
BBC-S TAC Diff
VPD TAC Diff
Entries 257390
BBC-S TAC Diff
ZDC TAC Diff
Entries 257390
VPD TAC Diff
ZDC TAC Diff

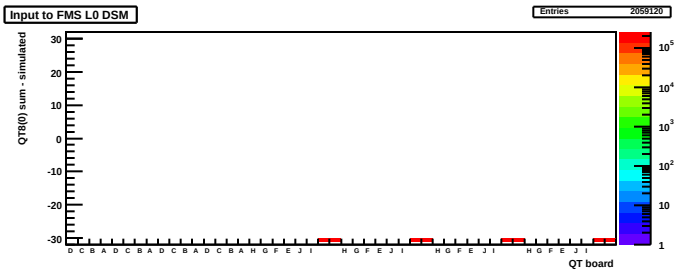

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

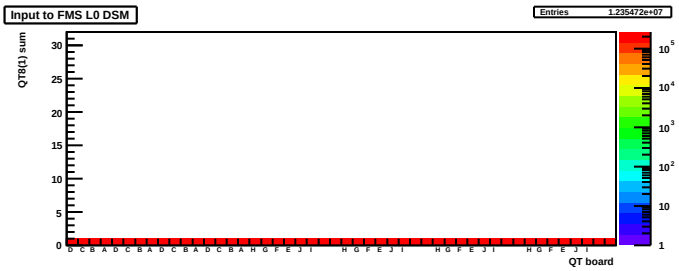

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

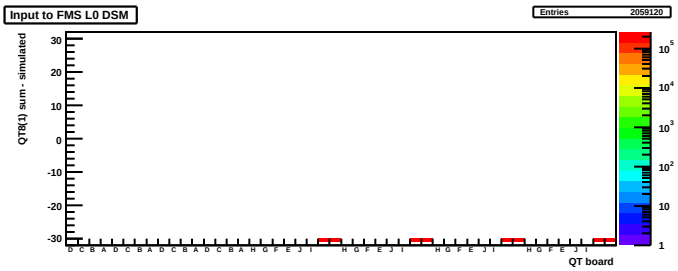

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

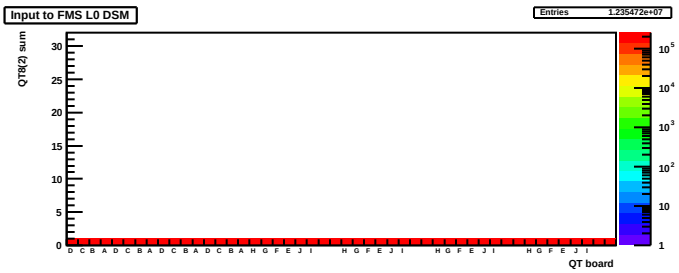

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

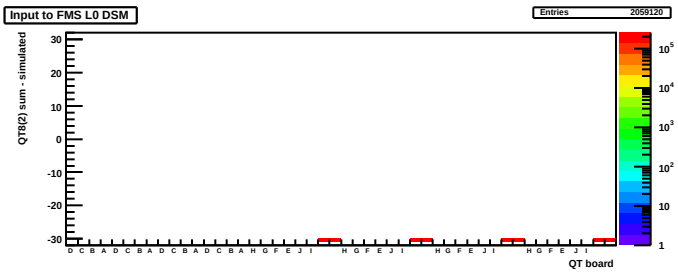

Input to FMS L0 DSM
Entries 1.235472e+07
QT8(0) sum
QT board

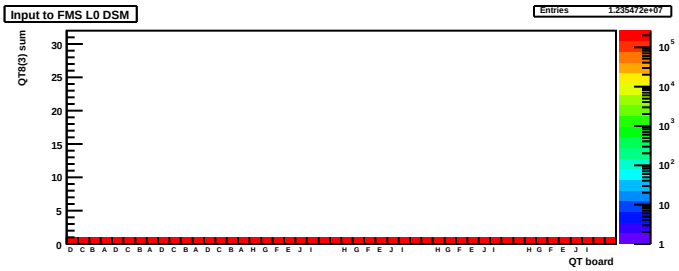

Input to FMS L0 DSM
Entries 2059120
QT8(0) sum - simulated
QT board

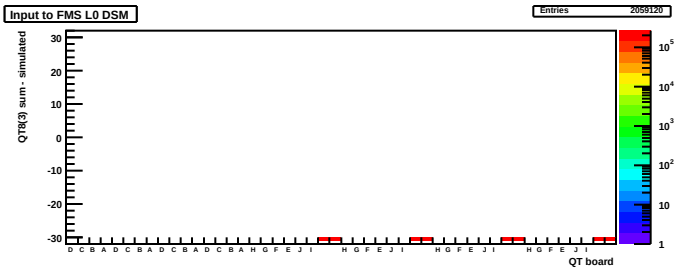

Input to FMS L0 DSM
Entries 1.235472e+07
QT8(1) sum
QT board

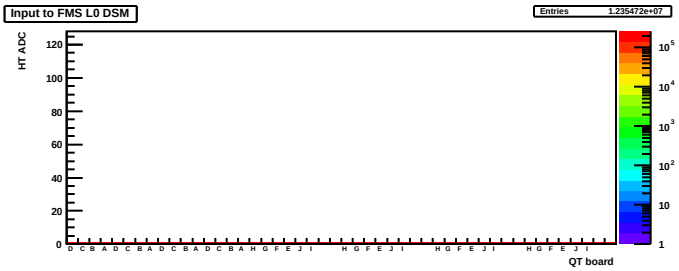

Input to FMS L0 DSM
Entries 2059120
QT8(1) sum - simulated
QT board

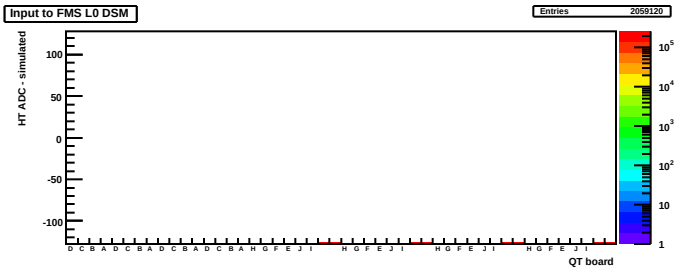

Input to FMS L0 DSM
Entries 1.235472e+07
QT8(2) sum
QT board

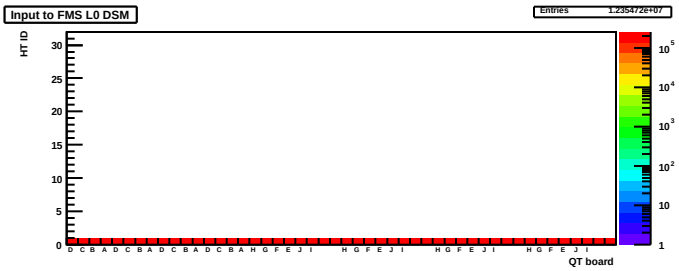

Input to FMS L0 DSM
Entries 2059120
QT8(2) sum - simulated
QT board

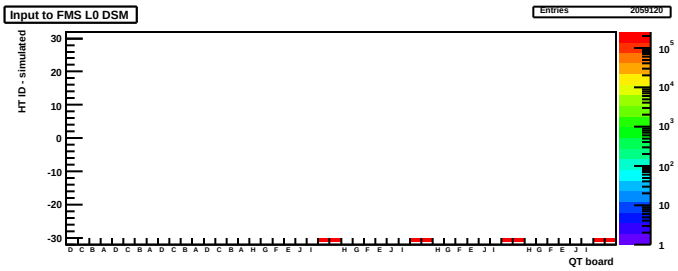

Input to FMS L0 DSM
Entries 1.235472e+07
QT8(3) sum
QT board

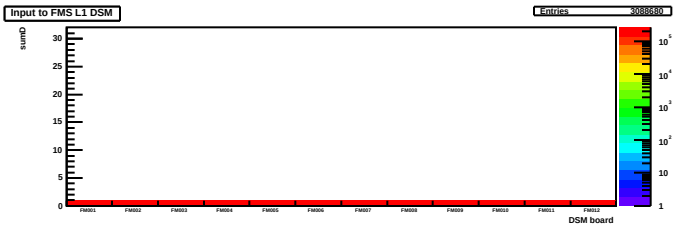

Input to FMS L0 DSM
Entries 2059120
QT8(3) sum - simulated
QT board

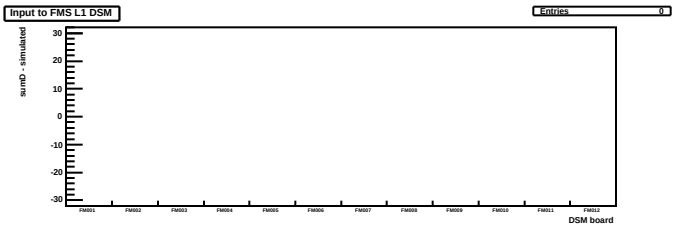

Input to FMS L0 DSM
Entries 1.235472e+07
HT ADC
QT board

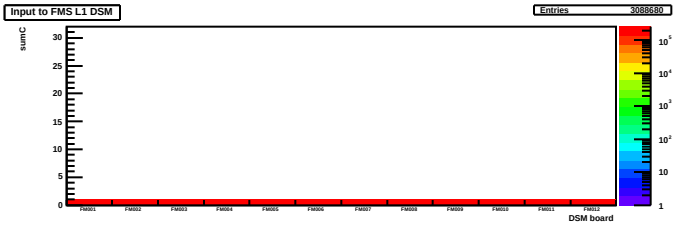

Input to FMS L0 DSM
Entries 2059120
HT ADC - simulated
QT board

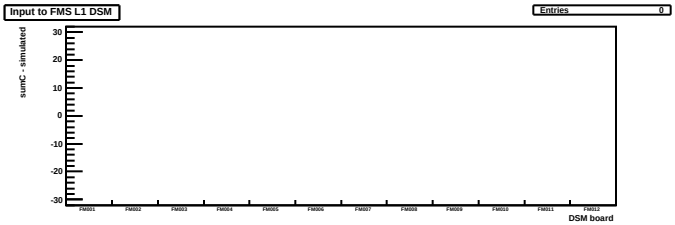

Input to FMS L0 DSM
Entries 1.235472e+07
HT ID
QT board

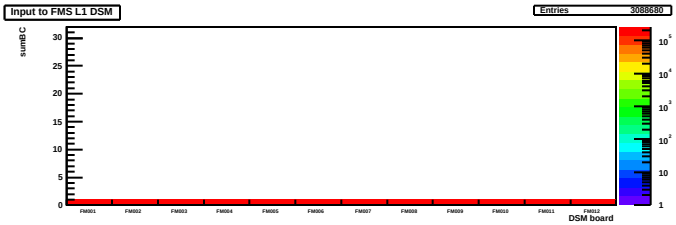

Input to FMS L0 DSM
Entries 2059120
HT ID - simulated
QT board

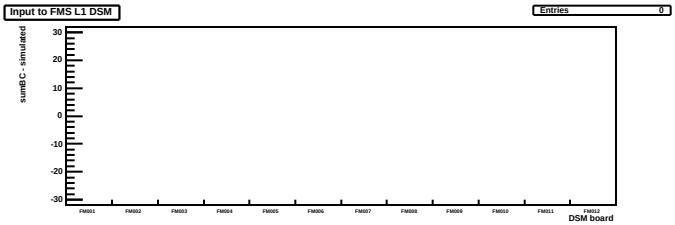

Input to FMS L1 DSM
Entries 3088680
sumD
DSM board

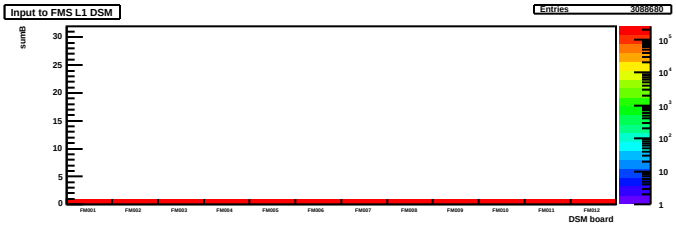

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

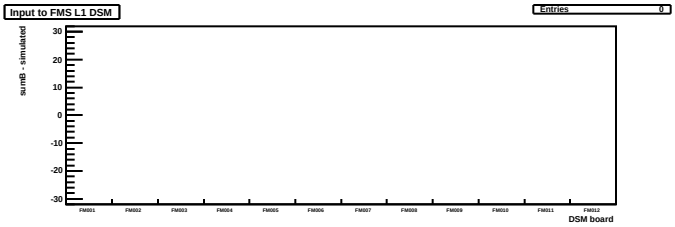

Input to FMS L1 DSM
Entries 3088680
sumC
DSM board

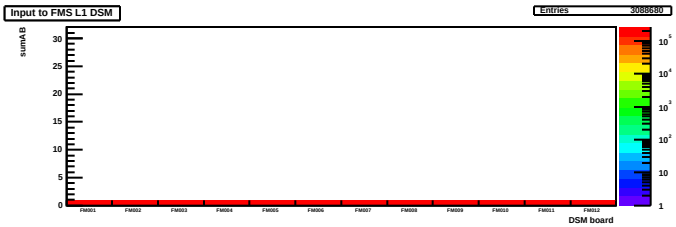

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

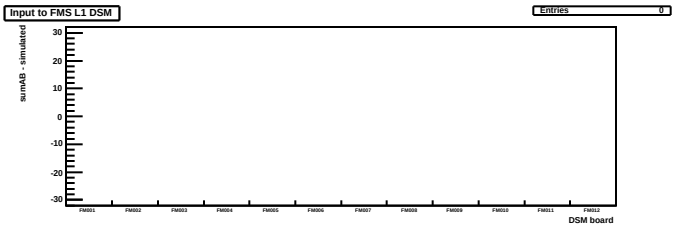

Input to FMS L1 DSM
Entries 3088680
sumBC
DSM board

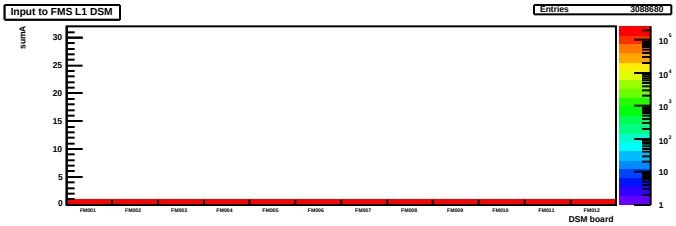

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

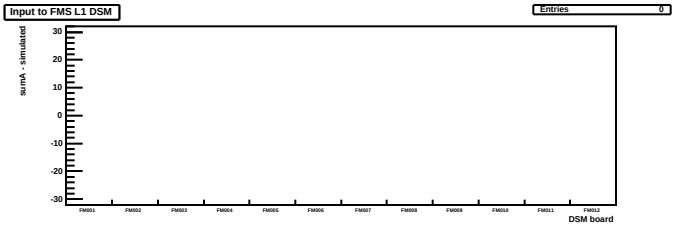

Input to FMS L1 DSM
Entries 3088680
sumB
DSM board

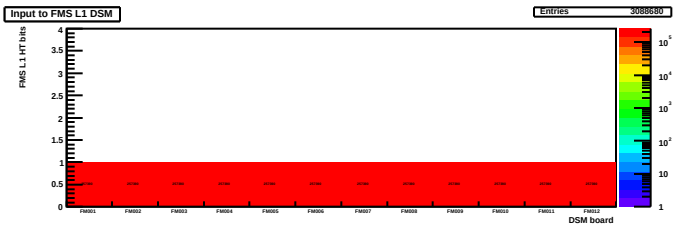

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

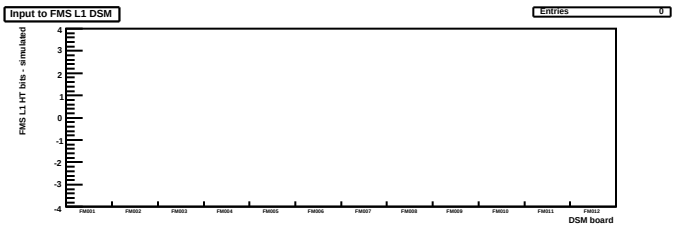

Input to FMS L1 DSM
Entries 3088680
sumAB
DSM board

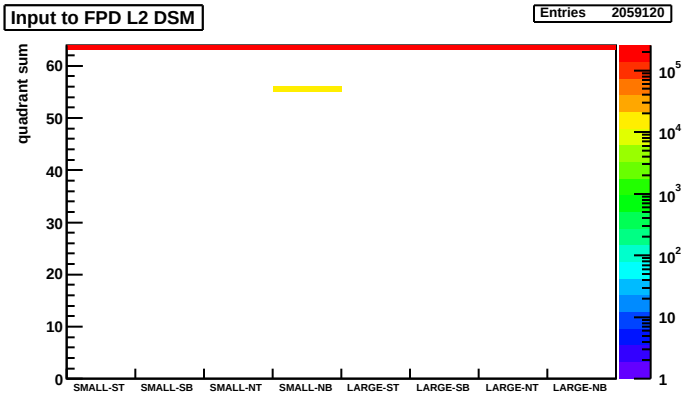

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

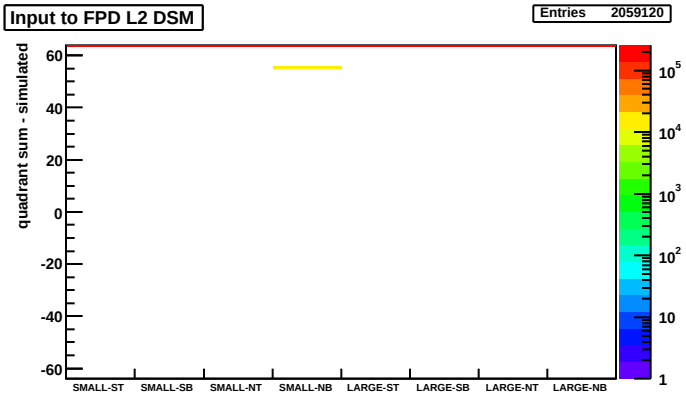

Input to FMS L1 DSM
Entries 3088680
sumA
DSM board

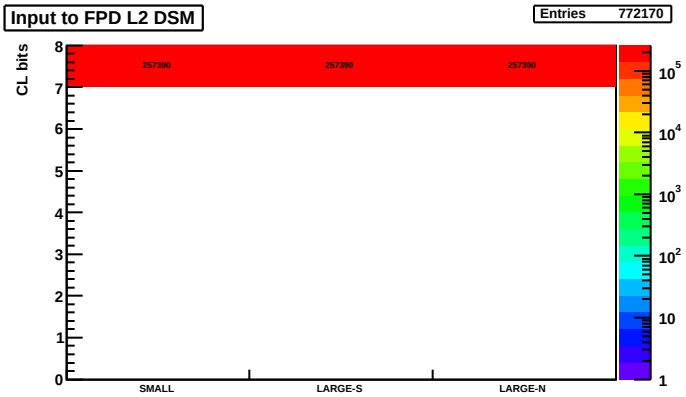

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

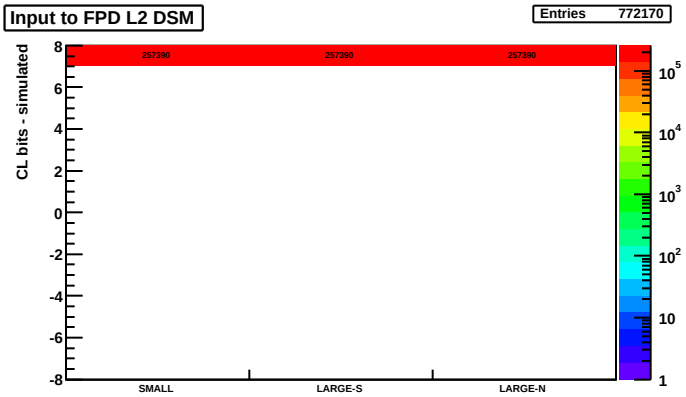

Input to FMS L1 DSM
Entries 3088680
FMS L1 HT bits
DSM board

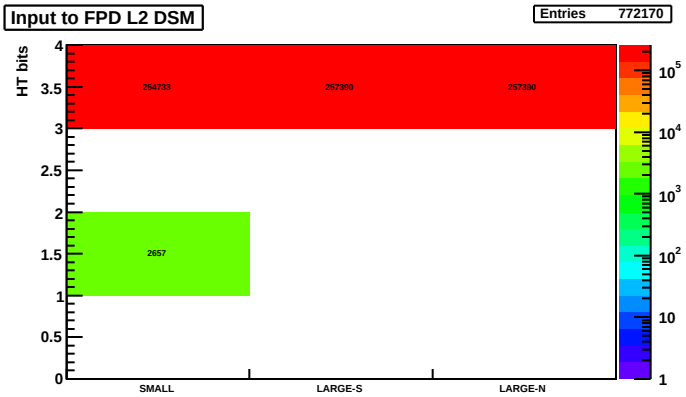

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

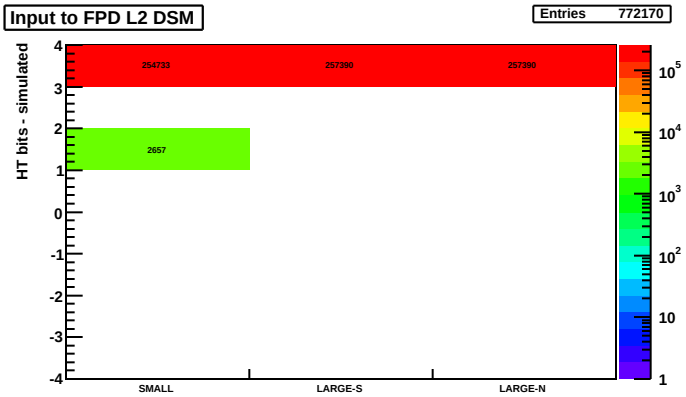

Input to FPD L2 DSM
Entries 2059120
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

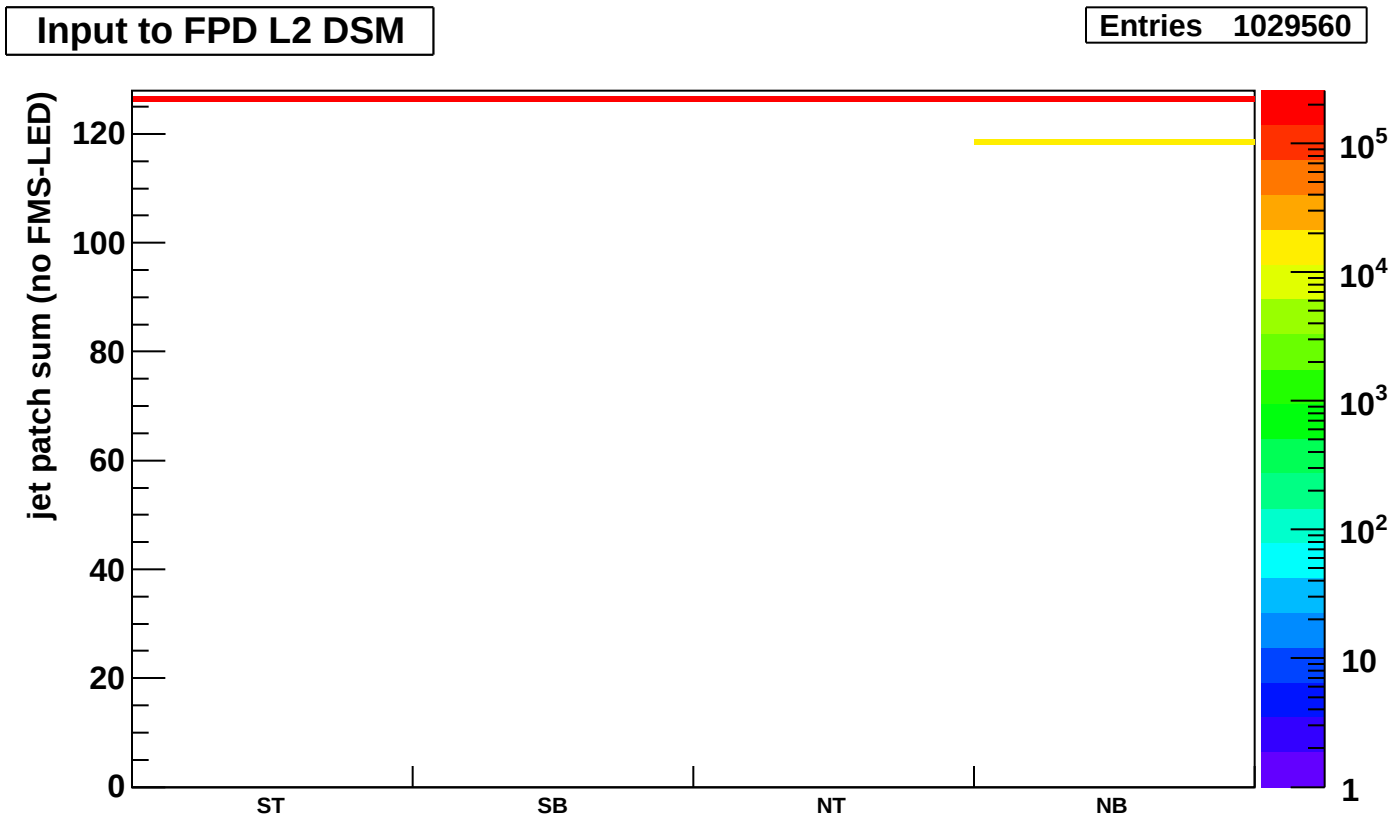

Input to FPD L2 DSM
Entries 2059120
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

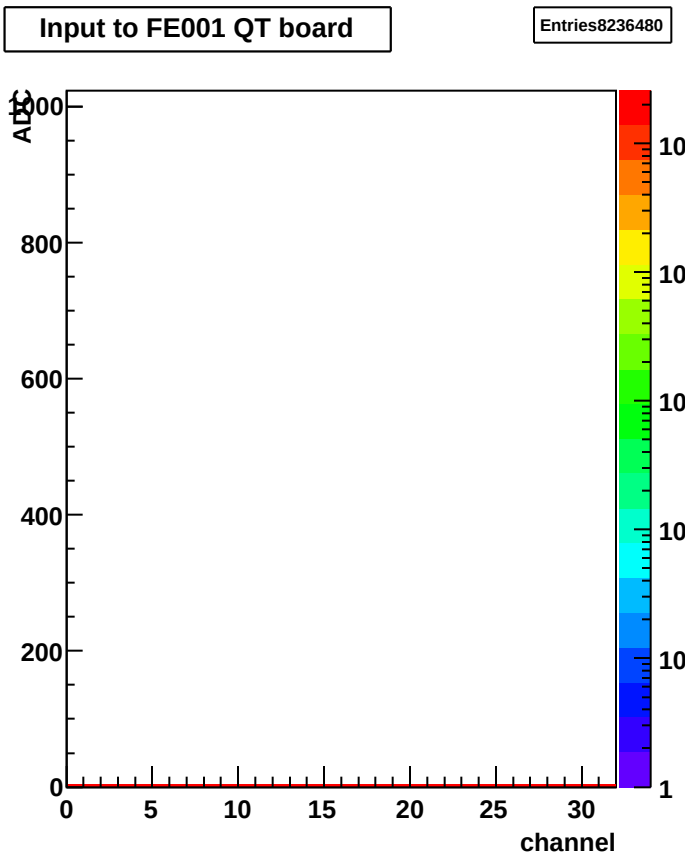

Input to FPD L2 DSM
Entries 772170
CL bits
257390
257390
257390
SMALL
LARGE-S
LARGE-N

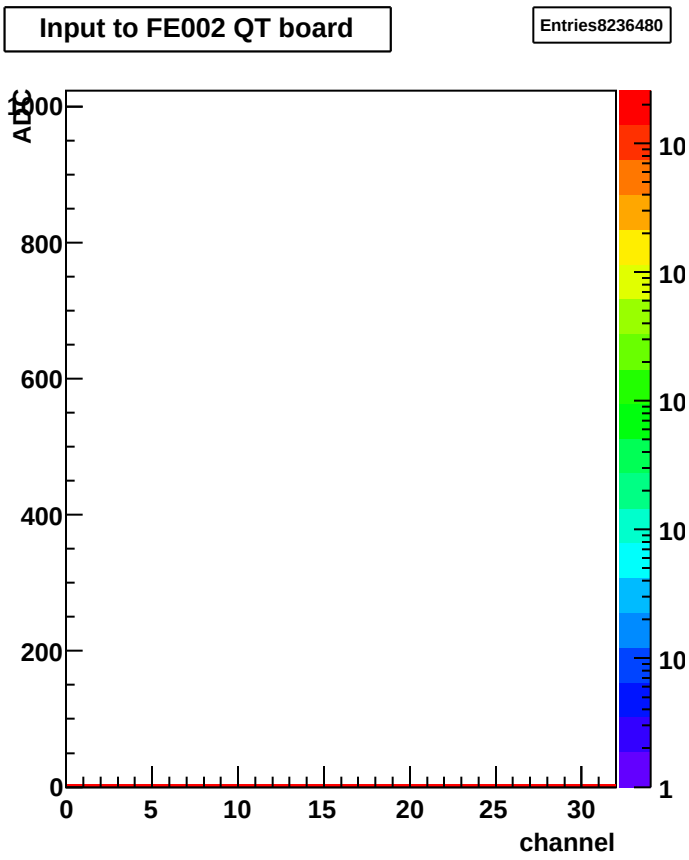

Input to FPD L2 DSM
Entries 772170
CL bits - simulated
257390
257390
257390
SMALL
LARGE-S
LARGE-N

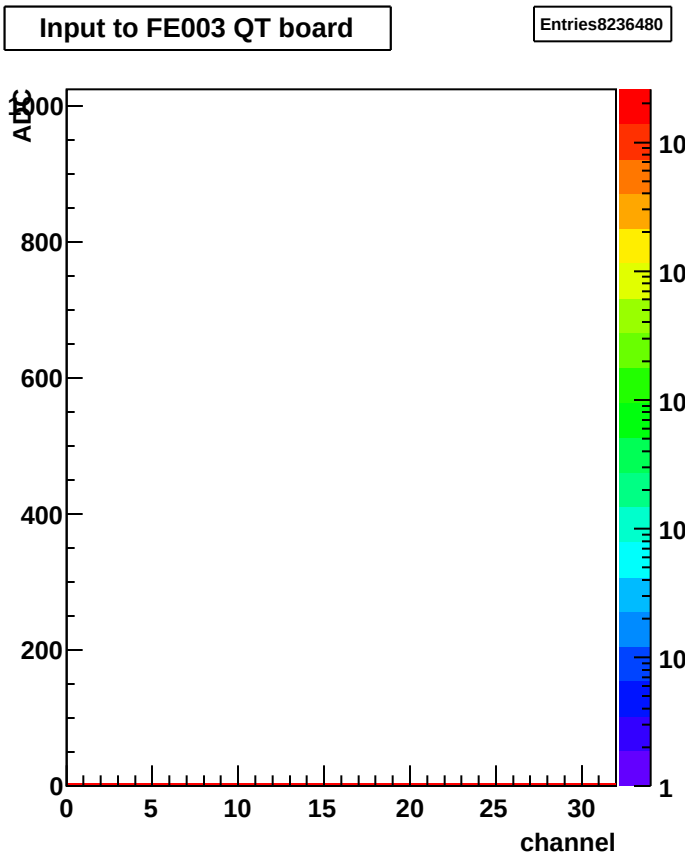

Input to FPD L2 DSM
Entries 772170
HT bits
254733
257390
257390
2657
SMALL
LARGE-S
LARGE-N

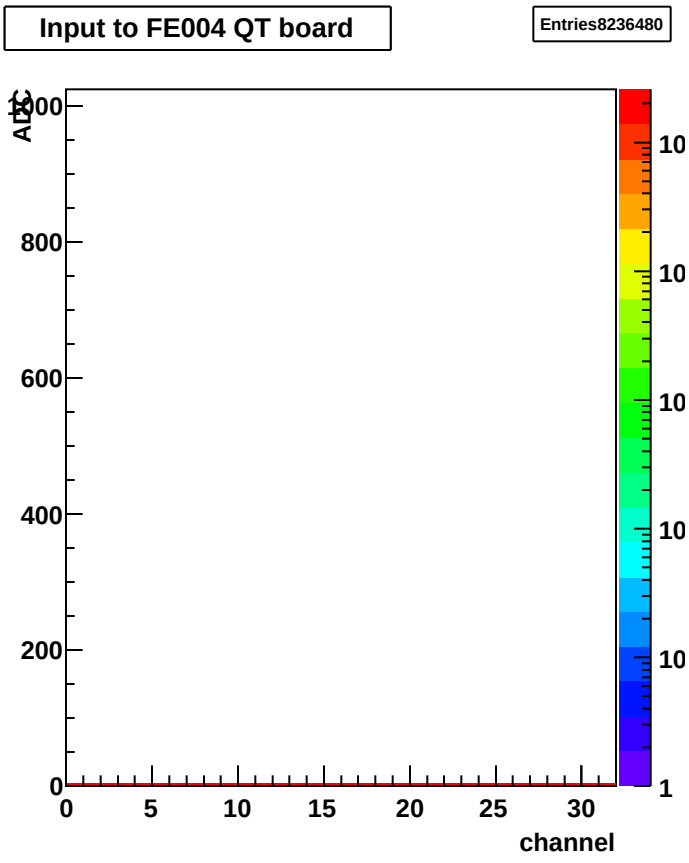

Input to FPD L2 DSM
Entries 772170
HT bits - simulated
254733
257390
257390
2657
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1029560
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

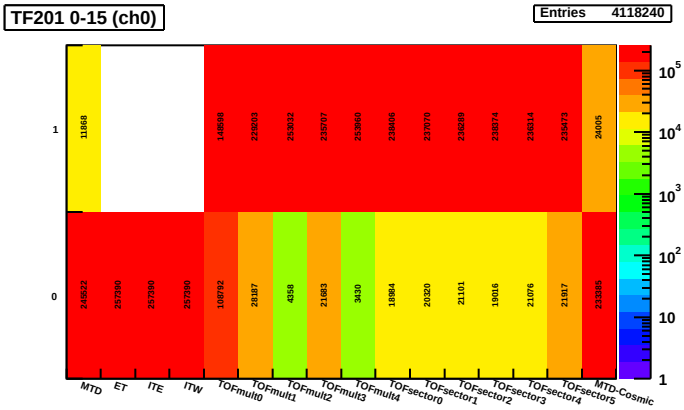

Input to FE001 QT board
Entries8236480
ADC
channel


Input to FE002 QT board
Entries8236480
ADC
channel

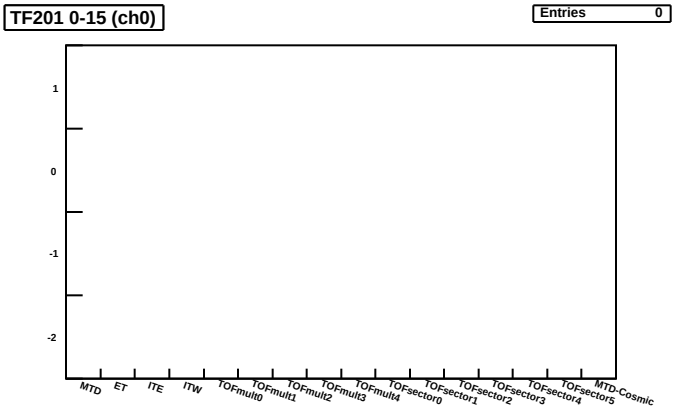

Input to FE003 QT board
Entries8236480
ADC
channel


Input to FE004 QT board
Entries8236480
ADC
channel

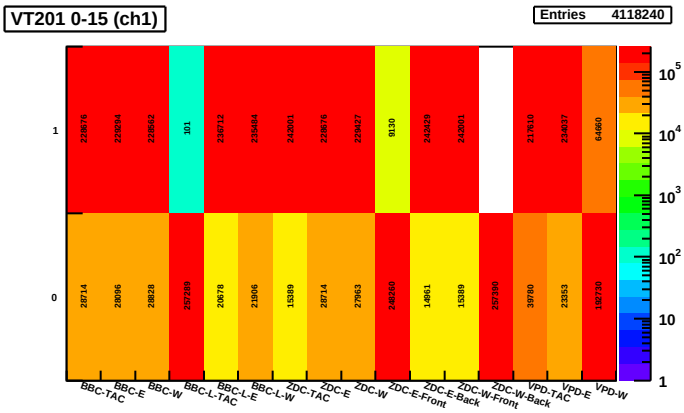
TF201 0-15 (ch0)



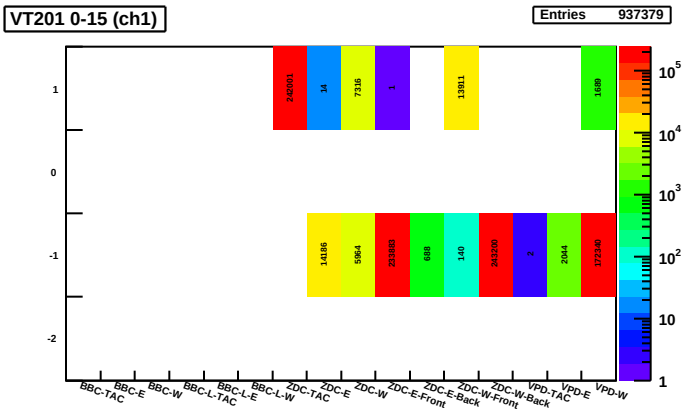
TF201 0-15 (ch0)



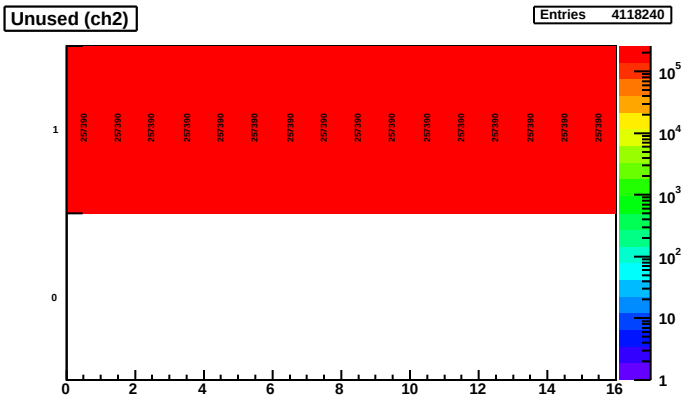
VT201 0-15 (ch1)

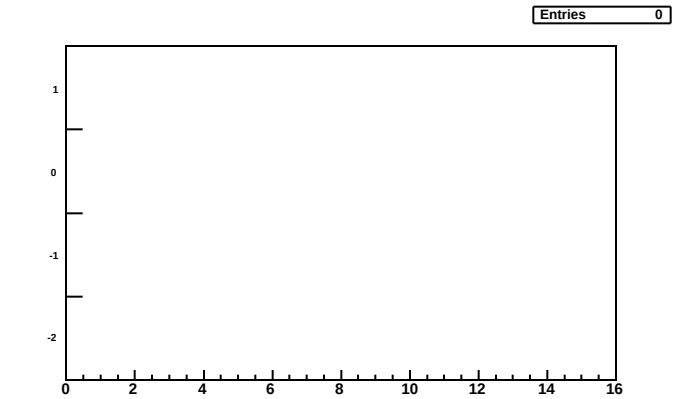


VT201 0-15 (ch1)

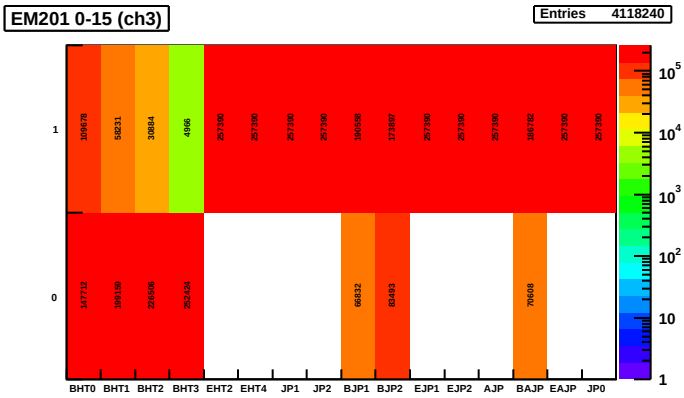


Unused (ch2)

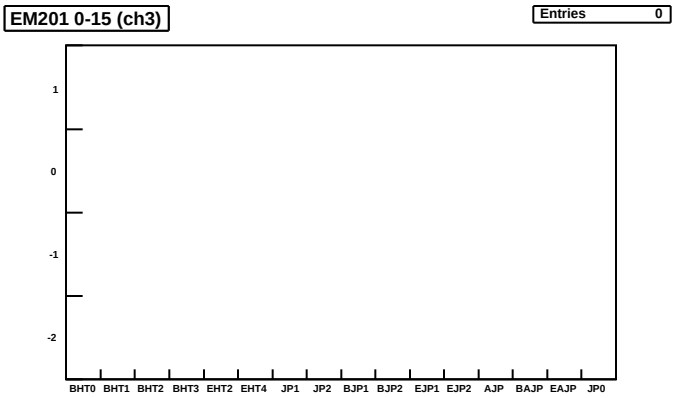




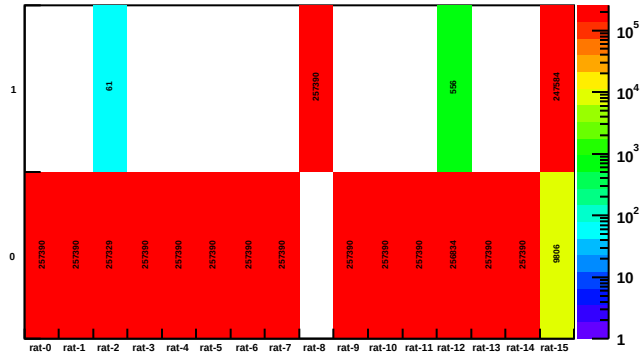
EM201 0-15 (ch3)

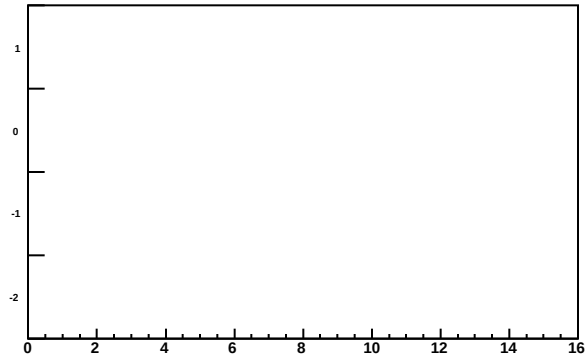


EM201 0-15 (ch3)

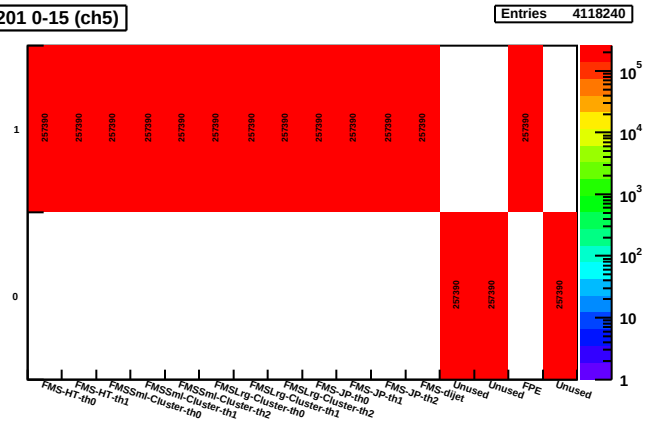


RAT board (ch4)

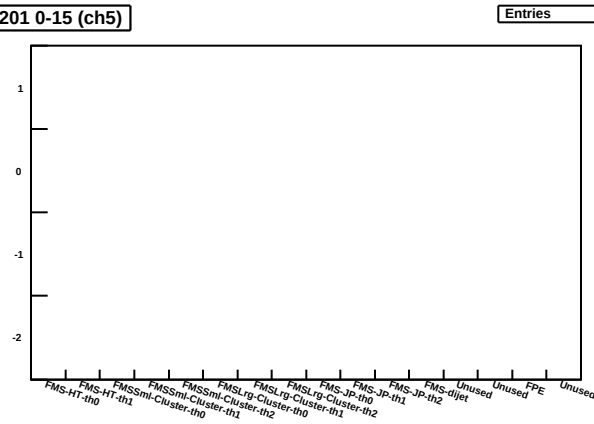




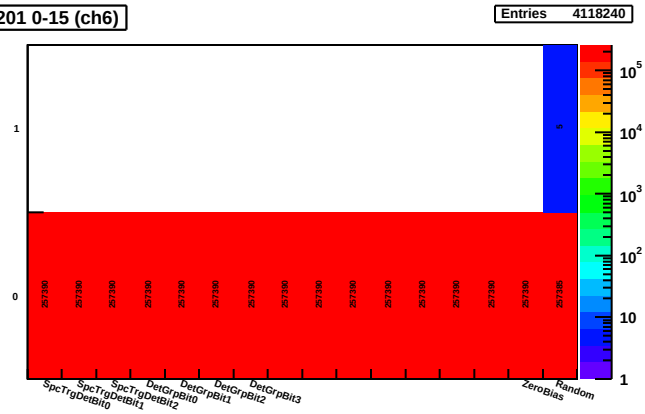
FP201 0-15 (ch5)

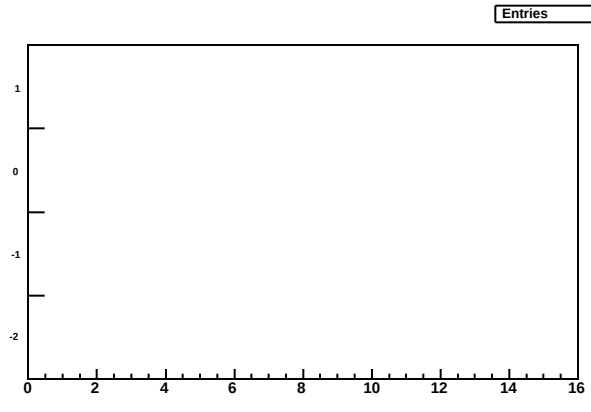
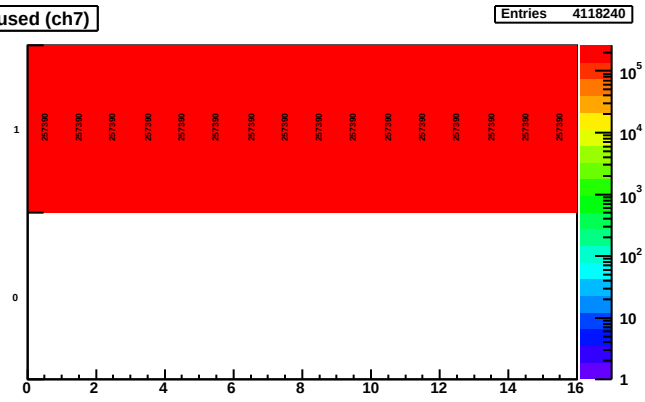
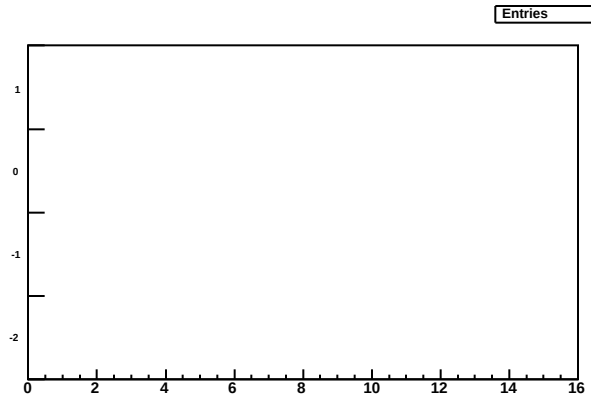


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)